

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT												
NATURE OF CONVEYANCE:	ASSIGNMENT												
CONVEYING PARTY DATA													
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Chung-Cheng CHOU</td> <td>08/27/2013</td> </tr> <tr> <td>Po-Hao LEE</td> <td>08/27/2013</td> </tr> <tr> <td>Jonathan Tehan CHEN</td> <td>08/27/2013</td> </tr> </tbody> </table>		Name	Execution Date	Chung-Cheng CHOU	08/27/2013	Po-Hao LEE	08/27/2013	Jonathan Tehan CHEN	08/27/2013				
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<table border="1"> <tr> <td>Name:</td> <td>Taiwan Semiconductor Manufacturing Co., Ltd.</td> </tr> <tr> <td>Street Address:</td> <td>No. 8, Li-Hsin Road 6</td> </tr> <tr> <td>Internal Address:</td> <td>Hsinchu Science Park</td> </tr> <tr> <td>City:</td> <td>Hsin-Chu</td> </tr> <tr> <td>State/Country:</td> <td>TAIWAN</td> </tr> <tr> <td>Postal Code:</td> <td>300-77</td> </tr> </table>		Name:	Taiwan Semiconductor Manufacturing Co., Ltd.	Street Address:	No. 8, Li-Hsin Road 6	Internal Address:	Hsinchu Science Park	City:	Hsin-Chu	State/Country:	TAIWAN	Postal Code:	300-77
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CORRESPONDENCE DATA													
Fax Number: 2156894905 <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i> Phone: 215-979-1000 Email: jsjulianti@duanemorris.com Correspondent Name: DUANE MORRIS LLP (TSMC) IP DEPARTMENT Address Line 1: 30 SOUTH 17TH STREET Address Line 4: PHILADELPHIA, PENNSYLVANIA 19103-4196													
ATTORNEY DOCKET NUMBER:	2013.0746/1085.01290												
NAME OF SUBMITTER:	Justus L. Getty												
Signature:	/Justus L. Getty/												

Date:

08/29/2013

Total Attachments: 2

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ATTORNEY DOCKET NO.: 2013.0746/1085.01290

ASSIGNMENT AND AGREEMENT

For value received, we, **Chung-Cheng CHOU, Po-Hao LEE, and Jonathan Tehan CHEN**, hereby transfer to **Taiwan Semiconductor Manufacturing Co., Ltd.**, with its principal place of business located at No. 8, Li-Hsin Road 6, Hsinchu Science Park, Hsin-Chu 300-77, Taiwan, Republic of China, and its successors, assigns and legal representatives, the entire right, title and interest, for the United States of America, in and to certain inventions related to **COOLING SYSTEM FOR 3D IC**, described in non-provisional patent application, and under any and all Letters Patent and any divisions, continuations, continuations-in-part, reexamination certificates, reissues, and extensions thereof, or claiming priority therefrom that may be granted in the United States for said inventions; and we also concurrently hereby sell, assign and transfer to Taiwan Semiconductor Manufacturing Co., Ltd. the entire right, title and interest in and to said inventions for all countries foreign to the United States, including all rights of priority arising from the application aforesaid, and all the rights and privileges under any and all forms of protection, including Letters Patent, that may be granted in said countries foreign to the United States for said inventions.

We authorize Taiwan Semiconductor Manufacturing Co., Ltd. to make application for such protection in its own name and maintain such protection in any and all countries foreign to the United States, and to invoke and claim for any application for patent or other form of protection for said inventions, without further authorization from us, any and all benefits, including the right of priority provided by any and all treaties, conventions, or agreements.

We hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country in proof of the right of Taiwan Semiconductor Manufacturing Co., Ltd. to apply for patent or other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.

We request that any and all patents for said inventions be issued to Taiwan Semiconductor Manufacturing Co., Ltd. in the United States and in all countries foreign to the United States, or to such nominees as Taiwan Semiconductor Manufacturing Co., Ltd. may designate.

We agree that, when requested, we shall, without charge to Taiwan Semiconductor Manufacturing Co., Ltd. but at its expense, sign all papers, and do all acts which may be necessary, desirable or convenient in connection with said applications, patents, or other forms of protection.

Inventor No. 1

Dated: Aug. 27, 2013

Chung-Cheng Chou
Chung-Cheng CHOU

Residence:

Unit 1, 4th Floor, No. 300, Chung-zheng Road, Hsin-Chu, Taiwan, R.O.C.

Inventor No. 2

Dated: Aug. 27, 2013

Po-Hao Lee
Po-Hao LEE

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Inventor No. 3

Dated: Aug 27, 2013

Jonathan Tehan Chen
Jonathan Tehan Chen

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