

## PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT												
NATURE OF CONVEYANCE:	ASSIGNMENT												
CONVEYING PARTY DATA													
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Joseph SHOR</td> <td>08/27/2013</td> </tr> <tr> <td>George L. GEANNOPOULOS</td> <td>08/27/2013</td> </tr> <tr> <td>Fabrice PAILLET</td> <td>08/27/2013</td> </tr> <tr> <td>Lan D. VU</td> <td>08/28/2013</td> </tr> <tr> <td>Oleg DADASHEV</td> <td>09/08/2013</td> </tr> </tbody> </table>		Name	Execution Date	Joseph SHOR	08/27/2013	George L. GEANNOPOULOS	08/27/2013	Fabrice PAILLET	08/27/2013	Lan D. VU	08/28/2013	Oleg DADASHEV	09/08/2013
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RECEIVING PARTY DATA													
<table border="1"> <tr> <td>Name:</td> <td>Intel Corporation</td> </tr> <tr> <td>Street Address:</td> <td>2200 Mission College Boulevard</td> </tr> <tr> <td>City:</td> <td>Santa Clara</td> </tr> <tr> <td>State/Country:</td> <td>CALIFORNIA</td> </tr> <tr> <td>Postal Code:</td> <td>95054</td> </tr> </table>		Name:	Intel Corporation	Street Address:	2200 Mission College Boulevard	City:	Santa Clara	State/Country:	CALIFORNIA	Postal Code:	95054		
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PROPERTY NUMBERS Total: 1													
<table border="1"> <thead> <tr> <th>Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr> <td>PCT Number:</td> <td>US2013058751</td> </tr> </tbody> </table>		Property Type	Number	PCT Number:	US2013058751								
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CORRESPONDENCE DATA													
<p>Fax Number: 4087208383  <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i></p> <p>Phone: 4087208300        Email: emily_wolford@bstz.com        Correspondent Name: Blakely, Sokoloff, Taylor &amp; Zafman LLP        Address Line 1: 1279 Oakmead Parkway        Address Line 4: Sunnyvale, CALIFORNIA 94085-4040</p>													
ATTORNEY DOCKET NUMBER:	42P58238PCT												
NAME OF SUBMITTER:	Usman A. Mughal, Reg. No. 62,887												
Signature:	/Usman A. Mughal/												

CH \$40.00 US2013058751

Date:

09/10/2013

**Total Attachments: 6**

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## **ASSIGNMENT**

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

**Joseph SHOR; George L. GEANNOPOULOS; Fabrice PAILLET; Lan D. VU; Oleg DADASHEV**

hereby sell, assign, and transfer to

### **Intel Corporation**

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

### **BANDGAP REFERENCE CIRCUIT WITH LOW IMPEDANCE OUTPUT STAGE AND SELF-POWER-ON-DETECT**

**(I hereby authorize and request my attorney, associated with customer number 45209, to insert on the designated lines below, the filing date and application number of said application when known.)**

which was filed on September 09, 2013 as

International Application Number PCT/US2013/058751 and

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

and agree that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all Additional Applications, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and legal representatives all facts known to the undersigned relating to said inventions and improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or legal representatives in securing and maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Each Inventor: Please Sign and Date Below:

27-8-2013  
Date Name: Joseph SHOR

Date Name: George L. GEANNOPOULOS

Date Name: Fabrice PAILLET

Date Name: Lan D. VU

Date Name: Oleg DADASHEV

Date Name:

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Date Name:

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Date Name:

Assignment Document Return Address:  
BLAKELY, SOROKOFF, TAYLOR & ZAPMAN LLP  
1279 Oakmead Parkway  
Sunnyvale, CA 94085-4040  
Telephone: (503) 439-8778

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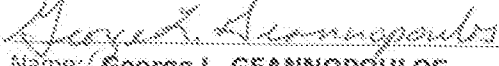
and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

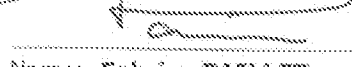
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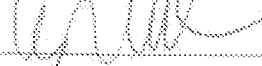
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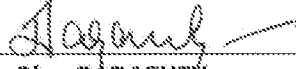
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