

RECORDATION FORM COVER SHEET  
PATENTS ONLY

Atty Ref/Docket No.: 303.000001

Patent and Trademark Office

---

To the Director of the U.S. Patent and Trademark Office: Please record the attached original documents or copy thereof.

---

1. Name of conveying party(ies):

Steven K. Hsu, Mark A. Anders and Ram K.  
Krishnamurthy

Additional name(s) of conveying party(ies) attached?

☐ Yes ☒ No

3. Nature of conveyance:

☒ Assignment☐ Merger☐ Security Agreement ☐ Change of Name☐ OtherExecution Date: November 22, 2011

2. Name and address of receiving party(ies):

Name: Micron Technology, Inc.

Street Address: 8000 So. Federal Way

City: Boise State: ID Zip: 83716-9632Country: United States of AmericaAdditional name(s) & address(es) attached? ☐ Yes  
☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s)

11/411,647, filed April 26, 2006

B. Patent No.(s)

7,352,209

Additional numbers attached? ☐ Yes ☒ No5. Name and address of party to whom correspondence  
concerning document should be mailed:Name: Mark V. Muller

Address:

Schwegman, Lundberg & Woessner, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402--09386. Total number of applications and patents involved: 17. Total fee (37 CFR 3.41): \$ 40.00☐ Enclosed☒ Authorized to be charged to deposit account  
19-07438. Please charge any additional fees or credit any over  
payments to our Deposit Account No.: 19-0743

---

**DO NOT USE THIS SPACE**

---

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy  
of the original document.Mark V. Muller/Reg. No. 37,509

Name of Person Signing

Mark V. Muller

Signature

SEPTEMBER 10, 2013

Date

Total number of pages including cover sheet: 116

Mail documents to be recorded with required cover sheet information to:

**Commissioner of Patents and Trademarks**  
**Mail Stop Assignment Recordation Services****P.O. Box 1450****Alexandria, VA 22313-1450**

CH \$40.00 190743 735220

**PATENT****700510119****REEL: 031305 FRAME: 0738**

FORM PTO-1594  
01-10-96:CMGRECORDATION FORM COVER SHEET  
PATENTS ONLYU.S. DEPARTMENT OF COMMERCE  
Patent and Trademark Office

Atty Ref/Docket No.: 884.453US1

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Steven K. Hsu, Mark A. Anders and Ram K. Krishnamurthy

2. Name and address of receiving party(ies):

Name: Intel Corporation

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

Street Address: 2625 Walsh Avenue

3. Nature of conveyance:

☒ Assignment☐ Merger☐ Security Agreement☐ Change of Name

City: Santa Clara State: CA ZIP: 95051

☐ OtherAdditional name(s) & address(es) attached? ☐ Yes ☒ No

Execution Date: June 28, 2001, June 28, 2001, June 28, 2001

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: June 28, 2001, June 28, 2001, June 28, 2001

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Danny J. Padys

6. Total number of applications and patents involved: 1

Address: Schwegman, Lundberg, Woessner & Kluth, P.A.  
P.O. Box 2938  
Minneapolis, MN 55403

7. Total fee (37 CFR 3.41)-\$ 40.00

☐ Enclosed☒ Authorized to be charged to deposit account8. Please charge any additional fees or credit any overpayments to our  
Deposit account number: 19-0743

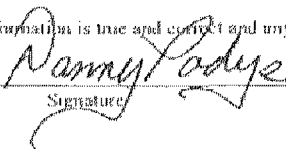
DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and my attached copy is a true copy of the original document.

Danny J. Padys/Reg. No. 35,635

Name of Person Signing

  
Signature

June 29, 2001

Date

Total number of pages including cover sheet: 3

OMB No. 0651-0011 (exp. 4/94)

Do not detach this portion

Mail documents to be recorded with required cover sheet information to:

Commissioner of Patents and Trademarks  
Box Assignments  
Washington, D.C. 20231

Public burden reporting for this sample cover sheet is estimated to average about 30 minutes per document to be recorded, including time for reviewing the document and gathering the data needed, and completing and reviewing the sample cover sheet. Send comments regarding this burden estimate to the U.S. Patent and Trademark Office, Office of Information Systems, PK2-10007, Washington, D.C. 20231, and to the Office of Management and Budget, Paperwork Reduction Project (0651-0011), Washington, D.C. 20503

PATENT

REEL: 031305 FRAME: 0739

## ASSIGNMENT

WHEREAS, WE, Steven K. Hsu, residing at One Dover Way, Lake Oswego, OR 97034, and Mark A. Anders, residing at 2250 NW Thorncroft Drive, #337, Hillsboro, OR 97124, and Ram K. Krishnamurthy, residing at 5263 NW 152nd Place, Portland, OR 97229, made certain new and useful inventions and improvements for which We executed an application for Letters Patent of the United States on Even Date Herewith, which is entitled VOLTAGE-LEVEL CONVERTER

AND WHEREAS, Intel Corporation, a corporation organized and existing under and by virtue of the laws of the State of Delaware, and having an office and place of business at 2625 Walsh Avenue, Santa Clara, CA 95051, (hereinafter "Assignee") is desirous of acquiring the entire right, title and interest in and to said inventions, improvements and application and in and to the Letters Patent to be obtained therefore;

NOW, THEREFORE, to all whom it may concern, be it known that for good and valuable consideration, the receipt and sufficiency whereof is hereby acknowledged, we have sold, assigned, and transferred, and by these presents do sell, assign and transfer unto said Assignee, its successors or assigns, the entire right, title and interest for all countries in and to all inventions and improvements disclosed in the aforesaid application, and in and to the said application, all divisions, continuations, continuations-in-part, or renewals thereof, all Letters Patent which may be granted therefrom, and all reissues or extensions of such patents, and in and to any and all applications which have been or shall be filed in any foreign countries for Letters Patent on the said inventions and improvements, including an assignment of all rights under the provisions of the International Convention, and all Letters Patent of foreign countries which may be granted therefrom; and we do hereby authorize and request the Commissioner of Patents and Trademarks to issue any and all United States Letters Patent for the aforesaid inventions and improvements to the said Assignee as the assignee of the entire right, title and interest in and to the same, for the use of the said Assignee, its successors and assigns.

AND, for the consideration aforesaid, we do hereby agree that we and our executors and legal representatives will make, execute and deliver any and all other instruments in writing including any and all further application papers, affidavits, assignments and other documents, and will communicate to said Assignee, its successors and representatives all facts known to us relating to said improvements and the history thereof and will testify in all legal proceedings and generally do all things which may be necessary or desirable more effectually to secure to and vest in said Assignee, its successors or assigns the entire right, title and interest in and to the said improvements, inventions, applications, Letters Patent, rights, titles, benefits, privileges and advantages hereby sold, assigned and conveyed, or intended so to be.

JUN-29-2001 14:15

MRL OREGON

583 696 1442 P.05/06

Docket No.: 684,453US1  
Serial No.: Not Assigned  
Filing Date: Event Date Herewith  
Page 2 of 2

AND, furthermore we covenant and agree with said Assignee, its successors and assigns, that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by me/us and that full right to convey the same as herein expressed is possessed by us.

IN TESTIMONY WHEREOF, I have hereunto set my hand this 28 day of June, 2001.

  
Steven K. Hsu

IN TESTIMONY WHEREOF, I have hereunto set my hand this 28 day of June, 2001.

  
Mark A. Anders

IN TESTIMONY WHEREOF, I have hereunto set my hand this 28 day of June, 2001.

  
Ram K. Krishnamurthy

PATENT

REEL: 031305 FRAME: 0741

ASSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, Intel Corporation, a Delaware Corporation, having an address of 2200 Mission College Boulevard, Santa Clara, CA 95052, U.S.A. ("Assignor"), hereby sells, assigns, transfers and conveys to Numonyx B.V., Acting Through Its Swiss Branch, having an address of A-1 Business Centre, Z.A. Vers la Piece, Rte de l'Etraz, 1180 Rolle, Switzerland ("Assignee"), its successors, assigns and legal representatives, Assignor's entire right, title and interest in and to the U.S. Patents and U.S. Patent Applications listed on Schedule A, and any and all causes of action for past, present, and future infringement of any of said Letters Patents, subject to prior encumbrances,

each and every of the foregoing rights, titles and interests herein assigned to be held and enjoyed by Assignee, its successors, assigns and legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor had this Assignment not been made.

IN TESTIMONY WHEREOF, Assignor and Assignee having caused this Assignment to be duly executed in their respective names and behalves by affixing their hands thereto by their respective designated officer, director, or agent, whose names and titles appear below.

Executed at SANTA CLARA, CA, this 25th day of MARCH, 2008

Signature: Cary E. Klafner Signature: \_\_\_\_\_

Name: Cary E. Klafner Name: \_\_\_\_\_  
(Intel Corporation) (Numonyx B.V.)



VICE PRESIDENT, LEGAL  
& CORPORATE SECRETARY

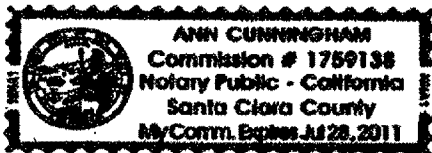
Not for recording

PATENT

REEL: 031305 FRAME: 0742

**CALIFORNIA ALL-PURPOSE ACKNOWLEDGMENT**

State of California

County of SANTA CLARAOn MARCH 25, 2008 before me, ANN CUNNINGHAM NOTARY PUBLIC  
Date Here Insert Name and Title of the Officerpersonally appeared CARY I. KLAFFER  
Name(s) of Signer(s)

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature

Signature of Notary Public

Place Notary Seal Above

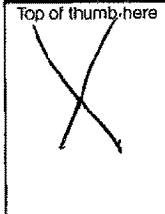
**OPTIONAL**

Though the information below is not required by law, it may prove valuable to persons relying on the document and could prevent fraudulent removal and reattachment of this form to another document.

**Description of Attached Document**Title or Type of Document: ASSIGNMENTDocument Date: MARCH 25, 2008 Number of Pages: ONESigner(s) Other Than Named Above: NUMONYX B.V.**Capacity(ies) Claimed by Signer(s)**Signer's Name: CARY I. KLAFFER  
VICE PRESIDENT, LEGAL

- ☐ Individual  
☒ Corporate Officer — Title(s): CORP. SECRETARY  
☐ Partner — ☐ Limited ☐ General  
☐ Attorney in Fact  
☐ Trustee  
☐ Guardian or Conservator  
☐ Other: \_\_\_\_\_

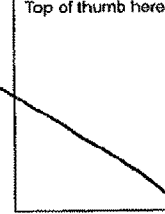
Signer Is Representing: \_\_\_\_\_

RIGHT THUMBPRINT  
OF SIGNER  
Top of thumb here

Signer's Name: \_\_\_\_\_

- ☐ Individual  
☐ Corporate Officer — Title(s): \_\_\_\_\_  
☐ Partner — ☐ Limited ☐ General  
☐ Attorney in Fact  
☐ Trustee  
☐ Guardian or Conservator  
☐ Other: \_\_\_\_\_

Signer Is Representing: \_\_\_\_\_

RIGHT THUMBPRINT  
OF SIGNER  
Top of thumb here

## Schedule A

| Country | Patent Number | Publication Number | Serial Number | Title  |
|---------|---------------|--------------------|---------------|--|
| US      | 5,202,291     |                    | 07/883,067    | HIGH C-FLOW REACTIVE ION ETCH FOR ALUMINUM PATTERNING  |
| US      | 5,216,843     |                    | 07/850,812    | POLISHING PAD CONDITIONING APPARATUS FOR WAFER PLANARIZATION PROCESS                                       |
| US      | 5,218,636     |                    | 07/665,970    | DIAL PULSE DIGIT DETECTOR  |
| US      | 5,219,791     |                    | 07/712,119    | TEOS INTERMETAL DIELECTRIC PRECLEAN FOR VIA FORMATION  |
| US      | 5,222,244     |                    | 07/630,497    | METHOD OF MODIFYING A MICROINSTRUCTION WITH OPERANDS SPECIFIED BY AN INSTRUCTION HELD IN AN ALIAS REGISTER |
| US      | 5,232,871     |                    | 07/869,227    | METHOD FOR FORMING A TITANIUM NITRIDE BARRIER LAYER  |
| US      | 5,237,202     |                    | 07/803,724    | LEAD FRAME AND SEMICONDUCTOR DEVICE USING SAME   |
| US      | 5,237,535     |                    | 07/773,228    | METHOD OF REPAIRING OVERERASED CELLS IN A FLASH MEMORY   |
| US      | 5,242,864     |                    | 07/893,765    | POLYIMIDE PROCESS FOR PROTECTING INTEGRATED PRODUCTS   |
| US      | 5,244,843     |                    | 07/809,971    | NOVEL PROCESS FOR FORMING A THIN OXIDE LAYER   |
| US      | 5,255,230     |                    | 07/816,635    | METHOD AND APPARATUS FOR TESTING THE CONTINUITY OF STATIC RANDOM ACCESS MEMORY CELLS                       |
| US      | 5,255,378     |                    | 07/981,943    | METHOD OF TRANSFERRING BURST DATA IN A MICROPROCESSOR  |
| US      | 5,256,994     |                    | 07/948,215    | PROGRAMMABLE SECONDARY CLOCK GENERATOR   |
| US      | 5,265,059     |                    | 07/698,347    | CIRCUITRY AND METHOD FOR DISCHARGING A DRAIN OF A CELL OF A NON-VOLATILE SEMICONDUCTOR MEMORY              |
| US      | 5,267,213     |                    | 07/861,093    | BIAS CIRCUITRY FOR CONTENT ADDRESSABLE MEMORY CELLS OF A FLOATING GATE NONVOLATILE MEMORY                  |
| US      | 5,270,264     |                    | 07/917,465    | PROCESS FOR FILLING SUBMICRON SPACES WITH DIELECTRIC   |
| US      | 5,274,277     |                    | 07/938,934    | HIGH SPEED OR CIRCUIT CONFIGURATION  |
| US      | 5,274,281     |                    | 07/875,961    | STATIC PLA WITH STATE MACHINE CONTROLLED BY MODEL OF BOTH MINTERM AND AN OUTPUT LINE                       |
| US      | 5,274,678     |                    | 07/815,819    | CLOCK SWITCHING APPARATUS AND METHOD FOR COMPUTER SYSTEMS  |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,276,690 | 07/830,209 | APPARATUS UTILIZING DUAL COMPARE LOGIC FOR SELF CHECKING OF FUNCTIONAL REDUNDANCY CHECK (FRC) LOGIC        |
| US | 5,276,704 | 07/971,689 | SAWC PHASE DETECTION METHOD AND APPARATUS  |
| US | 5,276,833 | 07/544,821 | DATA CACHE MANAGEMENT SYSTEM WITH TEST MODE USING INDEX REGISTERS AND CAS DISABLE AND POSTED WRITE DISABLE |
| US | 5,279,865 | 07/722,861 | HIGH THROUGHPUT INTERLEVEL DIELECTRIC GAP FILLING PROCESS  |
| US | 5,280,590 | 07/902,317 | LOGIC SUPPORT CHIP FOR AT-TYPE COMPUTER WITH IMPROVED BUS ARCHITECTURE                                     |
| US | 5,286,678 | 07/951,945 | SINGLE STEP SALICIDATION PROCESS   |
| US | 5,289,026 | 07/744,969 | ASYMMETRIC FLOATING GATE OVERLAP FOR IMPROVED DEVICE CHARACTERISTICS IN BURIED BIT-LINE DEVICES            |
| US | 5,289,412 | 07/901,395 | HIGH-SPEED BIAS-STABILIZED CURRENT MIRROR REFERENCEING CIRCUIT FOR NON-VOLATILE MEMORIES                   |
| US | 5,290,735 | 08/011,944 | THIN, HIGH LEAD COUNT PACKAGE  |
| US | 5,291,060 | 07/984,841 | LEAD FRAME AND SEMICONDUCTOR DEVICE USING SAME   |
| US | 5,291,071 | 07/881,843 | HIGH SPEED, LOW POWER OUTPUT CIRCUIT WITH TEMPERATURE COMPENSATED NOISE CONTROL                            |
| US | 5,296,094 | 07/897,768 | PROCESS FOR ETCHING SILICON DIOXIDE LAYER WITHOUT MICRO MASKING EFFECT                                     |
| US | 5,298,807 | 07/813,148 | BUFFER CIRCUITRY FOR TRANSFERRING SIGNALS FROM TTL CIRCUITRY TO DUAL RANGE CMOS CIRCUITRY                  |
| US | 5,301,097 | 07/896,195 | MULTI-STAGED CHARGE-PUMP WITH STAGGERED CLOCK PHASES FOR PROVIDING HIGH CURRENT CAPABILITY                 |
| US | 5,309,012 | 07/971,074 | PROTECTED ERASE VOLTAGE DISCHARGE TRANSISTOR IN A NONVOLATILE SEMICONDUCTOR MEMORY                         |
| US | 5,313,605 | 07/630,534 | HIGH BANDWIDTH OUTPUT HIERARCHICAL MEMORY STORE INCLUDING A CACHE, FETCH BUFFER AND ROM                    |
| US | 5,317,535 | 07/901,110 | GATE/SOURCE DISTURB PROTECTION FOR SIXTEEN-BIT FLASH EEPROM MEMORY ARRAYS                                  |
| US | 5,317,750 | 07/984,280 | MICROCONTROLLER PERIPHERAL EXPANSION BUS FOR ACCESS TO INTERNAL SPECIAL FUNCTION REGISTERS                 |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,321,583 | 07/984,336 | ELECTRICALLY CONDUCTIVE INTERPOSER AND ARRAY PACKAGE CONCEPT FOR INTERCONNECTING TO A CIRCUIT BOARD  |
| US | 5,339,442 | 07/954,743 | IMPROVED SYSTEM OF RESOLVING CONFLICTING DATA PROCESSING MEMORY ACCESS REQUESTS  |
| US | 5,340,370 | 08/146,923 | NOVEL SLURRIES FOR CHEMICAL MECHANICAL POLISHING   |
| US | 5,341,330 | 08/145,732 | METHOD FOR WRITING TO A FLASH MEMORY ARRAY DURING ERASE SUSPEND INTERVALS  |
| US | 5,345,577 | 07/981,329 | DRAM REFRESH CONTROLLER WITH IMPROVED BUS ARBITRATION SCHEME   |
| US | 5,350,484 | 07/941,412 | A METHOD FOR THE ANISOTROPIC ETCHING OF METAL FILMS IN THE FABRICATION OF INTERCONNECTS  |
| US | 5,358,891 | 08/084,795 | TRENCH ISOLATION WITH PLANAR TOPOGRAPHY AND METHOD OF FABRICATION  |
| US | 5,365,128 | 08/036,219 | HIGH-RESOLUTION SYNCHRONOUS DELAY LINE   |
| US | 5,367,679 | 07/998,555 | ROUND ROBIN SCHEDULER USING A SCHEDULER CARRY OPERATION FOR ARBITRATION  |
| US | 5,373,508 | 07/923,853 | DETECTING VALID DATA FROM A TWISTED PAIR MEDIUM  |
| US | 5,379,443 | 08/133,771 | MICROPROCESSOR PROVIDING ENCODED INFORMATION ON BYTE ENABLE LINES INDICATING WHETHER READING CODE OR DATA, LOCATION OF CODE DATA ON DATA LINES, AND BIT WIDTH OF CODE DATA |
| US | 5,398,075 | 08/154,955 | ANALOG CHROMA KEYING ON COLOR DATA   |
| US | 5,404,400 | 08/024,402 | OUTCALLING APPARATUS   |
| US | 5,410,263 | 08/023,673 | DELAY LINE LOOP FOR ON-CHIP CLOCK SYNTHESIS WITH ZERO SKEW AND 50% DUTY CYCLE  |
| US | 5,412,793 | 08/277,369 | METHOD FOR TESTING ERASE CHARACTERISTICS OF A FLASH MEMORY ARRAY   |
| US | 5,416,782 | 07/969,465 | METHOD AND APPARATUS FOR IMPROVING DATA FAILURE RATE TESTING FOR MEMORY ARRAYS   |
| US | 5,421,021 | 08/047,696 | A CIRCUIT INCLUDING A LATCHING UNIT FOR HOLDING INSTRUCTIONS DURING EXECUTION FOR RE-EXECUTION FOLLOWING EXCEPTION HANDLING  |
| US | 5,422,586 | 08/119,423 | METHOD AND APPARATUS FOR A TWO PHASE BOOTSTRAP CHARGE PUMP   |
| US | 5,426,462 | 08/319,779 | APPARATUS FOR ENCODING SIGNALS USING A CONFIGURABLE TRANSFORM CIRCUIT  |

## Schedule A

|    |           |            |   |
|----|-----------|------------|---|
| US | 5,428,565 | 08/212,350 | SINGLE STAGE SENSING APPARATUS FOR A CONTENT ADDRESSABLE MEMORY   |
| US | 5,432,469 | 08/248,419 | METHOD AND APPARATUS FOR A TWO PHASE BOOTSTRAP CHARGE PUMP  |
| US | 5,434,534 | 08/158,418 | CMOS VOLTAGE REFERENCE CIRCUIT  |
| US | 5,434,892 | 08/307,502 | A THROTLING CIRCUIT FOR A DATA TRANSFER SYSTEM  |
| US | 5,436,585 | 08/242,790 | BIMOS DRIVER CIRCUIT WITH INTEGRATED VOLTAGE SUPPLY CONVERSION  |
| US | 5,437,021 | 07/897,897 | PROCESSOR SPEED/TIME INDEPENDENT BIOS TIMER   |
| US | 5,442,586 | 08/119,425 | METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT CURRENT PROVIDED BY A CHARGE PUMP CIRCUIT   |
| US | 5,442,694 | 08/226,678 | RING TONE DETECTION FOR A TELEPHONE SYSTEM  |
| US | 5,444,602 | 08/201,895 | ELECTRONIC PACKAGE THAT HAS A DIE COUPLED TO A LEAD FRAME BY A DIELECTRIC TAPE AND A HEAT SINK THAT PROVIDES BOTH AN ELECTRICAL AND A THERMAL PATH BETWEEN THE DIE AND THE LEAD FRAME |
| US | 5,446,839 | 08/067,858 | METHOD FOR CONTROLLING DATAFLOW BETWEEN A PLURALITY OF CIRCULAR BUFFERS   |
| US | 5,446,867 | 07/890,937 | MICROPROCESSOR PLL CLOCK CIRCUIT WITH SELECTABLE DELAYED FEEDBACK   |
| US | 5,450,544 | 08/357,544 | METHOD AND APPARATUS FOR DATA BUFFERING AND QUEUE MANAGEMENT OF DIGITAL MOTION VIDEO SIGNALS  |
| US | 5,453,708 | 08/368,335 | CLOCKING SCHEME FOR LATCHING OF A DOMINO OUTPUT   |
| US | 5,454,089 | 08/141,685 | BRANCH LOOK AHEAD ADDER FOR USE IN AN INSTRUCTION PIPELINE SEQUENCER  |
| US | 5,455,794 | 08/403,633 | METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT CURRENT PROVIDED BY A CHARGE PUMP CIRCUIT   |
| US | 5,455,800 | 08/329,773 | APPARATUS FOR IMPROVING THE PROGRAM AND ERASE PERFORMANCE OF A FLASH EEPROM MEMORY ARRAY  |
| US | 5,455,957 | 08/394,560 | METHOD AND APPARATUS FOR CONDUCTING BUS TRANSACTIONS BETWEEN TWO CLOCK INDEPENDENT BUS AGENTS OF A COMPUTER SYSTEM  |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,457,648 | 08/208,800 | RANDOM ACCESS MEMORY WITH DIGITAL SIGNALS RUNNING OVER THE SMALL SIGNAL REGION OF THE ARRAY  |
| US | 5,469,365 | 08/285,495 | MULTIPLE LAYER PROGRAMMABLE LAYOUT FOR VERSION IDENTIFICATION  |
| US | 5,463,668 | 08/216,668 | LOW IMPACT COLLISION DETECTION METHOD  |
| US | 5,463,748 | 08/085,637 | INSTRUCTION BUFFER FOR ALIGNING INSTRUCTION SETS USING BOUNDARY DETECTION  |
| US | 5,463,757 | 08/185,449 | COMMAND INTERFACE BETWEEN USER COMMANDS AND A MEMORY DEVICE  |
| US | 5,467,460 | 08/234,587 | METHOD AND APPARATUS FOR MINIMIZING THE DATA TRANSFER FROM A MEMORY DURING A CACHE MISS  |
| US | 5,469,093 | 08/333,264 | CURRENT MIRROR DRIVE CIRCUIT WITH HIGH BREAKDOWN VOLTAGE   |
| US | 5,469,164 | 08/129,899 | CIRCUIT AND METHOD FOR DIGITAL TO ANALOG SIGNAL CONVERSION   |
| US | 5,471,604 | 07/869,780 | METHOD FOR LOCATING SECTOR DATA IN A MEMORY DISK BY EXAMINING A PLURALITY OF HEADERS NEAR AN INITIAL POINTER   |
| US | 5,471,637 | 08/429,658 | METHOD AND APPARATUS FOR CONDUCTING BUS TRANSACTIONS BETWEEN TWO CLOCK INDEPENDENT BUS AGENTS OF A COMPUTER SYSTEM USING A TRANSACTION BY TRANSACTION DETERMINISTIC REQUEST/RESPONSE PROTOCOL AND BURST TRANSFER |
| US | 5,473,190 | 08/167,531 | TAB TAPE   |
| US | 5,473,753 | 07/869,749 | METHOD OF MANAGING DEFECTS IN FLASH DISK MEMORIES  |
| US | 5,475,633 | 08/252,320 | CACHE MEMORY UTILIZING PSEUDO STATIC FOUR TRANSISTOR MEMORY CELL   |
| US | 5,475,693 | 08/364,546 | ERROR MANAGEMENT FOR FLASH PROCESSES FOR FLASH EEPROM MEMORY ARRAYS  |
| US | 5,477,418 | 08/275,599 | IMPROVED I/O CONNECTOR FOR ADD IN PRINTED CIRCUIT CARDS FOR COMPUTER SYSTEMS   |
| US | 5,479,633 | 07/869,763 | A METHOD OF CONTROLLING CLEAN-UP OF A SOLID STATE MEMORY DISK STORING FLOATING SECTOR DATA   |
| US | 5,485,422 | 08/252,864 | DRAIN BIAS MULTIPLEXING FOR MULTIPLE BIT FLASH CELL  |
| US | 5,488,570 | 08/268,270 | ENCODING AND DECODING VIDEO SIGNALS USING ADAPTIVE FILTER SWITCHING CRITERIA   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,488,639 | 08/171,554 | PARALLEL MULTISTAGE SYNCHRONIZATION METHOD AND APPARATUS   |
| US | 5,490,102 | 08/260,044 | LOW CAPACITANCE CONTENT ADDRESSABLE MEMORY CELL  |
| US | 5,490,109 | 08/267,472 | METHOD AND APPARATUS FOR PREVENTING OVER ERASURE OF FLASH EEPROM MEMORY DEVICES  |
| US | 5,490,264 | 08/130,023 | GENERALLY DIAGONAL MAPPING OF ADDRESS SPACE FOR ROW/COLUMN ORGANIZER MEMORIES  |
| US | 5,496,463 | 08/326,669 | A LOW POWER VOLTAGE DETECTOR CIRCUIT INCLUDING A FLASH MEMORY CELL   |
| US | 5,497,354 | 08/253,902 | BIT MAP ADDRESSING SCHEMES FOR FLASH MEMORY  |
| US | 5,497,355 | 08/253,842 | SYNCHRONOUS ADDRESS LATCHING FOR MEMORY ARRAYS   |
| US | 5,503,959 | 08/046,477 | LITHOGRAPHIC TECHNIQUE FOR PATTERNING A SEMICONDUCTOR DEVICE   |
| US | 5,504,875 | 08/032,686 | NONVOLATILE MEMORY WITH A PROGRAMMABLE OUTPUT OF SELECTABLE WIDTH AND A METHOD FOR CONTROLLING THE NONVOLATILE MEMORY TO SWITCH BETWEEN DIFFERENT OUTPUT WIDTHS  |
| US | 5,506,803 | 08/361,872 | APPARATUS AND METHOD FOR MINIMIZING VERIFY TIME IN A SEMICONDUCTOR MEMORY BY CONSTANTLY CHARGING N-WELL CAPACITANCE  |
| US | 5,506,885 | 08/289,315 | METHOD OF ADAPTING PROCESSING PARAMETERS IN A CALL PROCESSING SYSTEM   |
| US | 5,510,740 | 08/272,879 | METHOD FOR SYNCHRONIZING CLOCKS UPON RESET   |
| US | 5,511,195 | 08/152,348 | DRIVER, COMPUTER IMPLEMENTED PROCESS AND COMPUTER SYSTEM FOR PROCESSING DATA USING LOADABLE MICROCODE RUNNING ON A PROGRAMMABLE PROCESSOR  |
| US | 5,513,136 | 08/358,978 | NONVOLATILE MEMORY WITH BLOCKS AND CIRCUITRY FOR SELECTIVELY PROTECTING THE BLOCKS FROM MEMORY OPERATIONS  |
| US | 5,513,331 | 08/412,607 | METHOD AND APPARATUS FOR AUTOMATICALLY CONFIGURING SYSTEM MEMORY ADDRESS SPACE OF A COMPUTER SYSTEM HAVING A MEMORY SUBSYSTEM WITH INDETERMINISTIC NUMBER OF MEMORY UNITS OF INDETERMINISTIC SIZES DURING SYSTEM RESET |
| US | 5,517,138 | 08/316,546 | DUAL ROW SELECTION USING MULTIPLEXED TRI-LEVEL DECODER   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,517,452 | 08/403,407 | METHOD AND APPARATUS FOR COMPENSATING FOR SUBTHRESHOLD CURRENT IN A STATIC RAM   |
| US | 5,524,231 | 08/086,043 | NONVOLATILE MEMORY CARD WITH AN ADDRESS TABLE AND AN ADDRESS TRANSLATION LOGIC FOR MAPPING OUT DEFECTIVE BLOCKS WITHIN THE MEMORY CARD |
| US | 5,524,266 | 08/246,779 | SYSTEM HAVING MULTIPLE PHASE BOOSTED CHARGE PUMP WITH A PLURALITY OF STAGES  |
| US | 5,525,913 | 08/325,879 | POWER VALIDATION TOOL FOR MICROPROCESSOR SYSTEMS   |
| US | 5,526,311 | 08/175,599 | METHOD AND CIRCUITRY FOR ENABLING AND PERMANENTLY DISABLING TEST MODE ACCESS IN A FLASH MEMORY DEVICE                                  |
| US | 5,532,940 | 08/234,978 | PROCESS, APPARATUS AND SYSTEM FOR SELECTING QUANTIZATION LEVELS FOR ENCODING VIDEO SIGNALS   |
| US | 5,535,369 | 08/485,661 | METHOD FOR ALLOCATING MEMORY IN A SOLID STATE MEMORY DISK  |
| US | 5,535,397 | 08/085,409 | METHOD AND APPARATUS FOR PROVIDING A CONTEXT SWITCH IN RESPONSE TO AN INTERRUPT IN A COMPUTER PROCESS                                  |
| US | 5,537,069 | 08/413,951 | APPARATUS AND METHOD FOR SELECTING A TAP RANGE IN A DIGITAL DELAY LINE   |
| US | 5,538,141 | 08/312,831 | TEST FLOW ASSURANCE USING MEMORY IMPRINTING  |
| US | 5,544,071 | 08/174,729 | CRITICAL PATH PREDICTION FOR DESIGN OF CIRCUITS  |
| US | 5,546,447 | 08/268,265 | DISPLAYING CALLER IDENTIFICATION INFORMATION IN A COMPUTER SYSTEM  |
| US | 5,546,539 | 08/542,039 | METHOD AND APPARATUS FOR UPDATING FILES OF A PLURALITY OF STORAGE DEVICES THROUGH PROPAGATION OF FILES OVER A NETWORK                  |
| US | 5,546,568 | 08/176,944 | CPU CLOCK CONTROL UNIT   |
| US | 5,552,832 | 08/329,524 | RUN LENGTH ENCODING SEQUENCE FOR VIDEO SIGNALS   |
| US | 5,553,030 | 08/491,792 | METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT VOLTAGE PROVIDED BY A CHARGE PUMP CIRCUIT  |
| US | 5,556,811 | 08/468,972 | UTILIZING HIGH PERFORMANCE, LOW ESR AND LOW INDUCTANCE CAPACITORS TO IMPROVE TRANSIENT RESPONSE OF ON-BOARD VOLTAGE REGULATED PACKAGES |
| US | 5,557,075 | 08/266,150 | PARALLEL FLEXIBLE TRANSMISSION CABLE   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,557,330 | 08/332,583 | ENCODING VIDEO SIGNALS USING SELECTIVE PRE FILTERING   |
| US | 5,557,749 | 07/961,475 | SYSTEM FOR AUTOMATICALLY COMPRESSING AND DECOMPRESSING DATA FOR SENDER AND RECEIVER PROCESSES UPON DETERMINATION OF A COMMON COMPRESSION/DECOMPRESSION METHOD UNDERSTOOD BY BOTH SENDER AND RECEIVER PROCESSES |
| US | 5,559,722 | 08/234,976 | PROCESS, APPARATUS AND SYSTEM FOR TRANSFORMING SIGNALS USING PSEUDO SIMD PROCESSING  |
| US | 5,565,759 | 08/356,905 | SMART BATTERY PROVIDING BATTERY LIFE AND RECHARGE TIME PREDICTION  |
| US | 5,568,569 | 08/258,613 | METHOD AND APPARATUS FOR ANALYZING DIGITAL VIDEO IMAGES BY MERGING DISPLACEMENT VECTORS  |
| US | 5,570,050 | 08/590,726 | ZERO STANDBY CURRENT POWER UP RESET CIRCUIT  |
| US | 5,572,198 | 08/279,954 | METHOD AND APPARATUS FOR ROUTING IN REDUCED SWITCH MATRICES TO PROVIDE ONE HUNDRED PERCENT COVERAGE  |
| US | 5,572,465 | 08/451,037 | POWER SUPPLY CONFIGURED SENSING SCHEME FOR FLASH EEPROM  |
| US | 5,576,636 | 08/515,248 | LOW POWER PROGRAMMABLE LOGIC ARRAYS  |
| US | 5,579,530 | 08/389,078 | METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING ACCESS TIME TO A RESOURCE SHARED BETWEEN A PERIPHERAL BUS AND A HOST BUS BY DYNAMICALLY CONTROLLING THE SIZE OF BURST DATA TRANSFERS ON THE PERIPHERAL BUS     |
| US | 5,581,594 | 08/599,032 | METHOD AND APPARATUS FOR INITIATING COMMUNICATION VIA PAGING A MOBILE COMPUTING DEVICE   |
| US | 5,589,829 | 08/329,340 | DECODING VARIABLE LENGTH ENCODED SIGNALS   |
| US | 5,590,289 | 08/384,472 | METHOD AND APPARATUS FOR INITIALIZING A COMPUTER SYSTEM HAVING CENTRAL AND DISTRIBUTED ADDRESS DECODE MEMORY BUS RESOURCES   |
| US | 5,592,425 | 08/576,705 | METHOD AND APPARATUS FOR TESTING A MEMORY WHERE DATA IS PASSED THROUGH THE MEMORY FOR COMPARISON WITH DATA READ FROM THE MEMORY  |
| US | 5,594,360 | 08/607,521 | LOW CURRENT REDUCED AREA PROGRAMMING VOLTAGE DETECTOR FOR FLASH MEMORY   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,595,526 | 08/347,813 | METHOD AND APPARATUS FOR ENDPOINT DETECTION IN A CHEMICAL/MECHANICAL PROCESS FOR POLISHING A SUBSTRATE                     |
| US | 5,596,734 | 08/575,178 | METHOD AND APPARATUS FOR PROGRAMMING EMBEDDED MEMORIES OF A VARIETY OF INTEGRATED CIRCUITS USING THE IEEE TEST ACCESS PORT |
| US | 5,600,375 | 08/302,595 | RENDERING AN INTER VERSES INTRA VIDEO ENCODING DECISION BASED UPON A VERTICAL GRADIENT MEASURE OF TARGET VIDEO FRAMES      |
| US | 5,602,502 | 08/536,853 | CIRCUIT FOR DETECTING WHEN A SUPPLY OUTPUT VOLTAGE EXCEEDS A PREDETERMINED LEVEL   |
| US | 5,602,719 | 08/558,032 | NO HANDLE ZIP SOCKET   |
| US | 5,602,902 | 08/410,212 | FOUR WIRE MODEM SIGNAL SWITCHING FOR VOICE AND DATA APPLICATIONS   |
| US | 5,603,036 | 08/019,617 | POWER MANAGEMENT SYSTEM FOR COMPONENTS USED IN BATTERY POWERED APPLICATIONS  |
| US | 5,603,412 | 08/595,796 | APPARATUS AND SEMICONDUCTOR COMPONENT FOR ASSURING TEST FLOW COMPLIANCE  |
| US | 5,608,741 | 08/156,427 | FAST PARITY GENERATOR USING COMPLEMENT PASS-TRANSISTOR LOGIC   |
| US | 5,611,054 | 08/567,405 | METHOD AND APPARATUS FOR DECODING AND RECODING OF ADDRESSES  |
| US | 5,612,916 | 08/707,925 | MEMORY BYPASS MODE   |
| US | 5,612,991 | 08/414,914 | USE OF RSSI INDICATION FOR IMPROVED DATA TRANSMISSION OVER AMPS NETWORK  |
| US | 5,621,245 | 08/741,526 | APPARATUS FOR OPTIMIZING OPERATING PARAMETERS OF AN INTEGRATED CIRCUIT PACKAGE HAVING A VOLTAGE REGULATOR MOUNTED THEREON  |
| US | 5,621,798 | 08/423,306 | METHOD AND APPARATUS FOR COOPERATIVE MESSAGING   |
| US | 5,623,620 | 08/085,542 | SPECIAL TEST MODES SHARED RESOURCE IN A MEMORY DEVICE  |
| US | 5,625,303 | 08/534,485 | MULTIPLEXER HAVING A PLURALITY OF INTERNAL DATA PATHS THAT OPERATE AT DIFFERENT SPEEDS                                     |
| US | 5,627,788 | 08/437,090 | MEMORY UNIT WITH BIT LINE DISCHARGER   |
| US | 5,633,102 | 08/396,926 | LITHOGRAPHY USING A NEW PHASE-SHIFTING RETICLE   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,636,362 | 08/314,416 | PROGRAMMABLE HIGH WATERMARK IN STACK FRAME CACHE USING SECOND REGION AS A STORAGE IF FIRST REGION IS FULL AND AN EVENT HAVING A PREDETERMINED MINIMUM PRIORITY                               |
| US | 5,637,018 | 08/333,139 | HI-JACK HINGED CONNECTION ADAPTER FOR INPUT/OUTPUT CARDS   |
| US | 5,638,436 | 08/624,111 | VOICE DETECTION  |
| US | 5,640,083 | 08/458,805 | METHOD AND APPARATUS FOR IMPROVING POWER UP TIME OF FLASH EEPROM MEMORY ARRAYS   |
| US | 5,640,519 | 08/628,914 | METHOD AND APPARATUS TO IMPROVE LATENCY EXPERIENCED BY AN AGENT UNDER A ROUND ROBIN ARBITRATION SCHEME   |
| US | 5,642,263 | 08/689,698 | CIRCUIT BOARD RETENTION SYSTEM   |
| US | 5,643,000 | 08/691,859 | METHOD AND APPARATUS FOR PROVIDING PROCESSOR FIELD UPGRADABILITY TO A MOTHERBOARD  |
| US | 5,666,556 | 08/710,572 | A METHOD AND APPARATUS FOR REDIRECTING REGISTER ACCESS REQUESTS WHEREIN THE REGISTER SET IS SEPARATE FROM A CENTRAL PROCESSOR UNIT   |
| US | 5,668,949 | 08/545,374 | SYSTEM UTILIZING MULTIPLE ADDRESS DECODE RESOURCES AND DECODER RECEIVING ADDRESS DETERMINES ADDRESS CORRESPONDING TO RESOURCES BASED ON SELECT AND READY SIGNALS BY THAT PARTICULAR RESOURCE |
| US | 5,671,179 | 08/764,666 | LOW POWER PULSE GENERATOR FOR SMART VOLTAGE FLASH EEPROM   |
| US | 5,687,360 | 08/431,016 | BRANCH PREDICTOR USING MULTIPLE PREDICTION HEURISTICS AND A HEURISTIC IDENTIFIER IN THE BRANCH INSTRUCTION   |
| US | 5,691,883 | 08/578,984 | MULTIPLE INTAKE DUCT MICROPROCESSOR COOLING SYSTEM   |
| US | 5,694,149 | 08/253,737 | VERTICALLY SCALING IMAGE SIGNALS USING DIGITAL DIFFERENTIAL ACCUMULATOR PROCESSING   |
| US | 5,706,218 | 08/648,553 | RANDOM NUMBER GENERATOR  |
| US | 5,710,902 | 08/524,065 | INSTRUCTION DEPENDENCY CHAIN IDENTIFIER  |
| US | 5,712,578 | 08/579,095 | PLA ARCHITECTURE HAVING IMPROVED CLOCK SIGNAL TO OUTPUT TIMING USING A TYPE-I DOMINO AND PLANE   |
| US | 5,712,857 | 08/536,112 | METHODS AND APPARATUS FOR CORRELATING STUCK-AT FAULT TEST COVERAGE AND CURRENT LEAKAGE FAULT TEST COVERAGE   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,721,882 | 08/807,212 | METHOD AND APPARATUS FOR INTERFACING MEMORY DEVICES OPERATING AT DIFFERENT SPEEDS TO A COMPUTER SYSTEM BUS   |
| US | 5,721,927 | 08/689,367 | METHOD FOR VERIFYING CONTIGUITY OF A BINARY TRANSLATED BLOCK OF INSTRUCTIONS BY ATTACHING A COMPARE AND/OR BRANCH INSTRUCTION TO PREDECESSOR BLOCK OF INSTRUCTIONS |
| US | 5,726,865 | 08/665,405 | CIRCUIT BOARD RETENTION SYSTEM   |
| US | 5,735,701 | 08/570,239 | LOCKING POWER CABLE INTERFACE GUARD  |
| US | 5,740,385 | 08/358,359 | LOW LOAD HOST/PCI BUS BRIDGE   |
| US | 5,745,786 | 08/188,565 | METHOD AND APPARATUS FOR ASSIGNING MEMORY SPACE ADDRESSES TO MEMORY ON COMPUTER INTERFACE BOARDS   |
| US | 5,752,013 | 08/085,508 | METHOD AND APPARATUS FOR PROVIDING PRECISE FAULT TRACING IN A SUPERSCALAR MICROPROCESSOR   |
| US | 5,754,162 | 08/253,374 | HORIZONTALLY SCALING IMAGE SIGNALS USING SELECTED WEIGHT FACTORS   |
| US | 5,757,217 | 08/672,412 | SLOW SPEED DRIVER WITH CONTROLLED SLEW RATE  |
| US | 5,764,648 | 08/537,260 | FAST FLOATING-POINT TO INTEGER CONVERSION  |
| US | 5,764,759 | 08/319,159 | CALL PROCESSING USING PREVIOUSLY OBTAINED LINE CHARACTERISTICS   |
| US | 5,768,289 | 08/862,006 | DYNAMICALLY CONTROLLING THE NUMBER OF BOUNDARY-SCAN CELLS IN A BOUNDARY-SCAN PATH  |
| US | 5,768,351 | 08/831,578 | METHOD AND APPARATUS FOR CONNECTING A TELEPHONE TO A VOICE CAPABLE MODEM   |
| US | 5,771,183 | 08/672,678 | APPARATUS AND METHOD FOR COMPUTATION OF STICKY BIT IN A MULTI-STAGE SHIFTER USED FOR FLOATING POINT ARITHMETIC   |
| US | 5,773,895 | 08/627,058 | ANCHOR PROVISIONS TO PREVENT MOLD DELAMINATION IN AN OVERMOLDED PLASTIC ARRAY PACKAGE  |
| US | 5,775,951 | 08/724,625 | HI-JACK HINGED CONNECTION ADAPTER FOR INPUT/OUTPUT CARDS   |
| US | 5,777,824 | 08/682,200 | METHOD AND APPARATUS FOR ELIMINATING VISUAL ARTIFACTS CAUSED BY DIFFUSING ERRORS IN A DECMATED VIDEO SIGNAL  |
| US | 5,778,097 | 08/610,582 | TABLE-DRIVEN BI-DIRECTIONAL MOTION ESTIMATION USING SCRATCH AREA AND OFFSET VALVES   |

## Schedule A

|    |           |            |   |
|----|-----------|------------|---|
| US | 5,784,046 | 08/254,364 | HORIZONTALLY SCALING IMAGE SIGNALS USING DIGITAL DIFFERENTIAL ACCUMULATOR PROCESSING  |
| US | 5,784,263 | 08/584,330 | CONNECTOR WITH ATTACHABLE DAUGHTER CARD RETENTION SYSTEM  |
| US | 5,787,159 | 08/607,796 | USE OF CALLER ID INFORMATION  |
| US | 5,787,454 | 08/578,964 | RECORDER BUFFER WITH INTERLEAVING MECHANISM FOR ACCESSING A MULTI-PORTED CIRCULAR MEMORY ARRAY  |
| US | 5,793,069 | 08/672,411 | APPARATUS FOR PROTECTING GATE ELECTRODES OF TARGET TRANSISTORS IN A GATE ARRAY FROM GATE CHARGING BY EMPLOYING FREE TRANSISTORS IN THE GATE ARRAY |
| US | 5,794,007 | 08/553,017 | SYSTEM AND METHOD FOR PROGRAMMING PROGRAMMABLE ELECTRONIC COMPONENTS USING BOARD-LEVEL AUTOMATED TEST EQUIPMENT                                   |
| US | 5,795,172 | 08/768,961 | PRODUCTION PRINTED CIRCUIT BOARD (PCB) EDGE CONNECTOR TEST CONNECTOR  |
| US | 5,796,282 | 08/700,613 | LATCHING MECHANISM FOR PULSED DOMINO LOGIC WITH INHERENT RACE MARGIN AND TIME BORROWING   |
| US | 5,796,639 | 08/778,305 | METHOD AND APPARATUS FOR VERIFYING THE INSTALLATION OF STRAPPING DEVICES ON A CIRCUIT BOARD ASSEMBLY  |
| US | 5,801,821 | 08/497,321 | PHOTOLITHOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL  |
| US | 5,802,074 | 08/715,857 | METHOD AND APPARATUS FOR THE NON-INVASIVE TESTING OF PRINTED CIRCUIT BOARD ASSEMBLIES   |
| US | 5,802,213 | 08/324,923 | ENCODING VIDEO SIGNALS USING LOCAL QUANTIZATION LEVELS  |
| US | 5,802,674 | 08/849,031 | FIBRE METERING ARRANGEMENT  |
| US | 5,804,771 | 08/721,268 | ORGANIC SUBSTRATE (PCB) SLIP PLANE "STRESS DEFLECTOR" FOR FLIP CHIP DEVICES   |
| US | 5,809,110 | 08/594,411 | TECHNIQUE OF CONTROLLING TELEPHONE CALLS  |
| US | 5,809,247 | 08/685,952 | METHOD AND APPARATUS FOR GUIDED TOURING OF INTERNET/INTRANET WEBSITES   |
| US | 5,815,372 | 08/822,743 | PACKAGING MULTIPLE DIES ON A BALL GRID ARRAY SUBSTRATE  |
| US | 5,818,969 | 08/854,770 | INTELLIGENT START FOR MOTION ESTIMATION SEARCH  |
| US | 5,832,241 | 08/878,660 | DATA CONSISTENCY ACROSS A BUS TRANSACTIONS THAT IMPOSE ORDERING CONSTRAINTS   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,835,927 | 08/719,583 | SPECIAL TEST MODES FOR A PAGE BUFFER SHARED RESOURCE IN A MEMORY DEVICE  |
| US | 5,838,941 | 08/774,659 | OUT-OF-ORDER SUPERSCALAR MICROPROCESSOR WITH A RENAMING DEVICE THAT MAPS INSTRUCTIONS FROM MEMORY TO REGISTERS |
| US | 5,843,846 | 08/775,573 | ETCH PROCESS TO PRODUCE ROUNDED TOP CORNERS FOR SUB-MICRON SILICON TRENCH APPLICATIONS                         |
| US | 5,844,852 | 08/907,754 | MEMORY ARRAYS WITH INTEGRATED BIT LINE VOLTAGE STABILIZATION CIRCUITRY   |
| US | 5,847,366 | 08/668,045 | APPARATUS AND METHOD FOR CONTROLLING THE TEMPERATURE OF AN INTEGRATED CIRCUIT UNDER TEST                       |
| US | 5,848,288 | 08/530,076 | METHOD AND APPARATUS FOR ACCOMMODATING DIFFERENT ISSUE WIDTH IMPLEMENTATIONS OF VLIW ARCHITECTURES             |
| US | 5,867,367 | 08/984,766 | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE  |
| US | 5,867,721 | 08/978,998 | SELECTING AN INTEGRATED CIRCUIT FROM DIFFERENT INTEGRATED CIRCUIT ARRAY CONFIGURATIONS                         |
| US | 5,880,622 | 08/768,297 | METHOD AND APPARATUS FOR CONTROLLING A CHARGE PUMP FOR RAPID INITIALIZATION                                    |
| US | 5,881,076 | 08/682,108 | COMPARATOR UTILIZING REDUNDANCY  |
| US | 5,881,146 | 08/828,000 | METHOD AND APPARATUS FOR DETERMINING DIGITS DIALED USING A SELECTED ALGORITHM                                  |
| US | 5,888,897 | 08/739,879 | PROCESS FOR FORMING AN INTEGRATED STRUCTURE COMPRISING A SELF-ALIGNED VIA/CONTACT AND INTERCONNECT             |
| US | 5,889,408 | 08/670,545 | DELTA IDDQ TESTING   |
| US | 5,889,409 | 08/721,685 | LEAKAGE TRACKING DEVICE SAMPLE FOR IDDQ MEASUREMENT AND DEFECT RESOLUTION                                      |
| US | 5,889,950 | 08/771,709 | METHOD AND APPARATUS FOR DISTRIBUTION OF BROADCAST DATA  |
| US | 5,889,988 | 08/748,551 | DEBUGGER FOR DEBUGGING TASKS IN AN OPERATING SYSTEM VIRTUAL DEVICE DRIVER                                      |
| US | 5,894,408 | 09/024,860 | ELECTRONIC CARTRIDGE WHICH ALLOWS DIFFERENTIAL THERMAL EXPANSION BETWEEN COMPONENTS OF THE CARTRIDGE           |
| US | 5,900,770 | 08/874,496 | VARIABLE LOADING APPARATUS FOR OUTPUT LOADING OF INTEGRATED CIRCUITS   |
| US | 5,903,432 | 08/933,405 | COMPUTER PACKAGE WITH A POLYGONAL SHAPED MOTHERBOARD   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 5,903,500 | 08/834,032 | 1.8 VOLT OUTPUT BUFFER ON FLASH MEMORIES   |
| US | 5,909,635 | 08/673,277 | CLADDING OF AN INTERCONNECT FOR IMPROVED ELECTROMIGRATION PERFORMANCE  |
| US | 5,912,950 | 08/594,016 | TELEPHONE NETWORK WITH NORMALLY OFF HOOK TELEPHONES  |
| US | 5,915,018 | 08/740,976 | KEY MANAGEMENT SYSTEM FOR DVD COPYRIGHT MANAGEMENT   |
| US | 5,917,239 | 08/835,228 | RECESSED OR RAISED CHARACTERS ON A CERAMIC LID   |
| US | 5,918,025 | 08/775,807 | METHOD AND APPARATUS FOR CONVERTING A FIVE WIRE ARBITRATION/BUFFER MANAGEMENT PROTOCOL INTO A TWO WIRE PROTOCOL  |
| US | 5,918,033 | 08/780,675 | METHOD AND APPARATUS FOR DYNAMIC LOCATION AND CONTROL OF PROCESSOR RESOURCES TO INCREASE RESOLUTION OF DATA DEPENDENCY STALLS                                    |
| US | 5,918,192 | 08/787,865 | A METHOD FOR CALIBRATING A DIGITAL CAMERA TO A PC MONITOR TO ENHANCE PICTURE QUALITY OF A PICTURE CAPTURED BY THE DIGITAL CAMERA AND DISPLAYED ON THE PC MONITOR |
| US | 5,920,870 | 912,380    | MULTI-LAYER ABSTRACTION BUCKET MECHANISM   |
| US | 5,931,844 | 08/996,756 | BRANCH INSTRUCTION HANDLING IN A SELF-TIMED MARKING SYSTEM   |
| US | 5,941,982 | 08/997,461 | EFFICIENT SELF-TIMED MARKING OF LENGTHY VARIABLE LENGTH INSTRUCTIONS   |
| US | 5,943,487 | 08/679,197 | METHOD FOR EXTRACTING A RESISTOR NETWORK FROM AN INTEGRATED CIRCUIT POLYGON LAYOUT   |
| US | 5,943,693 | 08/412,555 | ALGORITHMIC ARRAY MAPPING TO DECREASE DEFECT SENSITIVITY OF MEMORY DEVICES   |
| US | 5,945,988 | 08/866,707 | METHOD AND APPARATUS FOR AUTOMATICALLY DETERMINING AND DYNAMICALLY UPDATING USER PREFERENCES IN AN ENTERTAINMENT SYSTEM  |
| US | 5,946,079 | 08/962,138 | PHOTOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL  |
| US | 5,946,258 | 09/040,523 | PUMP SUPPLY SELF REGULATION FOR FLASH MEMORY CELL PAIR REFERENCE CIRCUIT   |
| US | 5,946,396 | 08/738,086 | A SYSTEM AND METHOD FOR ENSURING INTEGRITY OF AUDIO  |
| US | 5,948,096 | 08/997,462 | APPARATUS AND METHOD FOR SELF-TIMED MARKING OF VARIABLE LENGTH INSTRUCTIONS HAVING LENGTH-AFFECTING PREFIX BYTES   |
| US | 5,948,960 | 09/020,661 | HTMU TEST HANDLER THROW MEASURING UNIT   |

## Schedule A

|    |           |            |   |
|----|-----------|------------|---|
| US | 5,949,651 | 09/179,016 | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE   |
| US | 5,951,867 | 08/778,192 | METHOD AND APPARATUS FOR CONNECTING EXPANSION BUSES TO A PERIPHERAL COMPONENT INTERCONNECT BUS                                    |
| US | 5,959,445 | 09/002,174 | STATIC, HIGH-SENSITIVITY, FUSE-BASED STORAGE CELL   |
| US | 5,961,728 | 08/874,388 | METHOD AND APPARATUS FOR CLEANING A TEST PROBE  |
| US | 5,963,970 | 08/770,958 | METHOD AND APPARATUS FOR TRACKING ERASE CYCLES UTILIZING ACTIVE AND INACTIVE WEAR BAR BLOCKS HAVING FIRST AND SECOND COUNT FIELDS |
| US | 5,964,513 | 09/085,779 | CHASSIS FOR ELECTRONIC COMPONENTS   |
| US | 5,964,870 | 08/934,964 | METHOD AND APPARATUS FOR USING FUNCTION CONTEXT TO IMPROVE BRANCH   |
| US | 5,965,937 | 08/990,856 | THERMAL INTERFACE ATTACH MECHANISM FOR ELECTRICAL PACKAGES  |
| US | 5,966,020 | 08/741,053 | METHOD AND APPARATUS FOR FACILITATING DETECTION OF SOLDER OPENS OF SMT COMPONENTS   |
| US | 5,966,544 | 08/746,547 | DATA SPECULATABLE PROCESSOR HAVING REPLY ARCHITECTURE   |
| US | 5,970,147 | 08/938,491 | SYSTEM AND METHOD FOR CONFIGURING AND REGISTERING A CRYPTOGRAPHIC DEVICE  |
| US | 5,973,957 | 08/931,247 | SENSE AMPLIFIER COMPRISING A PREAMPLIFIER AND A DIFFERENTIAL INPUT LATCH FOR FLASH MEMORIES                                       |
| US | 5,978,224 | 09/179,125 | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE   |
| US | 5,978,228 | 08/777,604 | APPARATUS FOR MOUNTING A VERY LARGE SCALE INTEGRATION (VLSI) CHIP TO A COMPUTER CHASSIS FOR COOLING                               |
| US | 5,978,263 | 08/895,613 | NEGATIVE VOLTAGE SWITCH ARCHITECTURE FOR A NONVOLATILE MEMORY   |
| US | 5,978,873 | 08/936,857 | COMPUTER SYSTEM INCLUDING RIGHT ANGLE PROCESSOR AND ADD-ON CARD CONNECTORS  |
| US | 5,978,909 | 08/979,579 | SYSTEM FOR SPECULATIVE BRANCH TARGET PREDICTION HAVING A DYNAMIC PREDICTION HISTORY BUFFER AND A STATIC PREDICTION HISTORY BUFFER |
| US | 5,982,684 | 09/086,330 | PARALLEL ACCESS TESTING OF A MEMORY ARRAY   |
| US | 5,987,504 | 08/801,508 | METHOD AND APPARATUS FOR DELIVERING DATA  |
| US | 5,987,552 | 09/013,774 | COHERENT VARIABLE LENGTH READS FROM SYSTEM MEMORY   |

## Schedule A

|    |           |            |   |
|----|-----------|------------|---|
| US | 5,987,572 | 08/937,874 | METHOD AND APPARATUS EMPLOYING A DYNAMIC ENCRYPTION INTERFACE BETWEEN A PROCESSOR AND A MEMORY  |
| US | 6,002,525 | 09/110,805 | CORRECTING LENS DISTORTION  |
| US | 6,002,879 | 08/829,856 | METHOD FOR PERFORMING COMMON SUBEXPRESSION ELIMINATION ON A RACK-N-STATIC SINGLE ASSIGNMENT LANGUAGE  |
| US | 6,006,282 | 08/565,953 | BLOCKING HOST INPUT WHILE A REMOTE GUEST HAS INPUT CONTROL TO A HOST-EXECUTED SHARED APPLICATION  |
| US | 6,009,497 | 09/189,000 | METHOD AND APPARATUS FOR UPDATING FLASH MEMORY RESIDENT FIRMWARE THROUGH A STANDARD DISK DRIVE INTERFACE  |
| US | 6,012,082 | 08/935,742 | CPU-CYCLE STEALING FOR MULTI-TASKING OPERATING SYSTEM   |
| US | 6,014,755 | 08/700,676 | METHOD OF MANAGING DEFECTS IN FLASH DISK MEMORIES   |
| US | 6,016,398 | 08/831,739 | METHOD FOR USING STATIC SINGLE ASSIGNMENT TO COLOR OUT ARTIFICIAL REGISTER DEPENDENCIES   |
| US | 6,019,166 | 09/000,981 | PICKUP CHUCK WITH AN INTEGRAL HEATSINK  |
| US | 6,021,505 | 09/105,479 | METHOD AND APPARATUS FOR UPDATING A TIMER FROM MULTIPLE TIMING DOMAINS  |
| US | 6,024,426 | 09/329,425 | CHASSIS FOR ELECTRONIC COMPONENTS   |
| US | 6,026,455 | 08/201,817 | ARCHITECTURE AND METHOD FOR PROVIDING GUARANTEED ACCESS FOR A RETRYING BUS MASTER TO A DATA TRANSFER BRIDGE CONNECTING TWO BUSES IN A COMPUTER SYSTEM |
| US | 6,026,465 | 08/897,499 | FLASH MEMORY INCLUDING A MODE REGISTER FOR INDICATING SYNCHRONOUS OR ASYNCHRONOUS MODE OF OPERATION   |
| US | 6,027,191 | 09/329,426 | CHASSIS FOR ELECTRONIC COMPONENTS   |
| US | 6,028,696 | 09/172,615 | CHARGE CONTROLLED MIRROR WITH IMPROVED FRAME TIME UTILIZATION AND METHOD OF ADDRESSING THE SAME   |
| US | 6,029,730 | 09/001,021 | HOT SHEAR APPARATUS AND METHOD FOR REMOVING A SEMICONDUCTOR CHIP FROM AN EXISTING PACKAGE   |
| US | 6,031,856 | 09/179,933 | BEAM-ADDRESSED MICROMIRROR DIRECT VIEW DISPLAY  |
| US | 6,031,857 | 09/208,041 | MEMBRANE-ACTUATED CHARGE CONTROLLED MIRROR (CCM) PROJECTION DISPLAY   |

## Schedule A

|    |           |            |   |
|----|-----------|------------|---|
| US | 6,047,035 | 09/103,683 | METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS   |
| US | 6,051,889 | 09/006,770 | LOW VOLTAGE PROGRAMMABLE COMPLEMENTARY INPUT STAGE SENSE AMPLIFIER  |
| US | 6,055,095 | 09/364,142 | MICROSCOPE WITH INFRARED IMAGING  |
| US | 6,055,614 | 08/778,015 | APPARATUS FOR PERFORMING A USER REQUESTED RESET DURING ALGORITHM EXECUTION  |
| US | 6,060,917 | 09/064,276 | FREQUENCY SYNTHESIZER   |
| US | 6,061,263 | 09/221,804 | SMALL OUTLINE RAMBUS IN-LINE MEMORY MODULE  |
| US | 6,061,265 | 09/221,233 | QUANTUM MAGNETIC MEMORY   |
| US | 6,064,571 | 09/074,951 | FAN DUCT MODULE   |
| US | 6,069,928 | 08/885,803 | SCALABLE RECEIVER STRUCTURE FOR EFFICIENT BIT SEQUENCE DECODING   |
| US | 6,070,252 | 08/698,465 | METHOD AND APPARATUS FOR INTERACTIVE BUILT-IN-SELF-TESTING WITH USER-PROGRAMMABLE TEST PATTERNS                                     |
| US | 6,072,723 | 09/306,322 | METHOD AND APPARATUS FOR PROVIDING REDUNDANCY IN NON-VOLATILE MEMORY DEVICES  |
| US | 6,073,030 | 08/820,129 | USE OF RSSI INDICATION FOR IMPROVED DATA TRANSMISSION OVER AMPS NETWORK   |
| US | 6,075,751 | 09/231,725 | SIGNAL TRANSITION DETECTOR FOR ASYNCHRONOUS CIRCUITS  |
| US | 6,077,337 | 09/203,432 | CHEMICAL-MECHANICAL POLISHING SLURRY  |
| US | 6,080,022 | 08/672,643 | MULTIVOLTAGE KEYED ELECTRICAL CONNECTOR   |
| US | 6,081,272 | 08/941,599 | MERGING DUMMY STRUCTURE REPRESENTATIONS FOR IMPROVED DISTRIBUTION OF ARTIFACTS IN A SEMICONDUCTOR LAYER                             |
| US | 6,083,839 | 09/001,509 | UNIQUE CHEMICAL MECHANICAL PLANARIZATION APPROACH WHICH UTILIZES MAGNETIC SLURRY FOR POLISH AND MAGNETIC FIELDS FOR PROCESS CONTROL |
| US | 6,084,773 | 09/024,921 | INTEGRATED PROCESSOR SUBSTRATE PROTECTIVE ENCLOSURE FOR USE IN DESKTOP PC SYSTEMS   |
| US | 6,085,341 | 09/030,522 | MEMORY TEST MODE FOR WORLDLINE RESISTIVE DEFECTS  |
| US | 6,094,530 | 09/069,527 | REMOTELY MONITORING EXECUTION OF A PROGRAM  |
| US | 6,100,709 | 09/087,753 | SILICON WAFER TESTING RIG AND A METHOD FOR TESTING A SILICON WAFER WHEREIN THE SILICON WAFER IS BENT INTO A DOME SHAPE              |
| US | 6,102,647 | 09/105,191 | CART FOR TRANSFERRING OBJECTS   |

## Schedule A

|    |           |            |   |
|----|-----------|------------|---|
| US | 6,104,720 | 08/848,554 | DYNAMIC COMMUNICATION PATH SELECTION FOR DATA TRANSMISSION BETWEEN COMPUTERS  |
| US | 6,108,130 | 09/393,757 | STEREOSCOPIC IMAGE SENSOR   |
| US | 6,108,743 | 09/021,688 | TECHNIQUE FOR PERFORMING DMA INCLUDING ARBITRATION BETWEEN A CHAINED LOW PRIORITY DMA AND HIGH PRIORITY DMA OCCURRING BETWEEN TWO LINKS IN THE CHAINED LOW PRIORITY |
| US | 6,113,848 | 09/024,830 | METHOD AND APPARATUS FOR PROTECTING GATE ELECTRODES OF TARGET TRANSISTORS IN THE GATE ARRAY FROM GATE CHARGING BY EMPLOYING FREE TRANSISTORS IN THE GATE ARRAY      |
| US | 6,124,168 | 08/337,131 | METHOD FOR FORMING AN ASYMMETRIC FLOATING GATE OVERLAP FOR IMPROVED DEVICE PERFORMANCE IN BURIED BIT-LINE DEVICES   |
| US | 6,125,412 | 08/046,109 | A SYSTEM FOR PERFORMING INPUT AND OUTPUT OPERATIONS TO AND FROM A PROCESSOR   |
| US | 6,127,696 | 08/087,140 | HIGH VOLTAGE MOS TRANSISTOR FOR FLASH EEPROM APPLICATIONS HAVING A UNISIDED LIGHTLY DOPED DRAIN   |
| US | 6,130,819 | 09/015,423 | FAN DUCT MODULE   |
| US | 6,134,635 | 08/987,279 | METHOD AND APPARATUS OF RESOLVING A DEADLOCK BY COLLAPSING WRITEBACKS TO A MEMORY   |
| US | 6,137,354 | 09/080,812 | BYPASSABLE AMPLIFIER  |
| US | 6,137,709 | 09/499,084 | SMALL OUTLINE MEMORY MODULE   |
| US | 6,144,218 | 09/012,201 | HIGH SPEED ANALOG COMPENSATED INPUT BUFFER  |
| US | 6,151,229 | 09/346,483 | CHARGE PUMP WITH GATED PUMPED OUTPUT DIODE AT INTERMEDIATE STAGE  |
| US | 6,151,678 | 08/927,370 | ANTI-THEFT MECHANISM FOR MOBILE COMPUTERS   |
| US | 6,162,073 | 09/223,811 | HOT PLUG STRUCTURE  |
| US | 6,163,474 | 09/225,927 | MEMORY CARD WHICH IS THERMALLY CONTROLLED   |
| US | 6,163,838 | 09/106,857 | COMPUTER PROCESSOR WITH A REPLAY SYSTEM   |
| US | 6,166,563 | 09/299,771 | METHOD AND APPARATUS FOR DUAL MODE OUTPUT BUFFER IMPEDANCE COMPENSATION   |
| US | 6,172,538 | 09/226,924 | UNIVERSAL PULSE SYNCHRONIZER  |
| US | 6,172,540 | 09/374,798 | APPARATUS FOR FAST LOGIC TRANSFER OF DATA ACROSS ASYNCHRONOUS CLOCK DOMAINS   |
| US | 6,173,489 | 09/032,628 | ORGANIC SUBSTRATE (PCB) SLIP PLANE "STRESS DEFLECTOR" FOR FLIP CHIP DEVICES   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 6,181,180 | 09/340,417 | FLIP-FLOP CIRCUIT  |
| US | 6,184,732 | 09/370,622 | SETTING THE COMMON MODE LEVEL OF A DIFFERENTIAL CHARGE PUMP OUTPUT   |
| US | 6,184,739 | 09/205,992 | ADVANCED NEAR IDEAL MIXER  |
| US | 6,192,044 | 08/829,608 | EMPLOYING A LOOK-UP SERVICE AND A CALLEE CONNECTION SERVICE TO ESTABLISH A NETWORK PHONE CALL BETWEEN A CALLER AND A CALLEE  |
| US | 6,195,259 | 09/358,373 | HOT PLUG CARTRIDGE ASSEMBLY  |
| US | 6,198,421 | 09/259,650 | NEURAL FLASH ANALOG-TO-DIGITAL CONVERTER USING WEIGHTED CURRENT SIMULATION   |
| US | 6,199,149 | 09/016,790 | OVERLAY COUNTER FOR ACCELERATED GRAPHICS PORT  |
| US | 6,208,527 | 09/098,086 | RETENTION MECHANISM ASSEMBLY FOR PROCESSOR CARTRIDGES WITH CAPTURED SCREW FASTENERS  |
| US | 6,211,718 | 09/003,908 | LOW VOLTAGE DOUBLE BALANCED MIXER  |
| US | 6,214,098 | 09/504,191 | CHEMICAL-MECHANICAL POLISHING SLURRY   |
| US | 6,221,789 | 09/124,562 | THIN OXIDES OF SILICON   |
| US | 6,232,826 | 09/005,560 | CHARGE PUMP AVOIDING GAIN DEGRADATION DUE TO THE BODY EFFECT   |
| US | 6,239,605 | 09/162,912 | METHOD TO PERFORM IDDQ TESTING IN THE PRESENCE OF HIGH BACKGROUND LEAKAGE CURRENT  |
| US | 6,239,606 | 09/163,592 | METHOD TO PERFORM IDDQ TESTING IN THE PRESENCE OF HIGH BACKGROUND LEAKAGE CURRENT  |
| US | 6,240,168 | 09/240,168 | METHOD AND APPARATUS FOR CONTROLLING A COMPUTER TO IMPLEMENT TELEPHONE FUNCTIONS WITH A DISPLAYED TELEPHONE OF VARIABLE SIZE |
| US | 6,246,334 | 09/299,162 | BOARD ID DISPLAY SYSTEM  |
| US | 6,247,120 | 08/912,048 | INSTRUCTION BUFFER FOR ISSUING INSTRUCTION SETS TO AN INSTRUCTION DECODER  |
| US | 6,250,821 | 08/787,983 | METHOD AND APPARATUS FOR PROCESSING BRANCH INSTRUCTIONS IN AN INSTRUCTION BUFFER   |
| US | 6,255,135 | 09/179,014 | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE  |
| US | 6,255,873 | 09/666,339 | SETTING THE COMMON MODE LEVEL OF A DIFFERENTIAL CHARGE PUMP OUTPUT   |
| US | 6,255,896 | 09/406,329 | METHOD AND APPARATUS FOR RAPID INITIALIZATION OF CHARGE PUMP CIRCUITS  |
| US | 6,256,059 | 09/003,732 | AUTOMATIC TRANSFER OF IMAGE INFORMATION BETWEEN IMAGING DEVICE AND HOST SYSTEM   |

## Schedule A

|    |           |            |   |
|----|-----------|------------|---|
| US | 6,256,673 | 09/213,220 | CYCLIC MULTICASTING OR ASYNCHRONOUS BROADCASTING OF COMPUTER FILES  |
| US | 6,268,774 | 09/434,250 | SELF-TUNING AMPLIFIER   |
| US | 6,272,676 | 09/006,276 | METHOD AND APPARATUS FOR FINDING LOOP-LEVER PARALLELISM IN A POINTER BASED APPLICATION  |
| US | 6,282,228 | 08/821,611 | SPREAD SPECTRUM CODES FOR USE IN COMMUNICATION  |
| US | 6,284,091 | 09/352,634 | UNIQUE CHEMICAL MECHANICAL PLANARIZATION APPROACH WHICH UTILIZES MAGNETIC SLURRY FOR POLISH AND MAGNETIC FIELDS FOR PROCESS CONTROL |
| US | 6,292,048 | 09/438,186 | GATE ENHANCEMENT CHARGE PUMP FOR LOW VOLTAGE POWER SUPPLY   |
| US | 6,297,492 | 09/003,477 | FAST BICMOS ACTIVE-PIXEL SENSOR CELL WITH FAST NPN EMITTER-FOLLOWER READOUT   |
| US | 6,307,355 | 09/148,033 | METHOD AND APPARATUS FOR REDUCING THE POWER CONSUMPTION OF A VOLTAGE REGULATOR  |
| US | 6,308,278 | 09/001,102 | SUPPLYING STANDBY VOLTAGE TO MEMORY AND WAKEUP CIRCUITRY TO WAKE A COMPUTER FROM A LOW POWER MODE                                   |
| US | 6,311,212 | 09/270,287 | SYSTEMS AND METHODS FOR ON-CHIP STORAGE OF VIRTUAL CONNECTION DESCRIPTORS   |
| US | 6,314,506 | 09/221,628 | METHOD AND APPARATUS FOR DETERMINING A NEXT ADDRESS WITHIN A BINARY SEARCH ALGORITHM  |
| US | 6,317,452 | 09/307,646 | METHOD AND APPARATUS FOR WIRELESS SPREAD SPECTRUM COMMUNICATION WITH PREAMBLE SOUNDING GAP  |
| US | 6,324,263 | 09/144,511 | DIGITAL TELEPHONE SYSTEM WITH AUTOMATIC ROUTING BASED UPON CALLER ID  |
| US | 6,346,144 | 09/723,092 | CHEMICAL-MECHANICAL POLISHING SLURRY  |
| US | 6,351,387 | 09/606,893 | SYSTEM AND METHOD OF HEAT EXTRACTION FROM AN INTEGRATED CIRCUIT DIE   |
| US | 6,351,555 | 09/482,551 | EFFICIENT COMPANDING ALGORITHM SUITABLE FOR COLOR IMAGING   |
| US | 6,359,814 | 09/763,351 | NEGATIVE OUTPUT VOLTAGE CHARGE PUMP AND METHOD THEREFOR   |
| US | 6,361,007 | 09/483,215 | MOUNTING BRACKET FOR PCS AND OTHER ANTENNAS   |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 6,364,331 | 09/410,400 | METHOD AND APPARATUS FOR TRANSFERRING WAFER CASSETTES IN MICROELECTRONIC MANUFACTURING ENVIRONMENT                           |
| US | 6,365,441 | 09/694,802 | PARTIAL UNDERFILL FOR FLIP-CHIP ELECTRONIC PACKAGES  |
| US | 6,366,158 | 09/752,247 | SELF INITIALIZATION FOR CHARGE PUMPS   |
| US | 6,366,516 | 09/753,005 | MEMORY SUBSYSTEM EMPLOYING POOL OF REFRESH CANDIDATES  |
| US | 6,369,642 | 09/748,611 | OUTPUT SWITCH FOR CHARGE PUMP RECONFIGURATION  |
| US | 6,370,885 | 09/226,804 | DATA-FLOW METHOD OF ANALYZING DEFINITIONS AND USES OF L VALUES IN PROGRAMS   |
| US | 6,373,324 | 09/475,459 | VOLTAGE BLOCKING METHOD AND APPARATUS FOR A CHARGE PUMP WITH DIODE CONNECTED PULL-UP AND PULL-DOWN ON BOOT NODES             |
| US | 6,374,278 | 09/276,366 | METHOD AND APPARATUS FOR THE GENERATION OF STATISTICALLY RANDOM NUMBERS  |
| US | 6,377,576 | 08/541,678 | TELEPHONE CALL SETUP PROCEDURE   |
| US | 6,378,061 | 08/150,784 | APPARATUS FOR ISSUING INSTRUCTIONS AND REISSUING A PREVIOUS INSTRUCTIONS BY RECIRCULATING USING THE DELAY CIRCUIT            |
| US | 6,384,658 | 09/676,313 | CLOCK SPLITTER CIRCUIT TO GENERATE SYNCHRONIZED CLOCK AND INVERTED CLOCK   |
| US | 6,384,838 | 08/425,231 | OPTIMIZED LOOKUP TABLE METHOD FOR CONVERTING YUV PIXEL VALUES TO RGB PIXEL VALUES  |
| US | 6,384,872 | 09/395,000 | METHOD AND APPARATUS FOR INTERLACED IMAGE ENHANCEMENT  |
| US | 6,388,475 | 09/474,566 | VOLTAGE TOLERANT HIGH DRIVE PULL-UP DRIVER FOR AN I/O BUFFER   |
| US | 6,400,370 | 09/393,477 | STOCHASTIC SAMPLING WITH CONSTANT DENSITY IN OBJECT SPACE FOR ANISOTROPIC TEXTURE MAPPING                                    |
| US | 6,400,709 | 09/204,607 | DTMF DETECTION IN INTERNET TELEPHONY   |
| US | 6,407,632 | 09/611,720 | RADIO FREQUENCY AMPLIFIER  |
| US | 6,418,201 | 09/510,311 | MULTIPLE PLATFORM VOICE PROCESSING SYSTEM WITH OPTIMIZED RESOURCE ALLOCATION   |
| US | 6,421,432 | 09/497,970 | METHOD AND APPARATUS FOR CONTROLLING A COMPUTER TO IMPLEMENT TELEPHONE FUNCTIONS WITH A DISPLAYED TELEPHONE OF VARIABLE SIZE |

## Schedule A

|    |           |                                  |   |
|----|-----------|----------------------------------|---|
| US | 6,425,067 | 09/340,282                       | SYSTEMS AND METHODS FOR IMPLEMENTING POINTER MANAGEMENT   |
| US | 6,438,275 | 09/296,451                       | METHOD FOR MOTION COMPENSATED FRAME RATE UPSAMPLING BASED ON PIECEWISE AFFINE WARPING                     |
| US | 6,441,678 | 10/051,696                       | SELF INITIALIZATION FOR CHARGE PUMPS  |
| US | 6,445,230 | 09/750,671                       | PROGRAMMABLE DIGITAL PHASE LOCK LOOP  |
| US | 6,449,211 | 09/945,021                       | VOLTAGE DRIVER FOR A MEMORY   |
| US | 6,449,308 | 09/318,287                       | HIGH-SPEED DIGITAL DISTRIBUTION SYSTEM  |
| US | 6,452,438 | 09/751,695                       | TRIPLE WELL NO BODY EFFECT NEGATIVE CHARGE PUMP   |
| US | 6,456,110 | 09/750,110                       | VOLTAGE LEVEL SHIFTER HAVING ZERO DC CURRENT AND STATE RETENTION IN DROWSY MODE                           |
| US | 6,461,169 | 09/849,506                       | INTERCONNECTING CIRCUIT MODULES TO A MOTHERBOARD USING AN EDGE CONNECTOR WITH CONDUCTIVE POLYMER CONTACTS |
| US | 6,462,612 | 09/894,151                       | CHOPPER STABILIZED BANDGAP REFERENCE CIRCUIT TO CANCEL OFFSET VARIATION                                   |
| US | 6,465,358 | 09/684,550                       | POST ETCH CLEAN SEQUENCE FOR MAKING A SEMICONDUCTOR DEVICE  |
| US | 6,471,170 | 09/804,489                       | KEYBOARD HOLDER   |
| US | 6,480,066 | 09/705,499                       | AMPLIFIERS  |
| US | 6,484,092 | US-2002-0152020-A1<br>09/818,993 | METHOD AND SYSTEM FOR DYNAMIC AND INTERACTIVE ROUTE FINDING   |
| US | 6,484,309 | TNT<br>09/166,592                | ENABLING SOFTWARE DESIGNED FOR ONE OPERATING SYSTEM TO OPERATE ON ANOTHER OPERATING SYSTEM                |
| US | 6,486,791 | 09/872,490                       | DRIVING A VISUAL INDICATOR ARRAY IN AN ELECTRONIC SIGNALING SYSTEM  |
| US | 6,488,571 | 09/746,470                       | APPARATUS FOR ENHANCED RATE CHEMICAL MECHANICAL POLISHING WITH ADJUSTABLE SELECTIVITY                     |
| US | 6,489,557 | 09/386,924                       | IMPLEMENTING MICRO BGA TML ASSEMBLY TECHNIQUES FOR SMALL DIE  |
| US | 6,489,955 | 09/327,015                       | RAY INTERSECTION REDUCTION USING DIRECTIONALLY CLASSIFIED TARGET LISTS                                    |
| US | 6,493,233 | 09/934,871                       | PCB-TO-CHASSIS MOUNTING SCHEMES   |
| US | 6,496,055 | 09/753,045                       | GATE ENHANCED TRI-CHANNEL POSITIVE CHARGE PUMP  |
| US | 6,498,305 | 09/713,702                       | INTERCONNECT MECHANICS FOR ELECTROMAGNETIC COUPLER  |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 6,498,605 | 09/442,663 | PIXEL SPAN DEPTH BUFFER  |
| US | 6,499,085 | 09/752,846 | METHOD AND SYSTEM FOR SERVICING CACHE LINE IN RESPONSE TO PARTIAL CACHE LINE REQUEST   |
| US | 6,504,760 | 09/888,252 | CHARGING A CAPACITANCE OF A MEMORY CELL AND CHARGER  |
| US | 6,507,061 | 09/945,331 | MULTIPLE LAYER PHASE-CHANGE MEMORY   |
| US | 6,512,861 | 09/892,921 | PACKAGING AND ASSEMBLY METHOD FOR OPTICAL COUPLING   |
| US | 6,514,805 | 09/896,532 | TRENCH SIDEWALL PROFILE FOR DEVICE ISOLATION   |
| US | 6,515,586 | 09/216,580 | TACTILE TRACKING SYSTEMS AND METHODS   |
| US | 6,515,901 | 09/751,514 | METHOD AND APPARATUS FOR ALLOWING CONTINUOUS APPLICATION OF HIGH VOLTAGE TO A FLASH MEMORY DEVICE POWER PIN                                  |
| US | 6,515,906 | 09/752,714 | METHOD AND APPARATUS FOR WATCHED-REFERENCE SENSING ARCHITECTURE FOR NON-VOLATILE MEMORIES  |
| US | 6,522,180 | 09/745,970 | BI-VOLTAGE LEVELS SWITCHES   |
| US | 6,525,726 | 09/432,134 | METHOD AND APPARATUS FOR ADAPTIVE HIERARCHICAL VISIBILITY IN A TILED THREE-DIMENSIONAL GRAPHICS ARCHITECTURE                                 |
| US | 6,533,586 | 09/751,527 | ELECTROMAGNETIC COUPLER SOCKET   |
| US | 6,539,366 | 09/071,049 | CODEC WITH GENETIC ADAPTATION  |
| US | 6,542,584 | 09/144,186 | DIGITAL TELEPHONE SYSTEM WITH AUTOMATIC VOICE MAIL REDIRECTION   |
| US | 6,544,047 | 09/821,114 | DUAL-SWIPING INTERCONNECTION CLIP, AND HOOK AND SLOT ARRANGEMENT FOR PRINTED CIRCUIT BOARD (PCB) ATTACHMENT                                  |
| US | 6,548,399 | 09/896,215 | METHOD OF FORMING A SEMICONDUCTOR DEVICE USING A CARBON DOPED OXIDE LAYER TO CONTROL THE CHEMICAL MECHANICAL POLISHING OF A DIELECTRIC LAYER |
| US | 6,552,600 | 09/808,623 | INITIALIZATION OF NEGATIVE CHARGE PUMP   |
| US | 6,552,896 | 09/566,185 | FAN DUCT MODULE  |
| US | 6,556,128 | 09/458,715 | METHOD FOR REDUCING POWER CONSUMPTION IN WAIT-MODE   |
| US | 6,560,286 | 09/474,183 | FIELD FRAME MOTION DESIGN FOR DIGITAL VIDEO DECODER  |
| US | 6,560,317 | 10/038,411 | RECEIVING CALLER IDENTIFICATION INFORMATION WITH A TELECOMMUNICATIONS DEVICE FOR THE DEAF  |

## Schedule A

|    |           |                    |  |
|----|-----------|--------------------|--|
| US | 6,564,811 | 09/818,177         | METHOD OF REDUCING RESIDUE DEPOSITION ONTO ASH CHAMBER BASE SURFACES                         |
| US | 6,569,705 | 09/745,835         | METAL STRUCTURE FOR A PHASE-CHANGE MEMORY DEVICE   |
| US | 6,570,789 | 09/752,535         | LOAD FOR NON-VOLATILE MEMORY DRAIN BIAS  |
| US | 6,573,747 | 09/965,627         | DIGITAL UPDATE SCHEME FOR ADAPTIVE IMPEDANCE CONTROL OF ON-DIE INPUT/OUTPUT CIRCUITS         |
| US | 6,574,141 | 09/982,246         | DIFFERENTIAL REDUNDANCY MULTIPLEXOR FOR FLASH MEMORY DEVICES                                 |
| US | 6,574,386 | 09/839,848         | DYNAMICALLY RECONFIGURABLE OPTICAL SWITCHING SYSTEM  |
| US | 6,576,847 | 09/751,442         | CLAMP TO SECURE CARRIER TO DEVICE FOR ELECTROMAGNETIC COUPLER                                |
| US | 6,577,274 | 10/028,472         | METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES                                |
| US | 6,580,837 | 09/457,058         | UP-SAMPLING DECATATED COLOR PLANE DATA   |
| US | 6,586,761 | 09/948,830         | PHASE CHANGE MATERIAL MEMORY DEVICE  |
| US | 6,586,836 | 09/516,652         | PROCESS FOR FORMING MICROELECTRONIC PACKAGES AND INTERMEDIATE STRUCTURES FORMED THEREWITH    |
| US | 6,590,807 | 09/921,853         | METHOD FOR READING A STRUCTURAL PHASE-CHANGE MEMORY  |
| US | 6,593,650 | 10/050,463         | PLASMA INDUCED DEPLETION OF FLUORINE FROM SURFACES OF FLUORINATED LOW-K DIELECTRIC MATERIALS |
| US | 6,594,329 | 09/432,050         | ELASTIC BUFFER   |
| US | 6,594,344 | 09/749,610         | AUTO LATENCY TEST TOOL   |
| US | 6,595,798 | 09/280,171         | TEST FIXTURE FOR TESTING A MULTI-PORT VOICE PROCESSING CARD AND THE LIKE                     |
| US | 6,596,614 | 10/246,273         | USE OF MEMBRANE PROPERTIES TO REDUCE RESIDUAL STRESS IN AN INTERLAYER REGION                 |
| US | 6,597,606 | 10/154,019         | CHARGING A CAPACITANCE OF A MEMORY CELL AND CHARGER  |
| US | 6,598,133 | US-2003-0005240-A1 | SUCCESSIVE TEMPLATE GENERATION USING MINIMAL RANDOM ACCESS MEMORY BANDWIDTH                  |
| US | 6,603,768 | 09/344,672         | MULTI-PROTOCOL CONVERSION ASSISTANCE METHOD AND SYSTEM FOR A NETWORK ACCELERATOR             |
| US | 6,603,893 | 09/819,160         | METHOD AND APPARATUS FOR SWITCHING AN OPTICAL BEAM IN A SEMICONDUCTOR SUBSTRATE              |

## Schedule A

|    |           |            |  |
|----|-----------|------------|--|
| US | 6,604,136 | 09/337,025 | APPLICATION PROGRAMMING INTERFACES AND METHODS ENABLING A HOST TO INTERFACE WITH A NETWORK PROCESSOR   |
| US | 6,605,527 | 09/896,531 | REDUCED AREA INTERSECTION BETWEEN ELECTRODE AND PROGRAMMING ELEMENT  |
| US | 6,605,984 | 10/038,499 | CHARGE PUMP RIPLE REDUCTION  |
| US | 6,614,065 | 10/238,081 | USE OF MEMBRANE PROPERTIES TO REDUCE RESIDUAL STRESS IN AN INTERLAYER REGION   |
| US | 6,617,593 | 09/730,206 | ION IMPLANTATION SYSTEM  |
| US | 6,621,287 | 09/858,224 | CONNECTOR ASSEMBLY WITH DECOUPLING CAPACITORS  |
| US | 6,622,802 | 09/821,247 | FASTENER INSTALLATION TOOL AND METHODS OF USE  |
|    |           | WWN        |  |
| US | 6,625,062 | 09/966,014 | WRITE-ONCE POLYMER MEMORY WITH E-BEAM WRITING AND READING  |
| US | 6,625,650 | 09/340,068 | SYSTEM FOR MULTI-LAYER BROADBAND PROVISIONING IN COMPUTER NETWORKS   |
| US | 6,628,552 | 08/834,029 | SELF-CONFIGURING INPUT BUFFER ON FLASH MEMORIES  |
| US | 6,628,865 | 09/738,800 | ALIGNMENT OF OPTICAL FIBERS TO AN ETCHED ARRAY WAVEGUIDE   |
| US | 6,629,271 | 09/472,839 | TECHNIQUE FOR SYNCHRONIZING FAULTS IN A PROCESSOR HAVING A REPLAY SYSTEM   |
| US | 6,630,390 | 10/354,361 | METHOD OF FORMING A SEMICONDUCTOR DEVICE USING A CARBON DOPED OXIDE LAYER TO CONTROL THE CHEMICAL MECHANICAL POLISHING OF A DIELECTRIC LAYER |
| US | 6,631,339 | 09/834,016 | DATA PATH EVALUATION SYSTEM AND METHOD   |
| US | 6,631,444 | 09/894,513 | CACHE ARCHITECTURE FOR PIPELINED OPERATION WITH ON-DIE PROCESSOR   |
| US | 6,633,972 | 09/878,062 | METHOD AND APPARATUS FOR UTILIZING STATIC QUEUES IN PROCESSOR STAGING  |
| US | 6,636,671 | 09/741,642 | MARKINGS FOR ALIGNING FIBER OPTIC BUNDLE   |
| US | 6,637,719 | 10/006,502 | CARRIER FOR DISK DRIVE HOT SWAPPING  |
| US | 6,642,102 | 09/896,530 | BARRIER MATERIAL ENCAPSULATION OF PROGRAMMABLE MATERIAL  |
| US | 6,643,161 | 10/282,534 | WRITE-ONCE POLYMER MEMORY WITH E-BEAM WRITING AND READING  |
| US | 6,643,367 | 09/301,855 | DYNAMIC AND SCHEDULED COMPUTER TELEPHONY RESOURCE ALLOCATION   |
| US | 6,646,991 | 09/470,189 | MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT   |

## Schedule A

|    |           |                    |            |  |
|----|-----------|--------------------|------------|--|
| US | 6,647,455 |                    | 09/893,779 | ON-DIE CACHE MEMORY WITH REPEATERS   |
| US | 6,647,545 |                    | 09/503,879 | METHOD AND APPARATUS FOR BRANCH TRACE MESSAGE SCHEME   |
| US | 6,650,802 |                    | 09/470,574 | METHOD AND APPARATUS FOR SWITCHING AN OPTICAL BEAM   |
| US | 6,650,823 |                    | 09/741,520 | METHOD OF CREATING A PHOTONIC VIA USING FIBER OPTIC  |
| US | 6,654,369 |                    | 09/322,152 | METHOD FOR DIRECTING THE ROUTE OF A CELL TRANSMITTING A NETWORK  |
| US | 6,655,022 |                    | 09/160,535 | IMPLEMENTING MICRO BGA ASSEMBLY TECHNIQUES FOR SMALL DIE   |
| US | 6,661,264 | TNT                | 09/967,180 | RELEASING FUNCTIONAL BLOCKS IN RESPONSE TO A DETERMINATION OF A SUPPLY VOLTAGE PREDETERMINED LEVEL AND A LOGIC PREDETERMINED INITIAL STATE |
| US | 6,662,200 |                    | 09/754,684 | MULTIPLIERLESS PYRAMID FILTER  |
| US | 6,662,271 |                    | 09/894,638 | CACHE ARCHITECTURE WITH REDUNDANT SUB ARRAY  |
| US | 6,665,792 |                    | 09/475,029 | INTERFACE TO A MEMORY SYSTEM FOR A PROCESSOR HAVING A REPLAY SYSTEM  |
| US | 6,668,298 |                    | 09/474,750 | SHIFTING AN INPUT SIGNAL FROM A HIGH-SPEED DOMAIN TO A LOWER-SPEED DOMAIN  |
| US | 6,671,172 | WWN                | 09/950,100 | ELECTRONIC ASSEMBLIES WITH HIGH CAPACITY CURVED FIN HEAT SINKS   |
| US | 6,671,771 |                    | 09/468,477 | HASH CAM HAVING A REDUCED WIDTH COMPARISON CIRCUITRY AND ITS APPLICATION   |
| US | 6,671,847 |                    | 09/709,000 | I/O DEVICE TESTING METHOD AND APPARATUS  |
| US | 6,672,891 | 2003-0864617       | 09/967,060 | ZERO INSERTION FORCE CONNECTOR FOR SUBSTRATES WITH EDGE CONTACTS   |
| US | 6,674,115 | TNT                | 10/309,633 | MULTIPLE LAYER PHASE-CHANGE MEMORY   |
| US | 6,675,057 | WWN                | 09/842,389 | INTEGRATED CIRCUIT ANNEALING METHODS AND APPARATUS   |
| US | 6,683,509 | US 2003/0048145 A1 | 10/223,869 | VOLTAGE CONTROLLED OSCILLATORS   |
| US | 6,683,884 |                    | 09/541,048 | SHARED CREDIT ROUND ROBIN QUEUEING   |
| US | 6,686,793 |                    | 10/273,689 | GATE ENHANCED TRI-CHANNEL POSITIVE CHARGE PUMP   |
| US | 6,687,840 |                    | 09/557,099 | MULTILINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT  |
| US | 6,689,057 |                    | 09/773,174 | METHOD AND APPARATUS FOR COMPRESSING CALORIE BURN CALCULATION DATA USING POLYNOMIAL COEFFICIENTS   |

## Schedule A

|    |           |                                  |   |
|----|-----------|----------------------------------|---|
| US | 6,694,076 | 09/822,380                       | METHOD FOR CENTERING A CORE OF A WAVEGUIDE AMPLIFIER  |
| US | 6,699,798 | US-2003-0203647-A1<br>10/131,017 | PROMOTING ADHESION OF FLUOROPOLYMER FILMS TO SEMICONDUCTOR SUBSTRATES   |
| US | 6,700,209 | 09/474,746                       | PARTIAL UNDERFILL FOR FLIP-CHIP ELECTRONIC PACKAGES   |
| US | 6,703,299 | JNG<br>10/032,115                | UNDERFILL PROCESS FOR FLIP-CHIP DEVICE  |
| US | 6,707,712 | 10/458,828                       | METHOD FOR READING A STRUCTURAL PHASE-CHANGE MEMORY   |
| US | 6,707,970 | 09/738,799                       | ALIGNMENT OF FIBER OPTIC BUNDLE TO ARRAY WAVEGUIDE USING AN EPOXY   |
| US | 6,710,845 | 09/752,938                       | PURGING GAS FROM A PHOTOLITHOGRAPHY ENCLOSURE BETWEEN A MASK PROTECTIVE DEVICE AND A PATTERNED MASK                     |
| US | 6,717,855 | 10/318,464                       | METHOD AND APPARATUS FOR ALLOWING CONTINUOUS APPLICATION OF HIGH VOLTAGE TO A FLASH MEMORY DEVICE POWER PIN             |
| US | 6,719,920 | 10/002,855                       | SLURRY FOR POLISHING A BARRIER LAYER  |
| US | 6,723,167 | 09/746,435                       | SPINDLE SLEEVE FOR COATER/DEVELOPER   |
| US | 6,724,442 | 09/277,637                       | OPTICALLY SWITCHABLE INFRARED DETECTOR  |
| US | 6,724,767 | 09/271,061                       | TWO-DIMENSIONAL QUEUEING/DE-QUEUEING METHODS AND SYSTEMS FOR IMPLEMENTING THE SAME                                      |
| US | 6,724,814 | 09/698,934                       | PAD AND CODEC DETECTION   |
| US | 6,728,249 | 09/344,608                       | SYSTEM AND METHOD FOR PERFORMING CUT-THROUGH FORWARDING IN AN ATM NETWORK SUPPORTING LAN EMULATION                      |
| US | 6,734,118 | 10/252,295                       | DIELECTRIC MATERIAL TREATMENT   |
| US | 6,734,443 | 09/850,766                       | APPARATUS AND METHOD FOR REMOVING PHOTOMASK CONTAMINATION AND CONTROLLING ELECTROSTATIC DISCHARGE                       |
| US | 6,741,978 | 09/547,624                       | ACCESSING FILE DATA STORED IN NON-VOLATILE RE-PROGRAMMABLE SEMICONDUCTOR MEMORIES                                       |
| US | 6,744,871 | 09/752,550                       | KICKER FOR NON-VOLATILE MEMORY DRAIN BIAS   |
| US | 6,747,598 | 10/417,540                       | METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES   |
| US | 6,748,118 | 09/507,399                       | METHOD OF QUANTIZING SIGNAL SAMPLES OF AN IMAGE DURING SAME   |
| US | 6,748,513 | 09/666,922                       | METHOD AND APPARATUS FOR A LOW LATENCY SOURCE-SYNCHRONOUS ADDRESS RECEIVER FOR A HOST SYSTEM BUS IN A MEMORY CONTROLLER |

## Schedule A

|    |           |                   |            |  |
|----|-----------|-------------------|------------|--|
| US | 6,751,640 |                   | 09/718,877 | METHOD AND APPARATUS FOR MULTIPLY-ACCUMULATE TWO-DIMENSIONAL SEPARABLE SYMMETRIC FILTERING   |
| US | 6,751,837 | us-220-0051343-A1 | 10/025,434 | METHOD OF HEAT EXTRACTION FROM AN INTEGRATED CIRCUIT DIE   |
| US | 6,754,407 |                   | 10/158,250 | FLIP-CHIP PACKAGE INTEGRATING OPTICAL AND ELECTRICAL DEVICES AND COUPLING TO A WAVEGUIDE ON A BOARD  |
| US | 6,754,764 |                   | 09/468,965 | METHOD AND APPARATUS FOR MAINTAINING ORDER IN A PIPELINED PROCESS AND ITS APPLICATION  |
| US | 6,759,646 |                   | 09/199,836 | COLOR INTERPOLATION FOR A FOUR COLOR MOSAIC PATTERN  |
| US | 6,765,681 |                   | 10/119,501 | MEASURING OPTICAL PHASE  |
| US | 6,766,286 |                   | 09/820,108 | PYRAMID FILTER   |
| US | 6,768,665 |                   | 10/212,630 | REFRESHING MEMORY CELLS OF A PHASE CHANGE MATERIAL MEMORY DEVICE   |
| US | 6,770,531 |                   | 09/896,008 | ADHESIVE MATERIAL FOR PROGRAMMABLE DEVICE  |
| US | 6,772,366 |                   | 09/802,451 | METHOD AND APPARATUS FOR DETECTING AC REMOVAL  |
| US | 6,776,493 |                   | 10/126,106 | TUNABLE OPTICAL DISPERSION COMPENSATOR   |
| US | 6,777,970 | VVT               | 09/838,730 | AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT  |
| US | 6,778,548 |                   | 09/603,957 | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK  |
| US | 6,779,089 |                   | 10/042,905 | LOCKED CONTENT ADDRESSABLE MEMORY FOR EFFICIENT ACCESS   |
| US | 6,784,004 |                   | 10/662,021 | METHOD OF DETERMINING POST-ETCH OFFSET IN EXPOSED-TO-EMBEDDED OVERLAY  |
| US | 6,787,443 |                   | 10/442,834 | PCB DESIGN AND METHOD FOR PROVIDING VENTED BLIND VIAS  |
| US | 6,791,414 | 2003/0206055      | 10/424,899 | SIGNAL PROCESSING STAGE AND RADIO FREQUENCY TUNER  |
| US | 6,792,102 |                   | 09/750,030 | MATCHING ROUTED CALL TO AGENT WORKSTATION  |
| US | 6,792,179 |                   | 10/334,749 | OPTICAL THUMBTRACK   |
| US | 6,797,979 |                   | 10/418,530 | METAL STRUCTURE FOR A PHASE-CHANGE MEMORY DEVICE   |
| US | 6,798,901 |                   | 09/411,697 | METHOD OF COMPRESSING A COLOR IMAGE  |
| US | 6,801,143 |                   | 10/186,564 | A METHOD AND APPARATUS FOR GENERATING GRAY CODE FOR ANY EVEN COUNT VALUE TO ENABLE EFFICIENT POINTER EXCHANGE MECHANISMS IN ASYNCHRONOUS FIFOs |

## Schedule A

|    |           |                    |            |   |
|----|-----------|--------------------|------------|---|
| US | 6,801,436 |                    | 09/964,747 | EXTENSION MECHANISM AND METHOD FOR ASSEMBLING OVERHANGING COMPONENTS  |
| US | 6,807,600 | US-2004-0019707-A1 | 10/205,546 | METHOD, SYSTEM, AND PROGRAM FOR MEMORY BASED DATA TRANSFER  |
| US | 6,810,443 |                    | 10/335,049 | OPTICAL STORAGE TRANSFER PERFORMANCE  |
| US | 6,826,100 |                    | 10/404,405 | PUSH BUTTON MODE AUTOMATIC PATTERN SWITCHING FOR INTERCONNECT BUILT-IN SELF TEST  |
| US | 6,828,512 |                    | 10/267,158 | APPARATUS AND METHODS FOR INTERCONNECTING COMPONENTS TO VIA-IN-PAD INTERCONNECTS  |
| US | 6,830,470 | US-2004-0259405-A1 | 10/600,668 | ELECTRICAL DEVICE CONNECTOR   |
| US | 6,831,862 |                    | 10/330,469 | METHOD AND APPARATUS FOR MATCHED-REFERENCE SENSING ARCHITECTURE FOR NON-VOLATILE MEMORIES   |
| US | 6,832,268 |                    | 10/324,863 | MECHANISM TO GUARANTEE FORWARD PROGRESS FOR INCOMING COHERENT INPUT/OUTPUT (I/O) TRANSACTIONS FOR CACHING I/O AGENT ON ADDRESS CONFLICT WITH PROCESSOR TRANSACTIONS |
| US | 6,836,176 | US-2003-0122610-A1 | 10/185,419 | CHARGE PUMP RIPPLE REDUCTION  |
| US | 6,842,462 |                    | 09/712,185 | METHOD FOR SWITCHING DATA STREAMS   |
| US | 6,844,738 |                    | 10/007,672 | COAXIAL RADIO FREQUENCY ADAPTER AND METHOD  |
| US | 6,846,737 |                    | 09/639,625 | PLASMA-INDUCED DEPLETION OF FLUORINE FROM SURFACES OF FLUORINATED LOW-K DIELECTRIC MATERIALS  |
| US | 6,851,027 | 2003-0056074       | 09/953,356 | MEMORY SYSTEM ORGANIZED INTO BLOCKS OF DIFFERENT SIZES AND ALLOCATION METHOD THEREFOR   |
| US | 6,853,720 |                    | 09/967,367 | SOFTWARE CALL CONTROL AGENT   |
| US | 6,853,723 |                    | 09/417,161 | HIGH IMPEDANCE POLARITY DETECTOR  |
| US | 6,856,733 | JEG                | 10/008,922 | 1 X N FANOUT WAVEGUIDE PHOTODETECTOR  |
| US | 6,861,267 | US-2003-0062351-A1 | 09/953,833 | FORMATION OF SHUNT-LAYER FREE OVONIC UNIFIED MEMORY (OUM) DEVICES   |
| US | 6,861,917 | US-2004-0233000-A1 | 10/444,311 | OSCILLATOR SYSTEM WITH SWITCHED-CAPACITOR NETWORK AND METHOD FOR GENERATING A PRECISION TIME REFERENCE  |
| US | 6,862,341 |                    | 09/834,713 | DEVICE AND METHOD OF INTERFACING VOICE DATA BETWEEN A SWITCH AND A COMPUTER SYSTEM  |
| US | 6,862,714 | US-2003-0200514-A1 | 10/126,402 | ACCURATELY TUNING RESISTORS   |
| US | 6,863,564 |                    | 10/645,229 | COAXIAL RADIO FREQUENCY ADAPTER AND METHOD  |

## Schedule A

|    |           |                    |  |
|----|-----------|--------------------|--|
| US | 6,865,344 | 09/12,822          | CODE-SWITCHED OPTICAL NETWORK  |
| US | 6,868,071 | 09/714,382         | SYSTEM AND METHOD OF TRANSMITTING DATA FRAMES IN A MESH OF DATA SWITCHES   |
| US | 6,873,380 | US-2003-0011303-A1 | PROVIDING OPTICAL ELEMENTS OVER EMISSIVE DISPLAYS  |
| US | 6,876,843 | US 2002/0137486 A1 | RADIO FREQUENCY AMPLIFIER WITH IMPROVED INTERMODULATION PERFORMANCE  |
| US | 6,877,060 | 09/933,547         | DYNAMIC DELAYED TRANSACTION BUFFER CONFIGURATION BASED ON BUS FREQUENCY  |
| US | 6,886,989 | 09/738,686         | ALIGNMENT OF FIBER OPTIC BUNDLE TO ARRAY WAVEGUIDE USING PINS  |
| US | 6,889,237 | 09/823,212         | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  |
| US | 6,891,889 | 09/947,331         | SIGNAL TO NOISE RATIO OPTIMIZATION FOR VIDEO COMPRESSION BIT-RATE CONTROL  |
| US | 6,894,801 | 10/199,382         | RECOVERY OF BIT-ROTATED FRAMES DURING FACSIMILE TRANSMISSIONS IN A GLOBAL SYSTEM FOR MOBILE COMMUNICATIONS (GSM) NETWORK |
| US | 6,898,852 | US-2004-0043662-A1 | CONNECTOR ASSEMBLY WITH DECOUPLING CAPACITORS  |
| US | 6,904,201 | 10/142,329         | PHASE-CONTROLLED FIBER BRAGG GRATINGS AND MANUFACTURING METHODS  |
| US | 6,904,482 | 10/086,410         | A COMMON BOOT ENVIRONMENT FOR A MODULAR SERVER SYSTEM  |
| US | 6,904,504 | 09/991,128         | METHOD AND APPARATUS FOR SOFTWARE SELECTION OF PROTECTED REGISTER SETTINGS   |
| US | 6,905,397 | 10/213,776         | APPARATUS FOR ENHANCED RATE CHEMICAL MECHANICAL POLISHING WITH ADJUSTABLE SELECTIVITY                                    |
| US | 6,908,812 | US-2003-0201469-A1 | PHASE CHANGE MATERIAL MEMORY DEVICE  |
| US | 6,909,645 | US-2004-0013007-A1 | CLUSTER BASED REDUNDANCY SCHEME FOR SEMICONDUCTOR MEMORIES   |
| US | 6,915,407 | 10/813,145         | METHOD AND APPARATUS FOR A LOW LATENCY SOURCE-SYNCHRONOUS ADDRESS RECEIVER FOR A HOST SYSTEM BUS IN A MEMORY CONTROLLER  |
| US | 6,915,522 | 10/055,528         | SYSTEM AND METHOD TO SYNCHRONIZE TWO OR MORE OBJECT MANAGEMENT SYSTEMS   |
| US | 6,917,381 | US-2002-0063788-A1 | COLOR FILTER ARRAY AND COLOR INTERPOLATION ALGORITHM   |
| US | 6,917,524 | US-2005-0014419-A1 | EXTENSION MECHANISM AND METHOD FOR ASSEMBLING OVERHANGING COMPONENTS   |
| US | 6,927,346 | US-2004-0118606-A1 | SURFACE MOUNT TECHNOLOGY TO VIA-IN-PAD INTERCONNECTIONS  |

## Schedule A

|    |           |                    |            |  |
|----|-----------|--------------------|------------|--|
| US | 6,927,497 | US-2004-0036163-A1 | 10/226,070 | MULTI-DIE SEMICONDUCTOR PACKAGE  |
| US | 6,928,200 | SWL                | 10/265,873 | ULTRA-THIN POLARIZATION MODE CONVERTERS BASED ON LIQUID CRYSTAL MATERIALS                    |
| US | 6,928,216 |                    | 10/672,329 | MARKINGS FOR ALIGNING FIBER OPTIC BUNDLE   |
| US | 6,931,121 |                    | 09/362,425 | DUAL PERSONALITY ANALOG PORT   |
| US | 6,931,151 |                    | 09/991,629 | METHOD AND APPARATUS FOR MODIFYING GRAPHICS CONTENT PRIOR TO DISPLAY FOR COLOR BLIND USE     |
| US | 6,933,944 |                    | 10/104,042 | DYNAMIC COMPUTATION OF CHIPSET-SUPPORTED ACCELERATED GRAPHICS PORT APERTURE SIZES            |
| US | 6,934,953 |                    | 09/997,669 | DEFERRED PROCEDURE CALL IN INTERFACE DESCRIPTION LANGUAGE                                    |
| US | 6,937,794 |                    | 10/409,287 | USING A TRANSVERSAL FILTER TO COMPENSATE FOR DISPERSION                                      |
| US | 6,937,948 | US-2003-0093233-A1 | 09/993,336 | FLASH MEMORY PROGRAM AND ERASE OPERATIONS  |
| US | 6,950,952 |                    | 10/211,465 | DYNAMIC POWER LEVEL CONTROL BASED ON A BOARD LATCH STATE                                     |
| US | 6,952,122 | US-2003-0062933-A1 | 09/966,481 | GENERATING PULSES FOR RESETTNG INTEGRATED CIRCUITS   |
| US | 6,952,764 |                    | 10/039,588 | STOPPING REPLAY TORNADOES  |
| US | 6,958,450 |                    | 10/405,098 | CONNECTION APPARATUS, SYSTEMS, AND METHODS   |
| US | 6,961,472 |                    | 08/507,213 | METHOD OF INVERSE QUANTIZING QUANTIZED SIGNAL SAMPLES OF AN IMAGE DURING IMAGE DECOMPRESSION |
| US | 6,967,496 | US-2004-0246017-A1 | 10/889,417 | AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT                  |
| US | 6,975,720 |                    | 09/523,935 | METHOD OF PROCESSING AN INBOUND CALL IN A CALL CENTER  |
| US | 6,976,172 |                    | 09/752,882 | SYSTEM AND METHOD FOR PROTECTED MESSAGING  |
| US | 6,982,988 |                    | 10/044,766 | PROGRAMMABLE FRAME SPLITTER  |
| US | 6,998,289 | US-2003-0071289-A1 | 10/302,421 | MULTIPLE LAYER PHASE-CHANGE MEMORY   |
| US | 7,000,109 |                    | 10/002,541 | METHOD AND APPARATUS FOR UNLOCKING A COMPUTER SYSTEM HARD DRIVE                              |
| US | 7,000,434 |                    | 09/741,237 | METHOD OF CREATING AN ANGLED WAVEGUIDE USING LITHOGRAPHIC TECHNIQUES                         |
| US | 7,002,873 | 2005-0135178       | 10/739,268 | MEMORY ARRAY WITH STAGED OUTPUT  |
| US | 7,003,059 |                    | 09/500,524 | JABBER COUNTER MECHANISM FOR ELASTIC BUFFER OPERATION  |

## Schedule A

|    |           |                    |            |   |
|----|-----------|--------------------|------------|---|
| US | 7,007,304 |                    | 09/665,826 | METHOD AND APPARATUS TO IMPROVE THE PROTECTION OF INFORMATION PRESENTED BY A COMPUTER                 |
| US | 7,010,780 |                    | 09/663,277 | METHOD AND SYSTEM FOR SOFTWARE MODULARIZATION AND AUTOMATIC CODE GENERATION FOR EMBEDDED SYSTEMS      |
| US | 7,013,120 |                    | 10/043,746 | WALKING WEAVER IMAGE REJECT MIXER FOR RADIO   |
| US | 7,017,062 |                    | 09/751,601 | METHOD AND APPARATUS FOR RECOVERING FROM AN OVERHEATED MICROPROCESSOR                                 |
| US | 7,024,575 |                    | 10/294,271 | APPARATUS AND METHOD FOR TIME SYNCHRONIZATION OF A PLURALITY OF MULTIMEDIA STREAMS                    |
| US | 7,029,978 | US-2005-0032319-A1 | 10/633,869 | CONTROLLING THE LOCATION OF CONDUCTION BREAKDOWN IN PHASE CHANGE MEMORIES                             |
| US | 7,031,861 | US-2005-0137831-A1 | 10/742,218 | METHOD, SYSTEM, AND ARTICLE OF MANUFACTURE FOR RUNNING DIAGNOSTICS RELATED TO A DEVICE                |
| US | 7,032,149 | US-2004-0243898-A1 | 10/449,332 | APPARATUS AND METHOD FOR ADAPTING A LEVEL SENSITIVE DEVICE TO PRODUCE EDGE-TRIGGERED BEHAVIOR         |
| US | 7,036,071 | AMM                | 10/214,630 | PRACTICAL CODING AND METRIC CALCULATION FOR THE LATTICE INTERFERED CHANNEL                            |
| US | 7,045,408 | US-2004-0235236-A1 | 10/443,152 | AN INTEGRATED CIRCUIT WITH IMPROVED CHANNEL STRESS PROPERTIES AND A METHOD FOR MAKING IT              |
| US | 7,047,458 |                    | 10/319,517 | TESTING METHODOLOGY AND APPARATUS FOR INTERCONNECTS   |
| US | 7,048,073 | WWN                | 10/655,822 | FASTENER INSTALLATION TOOL AND METHODS OF USE   |
| US | 7,049,704 |                    | 10/840,017 | A FLIP-CHIP PACKAGE INTEGRATING OPTICAL AND ELECTRICAL DEVICES AND COUPLING TO A WAVEGUIDE ON A BOARD |
| US | 7,050,063 |                    | 09/502,994 | 3D RENDERING TEXTURE CACHING SCHEME   |
| US | 7,064,344 | US-2003-0205809-A1 | 10/418,548 | BARRIER MATERIAL ENCAPSULATION OF PROGRAMMABLE MATERIAL   |
| US | 7,065,298 |                    | 09/454,164 | CODE-BASED OPTICAL NETWORKS, METHODS AND APPARATUS  |
| US | 7,072,352 |                    | 10/080,093 | INVERSE MULTIPLEXING OF UNMANAGED TRAFFIC FLOWS OVER A MULTI-STAR INFIBAND NETWORK                    |
| US | 7,073,070 |                    | 09/896,738 | METHOD AND APPARATUS TO IMPROVE THE PROTECTION OF INFORMATION PRESENTED BY A COMPUTER                 |
| US | 7,080,236 |                    | 10/262,214 | A STACK-POINTER UPDATE TECHNIQUE  |

## Schedule A

|    |           |                    |            |   |
|----|-----------|--------------------|------------|---|
| US | 7,089,409 | 2004-0083351       | 10/690,634 | INTERFACE TO A MEMORY SYSTEM FOR A PROCESSOR HAVING A REPLAY SYSTEM                                   |
| US | 7,091,108 | US-2005-0059221-A1 | 10/661,738 | METHODS AND APPARATUSES FOR MANUFACTURING ULTRA THIN DEVICE LAYERS FOR INTEGRATED CIRCUIT DEVICES     |
| US | 7,093,124 |                    | 10/002,337 | A MECHANISM TO IMPROVE AUTHENTICATION FOR REMOTE MANAGEMENT OF A COMPUTER SYSTEM                      |
| US | 7,099,180 | US-2006-0181922-A1 | 11/058,797 | RESETTING PHASE CHANGE MEMORY BITS  |
| US | 7,105,408 | US-2006-0073655-A1 | 10/948,884 | PHASE CHANGE MEMORY WITH A SELECT DEVICE HAVING A BREAKDOWN LAYER                                     |
| US | 7,107,202 |                    | 09/495,150 | METHOD AND APPARATUS FOR HARDWARE AND SOFTWARE CO-SIMULATION  |
| US | 7,111,103 |                    | 10/104,341 | METHOD AND APPARATUS FOR GENERATING TRAFFIC IN AN ELECTRONIC BRIDGE VIA A LOCAL CONTROLLER            |
| US | 7,112,970 | US-2004-0239334-A1 | 10/820,693 | COAXIAL RADIO FREQUENCY ADAPTER AND METHOD  |
| US | 7,120,904 |                    | 09/652,292 | DATA-FLOW METHOD FOR OPTIMIZING EXCEPTION-HANDLING INSTRUCTIONS IN PROGRAMS                           |
| US | 7,122,391 |                    | 10/659,898 | WAFER-LEVEL TEST STRUCTURE FOR EDGE-EMITTING SEMICONDUCTOR LASERS                                     |
| US | 7,123,265 |                    | 09/741,599 | METHOD & APPARATUS FOR ADDING REAL-TIME PRIMITIVES  |
| US | 7,123,712 |                    | 09/277,286 | COMPUTER TELEPHONY SERVER WITH IMPROVED FLEXIBILITY   |
| US | 7,124,412 |                    | 09/737,140 | EXTENSIBLE BIOS ERROR LOG   |
| US | 7,125,762 | US-2006-0024892-A1 | 10/900,666 | COMPENSATING THE WORKFUNCTION OF A METAL GATE TRANSISTOR FOR ABSTRACTION BY THE GATE DIELECTRIC LAYER |
| US | 7,133,585 | US-2006-0198597-A1 | 11/410,591 | METHOD TO REALIZE FAST SILICON-ON-INSULATOR (SOI) OPTICAL DEVICE                                      |
| US | 7,133,586 | US-2006-0198572-A1 | 11/411,059 | METHOD TO REALIZE FAST SILICON-ON-INSULATOR (SOI) OPTICAL DEVICE                                      |
| US | 7,135,696 | US-2006-0073631-A1 | 10/949,090 | PHASE CHANGE MEMORY WITH DAMASCENE MEMORY ELEMENT   |
| US | 7,139,842 |                    | 09/823,215 | METHOD AND APPARATUS FOR INTERSYSTEM CUT/COPY AND PASTE   |
| US | 7,140,023 | US-2005-0097295-A1 | 10/699,638 | SYMBOLIC BUFFER ALLOCATION IN LOCAL CACHE AT A NETWORK PROCESSING ELEMENT                             |
| US | 7,141,454 | US-2004-0260068-A1 | 10/731,177 | WIRE-BONDED PACKAGE WITH ELECTRICALLY INSULATING WIRE ENCAPSULANT AND THERMALLY CONDUCTIVE OVERMOLD   |

## Schedule A

|    |           |                    |            |  |
|----|-----------|--------------------|------------|--|
| US | 7,143,279 | US-2004-0243798-A1 | 10/448,696 | DYNAMIC BIOS EXECUTION AND CONCURRENT UPDATE FOR A BLADE SERVER  |
| US | 7,145,481 | US-2005-0068203-A1 | 10/676,888 | METHOD AND APPARATUS FOR TRUSTED KEYBOARD SCANNING   |
| US | 7,154,175 | US-2005-0280138-A1 | 10/873,817 | GROUND PLANE FOR INTEGRATED CIRCUIT PACKAGE  |
| US | 7,154,853 |                    | 10/137,753 | RATE POLICING ALGORITHM FOR PACKET FLOWS   |
| US | 7,155,370 |                    | 10/393,223 | REUSABLE, BUILT-IN SELF-TEST METHODOLOGY FOR COMPUTER SYSTEMS  |
| US | 7,155,589 | 2002-0087836       | 09/750,095 | RECOVERY SCHEMES FOR REGISTER RENAMING STRUCTURES IN THE PRESENCE OF EXCEPTIONS  |
| US | 7,155,615 |                    | 09/608,117 | METHOD AND APPARATUS FOR PROVIDING A SECURE-PRIVATE PARTITION ON A HARD DISK DRIVE OF A COMPUTER SYSTEM VIA IDE CONTROLLER |
| US | 7,158,631 |                    | 10/284,724 | EFFICIENT ECHO CANCELLATION TECHNIQUES<br>A TECHNIQUE FOR SYNCHRONIZING FAULTS IN A PROCESS HAVING A REPLAY SYSTEM         |
| US | 7,159,154 |                    | 10/614,215 |  |
| US | 7,160,127 | US-2004-0183558-A1 | 10/391,893 | A VARIABLE LATCH   |
| US | 7,161,225 | US-2005-0136557-A1 | 11/038,336 | FORMATION OF SHUNT-LAYER FREE OVONIC UNIFIED MEMORY (OUM) DEVICES  |
| US | 7,161,908 | US-2004-0062253-A1 | 10/256,475 | DETERMINISTIC BANDWIDTH THROTTLING TO FACILITATE OPERATION OF A CONTROL UNIT   |
| US | 7,161,999 | US-2003-0123588-A1 | 10/038,956 | SYNCHRONIZING DATA OR SIGNAL TRANSFER ACROSS CLOCKED LOGIC DOMAINS   |
| US | 7,162,028 |                    | 08/986,255 | UNIVERSAL TELEPHONY INTERFACE POLARITY DETECTOR  |
| US | 7,162,564 |                    | 10/190,851 | CONFIGURABLE MULTI-PORT MULTI-PROTOCOL NETWORK INTERFACE TO SUPPORT PACKET PROCESSING                                      |
| US | 7,162,814 | US-2004-0268092 A1 | 10/608,121 | ELIMINATION OF POTENTIAL RENAMING STALLS DUE TO USE OF PARTIAL REGISTERS   |
| US | 7,168,074 |                    | 09/675,286 | RUNTIME PREDICTION FRAMEWORK FOR CPU INTENSIVE APPLICATIONS  |
| US | 7,171,545 |                    | 10/747,625 | PREDICTIVE FILTERING OF REGISTER CACHE ENTRY   |
| US | 7,173,817 | US-2005-0068722-A1 | 10/748,385 | FRONT SIDE HOT-SWAP CHASSIS MANAGEMENT MODULE  |
| US | 7,174,060 | US-2006-0018585-A1 | 11/189,543 | ULTRA-THIN POLARIZATION MODE CONVERTERS BASED ON LIQUID CRYSTAL MATERIALS  |
| US | 7,176,122 | US-2004-0175925-A1 | 10/378,061 | DIELECTRIC WITH SIDEWALL PASSIVATING LAYER   |
| US | 7,177,502 | US-2005-0232543-A1 | 11/151,010 | HEATING OPTICAL DEVICES  |
| US | 7,180,429 |                    | 10/076,817 | SLOW MOTION DETECTION SYSTEM   |

## Schedule A

|    |           |                    |            |   |
|----|-----------|--------------------|------------|---|
| US | 7,180,523 |                    | 09/539,343 | TRIMMING SURFACES   |
| US | 7,180,929 |                    | 10/126,348 | WAFER-LEVEL TEST STRUCTURE FOR EDGE-EMITTING SEMICONDUCTOR LASERS                             |
| US | 7,181,601 | US-2005-0125645-A1 | 10/731,567 | METHOD AND APPARATUS FOR PREDICTION FOR FORK AND JOIN INSTRUCTIONS IN SPECULATIVE EXECUTION   |
| US | 7,183,500 | US-2006-0000353-A1 | 10/883,610 | ELECTROMAGNETIC INTERFERENCE (EMI) FILTER WITH PASSIVE NOISE CANCELLATION                     |
| US | 7,184,050 | US-2005-0237331-A1 | 11/172,342 | DYNAMIC COMPUTATION OF CHIPSET-SUPPORTED ACCELERATED GRAPHICS PORT APERTURE SIZES             |
| US | 7,184,536 |                    | 09/967,108 | INTELLIGENT FORWARDED TELEPHONE CALL HANDLING WITH A CALL ANSWERING SYSTEM                    |
| US | 7,185,253 | US-2003-0188269-A1 | 10/107,628 | COMPACTING CIRCUIT RESPONSES  |
| US | 7,193,965 |                    | 09/665,215 | MULTI-WIRELESS NETWORK CONFIGURABLE BEHAVIOR  |
| US | 7,194,182 |                    | 10/304,195 | METHODS AND APPARATUSES USING A FIBER-FLEXURE INDUCTION SOLDERING STATION                     |
| US | 7,194,559 |                    | 10/231,863 | SLAVE I/O DRIVER CALIBRATION USING ERROR-NULLING MASTER REFERENCE                             |
| US | 7,197,174 |                    | 09/396,407 | MAGNETIC INK ENCODING PEN   |
| US | 7,197,498 | US-2004-0267772-A1 | 10/609,954 | APPARATUS, SYSTEM AND METHOD FOR UPDATING A SORTED LIST                                       |
| US | 7,197,659 |                    | 09/965,223 | GLOBAL I/O TIMING ADJUSTMENT USING CALIBRATED DELAY ELEMENTS                                  |
| US | 7,200,060 | US-2005-0013190-A1 | 10/620,469 | A MEMORY DRIVER ARCHITECTURE AND ASSOCIATED METHODS   |
| US | 7,203,631 | US-2004-0049371-A1 | 10/334,113 | SYSTEM AND METHOD TO ANALYZE VLSI DESIGNS   |
| US | 7,203,767 | US-2006-0168311-A1 | 11/351,991 | CONSOLE REDIRECTION AMONG LINKED COMPUTERS  |
| US | 7,203,825 |                    | 09/970,485 | SHARING INFORMATION TO REDUCE REDUNDANCY IN HYBRID BRANCH PREDICTION                          |
| US | 7,205,074 |                    | 10/334,841 | VENTING OF PELLICLE CAVITY FOR A MASK   |
| US | 7,205,562 |                    | 10/318,984 | PHASE CHANGE MEMORY AND METHOD THEREFOR   |
| US | 7,206,865 |                    | 10/402,125 | APPARATUS AND METHOD FOR COMBINING WRITES TO I/O  |
| US | 7,206,955 |                    | 10/745,903 | BUNDLE SKEW MANAGEMENT AND CELL SYNCHRONIZATION   |
| US | 7,207,039 | US-2005-0149924-A1 | 10/746,975 | SECURE BOOTING AND PROVISIONING   |
| US | 7,210,079 | US-2006-0123275-A1 | 11/321,012 | APPARATUS AND METHOD FOR ADAPTING A LEVEL SENSITIVE DEVICE TO PRODUCE EDGE-TRIGGERED BEHAVIOR |
| US | 7,211,501 |                    | 10/319,310 | METHOD AND APPARATUS FOR LASER ANNEALING  |

## Schedule A

|    |           |                    |            |  |
|----|-----------|--------------------|------------|--|
| US | 7,212,021 |                    | 10/097,136 | MANUFACTURING INTEGRATED CIRCUITS AND TESTING ON-DIE POWER SUPPLIES USING DISTRIBUTED PROGRAMMABLE DIGITAL CURRENT SINKS |
| US | 7,212,497 |                    | 09/741,405 | METHOD AND DRIVER FOR DETECTING GLITCHES   |
| US | 7,213,142 |                    | 10/421,287 | SYSTEM AND METHOD TO INITIALIZE REGISTERS WITH AN EEPROM STORED BOOT SEQUENCE  |
| US | 7,215,173 | US-2006-0170481-A1 | 11/047,442 | A LOW-SWING LEVEL SHIFTER  |
| US | 7,215,383 | US-2004-0183954-A1 | 10/767,324 | COHERENT LIGHT DESPECKLING   |
| US | 7,216,140 |                    | 09/676,556 | EFFICIENT IMPLEMENTATION OF N-POINT DCT, N-POINT IDCT, SA-DCT AND SA-IDCT ALGORITHMS                                     |
| US | 7,216,201 |                    | 11/327,454 | PARALLEL CACHELETS   |
| US | 7,218,491 | DJP1               | 10/329,058 | ELECTROSTATIC DISCHARGE PROTECTION UNIT INCLUDING EQUALIZATION   |
| US | 7,223,660 | US-2004-0023418-A1 | 10/210,866 | FLASH ASSISTED ANNEALING   |
| US | 7,223,701 | US-2004-0048485-A1 | 10/236,203 | IN-SITU SEQUENTIAL HIGH DENSITY PLASMA DEPOSITION AND ETCH PROCESSING FOR GAP FILL                                       |
| US | 7,231,083 |                    | 09/430,282 | CONTROLLING PROCESSOR-BASED SYSTEMS USING A DIGITAL CAMERA   |
| US | 7,231,468 | US-2005-0033869-A1 | 10/456,185 | FUTURE ACTIVITY LIST FOR PERIPHERAL BUS HOST CONTROLLER  |
| US | 7,231,562 |                    | 10/280,458 | METHOD AND APPARATUS FOR INDEPENDENT CONTROL OF DEVICES UNDER TEST CONNECTED IN PARALLEL                                 |
| US | 7,233,725 | US-2005-0145783-A1 | 11/042,537 | 1 X N FANOUT WAVEGUIDE PHOTODETECTOR   |
| US | 7,236,059 |                    | 11/172,133 | APPARATUS, SYSTEM, AND METHOD FOR OSCILLATOR NETWORK WITH MULTIPLE PARALLEL OSCILLATOR CIRCUITS                          |
| US | 7,237,102 |                    | 10/283,920 | METHODS AND APPARATUS FOR CONFIGURING HARDWARE RESOURCES IN A PRE-BOOT ENVIRONMENT WITHOUT REQUIRING A SYSTEM RESET      |
| US | 7,239,999 | 2004-0019491       | 10/200,328 | SPEED CONTROL PLAYBACK OF PARAMETRIC SPEECH ENCODED DIGITAL AUDIO  |
| US | 7,240,247 |                    | 10/133,625 | PROVIDING A CHAIN OF TOKENIZED ERROR AND STATE INFORMATION FOR A CALL STACK  |
| US | 7,240,260 |                    | 10/317,605 | STIMULUS GENERATION  |
| US | 7,240,342 |                    | 09/608,616 | USER TRANSPARENT CONTINUOUS COMPILATION  |
| US | 7,242,760 |                    | 09/750,025 | AUTOMATIC DETECTING WHEN AN AGENT IS AVAILABLE   |

## Schedule A

|    |           |                    |            |   |
|----|-----------|--------------------|------------|---|
| US | 7,245,945 |                    | 10/289,081 | PORTABLE COMPUTING DEVICE ADAPTED TO UPDATE DISPLAY INFORMATION WHILE IN A LOW POWER MODE                                       |
| US | 7,246,135 | US-2002-0091867-A1 | 09/756,579 | SHARING CLASSES BETWEEN PROGRAMS  |
| US | 7,248,475 | US-2006-0268534-A1 | 11/140,769 | WIRELESS DEVICE ENCLOSURE USING PIEZOELECTRIC COOLING STRUCTURES  |
| US | 7,249,230 | US-2006-0289305-A1 | 10/874,998 | QUEUE STRUCTURE WITH VALIDITY VECTOR AND ORDER ARRAY  |
| US | 7,251,880 | AMM                | 09/964,746 | METHOD AND STRUCTURE FOR IDENTIFYING LEAD-FREE SOLDER   |
| US | 7,256,872 |                    | 10/759,641 | PURGING GAS FROM A PHOTOLITHOGRAPHY ENCLOSURE BETWEEN A MASK PROTECTIVE DEVICE AND A PATTERNED MASK                             |
| US | 7,257,538 |                    | 10/266,226 | GENERATING ANIMATION FROM VISUAL AND AUDIO INPUT  |
| US | 7,259,023 | US-2006-0054991-A1 | 10/939,145 | FORMING PHASE CHANGE MEMORY ARRAYS  |
| US | 7,259,982 | US-2006-0146600-A1 | 11/029,981 | READING PHASE CHANGE MEMORIES TO REDUCE READ DISTURBS   |
| US | 7,260,661 | US-2006-0050694-A1 | 10/934,640 | PROCESSING REPLIES TO REQUEST PACKETS IN AN ADVANCED SWITCHING CONTEXT  |
| US | 7,270,056 | US-2006-0042482    | 10/930,251 | PRINT STRIPPER FOR ESD CONTROL  |
| US | 7,271,403 | US-2004-013154-A1  | 10/319,183 | ISOLATING PHASE CHANGE MEMORY DEVICES   |
| US | 7,271,673 | US-2007-0001775-A1 | 11/171,860 | VOLTAGE CONTROLLED OSCILLATOR (VCO) TUNING  |
| US | 7,272,211 |                    | 10/038,917 | PROPERTY MANAGEMENT SYSTEM PROTOCOL AUTO-DETECTION  |
| US | 7,272,623 | 2003-0191787-A1    | 10/118,349 | METHODS AND APPARATUS FOR DETERMINING A FLOATING-POINT EXPONENT ASSOCIATED WITH AN UNDERFLOW CONDITION OR AN OVERFLOW CONDITION |
| US | 7,272,701 | US-2005-0091475-A1 | 10/692,436 | METHOD AND APPARATUS FOR LIMITING PORTS IN A REGISTER ALIAS TABLE HAVING HIGH-BANDWIDTH AND LOW-BANDWIDTH STRUCTURES            |
| US | 7,275,254 |                    | 09/717,579 | METHOD AND APPARATUS FOR DETERMINING AND DISPLAYING THE SERVICE LEVEL OF A DIGITAL TELEVISION BROADCAST SIGNAL                  |
| US | 7,275,312 | US-2004-0261262-A1 | 10/611,215 | APPARATUS FOR PRECISE ALIGNMENT OF PACKAGING CAPS ON A SUBSTRATE  |
| US | 7,277,066 | US-2003-0011724-A1 | 09/906,342 | REPAIRABLE TILED DISPLAYS   |
| US | 7,282,730 | US-2006-0157689-A1 | 11/037,850 | FORMING A CARBON LAYER BETWEEN PHASE CHANGE LAYERS OF A PHASE CHANGE MEMORY   |

## Schedule A

|    |           |                    |            |   |
|----|-----------|--------------------|------------|---|
| US | 7,289,382 | US-2005-0135170-A1 | 10/744,664 | REWRITABLE FUSE MEMORY NETWORK STATISTICS   |
| US | 7,289,455 | US-2005-0025055-A1 | 10/628,997 | ANALYZING INTERCONNECT STRUCTURES   |
| US | 7,289,945 | US-2004-0082230-A1 | 10/281,857 | SYSTEM AND METHOD FOR AUTOMATICALLY IDENTIFYING AND ATTACHING RELATED DOCUMENTS                           |
| US | 7,296,060 | 2002-0107924       | 09/220,910 | METHOD AND APPARATUS TO CREATE BYPASS LOGIC IN A DIGITAL CIRCUIT DESIGN                                   |
| US | 7,299,425 | US-2004-0268273-A1 | 10/606,868 | TIMING ANALYSIS APPARATUS, SYSTEMS, AND METHODS   |
| US | 7,299,433 | US-2004-0250224-A1 | 10/458,537 | LEAKAGE CONTROL IN INTEGRATED CIRCUITS  |
| US | 7,302,652 | US-2004-0262039-A1 | 10/404,937 | BOND FINGER ON VIA SUBSTRATE, PROCESS OF MAKING SAME, PACKAGE MADE THEREBY, AND METHOD OF ASSEMBLING SAME |
| US | 7,302,756 |                    | 10/612,281 | IMPROVED POWER DISTRIBUTION WITHIN A FOLDED FLEX PACKAGE METHOD AND APPARATUS                             |
| US | 7,304,373 | US-2006-0091508-A1 | 10/977,908 | A METHOD AND AN APPARATUS FOR A HARD-CODED BIT VALUE CHANGEABLE IN ANY LAYER OF METAL                     |
| US | 7,307,285 | US-2005-0130363-A1 | 11/048,524 | SMALLER AND LOWER POWER STATIC MUX CIRCUITRY IN GENERATING MULTIPLIER PARTIAL PRODUCT SIGNALS             |
| US | 7,308,470 | US-2005-0125478-A1 | 10/728,395 | DUAL-TARGET BLOCK REGISTER ALLOCATION   |
| US | 7,308,563 |                    | 09/968,278 | AUTOMATIC SYMBOLIC INDEXING METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN                  |
| US | 7,310,790 |                    | 10/309,529 | RESETTING PHASE CHANGE MEMORY BITS  |
| US | 7,313,016 | US-2006-0256613-A1 | 11/486,872 | EMBEDDED PCB IDENTIFICATION   |
| US | 7,317,310 |                    | 09/954,402 | SYSTEMS AND METHODS FOR USING HDLC CHANNEL CONTEXT TO SIMULTANEOUSLY PROCESS MULTIPLE HDLC CHANNELS       |
| US | 7,317,737 | US-2006-0053083-A1 | 10/655,565 | METHOD AND APPARATUS FOR PERFORMING DISTRIBUTED SIMULATION UTILIZING A SIMULATION BACKPLANE               |
| US | 7,319,947 |                    | 09/470,875 | DYNAMIC LOCAL DRIVE AND PRINTER SHARING   |
| US | 7,320,033 |                    | 09/917,320 | INITIALIZING PHASE CHANGE MEMORIES  |
| US | 7,323,707 | US-2006-0001016-A1 | 10/881,664 | DATA PACKET HEADER CONVERSION   |
| US | 7,324,516 |                    | 10/217,644 | IMPROVED LOOK-AHEAD CARRY ADDER CIRCUIT   |
| US | 7,325,025 | 2003-0115237       | 10/020,447 | INTERCONNECT STRUCTURE IN INTEGRATED CIRCUITS   |
| US | 7,326,972 | US-2006-0001103-A1 | 10/880,988 | A METHOD AND AN APPARATUS FOR A HARD-CODED BIT VALUE CHANGEABLE IN ANY LAYER OF METAL                     |
| US | 7,326,973 | US-2005-0127506-A1 | 11/049,435 |   |

## Schedule A

|    |           |                    |            |  |
|----|-----------|--------------------|------------|--|
| US | 7,327,370 | US-2005-0243096-A1 | 11/174,662 | MEMORY CONTROLLER HUB INTERFACE  |
| US | 7,328,313 | US-2006-0224834-A1 | 11/094,687 | WRITE-BACK CACHE METHODS AND APPARATUS   |
| US | RE38,674  | 5,244,843          | 08/528,188 | PROCESS FOR FORMING A THIN OXIDE LAYER   |
| US |           |                    | 07/999,148 | METHOD AND APPARATUS FOR ANALYZING DIGITAL VIDEO IMAGES BY MERGING DISPLACEMENT VECTORS  |
| US |           |                    | 08/623,672 | CLEANING STEP WHICH IMPROVES ELECTROMIGRATION PERFORMANCE OF INTERLAYER CONNECTION IN INTEGRATED CIRCUITS  |
| US |           |                    | 08/772,016 | METHOD AND APPARATUS FOR PERFORMING BLOCK BASED FREQUENCY DOMAIN FILTERING   |
| US |           |                    | 08/799,654 | METHOD AND APPARATUS FOR SCALING IMAGE DATA  |
| US |           |                    | 08/874,821 | METHOD AND APPARATUS FOR SCRUBBING THE BOND PADS OF AN INTEGRATED CIRCUIT DURING WAFER SORT  |
| US |           |                    | 08/944,041 | POLYSILICON POLISH FOR PATTERNING IMPROVEMENT  |
| US |           |                    | 08/965,592 | METHOD AND APPARATUS FOR SEARCHING A MASS STORAGE MEDIUM   |
| US |           |                    | 09/000,712 | METHOD AND APPARATUS FOR CHANGING THE FONT USED TO DISPLAY TEXT ENTERED INTO AN INFORMATION INPUT FIELD  |
| US |           |                    | 09/004,052 | PORTABLE DIGITAL PHOTOGRAPH VIEWING APPARATUSES  |
| US |           |                    | 09/122,270 | ADVANCED REGISTER REMAINING SYSTEM FOR IMPROVED INSTRUCTION LEVEL PARALLELISM  |
| US |           |                    | 09/148,392 | SELECTING DESIGN POINTS ON PARAMETER FUNCTIONS HAVING FIRST SUM OF CONSTRAINT SET AND SECOND SUM OF OPTIMIZING SET TO IMPROVE SECOND SUM WITHIN DESIGN CONSTRAINTS |
| US |           |                    | 09/156,272 | PHOTOLITHOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL   |
| US |           |                    | 09/250,940 | SYSTEM AND METHOD FOR CONTROLLING AN ELECTRONIC DEVICE   |
| US |           |                    | 09/258,060 | CALIBRATING PROJECTION DISPLAYS  |
| US |           |                    | 09/259,394 | CORRECTING NON-UNIFORMITY IN DISPLAYS  |
| US |           |                    | 09/344,453 | NETWORK ACCELERATOR SUBSYSTEM BASED ON SINGLE-CHIP NETWORK   |
| US |           |                    | 09/350,269 | TRANSMITTING DATA AND COMMANDS OVER A BUS  |
| US |           |                    | 09/409,522 | AUTOMATICALLY RECORDING VIDEO  |

## Schedule A

|    |                    |   |
|----|--------------------|---|
| US | 09/471,964         | REAL-TIME PERFORMANCE ASSESSMENT OF LARGE AREA NETWORK USER EXPERIENCE                      |
| US | 09/478,994         | SYSTEM AND METHOD FOR STALLING A DISPLAY CONTROLLER UPON REQUEST                            |
| US | 09/515,556         | OBSCURING VIDEO SIGNALS FOR CONDITIONAL ACCESS  |
| US | 09/524,701         | MICROSCOPE WITH INFRARED IMAGING  |
| US | 09/533,024         | METHOD AND APPARATUS TO PROVIDE CLIENT-SIDE FILTERING IN A BROADCAST SYSTEM                 |
| US | 09/603,800         | COMBINED PROPAGATE/GENERATE/PARTIAL SUM CIRCUIT FOR SINGLE-RAIL ADDERS                      |
| US | 09/608,637         | METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN                                |
| US | 09/609,496         | METHOD AND APPARATUS FOR FAST ARGUMENT REDUCTION IN A COMPUTING SYSTEM                      |
| US | 09/611,938         | DYNAMICALLY LOCATING ICONS ON A VIDEO DISPLAY SCREEN  |
| US | 09/676,175         | METHOD AND APPARATUS FOR GENERATING AN EXPECTED TOP OF STACK DURING INSTRUCTION TRANSLATION |
| US | 09/715,529         | METHOD AND APPARATUS FOR SYNCHRONIZING NETWORK POLICIES                                     |
| US | 09/723,210         | PROVIDING A VALID INITIAL FRAME   |
| US | JCS                | APPARATUS FOR SHIELDING TRANSMISSION LINE EFFECTS ON A PRINTED CIRCUIT BOARD                |
| US | 09/732,115         | CLOCK NETWORK   |
| US | DUP1               | METHOD AND APPARATUS FOR PREDICTING BRANCHES USING A META PREDICTOR                         |
| US | 2002-0087852       | METHOD AND APPARATUS FOR PREDICTING BRANCHES USING A META PREDICTOR                         |
| US | 09/749,405         | CLOCK NETWORK   |
| US | 2002-0087833       | A METHOD AND APPARATUS FOR DISTRIBUTED DISPERSAL LOGIC FOR PIPELINED PROCESSORS             |
| US | 09/749,725         | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE   |
| US | 09/817,711         | TIMING MULTIPLE EVENTS WITH A SINGLE TIMER  |
| US | 09/835,034         | LOCATING A POSITION ON A DISPLAY SCREEN   |
| US | US-2002-0154098-A1 | LARGE FORMAT EMISSIVE DISPLAY   |
| US | US-2002-0163301-A1 | PARALLEL CACHELETS  |
| US | 2002-0188806       | CASCADED DOMINO FOUR-TO-TWO REDUCER CIRCUIT AND METHOD                                      |
| US | 2003-0014459       | SYSTEM FOR SHARING CHANNELS BY INTERLEAVING FLITS   |
| US | 09/894,136         | METHOD OF SCHEDULING MODULES ON A CAROUSEL  |
| US | 09/895,113         |   |

## Schedule A

|    |                    |            |  |
|----|--------------------|------------|--|
| US | US-2003-0011108-A1 | 09/904,269 | ASSEMBLING DISPLAY MODULES   |
| US |                    | 09/951,904 | SYSTEM AND METHOD FOR SPLIT AUTOMATIC GAIN CONTROL   |
| US | 2003-0126179       | 09/956,903 | SYMMETRIC CASCADED DOMINO CARRY GENERATE CIRCUIT   |
| US |                    | 09/964,502 | NETWORK COMMUNICATION CASING   |
| US |                    | 09/982,242 | METHOD AND APPARATUS TO EMULATE EXTERNAL IO INTERCONNECTION  |
| US | US-2003-0077076-A1 | 09/999,784 | PORTABLE DOWNLOAD UNIT INCLUDING A MEMORY CHIP-TO-CD BURNER  |
| US |                    | 10/027,053 | BIT ERROR RATE TEST SYSTEM FOR MULTI-SOURCE AGREEMENT COMPLIANT TRANSCEIVERS                         |
| US |                    | 10/041,543 | METHOD AND APPARATUS FOR HEADER UPDATING   |
| US |                    | 10/061,907 | DIGITAL CAMERA WITH ISO PICKUP SENSITIVITY ADJUSTMENT  |
| US | US-2003-0142058-A1 | 10/062,268 | LCD CONTROLLER ARCHITECTURE FOR HANDLING FLUCTUATING BANDWIDTH CONDITIONS                            |
| US |                    | 10/062,863 | A PROTOCOL DRIVER APPLICATION PROGRAMMING INTERFACE FOR OPERATING SYSTEMS                            |
| US |                    | 10/112,194 | TELEPHONE CONFERENCE BRIDGE PROVIDED VIA A PLURALITY OF COMPUTER TELEPHONY RESOURCE ALGORITHMS       |
| US | AMM                | 10/154,948 | SEARCH RECEIVER USING ADAPTIVE DETECTION THRESHOLDS  |
| US | US-2003-0224535-A1 | 10/160,641 | FORMING FERROELECTRIC POLYMER MEMORIES   |
| US |                    | 10/179,790 | CONTROLLING SNOOP ACTIVITIES USING TASK TABLE IN MULTIPROCESSOR SYSTEM                               |
| US | US-2004-0015763-A1 | 10/202,624 | TEST INTERFACE, SYSTEM, AND METHOD FOR TESTING COMMUNICATIONS DEVICES WITH NON-DETERMINISTIC LATENCY |
| US | US-2004-0031031-A1 | 10/215,549 | EXECUTING APPLICATIONS FROM A SEMICONDUCTOR NONVOLATILE MEMORY                                       |
| US |                    | 10/228,617 | PROCESS AND APPARATUS FOR PACKAGING A TAPE SUBSTRATE   |
| US | US-2004-0047408-A1 | 10/238,669 | DATA LINK ANALYZER   |
| US |                    | 10/253,229 | LOW LATENCY MEMORY ACCESS METHOD USING UNIFIED QUEUE MECHANISM                                       |
| US | US-2004-0079388-A1 | 10/278,535 | REMOVING FLUORINE-BASED PLASMA ETCH RESIDUES   |
| US | US-2004-0080356-A1 | 10/280,926 | COMPACT INPUT/OUTPUT SIGNAL DRIVER FOR ELECTROSTATIC DISCHARGE PROTECTION                            |
| US |                    | 10/286,021 | PHOTOACTIVE ADHESION PROMOTER  |

## Schedule A

|    |                    |            |  |
|----|--------------------|------------|--|
| US | JNG                | 10/299,135 | PROTECTIVE FILM FOR THE FABRICATION OF DIRECT BUILD-UP LAYERS ON AN ENCAPSULATED DIE PACKAGE     |
| US |                    | 10/304,303 | USE OF CHROMELESS PHASE SHIFT MASKS TO PATTERN CONTACTS  |
| US | US-2004-0102013-A1 | 10/306,320 | CODOPING OF SOURCE DRAINS USING CARBON OR FLUORINE ION IMPLANTS TO IMPROVE POLYSILICON DEPLETION |
| US |                    | 10/309,472 | PARAMETRIC REPRESENTATION METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN           |
| US | WWN                | 10/322,902 | SUBSTRATE-IMPRINTING APPARATUS, METHODS OF MANUFACTURE, AND PRODUCTS FORMED THEREFROM            |
| US |                    | 10/324,692 | PORTABLE DIGITAL PHOTOGRAPH VIEWING APPARATUSES  |
| US |                    | 10/331,122 | ON-CHIP JITTER TESTING   |
| US |                    | 10/335,012 | DIGITAL PHASE DETECTION WITH JITTER FILTER   |
| US | US-2004-0132503-A1 | 10/336,629 | THERMAL MANAGEMENT FOR TELECOMMUNICATION DEVICES   |
| US | 2003-0135715       | 10/351,444 | ENHANCED VIRTUAL RENAMING SCHEME AND DEADLOCK PREVENTION THEREFOR                                |
| US | 2005-0262332       | 10/404,384 | METHOD AND SYSTEM FOR BRANCH TARGET PREDICTION USING PATH INFORMATION                            |
| US |                    | 10/425,987 | LOW POWER ADDER  |
| US |                    | 10/426,044 | LOW POWER ADDER CIRCUIT UTILIZING BOTH STATIC AND DYNAMIC LOGIC                                  |
| US |                    | 10/427,168 | INTERCONNECTION DESIGNS AND MATERIALS HAVING IMPROVED STRENGTH AND FATIGUE LIFE                  |
| US |                    | 10/427,377 | SMART CONTROL POINTS   |
| US | US-2004-0220877 A1 | 10/428,286 | MEDIA CENTER STORAGE DEVICE PROXY  |
| US | US-2004-0243896-A1 | 10/436,775 | TEST SCAN CELLS  |
| US |                    | 10/438,146 | REDUCED AREA INTERSECTION BETWEEN ELECTRODE AND PROGRAMMING ELEMENT                              |
| US | US-2004-0236562-A1 | 10/446,272 | USING MULTIPLE SIMULATION ENVIRONMENTS   |
| US |                    | 10/453,115 | METHOD AND APPARATUS FOR PREFETCHING BASED UPON TYPE IDENTIFIER TAGS                             |
| US | US-2004-0246955-A1 | 10/454,790 | A METHOD AND PROCESS FOR DETERMINING A QUOTIENT  |
| US | US-2004-0254775-A1 | 10/461,217 | METHOD AND APPARATUS TO CHARACTERIZE AN ELECTRONIC DEVICE  |

## Schedule A

|    |                    |            |  |
|----|--------------------|------------|--|
| US | US-2004-0258389-A1 | 10/463,651 | PASS THROUGH MODE FOR A PERSONAL VIDEO RECORDER  |
| US |                    | 10/465,666 | SYSTEM AND PROCESS TO CONTROL ELECTROPLATING A METAL ONTO A SUBSTRATE                        |
| US | 2004-0098565       | 10/601,172 | INSTRUCTION QUEUE FOR AN INSTRUCTION PIPELINE  |
| US | US-2004-0264413-A1 | 10/603,859 | DEVICE, SYSTEM AND METHOD FOR CHANNEL SCANNING   |
| US | US-2004-0268032-A1 | 10/609,714 | A MODULAR CONTENT ADDRESSABLE MEMORY   |
| US | US-2004-0266184-A1 | 10/609,963 | POST-DEPOSITION MODIFICATION OF INTERLAYER DIELECTRICS                                       |
| US | US-2005-0010683-A1 | 10/610,104 | APPARATUS, SYSTEM AND METHOD FOR PERFORMING TABLE MAINTENANCE                                |
| US |                    | 10/610,713 | A METHOD AND APPARATUS FOR INTEGRATED CIRCUIT SELF-DESCRIPTION                               |
| US | 2004 0267977       | 10/610,855 | TOPOLOGY BASED REPLACEMENT ROUTING OF SIGNAL PATHS   |
| US | US-2005-0050303-A1 | 10/611,380 | HIERARCHICAL REORDER BUFFERS FOR CONTROLLING SPECULATIVE EXECUTION IN A MULTI-CLUSTER SYSTEM |
| US | US-2004-0267914-A1 | 10/611,617 | METHOD, APPARATUS AND SYSTEM FOR CREATING EFFICIENT UNIVERSAL PLUG AND PLAY CONTROL POINTS   |
| US | US-2004-0267480-A1 | 10/612,293 | SELECTIVE CONTROL OF TEST-ACCESS PORTS IN INTEGRATED CIRCUITS                                |
| US |                    | 10/628,597 | FASTENER INSTALLATION TOOL AND METHODS OF USE  |
| US | US-2005-0102465-A1 | 10/629,093 | MANAGING A CACHE WITH PINNED DATA  |
| US |                    | 10/641,614 | PROCESSING INSTRUCTIONS  |
| US |                    | 10/654,252 | METHOD AND APPARATUS FOR MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT                    |
| US | US-2005-0060376-A1 | 10/660,876 | SECURE COMPUTER TELEPHONY INTEGRATION ACCESS   |
| US | US-2004-0162105-A1 | 10/663,165 | ENHANCED GENERAL PACKET RADIO SERVICE (GPRS) MOBILITY MANAGEMENT                             |
| US | US-2005-0086508-A1 | 10/666,077 | REGISTER ALIAS TABLE CACHE   |
| US | US-2005-066328-A1  | 10/669,309 | METHOD AND APPARATUS TO PERFORM TASK SCHEDULING  |
| US | US-2005-0071795-A1 | 10/676,893 | A METHOD AND APPARATUS FOR INTEGRATED CIRCUIT DATAPATH LAYOUT USING A VECTOR EDITOR          |
| US | US-2005-0071846-A1 | 10/677,081 | PASSING PARAMETERS BY IMPLICIT REFERENCE   |

## Schedule A

|    |                    |            |   |
|----|--------------------|------------|---|
| US | US-2005-0077180-A1 | 10/682,275 | MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A HIGH-ACID ELECTROLYTE SOLUTION                                   |
| US | US-2005-0077181-A1 | 10/682,276 | MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A LOW-ACID ELECTROLYTE SOLUTION                                    |
| US | US-2005-0122389-A1 | 10/723,413 | MULTI-CONFERENCE STREAM MIXING  |
| US |                    | 10/743,397 | SHIFTING AN INPUT SIGNAL FROM A HIGH-SPEED DOMAIN TO A LOWER-SPEED DOMAIN   |
| US | 2005-0147036       | 10/749,271 | METHOD AND APPARATUS FOR ENABLING AN ADAPTIVE REPLAY LOOP IN A PROCESSOR  |
| US | CE5                | 10/758,055 | SOCKET WARPAGE REDUCTION APPARATUS AND METHOD   |
| US |                    | 10/761,394 | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK                             |
| US |                    | 10/761,395 | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK                             |
| US | US-2004-0159945-A1 | 10/774,869 | UNDERFILL PROCESS FOR FLIP-CHIP DEVICE  |
| US |                    | 10/774,952 | DIE-IN-HEAT SPREADER MICROELECTRONIC PACKAGE  |
| US | US-2005-0188156-A1 | 10/783,621 | METHOD AND APPARATUS FOR DEDICATING CACHE ENTRIES TO CERTAIN STREAMS FOR PERFORMANCE OPTIMIZATION                 |
| US | US-2005-0204099-A1 | 10/799,555 | METHOD AND APPARATUS TO WRITE BACK DATA   |
| US | US-2005-0202252-A1 | 10/799,928 | USE OF ALTERNATIVE POLYMER MATERIALS FOR "SOFT" POLYMER PELLICLES   |
| US | US-2005-0251795-A1 | 10/805,106 | METHOD, SYSTEM, AND PROGRAM FOR OPTIMIZING CODE   |
| US | US-2005-0210225-A1 | 10/805,947 | HYBRID BRANCH PREDICTION  |
| US |                    | 10/814,398 | METHOD AND APPARATUS FOR ANALOG COMPENSATION OF DRIVER OUTPUT SIGNAL SLEW RATE AGAINST DEVICE IMPEDANCE VARIATION |
| US | US-2005-0240820-A1 | 10/815,904 | A METHOD AND APPARATUS FOR MULTIPROCESSOR DEBUG SUPPORT   |
| US |                    | 10/817,263 | PRESSBYOPIC BRANCH TARGET PREFETCH METHOD AND APPARATUS   |
| US | US-2005-0239275-A1 | 10/832,178 | COMPLIANT MULTI-COMPOSITION INTERCONNECTS   |
| US |                    | 10/842,752 | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK                             |

## Schedule A

|    |                    |            |   |
|----|--------------------|------------|---|
| US | US-2005-0270983-A1 | 10/863,661 | A METHOD AND APPARATUS TO MANAGE EXCEPTIONS IN NETWORK PROCESSORS   |
| US |                    | 10/868,703 | APPARATUS AND METHOD OF DYNAMIC USAGE PROFILE ACQUISITION AND UTILIZATION IN A WIRELESS MOBILE DEVICE   |
| US | US-2004-0231886-A1 | 10/870,506 | PCB DESIGN AND METHOD FOR PROVIDING VENTED BLIND VIAS   |
| US | US-2006-0002172-A1 | 10/880,692 | PROVIDING CURRENT FOR PHASE CHANGE MEMORIES   |
| US | US-2006-0002425-A1 | 10/883,107 | DETERMINING AVAILABLE BANDWIDTH IN A NETWORK  |
| US | US-2006-0000806-A1 | 10/883,404 | SUBSTRATE CARRIER FOR SURFACE PLANARIZATION   |
| US | US-2006-0007855-A1 | 10/886,821 | PRIORITIZATION OF NETWORK TRAFFIC   |
| US |                    | 10/893,015 | METHOD, SYSTEM, AND APPARATUS FOR TRACKING DEFECTIVE CACHE LINES  |
| US | US-2006-0033217-A1 | 10/915,293 | FLIP-CHIPS ON FLEX SUBSTRATES, FLIP-CHIP AND WIRE-BONDED CHIP STACKS, AND METHODS OF ASSEMBLING SAME  |
| US | US-2005-0014096-A1 | 10/917,679 | PHOTOACTIVE ADHESION PROMOTER   |
| US | 20060053328        | 10/935,902 | TRAINING PATTERN BASED DE-SKEW MECHANISM AND FRAME ALIGNMENT  |
| US | US-2005-0054143-A1 | 10/939,226 | USING BENZOCYCLOBUTENE BASED POLYMERS AS UNDERFILL MATERIALS  |
| US | US-2006-0054996-A1 | 10/939,237 | BREAK DOWN FORMED MEMORY DEVICE WITH SELF ALIGNED GLUE LAYER  |
| US | US 2006-0062174 A1 | 10/945,797 | DEVICE AND METHODS FOR INCREASING WIRELESS CONNECTION SPEEDS  |
| US |                    | 10/954,947 | OVERHEAD PROCESSING AND GENERATION TECHNIQUES   |
| US | US-2006-0072634-A1 | 10/955,394 | CALIBRATION METHODS FOR TUNABLE LASERS  |
| US | US-2006-0075296-A1 | 10/956,994 | METHOD, APPARATUS AND SYSTEM FOR DATA INTEGRITY OF STATE RETENTIVE ELEMENTS UNDER LOW POWER MODES   |
| US | 2005-0060154       | 10/962,685 | VOICE DRIVEN WIRELESS POINTER   |
| US | 2005-0060502-A1    | 10/970,015 | MECHANISM TO GUARANTEE FORWARD PROGRESS FOR INCOMING COHERENT INPUT/OUTPUT (I/O) TRANSACTIONS FOR CACHING I/O AGENT ON ADDRESS CONFLICT WITH PROCESSOR TRANSACTIONS |
| US | US-2005-0112793-A1 | 10/973,161 | METHODS OF FORMING A HIGH CONDUCTIVITY DIAMOND FILM AND STRUCTURES FORMED THEREBY   |

## Schedule A

|    |                    |            |   |
|----|--------------------|------------|---|
| US | US-2006-0117231-A1 | 10/976,572 | ADAPTIVE COMMUNICATION INTERFACE  |
| US | US-2006-0091492-A1 | 10/977,186 | DEPOSITING TITANIUM SILICON NITRIDE FILMS FOR FORMING PHASE CHANGE MEMORIES                     |
| US | US-2006-0106954-A1 | 10/988,417 | MEMORY REUSE FOR MULTIPLE ENDPOINTS IN USB DEVICE   |
| US | US-2006-0105764-A1 | 10/989,539 | ADAPTIVE WIRELESS NETWORKS AND METHODS FOR COMMUNICATING MULTIMEDIA IN A PROACTIVE ENTERPRISE   |
| US | US-2006-0107077-A1 | 10/989,948 | PROGRAMMABLE POWER TRANSITION COUNTER   |
| US | US-2006-0106962-A1 | 10/992,250 | METHOD FOR IMPLEMENTATION OF USB ON-THE-GO  |
| US | US-2005-0100266-A1 | 10/994,021 | METHOD AND APPARATUS FOR WAFER LEVEL TESTING OF INTEGRATED OPTICAL WAVEGUIDE CIRCUITS           |
| US | US-2006-0110883-A1 | 10/997,609 | METHOD FOR FORMING A MEMORY DEVICE  |
| US | US-2005-0099762-A1 | 11/003,956 | CAPACITOR   |
| US | US-2005-0083349-A1 | 11/005,694 | PHYSICALLY INTERACTING WITH A PROCESSOR-BASED DISPLAY   |
| US | US-2006-0129701-A1 | 11/012,316 | COMMUNICATING AN ADDRESS TO A MEMORY DEVICE   |
| US |                    | 11/015,965 | METHOD AND APPARATUS FOR CHANNEL HEALTH ENCODING  |
| US |                    | 11/015,967 | METHOD AND APPARATUS FOR PROVIDING A CONNECTION MATRIX  |
| US | US-2006-0136679-A1 | 11/018,958 | PROTECTED PROCESSING APPARATUS, SYSTEMS, AND METHODS  |
| US | US-2006-0143411-A1 | 11/021,323 | TECHNIQUES TO MANAGE PARTITION PHYSICAL MEMORY  |
| US | US-2006-0143554-A1 | 11/022,212 | SCALABLE TRACEBACK TECHNIQUE FOR CHANNEL DECODER  |
| US | US-2006-019931-A1  | 11/024,164 | RUNAHEAD EXECUTION IN A CENTRAL PROCESSING UNIT   |
| US | US-2006-0140226-A1 | 11/024,928 | TECHNIQUES FOR PROCESSING TRAFFIC TRANSMITTED OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS |
| US | US-2006-0161688-A1 | 11/024,956 | SYSTEMS AND METHODS FOR EFFICIENT OPERATIONS OF COMPONENTS IN A WIRELESS COMMUNICATIONS DEVICE  |
| US | US-2006-0142991-A1 | 11/025,116 | REMOTE USB NETWORK DEVICE CONTROL   |
| US | US-2006-0149917-A1 | 11/027,784 | SECURE MEMORY CONTROLLER  |
| US | US-2006-0143687-A1 | 11/027,913 | SECURE CONTROLLER FOR BLOCK ORIENTED STORAGE  |

## Schedule A

|    |                    |            |   |
|----|--------------------|------------|---|
| US | US-2006-0206807-A1 | 11/032,828 | PROVIDING OPTICAL ELEMENTS OVER EMISSIVE DISPLAYS   |
| US | US-2006-0190517-A1 | 11/050,369 | TECHNIQUES FOR TRANSPOSITION OF A MATRIX ARRANGED IN A MEMORY AS MULTIPLE ITEMS PER WORD                |
| US | US-2005-0145885-A1 | 11/053,786 | NON STROBE SENSING CIRCUIT  |
| US | US-2006-0145885-A1 | 11/063,963 | I/O ARCHITECTURE FOR INTEGRATED CIRCUIT PACKAGE   |
| US | US-2006-0200684-A1 | 11/069,924 | Power Mode Change Voltage Control In A Computerized System  |
| US | US-2006-0215567-A1 | 11/090,571 | METHOD AND APPARATUS FOR MONITORING PATH STATISTICS   |
| US | US-2006-0224857-A1 | 11/092,432 | LOCKING ENTRIES INTO TRANSLATION LOOKASIDE BUFFERS  |
| US | US-2006-0221944-A1 | 11/093,907 | DIFFERENTIAL DELAY COMPENSATION   |
| US | US-2006-0228858-A1 | 11/096,330 | FLASH MEMORY CELL HAVING REDUCED FLOATING GATE TO FLOATING GATE COUPLING                                |
| US | US-2006-0223068-A1 | 11/096,414 | Sorting Of Carbon Nanotubes Through Selective DNA Delamination Of DNA/Carbon Nanotube Hybrid Structures |
| US | US-2006-0224337-A1 | 11/096,950 | PROGRAMMABLE CURRENT LOAD SYSTEMS AND METHODS   |
| US | US-2005-0189544-A1 | 11/100,885 | METHODS OF FORMING A HIGH CONDUCTIVITY DIAMOND FILM AND STRUCTURES FORMED THEREBY                       |
| US | US-2006-0230235-A1 | 11/101,785 | LOW LOCALITY-OF-REFERENCE SUPPORT IN A MULTI-LEVEL CACHE HIERARCHY                                      |
| US | US-2006-0226410-A1 | 11/103,188 | HEATING PHASE CHANGE MATERIAL   |
| US | US-2006-0239800-A1 | 11/114,261 | PULSED DC AND RF PHYSICAL VAPOR DEPOSITION CLUSTER TOOL   |
| US | US-2005-0192789-A1 | 11/124,315 | METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN  |
| US |                    | 11/137,314 | DEVICE AND METHOD FOR MAXIMIZING PERFORMANCE ON A MEMORY INTERFACE WITH A VARIABLE NUMBER OF CHANNELS   |
| US | US-2005-0248579-A1 | 11/137,700 | METHOD AND APPARATUS FOR REORDERING MEMORY REQUESTS FOR PAGE COHERENCY                                  |
| US | US-2005-0225860-A1 | 11/144,583 | SEGMENTED COMPLEX DIFFRACTION GRATINGS  |
| US | US-2005-0231804-A1 | 11/145,251 | SEGMENTED COMPLEX DIFFRACTION GRATINGS  |
| US | US-2005-0225861-A1 | 11/145,291 | SEGMENTED COMPLEX DIFFRACTION GRATINGS  |
| US | US-2006-0288098-A1 | 11/156,838 | PERIPHERAL INTERFACE ALERT MESSAGE FOR DOWNSTREAM DEVICE  |

## Schedule A

|    |                    |            |  |
|----|--------------------|------------|--|
| US | US-2006-0293039-A1 | 11/166,143 | METHOD AND SYSTEM FOR TRANSFERRING MESSAGES TO A MOBILE STATION ACCORDING TO SPECIFIC PARAMETERS |
| US |                    | 11/167,495 | METHOD OF FORMING A MULTI-DIE SEMICONDUCTOR PACKAGE  |
| US | US-2006-0289648-A1 | 11/168,780 | REDUCING OXIDATION OF PHASE CHANGE MEMORY ELECTRODES   |
| US | US-2007-0002059-A1 | 11/169,537 | PIXEL DATA COMPRESSION FROM CONTROLLER TO DISPLAY  |
| US | US-2007-0002017-A1 | 11/170,110 | DEVICE, SYSTEM AND METHOD FOR WIRELESS COMMUNICATION AND CURSOR POINTING                         |
| US | US-2007-0006007-A1 | 11/173,218 | FREQUENCY-DEPENDENT VOLTAGE CONTROL IN DIGITAL LOGIC   |
| US | US-2007-0001762-A1 | 11/173,760 | CURRENT SENSING METHOD FOR INTEGRATED SWITCHING TRANSISTORS                                      |
| US | US-2007-0018148-A1 | 11/185,488 | PHASE CHANGE MEMORY WITH U-SHAPED CHALCOGENIDE CELL  |
| US | US-2005-0273603-A1 | 11/188,420 | A MECHANISM TO IMPROVE AUTHENTICATION FOR REMOTE MANAGEMENT OF A COMPUTER SYSTEM                 |
| US | US-2007-0028031-A1 | 11/189,448 | Universal Nonvolatile Memory Boot Mode   |
| US | 2005-0268083       | 11/192,025 | METHOD AND SYSTEM FOR USING INTERNAL FIFO RAM TO IMPROVE SYSTEM BOOT TIMES                       |
| US | US-2007-0023857-A1 | 11/193,952 | FABRICATING SUB-LITHOGRAPHIC CONTACTS  |
| US | US-2005-0270088-A1 | 11/197,806 | ACTIVE PHASE CANCELLATION FOR INDUCTOR / CAPACITOR NETWORKS                                      |
| US | US-2005-0274619-A1 | 11/207,305 | MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A LOW-ACID ELECTROLYTE SOLUTION                   |
| US | US-2007-0055816-A1 | 11/218,371 | POWER LOSS RECOVERY IN NON-VOLATILE MEMORY   |
| US | US-2007-0058584-A1 | 11/225,303 | TECHNIQUES TO TRANSMIT AND DUPLEX WITH CHANNEL KNOWLEDGE AT A BASE STATION                       |
| US | US-2007-0079054-A1 | 11/240,004 | METHOD AND APPARATUS FOR OPTIMIZING FLASH DEVICE ERASE DISTRIBUTION                              |
| US | US-2007-0076708-A1 | 11/241,339 | ERROR PROTECTION TECHNIQUES FOR FRAMES ON A WIRELESS NETWORK                                     |
| US | US-2007-0082469-A1 | 11/248,488 | FORMING HEATERS FOR PHASE CHANGE MEMORIES  |
| US | US-2006-0036985-A1 | 11/251,664 | COMPACTING CIRCUIT RESPONSES   |
| US | US-2006-0033522-A1 | 11/253,377 | AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT                      |
| US | US-2007-0106871-A1 | 11/271,778 | PROCESSOR MULTI-PARTITION SECURITY ARCHITECTURE  |

## Schedule A

|    |                    |            |   |
|----|--------------------|------------|---|
| US | US-2007-0111492-A1 | 11/274,622 | A STRUCTURED, ELECTRICALLY-FORMED FLOATING GATE FOR FLASH MEMORIES                        |
| US | US-2006-0078198-A1 | 11/283,375 | APPARATUS AND METHOD FOR IMAGER DEVICE COLOR CALIBRATION USING SPECTRAL LIGHT SOURCE      |
| US | US-2006-0074619-A1 | 11/286,565 | RECOGNIZING SIGNALS IN DESIGN SIMULATION  |
| US | US-2007-0120588-A1 | 11/291,204 | LOW-JITTER CLOCK DISTRIBUTION   |
| US | US-2006-0098524-A1 | 11/303,417 | FORMING PLANARIZED SEMICONDUCTOR STRUCTURES   |
| US | US-2006-0106993-A1 | 11/321,632 | MECHANISM FOR HANDLING EXPLICIT WRITEBACK IN A CACHE COHERENT MULTI-NODE ARCHITECTURE     |
| US | US-2006-0149918-A1 | 11/323,473 | MEMORY WITH MODIFIABLE ADDRESS MAP  |
| US |                    | 11/325,961 | LARGE FORMAT EMISSIVE DISPLAY   |
| US |                    | 11/334,729 | REPAIRABLE TILED DISPLAYS   |
| US | US-2006-0121936-A1 | 11/338,932 | PORTABLE COMPUTING DEVICE ADAPTED TO UPDATE DISPLAY INFORMATION WHILE IN A LOW POWER MODE |
| US | US-2006-0168442-A1 | 11/351,934 | CONSOLE REDIRECTION AMONG LINKED COMPUTERS  |
| US | US-2007-0192830-A1 | 11/354,676 | SECURITY MODULE HAVING ACCESS LIMITED BASED UPON SECURITY LEVEL OF CODE SEEKING ACCESS    |
| US | US-2007-0204121-A1 | 11/361,228 | MOVEABLE LOCKED LINES IN A MULTI-LEVEL CACHE  |
| US | US-2007-0227878-A1 | 11/392,135 | FORMING OVONIC THRESHOLD SWITCHES WITH REDUCED DEPOSITION CHAMBER GAS PRESSURE            |
| US | US-2007-0230638-A1 | 11/394,533 | METHOD AND APPARATUS TO EFFICIENTLY CONFIGURE MULTI-ANTENNA EQUALIZERS                    |
| US | US-2007-0230804-A1 | 11/394,929 | ENCODING TECHNIQUES EMPLOYING NOISE-BASED ADAPTATION                                      |
| US | US-2007-0230538-A1 | 11/394,994 | WIRELESS COMMUNICATIONS WITH AUXILIARY ANTENNAS   |
| US | US-2007-0234189-A1 | 11/396,329 | SYSTEM AND METHOD FOR REDUCING FALSE ALARM IN THE PRESENCE OF RANDOM SIGNALS              |
| US | US-2006-0175068-A1 | 11/396,769 | FASTENER INSTALLATION TOOLS, SYSTEMS, AND METHODS OF USE                                  |

## Schedule A

|    |                    |            |  |
|----|--------------------|------------|--|
| US | US-2007-0254446-A1 | 11/411,311 | SELF-ALIGNED BIPOLAR JUNCTION TRANSISTORS  |
| US | US-2006-0186924-A1 | 11/411,647 | VOLTAGE-LEVEL CONVERTER  |
| US | US-2006-0225031-A1 | 11/437,362 | METHOD AND APPARATUS FOR ACCESSING THREAD-PRIVATE GLOBAL STORAGE OBJECTS                   |
| US | US-2006-0285541-A1 | 11/462,264 | METHOD AND APPARATUS FOR GENERATING TRAFFIC IN AN ELECTRONIC BRIDGE VIA A LOCAL CONTROLLER |
| US | US-2006-0294321-A1 | 11/514,802 | COMMUNICATION REGISTERS FOR PROCESSING ELEMENTS  |
| US |                    | 11/527,896 | COMPOSITE ORGANIC INORGANIC NANOCLOUDS AS CARRIERS AND IDENTIFIERS OF TESTER MOLECULES     |
| US |                    | 11/529,022 | AUTHENTICATING DATA RETURNED FROM NON-VOLATILE MEMORY COMMANDS                             |
| US |                    | 11/529,987 | ARCHITECTURE FOR VIRTUAL SECURITY MODULE   |
| US | US-2007-0026566-A1 | 11/542,712 | PHASE CHANGE MEMORY WITH DAMASCENE MEMORY ELEMENT  |
| US |                    | 11/545,234 | ASYMMETRIC CHALCOGENIDE DEVICE   |
| US | US-2007-0060181-A1 | 11/553,905 | ANTECEDENT STRENGTHENING TO PERFORM GENERALIZED TRAJECTORY EVALUATION                      |
| US | US-2007-0093103-A1 | 11/561,684 | VARIABLE LATCH   |
| US | US-2007-0037350-A1 | 11/582,881 | FLASH MEMORY CELL HAVING REDUCED FLOATING GATE TO FLOATING GATE COUPLING                   |
| US |                    | 11/595,055 | READ WINDOW IN CHALCOGENIDE SEMICONDUCTOR MEMORIES   |
| US |                    | 11/606,800 | A PHASE CHANGE MEMORY CELL HAVING A TAPERED MICROTRENCH                                    |
| US | US-2007-0105312-A1 | 11/615,862 | MEMORY CELL WITH NANOCRYSTAL AS DISCRETE STORAGE ELEMENT                                   |
| US | US-2007-0102817-A1 | 11/616,164 | METHOD AND APPARATUS FOR REDUCING ELECTRICAL INTERCONNECTION FATIGUE                       |
| US |                    | 11/823,518 | HIGH DENSITY NOR FLASH ARRAY ARCHITECTURE  |
| US |                    | 60/090,939 | SINGLE-CHIP NETWORK PROCESSOR & INTERFACE SYSTEM   |
| US |                    | 60/140,705 | DIGITAL IMPAIRMENT LEARNING SEQUENCE   |
| US |                    | 60/282,961 | COMPOSITE DIFFRACTION GRATINGS FOR SIGNAL PROCESSING AND OPTICAL CONTROL APPLICATIONS      |
| US |                    | 60/287,707 | PARALLEL CACHELETS   |
| US |                    | 60/315,797 | UNIVERSAL TELEPHONE INTERFACE POLARITY DETECTOR  |

Schedule A

|    |            |   |
|----|------------|---|
| US | 60/317,802 | TIME VARYING FILTER WITH ZERO AND/OR POLE MIGRATION |
| US | 60/447,665 | ENHANCED GPRS MOBILITY MANAGEMENT                   |

**PATENT ASSIGNMENT AGREEMENT**

**WHEREAS**, Numonyx B.V., a company organized under the laws of Netherlands, having a place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte. De l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignor") has rights in and to the following:

1. U.S. Patents and patent applications listed on Exhibit A, and
2. Foreign patents and patent applications listed on Exhibit B.

(said patents and patent applications listed in Exhibits A and B being hereinafter referred to as "SAID PATENT RIGHTS"); and

**WHEREAS**, Micron Technology, Inc. is a corporation having a principal place of business at 8000 S. Federal Way, Boise, Idaho 83707, United States (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

**NOW, THEREFORE**, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuation-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

**IN WITNESS WHEREOF**, Assignor has caused this Assignment to be executed by its duly authorized officer effective as of the 22 day of November, 2011.

**Numonyx B.V.**

By: Thomas C. Carson

Printed Name: Thomas C. Carson

Title: Director

**Micron Technology, Inc.**

By: Russell Scifer

Printed Name: Russell Scifer

Title: Chief Patent Counsel

Intel originated

**PATENT**

**REEL: 031305 FRAME: 0795**

## Exhibit A - US Applications and Issued Patents

| Pat. No.        | Matter Type   | Matter Status | Title  | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|------------|---------|
| NMXI-0001.00/US | Utility - ORG | Expired       | APPARATUS WITH A SINGLE MEMORY AND A PLURALITY OF QUEUE COUNTERS FOR QUEUING REQUESTS AND REPLIES ON A PIPELINED PACKET BUS  | 10/14/1988 | 07/257,857 | 9/17/1991  | 5,050,066  | US      |
| NMXI-0002.00/US | Utility - ORG | Expired       | VOLTAGE MARGINING CIRCUIT FOR FLASH EPROM  | 1/12/1988  | 07/144,567 | 10/17/1989 | 4,875,188  | US      |
| NMXI-0003.00/US | Utility - ORG | Expired       | LEAKAGE VERIFICATION FOR FLASH EPROM   | 2/17/1988  | 07/157,364 | 6/20/1989  | 4,841,482  | US      |
| NMXI-0004.00/US | Utility - ORG | Expired       | A CIRCUIT FOR PROVIDING A LOAD FOR THE CHARGING OF AN EPROM CELL   | 1/12/1988  | 07/144,569 | 8/15/1989  | 4,858,186  | US      |
| NMXI-0005.00/US | Utility - CON | Expired       | PROCESSOR CONTROLLED COMMAND PORT ARCHITECTURE FOR FLASH MEMORY  | 10/19/1990 | 07/601,012 | 6/22/1993  | 5,222,046  | US      |
| NMXI-0006.00/US | Utility - ORG | Abandoned     | PROCESS FOR SIMULTANEOUSLY FABRICATING EPROM CELL AND FLASH EPROM CELL   | 11/21/1988 | 07/274,420 | 9/18/1990  | 4,957,877  | US      |
| NMXI-0007.00/US | Utility - CON | Issued        | MICROCONTROLLER PERIPHERAL EXPANSION BUS FOR ACCESS TO INTERNAL SPEC-IAL FUNCTION REGISTERS  | 12/1/1992  | 07/984,280 | 5/31/1994  | 5,317,750  | US      |
| NMXI-0008.00/US | Utility - ORG | Expired       | MICROPROCESSOR BUS INTERFACE UNIT WHICH CHANGES SCHEDULED DATA TRANSFER INDICATIONS UPON SENSING CHANGE IN ENABLE SIGNALS BEFORE RECEIVING READY SIGNAL  | 8/1/1988   | 07/227,078 | 12/17/1991 | 5,073,969  | US      |
| NMXI-0009.00/US | Utility - CON | Issued        | METHOD AND APPARATUS FOR CONDUCTING BUS TRANSACTIONS BETWEEN TWO CLOCK INDEPENDENT BUS AGENTS OF A COMPUTER SYSTEM USING A TRANSACTION BY TRANSACTION DETERMINISTIC REQUEST/RESPONSE PROTOCOL                          | 2/27/1995  | 08/394,560 | 10/3/1995  | 5,455,957  | US      |
| NMXI-0010.00/US | Utility - CON | Expired       | SELF-BIASED, HIGH-GAIN DIFFERENTIAL AMPLIFIER WITH FEEDBACK CIRCUIT INCLUDING A LATCHING UNIT FOR HOLDING INSTRUCTIONS DURING EXECUTION FOR RE-EXECUTION FOLLOWING EXCEPTION HANDLING                                  | 1/2/1990   | 07/462,879 | 6/26/1990  | 4,937,476  | US      |
| NMXI-0011.00/US | Utility - DIV | Issued        | PORT EXPANDER ARCHITECTURE FOR MAPPING A FIRST SET OF ADDRESSES TO EXTERNAL MEMORY AND MAPPING A SECOND SET OF ADDRESSES TO AN I/O PORT  | 4/14/1993  | 08/047,696 | 5/30/1995  | 5,421,021  | US      |
| NMXI-0012.00/US | Utility - CON | Expired       | TEST MODE ENABLE SCHEME FOR MEMORY   | 6/12/1992  | 07/898,190 | 9/7/1993   | 5,243,700  | US      |
| NMXI-0013.00/US | Utility - CON | Expired       | CHEMICAL MODIFICATION OF SPIN-ON GLASS FOR IMPROVED PERFORMANCE IN IC FABRICATION  | 5/22/1991  | 07/707,241 | 12/31/1991 | 5,077,738  | US      |
| NMXI-0014.00/US | Utility - ORG | Expired       | METHOD AND APPARATUS FOR AUTOMATICALLY CONFIGURING SYSTEM MEMORY ADDRESS SPACE OF A COMPUTER SYSTEM HAVING A MEMORY SUBSYSTEM WITH INDETERMINISTIC NUMBER OF MEMORY UNITS OF INDETERMINISTIC SIZES DURING SYSTEM RESET | 3/8/1989   | 07/320,763 | 12/5/1989  | 4,885,262  | US      |
| NMXI-0015.00/US | Utility - CON | Abandoned     | LEAKAGE VERIFICATION FOR FLASH EPROM   | 3/29/1995  | 08/412,607 | 4/30/1996  | 5,513,331  | US      |
| NMXI-0016.00/US | Utility - DIV | Expired       | METHOD OF TRANSFERRING BURST DATA IN A MICROPROCESSOR  | 3/14/1989  | 07/323,310 | 8/22/1989  | 4,860,261  | US      |
| NMXI-0017.00/US | Utility - ORG | Expired       | METHOD OF TRANSFERRING BURST DATA IN A MICROPROCESSOR  | 4/5/1989   | 07/333,980 | 7/14/1992  | 5,131,083  | US      |
| NMXI-0017.01/US | Utility - CON | Expired       | NOVEL ARCHITECTURE FOR VIRTUAL GROUND HIGH-DENSITY EPROMS  | 11/24/1992 | 07/981,943 | 10/19/1993 | 5,255,378  | US      |
| NMXI-0018.00/US | Utility - ORG | Expired       | MICROPROCESSOR PROVIDING ENCODED INFORMATION ON BYTE ENABLE LINES INDICATING WHETHER READING CODE OR DATA, LOCATION OF CODE/DATA ON DATA LINES, AND BIT WIDTH OF CODE/DATA   | 8/7/1989   | 07/390,159 | 2/12/1991  | 4,992,980  | US      |
| NMXI-0019.00/US | Utility - CON | Issued        | M & A FOR MINIMIZING DATA TRANSFER TO MAIN MEMORY FROM A WRITE CACHE DURING A CACHE MISS   | 10/8/1993  | 08/133,771 | 1/3/1995   | 5,379,443  | US      |
| NMXI-0020.00/US | Utility - CON | Issued        |  | 4/28/1994  | 08/234,587 | 11/14/1995 | 5,467,460  | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Pat. No.        | Mat. Type     | Mat. State | Title   | Filed      | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|------------|---|------------|------------|------------|------------|---------|
| NMXI-0021.00/US | Utility - ORG | Expired    | APPARATUS AND METHOD FOR ALIGNMENT OF ADJACENT SURFACES   | 8/6/1990   | 07/563,560 | 9/17/1991  | 5,048,195  | US      |
| NMXI-0022.00/US | Utility - ORG | Expired    | SYSTEM FOR EXECUTING DIFFERENT CYCLE INSTRUCTIONS BY SELECTIVELY BYPASSING SCOREBOARD REGISTER AND CANCELING THE EXECUTION OF CONDITIONALLY ISSUED INSTRUCTION IF NEEDED RESOURCES ARE BUSY | 2/28/1990  | 07/486,407 | 2/9/1993   | 5,185,872  | US      |
| NMXI-0023.00/US | Utility - ORG | Expired    | ETCHBACK PROCESS FOR TUNGSTEN UTILIZING A NF3/AR CHEMISTRY  | 4/17/1991  | 07/686,683 | 11/17/1992 | 5,164,330  | US      |
| NMXI-0024.00/US | Utility - ORG | Expired    | SIX-WAY ACCESS PORTED RAM ARRAY CELL  | 2/28/1990  | 07/486,408 | 6/11/1991  | 5,023,844  | US      |
| NMXI-0025.00/US | Utility - ORG | Expired    | COMPUTER MEMORY WITH STATUS CELL  | 9/26/1990  | 07/589,388 | 8/4/1992   | 5,136,544  | US      |
| NMXI-0026.00/US | Utility - ORG | Expired    | MEMORY DEVICE WITH A TEST MODE  | 9/26/1990  | 07/589,391 | 1/5/1993   | 5,177,745  | US      |
| NMXI-0027.00/US | Utility - CON | Issued     | APPARATUS FOR ISSUING INSTRUCTIONS AND REISSUING A PREVIOUS INSTRUCTIONS BY RECIRCULATING USING THE DELAY CIRCUIT   | 11/12/1993 | 08/150,784 | 4/23/2002  | 6,378,061  | US      |
| NMXI-0028.00/US | Utility - ORG | Expired    | METHOD FOR IMPROVING DEVICE SCALABILITY OF BURIED BIT LINE FLASH EPROM DEVICES HAVING SHORT REOXIDATION BEAKS AND SHALLOWER JUNCTIONS   | 11/2/1990  | 07/609,192 | 4/7/1992   | 5,102,814  | US      |
| NMXI-0029.00/US | Utility - CON | Issued     | BRANCH LOOK AHEAD ADDER FOR USE IN AN INSTRUCTION PIPELINE SEQUENCER WITH MULTIPLE INSTRUCTION DECODING   | 10/26/1993 | 08/141,685 | 9/26/1995  | 5,454,089  | US      |
| NMXI-0030.00/US | Utility - ORG | Expired    | METHOD OF MODIFYING A MICROINSTRUCTION WITH OPERANDS SPECIFIED BY AN INSTRUCTION HELD IN AN ALIAS REGISTER  | 12/20/1990 | 07/630,497 | 6/22/1993  | 5,222,244  | US      |
| NMXI-0031.00/US | Utility - CON | Expired    | HIGH G4FLOW-REACTIVE ION ETCH FOR ALUMINUM PATTERNING   | 5/8/1992   | 07/883,067 | 4/13/1993  | 5,202,291  | US      |
| NMXI-0032.00/US | Utility - ORG | Expired    | HIGH BANDWIDTH OUTPUT HIERARCHICAL MEMORY STORE INCLUDING A CACHE, FETCH BUFFER AND ROM   | 12/20/1990 | 07/630,534 | 5/17/1994  | 5,313,605  | US      |
| NMXI-0033.00/US | Utility - ORG | Expired    | HANDLE AND HANDLED IMPLEMENT RADIATION SHIELD FOR EPROM CELLS   | 12/27/1990 | 07/635,868 | 12/29/1992 | 5,175,126  | US      |
| NMXI-0034.00/US | Utility - ORG | Expired    | HIGH VOLTAGE MOS TRANSISTOR FOR FLASH EPROM APPLICATIONS  | 11/13/1990 | 07/612,452 | 9/17/1991  | 5,050,123  | US      |
| NMXI-0035.00/US | Utility - CON | Issued     | HAVING A UNI-SIDED LIGHTLY DOPED DRAIN  | 7/2/1993   | 08/087,140 | 10/3/2000  | 6,127,696  | US      |
| NMXI-0036.00/US | Utility - ORG | Expired    | TEOS INTERMETAL DIELECTRIC PRECLEAN FOR VIA FORMATION   | 6/7/1991   | 07/712,119 | 6/15/1993  | 5,219,791  | US      |
| NMXI-0037.00/US | Utility - ORG | Expired    | ANNEAL TO DECREASE MOISTURE ABSORBANCE OF INTERMETAL DIELECTRICS  | 6/7/1991   | 07/712,116 | 8/18/1992  | 5,139,971  | US      |
| NMXI-0038.00/US | Utility - CON | Issued     | SINGLE STEP SALICIDATION PROCESS  | 9/28/1992  | 07/951,945 | 2/15/1994  | 5,286,678  | US      |
| NMXI-0039.00/US | Utility - ORG | Issued     | MICROPROCESSOR PLL CLOCK CIRCUIT WITH SELECTABLE DELAYED FEEDBACK   | 5/29/1992  | 07/890,937 | 8/29/1995  | 5,446,867  | US      |
| NMXI-0040.00/US | Utility - CON | Issued     | NOVEL LITHOGRAPHIC TECHNIQUE FOR PATTERNING A SEMICONDUCTOR DEVICE  | 4/13/1993  | 08/046,477 | 4/2/1996   | 5,503,959  | US      |
| NMXI-0041.00/US | Utility - CON | Issued     | CIRCUITRY AND METHOD FOR PROGRAMMING AND ERASING A NONVOLATILE SEMICONDUCTOR MEMORY   | 2/24/1994  | 08/201,044 | 9/5/1995   | 5,448,712  | US      |
| NMXI-0042.00/US | Utility - CON | Issued     | COMMAND INTERFACE BETWEEN USER COMMANDS AND A MEMORY DEVICE   | 1/21/1994  | 08/185,449 | 10/31/1995 | 5,463,757  | US      |
| NMXI-0043.00/US | Utility - CON | Issued     | ARCHITECTURE FOR READING INFORMATION FROM A MEMORY ARRAY  | 5/9/1994   | 08/239,939 | 7/4/1995   | 5,430,677  | US      |
| NMXI-0044.00/US | Utility - ORG | Expired    | APPARATUS AND METHOD FOR ACHIEVING VIA STEP COVERAGE SYMMETRY   | 12/27/1990 | 07/635,662 | 6/30/1992  | 5,126,029  | US      |
| NMXI-0045.00/US | Utility - ORG | Expired    | CIRCUITRY AND METHOD FOR DISCHARGING A DRAIN OF A CELL OF A NON-VOLATILE SEMICONDUCTOR MEMORY   | 5/10/1991  | 07/698,547 | 11/23/1993 | 5,265,059  | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| App. No.        | Matter Type     | Matter Status | Title  | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|-----------------|---------------|--|------------|------------|------------|------------|---------|
| NMXI-0046.00/US | Utility - CON   | Issued        | METHOD AND APPARATUS FOR CONDUCTING BUS TRANSACTIONS BETWEEN TWO CLOCK INDEPENDENT BUS AGENTS OF A COMPUTER SYSTEM USING A TRANSACTION BY TRANSACTION DETERMINISTIC REQUEST/RESPONSE PROTOCOL AND BURST TRANSFER | 5/26/1995  | 08/429,658 | 11/28/1995 | 5,471,637  | US      |
| NMXI-0047.00/US | Utility - CON   | Issued        | HIGH-RESOLUTION SYNCHRONOUS DELAY LINE   | 3/24/1993  | 08/036,219 | 11/15/1994 | 5,365,128  | US      |
| NMXI-0048.00/US | Utility - ORG   | Issued        | METHOD OF REPAIRING OVERERASED CELLS IN A FLASH MEMORY   | 10/9/1991  | 07/773,228 | 8/17/1993  | 5,237,535  | US      |
| NMXI-0049.00/US | Utility - DIV   | Expired       | A METHOD FOR PROGRAMMING A VIRTUAL GROUND EPROM CELL INCLUDING SLOW RAMPING OF THE COLUMN LINE VOLTAGE   | 2/20/1991  | 07/658,184 | 7/7/1992   | 5,128,895  | US      |
| NMXI-0050.00/US | Utility - ORG   | Expired       | COMPOSITE POLISHING PAD FOR SEMICONDUCTOR PROCESS  | 7/9/1991   | 07/727,829 | 5/25/1993  | 5,212,910  | US      |
| NMXI-0051.00/US | Utility - CON   | Issued        | METHOD FOR TESTING ERASE CHARACTERISTICS OF A FLASH MEMORY ARRAY   | 7/19/1994  | 08/277,369 | 5/2/1995   | 5,412,793  | US      |
| NMXI-0052.00/US | Utility - CON   | Abandoned     | ADVANCE MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY   | 8/18/1993  | 08/108,766 |            |            | US      |
| NMXI-0053.01/US | Utility - NSPCT | Issued        | ADVANCE MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY   | 6/14/1994  | 08/140,070 | 9/17/1996  | 5,556,807  | US      |
| NMXI-0052.02/US | Utility - CON   | Issued        | ADVANCE MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY   | 2/6/1995   | 08/384,629 | 1/30/1996  | 5,488,257  | US      |
| NMXI-0053.00/US | Utility - ORG   | Expired       | ASYMMETRIC FLOATING GATE OVERLAP FOR IMPROVED DEVICE CHARACTERISTICS IN BURIED BIT-LINE DEVICES  | 8/14/1991  | 07/744,969 | 2/22/1994  | 5,289,026  | US      |
| NMXI-0053.01/US | Utility - CON   | Issued        | A METHOD FOR FORMING AN ASYMMETRIC FLOATING GATE OVERLAP FOR IMPROVED DEVICE PERFORMANCE IN BURIED BIT-LINE DEVICES  | 11/10/1994 | 08/337,131 | 9/26/2000  | 6,124,168  | US      |
| NMXI-0054.00/RE | Utility - REIS  | Issued        | PROCESS FOR FORMING A THIN OXIDE LAYER   | 9/14/1995  | 08/528,188 | 9/14/1993  | RE38,674   | US      |
| NMXI-0054.00/US | Utility - ORG   | Issued        | NOVEL PROCESS FOR FORMING A THIN OXIDE LAYER   | 12/17/1991 | 07/809,971 | 9/14/1993  | 5,244,843  | US      |
| NMXI-0055.00/US | Utility - CIP   | Expired       | CHARGE DOMAIN DIFFERENTIAL CONDUCTANCE SYNAPSE CELL FOR NEURAL NETWORKS  | 1/16/1992  | 07/821,634 | 10/13/1992 | 5,155,377  | US      |
| NMXI-0056.00/US | Utility - ORG   | Issued        | BUFFER CIRCUITRY FOR TRANSFERRING SIGNALS FROM TTL CIRCUITRY TO DUAL RANGE CMOS CIRCUITRY  | 12/23/1991 | 07/813,148 | 3/29/1994  | 5,298,807  | US      |
| NMXI-0057.00/US | Utility - CIP   | Issued        | PROCESS FOR FILLING SUBMICRON SPACES WITH DIELECTRIC   | 7/21/1992  | 07/917,465 | 12/14/1993 | 5,270,264  | US      |
| NMXI-0058.00/US | Utility - ORG   | Expired       | MULTI-QUADRANT CHARGE DOMAIN SYNAPSE CELL  | 8/20/1991  | 07/747,642 | 8/4/1992   | 5,136,177  | US      |
| NMXI-0059.00/US | Utility - CON   | Issued        | LITHOGRAPHY USING A NEW PHASE-SHIFTING RETICLE   | 3/1/1995   | 08/396,926 | 5/27/1997  | 5,653,102  | US      |
| NMXI-0060.00/US | Utility - ORG   | Issued        | CLOCK SWITCHING APPARATUS AND METHOD FOR COMPUTER SYSTEMS  | 12/30/1991 | 07/815,819 | 12/28/1993 | 5,274,678  | US      |
| NMXI-0061.00/US | Utility - ORG   | Expired       | HIGH SPEED NORING INVERTING, MUXING AND LATCHING CIRCUIT WITH TEMPERATURE COMPENSATED OUTPUT NOISE CONTROL   | 8/30/1991  | 07/752,780 | 12/1/1992  | 5,168,178  | US      |
| NMXI-0062.00/US | Utility - ORG   | Expired       | HIGH THROUGHPUT INTERLEVEL DIELECTRIC GAP FILLING PROCESS  | 6/28/1991  | 07/722,861 | 1/18/1994  | 5,279,865  | US      |
| NMXI-0063.00/US | Utility - CIP   | Expired       | HIGH SPEED, LOW POWER OUTPUT CIRCUIT WITH TEMPERATURE COMPENSATED NOISE CONTROL  | 5/12/1992  | 07/881,843 | 3/1/1994   | 5,291,071  | US      |
| NMXI-0064.00/US | Utility - CON   | Issued        | SURFACE SWEEPING METHOD FOR SURFACE MOVEMENT IN THREE DIMENSIONAL TOPOGRAPHY SIMULATION  | 11/5/1993  | 08/148,181 | 12/17/1996 | 5,586,230  | US      |
| NMXI-0065.00/US | Utility - ORG   | Issued        | PARTICLE FLUX SHADOWING FOR THREE-DIMENSIONAL TOPOGRAPHY SIMULATION  | 6/24/1992  | 07/904,001 | 1/25/1994  | 5,282,140  | US      |
| NMXI-0066.00/US | Utility - ORG   | Issued        | METHOD AND APPARATUS FOR TESTING THE CONTINUITY OF STATIC RANDOM ACCESS MEMORY CELLS   | 12/31/1991 | 07/816,635 | 10/19/1993 | 5,255,230  | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| Pub. No.        | Matter Type   | Matter Status | Title  | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|------------|---------|
| NMXI-0067.00/US | Utility - ORG | Issued        | DELAY LINE LOOP FOR ON-CHIP CLOCK SYNTHESIS WITH ZERO SKEW AND 50% DUTY CYCLE  | 2/26/1993  | 08/023,673 | 4/25/1995  | 5,410,263  | US      |
| NMXI-0068.00/US | Utility - ORG | Issued        | MULTI-STAGED CHARGE-PUMP WITH STAGGERED CLOCK PHASES FOR PROVIDING HIGH CURRENT CAPABILITY   | 6/10/1992  | 07/896,195 | 4/5/1994   | 5,301,097  | US      |
| NMXI-0069.00/US | Utility - CON | Issued        | RANDOM ACCESS MEMORY WITH DIGITAL SIGNALS RUNNING OVER THE SMALL SIGNAL REGION OF THE ARRAY  | 3/9/1994   | 08/208,800 | 10/10/1995 | 5,457,648  | US      |
| NMXI-0070.00/US | Utility - ORG | Issued        | STATIC PLA WITH STATE MACHINE CONTROLLED BY MODEL OF BOTH MINTERM AND AN OUTPUT LINE   | 4/29/1992  | 07/875,661 | 12/28/1993 | 5,274,281  | US      |
| NMXI-0071.00/US | Utility - ORG | Issued        | DETECTING VALID DATA FROM A TWISTED PAIR MEDIUM  | 7/31/1992  | 07/923,853 | 12/13/1994 | 5,373,508  | US      |
| NMXI-0072.00/US | Utility - ORG | Issued        | BIAS CIRCUITRY FOR CONTENT ADDRESSABLE MEMORY CELLS OF A FLOATING GATE NONVOLATILE MEMORY  | 3/31/1992  | 07/861,093 | 11/30/1993 | 5,267,213  | US      |
| NMXI-0073.00/US | Utility - CON | Issued        | APPARATUS AND METHOD FOR MINIMIZING VERIFY TIME IN A SEMICONDUCTOR MEMORY BY CONSTANTLY CHARGING A WELL CAPACITANCE  | 12/22/1994 | 08/361,872 | 4/9/1996   | 5,506,803  | US      |
| NMXI-0074.00/US | Utility - ORG | Issued        | PROTECTED ERASE VOLTAGE DISCHARGE TRANSISTOR IN A NONVOLATILE SEMICONDUCTOR MEMORY   | 11/3/1992  | 07/971,074 | 5/3/1994   | 5,309,012  | US      |
| NMXI-0075.00/US | Utility - ORG | Issued        | POLYIMIDE PROCESS FOR PROTECTING INTEGRATED CIRCUITS   | 6/5/1992   | 07/893,765 | 9/7/1993   | 5,242,864  | US      |
| NMXI-0076.00/US | Utility - ORG | Issued        | METHOD FOR THE ANISOTROPIC ETCHING OF METAL FILMS IN THE FABRICATION OF INTERCONNECTS  | 9/8/1992   | 07/941,412 | 9/27/1994  | 5,350,484  | US      |
| NMXI-0077.00/US | Utility - ORG | Issued        | METHOD OF MANAGING DEFECTS IN FLASH DISK MEMORIES  | 10/30/1992 | 07/969,749 | 12/5/1995  | 5,473,753  | US      |
| NMXI-0077.01/US | Utility - DIV | Issued        | METHOD OF MANAGING DEFECTS IN FLASH DISK MEMORIES  | 8/12/1996  | 08/700,676 | 1/11/2000  | 6,014,755  | US      |
| NMXI-0078.00/US | Utility - ORG | Issued        | METHOD OF CONTROLLING CLEAN-UP OF A SOLID STATE MEMORY DISK STORING FLOATING SECTOR DATA   | 10/30/1992 | 07/969,763 | 12/26/1995 | 5,479,633  | US      |
| NMXI-0079.00/US | Utility - ORG | Issued        | POWER MANAGEMENT SYSTEM FOR COMPONENTS USED IN BATTERY POWERED APPLICATIONS  | 2/19/1993  | 08/019,617 | 2/11/1997  | 5,603,036  | US      |
| NMXI-0080.00/US | Utility - ORG | Issued        | PROCESS FOR ETCHING SILICON DIOXIDE LAYER WITHOUT MICRO MASKING EFFECT   | 6/12/1992  | 07/897,768 | 3/22/1994  | 5,296,094  | US      |
| NMXI-0081.00/US | Utility - CON | Issued        | A METHOD AND APPARATUS FOR DYNAMICALLY ALLOCATING ACCESS TIME TO A RESOURCE SHARED BETWEEN A PERIPHERAL BUS AND A HOST BUS BY DYNAMICALLY CONTROLLING THE SIZE OF BURST DATA TRANSFERS ON THE PERIPHERAL BUS | 2/14/1995  | 08/389,078 | 11/26/1996 | 5,579,530  | US      |
| NMXI-0082.00/US | Utility - ORG | Issued        | GENERALLY-DIAGONAL MAPPING OF ADDRESS SPACE FOR ROW/COLUMN ORGANIZER MEMORIES  | 9/30/1993  | 08/130,023 | 2/6/1996   | 5,490,264  | US      |
| NMXI-0083.00/US | Utility - ORG | Issued        | APPARATUS UTILIZING DUAL COMPARE LOGIC FOR SELF CHECKING OF FUNCTIONAL REDUNDANCY CHECK (FRC) LOGIC  | 1/30/1992  | 07/830,209 | 1/4/1994   | 5,276,690  | US      |
| NMXI-0084.00/US | Utility - DIV | Expired       | METHOD FOR FORMING A TITANIUM NITRIDE BARRIER LAYER  | 4/14/1992  | 07/869,227 | 8/3/1993   | 5,232,871  | US      |
| NMXI-0085.00/US | Utility - ORG | Issued        | PROGRAMMABLE SECONDARY CLOCK GENERATOR   | 9/21/1992  | 07/948,215 | 10/26/1993 | 5,256,994  | US      |
| NMXI-0086.00/US | Utility - ORG | Issued        | PROGRAMMABLE DEDICATED TIMER OPERATED ON A CLOCK INDEPENDENT OF PROCESSOR TIMER  | 6/12/1992  | 07/897,897 | 7/25/1995  | 5,437,021  | US      |
| NMXI-0087.00/US | Utility - ORG | Issued        | GATE/SOURCE DISTURB PROTECTION FOR SIXTEEN-BIT FLASH EEPROM MEMORY ARRAYS  | 6/19/1992  | 07/901,110 | 5/31/1994  | 5,317,535  | US      |
| NMXI-0088.00/US | Utility - ORG | Issued        | HIGH-SPEED BIAS-STABILIZED CURRENT-MIRROR REFERENCEING CIRCUIT FOR NON-VOLATILE MEMORIES   | 6/19/1992  | 07/901,395 | 2/22/1994  | 5,289,412  | US      |
| NMXI-0089.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR IMPROVING DATA FAILURE RATE TESTING FOR MEMORY ARRAYS   | 10/30/1992 | 07/969,465 | 5/16/1995  | 5,416,782  | US      |
| NMXI-0090.00/US | Utility - CON | Issued        | METHOD FOR WAITING TO A FLASH MEMORY ARRAY DURING ERASE SUSPEND INTERVALS  | 10/30/1993 | 08/145,732 | 8/23/1994  | 5,341,330  | US      |

## Exhibit A - US Applications and Issued Patents

| MTI-DefNo       | MatterType    | MatterStatus | Title   | FileDate   | SerialNo   | IssueDate  | PatentNo  | Country |
|-----------------|---------------|--------------|---|------------|------------|------------|-----------|---------|
| NMXI-0091.00/US | Utility - ORG | Issued       | NONVOLATILE MEMORY WITH A PROGRAMMABLE OUTPUT OF SELECTABLE WIDTH AN A METHOD FOR CONTROLLING THE NONVOLATILE MEMORY TO SWITCH BETWEEN DIFFERENT OUT PUT WIDTHS   | 3/17/1993  | 08/032,686 | 4/2/1996   | 5,504,875 | US      |
| NMXI-0092.00/US | Utility - ORG | Issued       | POLISHING PAD CONDITIONING APPARATUS FOR WAFER PLANARIZATION PROCESS  | 9/24/1992  | 07/950,812 | 6/8/1993   | 5,216,843 | US      |
| NMXI-0093.00/US | Utility - ORG | Issued       | ELECTRICALLY CONDUCTIVE INTERPOSER AND ARRAY PACKAGE CONCEPT FOR INTERCONNECTING TO A CIRCUIT BOARD   | 12/2/1992  | 07/984,335 | 6/14/1994  | 5,321,583 | US      |
| NMXI-0094.00/US | Utility - CON | Issued       | OPTIMIZED LOOKUP TABLE METHOD FOR CONVERTING YUV PIXEL VALUES TO RGB PIXEL VALUES   | 4/17/1995  | 08/425,231 | 5/7/2002   | 6,384,838 | US      |
| NMXI-0095.00/US | Utility - ORG | Issued       | FAST PARITY GENERATOR USING COMPLEMENT PASS-TRANSISTOR LOGIC  | 11/23/1993 | 08/156,427 | 3/4/1997   | 5,608,741 | US      |
| NMXI-0096.00/US | Utility - CON | Issued       | METHOD AND APPARATUS FOR UPDATING FLASH MEMORY RESIDENT FIRMWARE THROUGH A STANDARD DISK DRIVE INTERFACE  | 11/9/1998  | 09/189,000 | 12/28/1999 | 6,009,497 | US      |
| NMXI-0096.00/US | Utility - CON | Issued       | METHOD AND APPARATUS FOR UPDATING FLASH MEMORY RESIDENT FIRMWARE THROUGH A STANDARD DISK DRIVE INTERFACE  | 11/9/1998  | 09/189,000 | 12/28/1999 | 6,009,497 | US      |
| NMXI-0097.00/US | Utility - CON | Issued       | METHOD AND APPARATUS FOR DATA BUFFERING AND QUEUE MANAGEMENT OF DIGITAL MOTION VIDEO SIGNALS  | 12/16/1994 | 08/357,544 | 9/12/1995  | 5,450,544 | US      |
| NMXI-0098.00/US | Utility - ORG | Issued       | SYSTEM FOR AUTOMATICALLY COMPRESSING AND DECOMPRESSING DATA FOR SENDER AND RECEIVER PROCESSES UPON DETERMINATION OF A COMMON COMPRESSION/ DECOMPRESSION METHOD UNDERSTOOD BY BOTH SENDER AND RECEIVER PROCESSES | 10/15/1992 | 07/964,475 | 9/17/1996  | 5,557,749 | US      |
| NMXI-0099.00/US | Utility - CON | Issued       | METHOD FOR ALLOCATING MEMORY IN A SOLID STATE MEMORY DISK   | 6/7/1995   | 08/485,661 | 7/9/1996   | 5,535,369 | US      |
| NMXI-0100.00/US | Utility - ORG | Issued       | HIGH SPEED OR CIRCUIT CONFIGURATION   | 9/1/1992   | 07/938,934 | 12/28/1993 | 5,274,277 | US      |
| NMXI-0101.00/US | Utility - CON | Issued       | MULTIPLE LAYER PROGRAMMABLE LAYOUT FOR VERSION IDENTIFICATION   | 8/3/1994   | 08/285,495 | 10/17/1995 | 5,459,355 | US      |
| NMXI-0102.00/US | Utility - ORG | Issued       | IMPROVED SYSTEM OF RESOLVING CONFLICTING DATA PROCESSING MEMORY REQUESTS  | 9/30/1992  | 07/954,743 | 8/16/1994  | 5,339,442 | US      |
| NMXI-0103.00/US | Utility - ORG | Issued       | METHOD FOR LOCATING SECTOR DATA IN A MEMORY DISK BY EXAMINING A PLURALITY OF HEADERS NEAR AN INITIAL POINTER  | 10/30/1992 | 07/969,780 | 11/28/1995 | 5,471,604 | US      |
| NMXI-0104.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR PROVIDING A CONTEXT SWITCH IN RESPONSE TO AN INTERRUPT IN A COMPUTER PROCESS   | 6/30/1993  | 08/085,409 | 7/9/1996   | 5,535,397 | US      |
| NMXI-0105.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR EXECUTION OF OPERATIONS IN A FLASH MEMORY ARRAY  | 6/30/1993  | 08/086,186 | 4/16/1996  | 5,509,134 | US      |
| NMXI-0106.00/US | Utility - ORG | Issued       | CRITICAL PATH PREDICTION FOR DESIGN OF CIRCUITS   | 12/29/1993 | 08/174,729 | 8/6/1996   | 5,544,071 | US      |
| NMXI-0107.00/US | Utility - ORG | Issued       | INSTRUCTION BUFFER FOR ALIGNING INSTRUCTION SETS USING BOUNDARY DETECTION   | 6/30/1993  | 08/085,637 | 10/31/1995 | 5,463,748 | US      |
| NMXI-0107.01/US | Utility - CON | Issued       | BRANCH TARGET BUFFER WITH ADDRESS TAGS APPENDED TO STORED INSTRUCTIONS FOR FACILITATING BUFFER FLUSHING   | 1/23/1997  | 08/787,983 | 6/26/2001  | 6,250,821 | US      |
| NMXI-0107.02/US | Utility - CON | Issued       | APPARATUS FOR CONCURRENT ISSUE OF INSTRUCTIONS IN PARALLEL FROM FIRST AND SECOND STORAGE AREA   | 8/15/1997  | 08/912,048 | 6/12/2001  | 6,247,120 | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Patent No.        | Material      | Material Status | Title  | Filed Date | Serial No. | Issued Date | Patent No. | Country |
|-------------------|---------------|-----------------|--|------------|------------|-------------|------------|---------|
| NMXXI-01.08.00/US | Utility - CON | Issued          | NONVOLATILE MEMORY WITH BLOCKS AND CIRCUITRY FOR SELECTIVELY PROTECTING THE BLOCKS FROM MEMORY OPERATIONS                              | 12/19/1994 | 08/358,978 | 4/30/1996   | 5,513,135  | US      |
| NMXXI-01.09.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR PROVIDING PRECISE FAULT TRACING IN A SUPERSCALAR MICROPROCESSOR   | 6/30/1993  | 08/085,508 | 5/12/1998   | 5,752,013  | US      |
| NMXXI-01.10.00/US | Utility - ORG | Issued          | NONVOLATILE MEMORY CARD WITH AN ADDRESS TABLE AND AN ADDRESS TRANSLATION LOGIC FOR MAPPING OUT DEFECTIVE BLOCKS WITHIN THE MEMORY CARD | 6/30/1993  | 08/086,043 | 6/4/1996    | 5,524,231  | US      |
| NMXXI-01.11.00/US | Utility - CON | Issued          | APPARATUS AND A METHOD FOR IMPROVING THE PROGRAM AND ERASE PERFORMANCE OF A FLASH EEPROM MEMORY ARRAY                                  | 10/27/1994 | 08/329,773 | 10/3/1995   | 5,455,800  | US      |
| NMXXI-01.12.00/US | Utility - ORG | Issued          | METHOD AND CIRCUITRY FOR ENABLING AND PERMANENTLY DISABLING TEST MODE ACCESS IN A FLASH MEMORY DEVICE                                  | 12/30/1993 | 08/175,599 | 6/11/1996   | 5,526,311  | US      |
| NMXXI-01.13.00/US | Utility - ORG | Issued          | PARALLEL MULTISTAGE SYNCHRONIZATION METHOD AND APPARATUS   | 12/21/1993 | 08/171,554 | 1/30/1996   | 5,488,639  | US      |
| NMXXI-01.14.00/US | Utility - ORG | Issued          | A ROLUND ROBIN SCHEDULER USING A SCHEDULER CARRY OPERATION FOR ARBITRATION   | 12/30/1992 | 07/998,555 | 11/22/1994  | 5,367,679  | US      |
| NMXXI-01.15.00/US | Utility - ORG | Abandoned       | METHOD AND APPARATUS FOR ANALYZING DIGITAL VIDEO IMAGES BY MERGING DISPLACEMENT VECTORS  | 12/31/1992 | 07/999,148 |             |            | US      |
| NMXXI-01.15.01/US | Utility - CON | Issued          | METHOD AND APPARATUS FOR ANALYZING DIGITAL VIDEO IMAGES BY MERGING DISPLACEMENT VECTORS  | 6/10/1994  | 08/258,613 | 10/22/1996  | 5,568,569  | US      |
| NMXXI-01.16.00/US | Utility - CON | Issued          | METHOD AND APPARATUS FOR INITIATING COMMUNICATION VIA PAGING A MOBILE COMPUTING DEVICE   | 2/9/1996   | 08/599,032 | 12/3/1996   | 5,581,594  | US      |
| NMXXI-01.17.00/US | Utility - DIV | Issued          | THIN, HIGH LEAD COUNT PACKAGE  | 2/1/1993   | 08/011,944 | 3/1/1994    | 5,290,735  | US      |
| NMXXI-01.18.00/US | Utility - ORG | Issued          | NOVEL SLURRIES FOR CHEMICAL MECHANICAL POLISHING   | 11/3/1993  | 08/146,923 | 8/23/1994   | 5,340,370  | US      |
| NMXXI-01.19.00/US | Utility - ORG | Issued          | SPECIAL TEST MODES FOR A PAGE BUFFER SHARED RESOURCE IN A MEMORY DEVICE  | 6/30/1993  | 08/085,542 | 4/22/1997   | 5,623,620  | US      |
| NMXXI-01.19.01/US | Utility - CON | Issued          | SPECIAL TEST MODES FOR A PAGE BUFFER SHARED RESOURCE IN A MEMORY DEVICE  | 9/25/1996  | 08/719,583 | 11/10/1998  | 5,835,927  | US      |
| NMXXI-01.20.00/US | Utility - CON | Issued          | SENSING SCHEME FOR FLASH MEMORY WITH MULTILEVEL CELLS  | 2/19/1997  | 08/801,004 | 10/27/1998  | 5,828,616  | US      |
| NMXXI-01.20.01/US | Utility - CON | Issued          | SENSING SCHEME FOR FLASH MEMORY WITH MULTILEVEL CELLS  | 4/10/1997  | 08/827,670 | 5/5/1998    | 5,748,546  | US      |
| NMXXI-01.21.00/US | Utility - ORG | Issued          | WRITE VERIFY SCHEMES FOR FLASH MEMORY WITH MULTILEVEL CELLS  | 6/2/1994   | 08/252,747 | 7/23/1996   | 5,539,690  | US      |
| NMXXI-01.22.00/US | Utility - ORG | Issued          | ADDRESSING MODES FOR A DYNAMIC SINGLE BIT PER CELL TO MULTIPLE BIT PER CELL MEMORY   | 6/2/1994   | 08/252,920 | 5/7/1996    | 5,515,317  | US      |
| NMXXI-01.23.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR PROGRAMMING AND ERASING FLASH EEPROM MEMORY ARRAYS UTILIZING A CHARGE PUMP CIRCUIT                            | 9/10/1993  | 08/119,719 | 5/9/1995    | 5,414,669  | US      |
| NMXXI-01.24.00/US | Utility - CON | Issued          | METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT VOLTAGE PROVIDED BY A CHARGE PUMP CIRCUIT  | 6/19/1996  | 08/491,792 | 9/3/1996    | 5,553,030  | US      |
| NMXXI-01.25.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR A TWO PHASE BOOTSTRAP CHARGE PUMP   | 9/10/1993  | 08/119,423 | 6/6/1995    | 5,422,586  | US      |
| NMXXI-01.25.01/US | Utility - DIV | Issued          | SYSTEM HAVING MULTIPLE PHASE BOOSTED CHARGE PUMP WITH A PLURALITY OF STAGES  | 5/20/1994  | 08/246,779 | 6/4/1996    | 5,524,266  | US      |
| NMXXI-01.25.02/US | Utility - DIV | Issued          | METHOD AND APPARATUS FOR A TWO PHASE BOOTSTRAP CHARGE PUMP   | 5/24/1994  | 08/248,419 | 7/11/1995   | 5,432,469  | US      |
| NMXXI-01.26.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT CURRENT PROVIDED BY A CHARGE PUMP CIRCUIT  | 9/10/1993  | 08/119,425 | 8/15/1995   | 5,442,586  | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| App. No.         | Mat. Type     | Mat. Status | Title   | Filed Date | Serial No. | Issued Date | Patent No. | Country |
|------------------|---------------|-------------|---|------------|------------|-------------|------------|---------|
| NMXXI-0126.00/US | Utility - DIV | Issued      | METHOD AND APPARATUS FOR CONTROLLING THE OUTPUT CURRENT PROVIDED BY A CHARGE PUMP CIRCUIT   | 3/14/1995  | 08/403,633 | 10/3/1995   | 5,455,794  | US      |
| NMXXI-0127.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR SEQUENTIAL PROGRAMMING OF THE BITS IN A WORD OF A FLASH EEPROM MEMORY ARRAY  | 9/10/1993  | 08/119,520 | 7/16/1996   | 5,537,350  | US      |
| NMXXI-0128.00/US | Utility - ORG | Issued      | TRENCH ISOLATION WITH PLANAR TOPOGRAPHY AND METHOD OF FABRICATION   | 6/29/1993  | 08/084,795 | 10/25/1994  | 5,358,891  | US      |
| NMXXI-0129.00/US | Utility - CON | Issued      | ZERO STANDBY CURRENT POWER-UP RESET CIRCUIT   | 1/24/1996  | 08/590,726 | 10/29/1996  | 5,570,050  | US      |
| NMXXI-0130.00/US | Utility - CIP | Issued      | METHOD AND APPARATUS FOR SEQUENTIAL PROGRAMMING OF A FLASH EEPROM MEMORY ARRAY  | 6/2/1994   | 08/752,850 | 7/4/1995    | 5,430,674  | US      |
| NMXXI-0131.00/US | Utility - DIV | Issued      | METHOD OF OPTIMIZING OPERATING PARAMETERS OF AN INTEGRATED CIRCUIT PACKAGE HAVING A VOLTAGE REGULATOR MOUNTED THEREON                                 | 6/6/1995   | 08/468,972 | 9/17/1996   | 5,556,811  | US      |
| NMXXI-0131.01/US | Utility - CON | Issued      | APPARATUS FOR OPTIMIZING OPERATING PARAMETERS OF AN INTEGRATED CIRCUIT PACKAGE HAVING A VOLTAGE REGULATOR MOUNTED THEREON                             | 10/31/1996 | 08/741,526 | 4/15/1997   | 5,621,245  | US      |
| NMXXI-0132.00/US | Utility - CON | Issued      | METHOD AND CIRCUITRY FOR STORING DISCRETE AMOUNTS OF CHARGE IN A SINGLE MEMORY ELEMENT  | 8/13/1997  | 08/910,866 | 4/6/1999    | 5,892,710  | US      |
| NMXXI-0133.00/US | Utility - ORG | Issued      | METHOD OF PIPELINING SEQUENTIAL WRITES IN A FLASH MEMORY  | 6/30/1993  | 08/085,969 | 5/21/1996   | 5,519,847  | US      |
| NMXXI-0134.00/US | Utility - CON | Issued      | METHOD AND APPARATUS FOR PROGRAMMING EMBEDDED MEMORIES OF A VARIETY OF INTEGRATED CIRCUITS USING THE IEEE TEST ACCESS PORT                            | 12/19/1995 | 08/575,178 | 1/21/1997   | 5,596,734  | US      |
| NMXXI-0135.00/US | Utility - ORG | Issued      | CMOS VOLTAGE REFERENCE CIRCUIT  | 11/29/1993 | 08/158,418 | 7/18/1995   | 5,434,534  | US      |
| NMXXI-0136.00/US | Utility - ORG | Issued      | CPU CLOCK CONTROL UNIT  | 12/29/1993 | 08/176,944 | 8/13/1996   | 5,546,568  | US      |
| NMXXI-0137.00/US | Utility - ORG | Issued      | BINOMOS DRIVER CIRCUIT WITH INTEGRATED VOLTAGE SUPPLY CONVERSION  | 5/16/1994  | 08/242,790 | 7/25/1995   | 5,436,585  | US      |
| NMXXI-0138.00/US | Utility - CON | Issued      | METHOD AND APPARATUS FOR REDIRECTING REGISTER ACCESS REQUESTS WHEREIN THE REGISTER SET IS SEPARATE FROM A CENTRAL PROCESSING UNIT                     | 9/19/1996  | 08/710,572 | 9/9/1997    | 5,666,556  | US      |
| NMXXI-0139.00/US | Utility - ORG | Issued      | ARCHITECTURE AND METHOD FOR PROVIDING GUARANTEED ACCESS FOR A RETRYING BUS MASTER TO A DATA TRANSFER BRIDGE CONNECTING TWO BUSES IN A COMPUTER SYSTEM | 2/24/1994  | 08/201,817 | 2/15/2000   | 6,026,455  | US      |
| NMXXI-0140.00/US | Utility - DIV | Issued      | CIRCUITRY AND METHOD FOR PROGRAMMING AND ERASING A NON-VOLATILE SEMICONDUCTOR MEMORY  | 7/29/1993  | 08/100,508 | 4/30/1996   | 5,513,333  | US      |
| NMXXI-0141.00/US | Utility - ORG | Issued      | ANALOG CHROMA KEYING ON COLOR DATA  | 11/19/1993 | 08/154,955 | 3/14/1995   | 5,398,075  | US      |
| NMXXI-0142.00/US | Utility - CON | Issued      | METHOD AND SYSTEM FOR UPDATING FILES OF A PLURALITY OF STORAGE DEVICES THROUGH PROPAGATION OF FILES OVER A NETWORK                                    | 10/12/1995 | 08/542,039 | 8/13/1996   | 5,546,539  | US      |
| NMXXI-0143.00/US | Utility - CON | Issued      | METHOD AND APPARATUS FOR INITIALIZING A COMPUTER SYSTEM HAVING CENTRAL AND DISTRIBUTED ADDRESS DECODE MEMORY BUS RESOURCES                            | 1/31/1995  | 08/384,472 | 12/31/1996  | 5,590,289  | US      |
| NMXXI-0144.00/US | Utility - CON | Issued      | APPARATUS FOR ENCODING SIGNALS USING A CONFIGURABLE TRANSFORM CIRCUIT   | 10/7/1994  | 08/319,779 | 6/20/1995   | 5,426,462  | US      |
| NMXXI-0145.00/US | Utility - ORG | Issued      | METHOD FOR CONTROLLING DATA FLOW BETWEEN A PLURALITY OF CIRCULAR BUFFERS  | 5/26/1993  | 08/067,858 | 8/29/1995   | 5,446,839  | US      |

11/23/2011

Exhibit A - US Applications and Issued Patents

| Patent No.      | Material      | Material Status | Title  | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|-----------------|--|------------|------------|------------|------------|---------|
| NMXI-0146.00/US | Utility - ORG | Issued          | AN ELECTRONIC PACKAGE THAT HAS A DIE COUPLED TO A LEAD FRAME BY A DIELECTRIC TAPE AND A HEAT SINK THAT PROVIDES BOTH AN ELECTRICAL AND A THERMAL PATH BETWEEN THE DIE AND THE LEAD FRAME | 2/25/1994  | 08/201,895 | 8/22/1995  | 5,444,602  | US      |
| NMXI-0147.00/US | Utility - ORG | Issued          | TAB TAPE   | 12/14/1993 | 08/167,531 | 12/5/1995  | 5,473,190  | US      |
| NMXI-0148.00/US | Utility - CON | Issued          | METHOD AND APPARATUS FOR INTERFACING MEMORY DEVICES OPERATING AT DIFFERENT SPEEDS TO A COMPUTER SYSTEM BUS   | 2/28/1997  | 08/807,212 | 2/24/1998  | 5,721,882  | US      |
| NMXI-0149.00/US | Utility - CON | Issued          | METHOD FOR SYNCHRONIZING CLOCKS UPON RESET   | 7/11/1994  | 08/272,879 | 4/23/1996  | 5,510,740  | US      |
| NMXI-0150.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR SENSING THE STATE OF FLOATING GATE MEMORY CELLS BY APPLYING A VARIABLE GATE VOLTAGE   | 9/29/1994  | 08/315,292 | 4/16/1996  | 5,508,958  | US      |
| NMXI-0151.00/US | Utility - CON | Issued          | RECEIVING ADDRESS DETERMINES ADDRESS DECODE CORRESPONDING TO RESOURCES BASED ON SELECT AND READY SIGNALS BY THAT PARTICULAR RESOURCE   | 10/19/1995 | 08/545,374 | 9/16/1997  | 5,668,949  | US      |
| NMXI-0152.00/US | Utility - ORG | Issued          | SINGLE STAGE SENSING APPARATUS FOR A CONTENT ADDRESSABLE MEMORY  | 3/11/1994  | 08/212,350 | 6/27/1995  | 5,428,565  | US      |
| NMXI-0153.00/US | Utility - ORG | Issued          | THROTTLING CIRCUIT FOR A DATA TRANSFER SYSTEM  | 9/15/1994  | 08/307,502 | 7/18/1995  | 5,434,892  | US      |
| NMXI-0154.00/US | Utility - ORG | Lapsed          | DRIVER, COMPUTER-IMPLEMENTED PROCESS, AND COMPUTER SYSTEM FOR PROCESSING DATA USING LOADABLE MICROCODE RUNNING ON A PROGRAMMABLE PROCESSOR   | 11/12/1993 | 08/152,348 | 4/23/1996  | 5,511,195  | US      |
| NMXI-0155.00/US | Utility - ORG | Issued          | DUAL ROW SELECTION USING MULTIPLEXED TRI-LEVEL DECODER   | 9/30/1994  | 08/316,546 | 5/14/1996  | 5,517,138  | US      |
| NMXI-0156.00/US | Utility - ORG | Issued          | LOW IMPACT COLLISION DETECTION METHOD  | 3/23/1994  | 08/216,668 | 10/31/1995 | 5,463,658  | US      |
| NMXI-0157.00/US | Utility - CON | Issued          | ENCODING AND DECODING VIDEO SIGNALS USING ADAPTIVE FILTER SWITCHING CRITERIA   | 6/29/1994  | 08/268,270 | 1/30/1996  | 5,488,570  | US      |
| NMXI-0158.00/US | Utility - CON | Issued          | METHOD AND APPARATUS FOR DECODING AND RECODING OF ADDRESSES  | 12/5/1995  | 08/567,405 | 3/11/1997  | 5,611,054  | US      |
| NMXI-0159.00/US | Utility - ORG | Issued          | LEAD FRAME AND SEMICONDUCTOR DEVICE USING SAME   | 12/9/1991  | 07/803,724 | 8/17/1993  | 5,237,202  | US      |
| NMXI-0159.01/US | Utility - CON | Issued          | LEAD FRAME AND SEMICONDUCTOR DEVICE USING SAME   | 12/3/1992  | 07/984,841 | 3/1/1994   | 5,291,060  | US      |
| NMXI-0160.00/US | Utility - ORG | Issued          | CACHE MEMORY UTILIZING PSEUDO-STATIC FOUR TRANSISTOR MEMORY CELL   | 6/1/1994   | 08/252,320 | 12/12/1995 | 5,475,633  | US      |
| NMXI-0161.00/US | Utility - ORG | Issued          | LOW CAPACITANCE CONTENT-ADDRESSABLE MEMORY CELL  | 6/15/1994  | 08/260,044 | 2/6/1996   | 5,490,102  | US      |
| NMXI-0162.00/US | Utility - ORG | Issued          | BIT MAP ADDRESSING SCHEMES FOR FLASH MEMORY  | 6/2/1994   | 08/253,902 | 3/5/1996   | 5,497,354  | US      |
| NMXI-0163.00/US | Utility - CON | Issued          | FLASH MEMORY INCLUDING A MODE REGISTER FOR INDICATING SYNCHRONOUS OR ASYNCHRONOUS MODE OF OPERATION  | 6/18/1997  | 08/897,499 | 2/15/2000  | 6,026,465  | US      |
| NMXI-0163.01/US | Utility - CON | Issued          | SYNCHRONOUS INTERFACE FOR A NONVOLATILE MEMORY   | 6/14/2000  | 09/595,327 | 5/13/2003  | 6,564,285  | US      |
| NMXI-0164.00/US | Utility - CON | Issued          | PIPELINED READ ARCHITECTURE FOR MEMORY   | 12/20/1995 | 08/575,339 | 1/7/1997   | 5,592,435  | US      |
| NMXI-0164.01/US | Utility - DIV | Issued          | PIPELINED READ ARCHITECTURE FOR MEMORY   | 9/18/1996  | 08/724,270 | 11/4/1997  | 5,684,752  | US      |
| NMXI-0165.00/US | Utility - ORG | Issued          | SYNCHRONOUS ADDRESS LATCHING FOR MEMORY ARRAYS   | 6/3/1994   | 08/253,842 | 3/5/1996   | 5,497,355  | US      |
| NMXI-0166.00/US | Utility - ORG | Issued          | METHOD FOR PRECONDITIONING A NONVOLATILE MEMORY ARRAY  | 6/27/1994  | 08/266,132 | 7/16/1996  | 5,537,357  | US      |
| NMXI-0167.00/US | Utility - CON | Issued          | FAST INTERNAL REFERENCE CELL TRIMMING FOR FLASH EEPROM MEMORY  | 1/31/1996  | 08/594,415 | 3/4/1997   | 5,608,679  | US      |
| NMXI-0167.01/US | Utility - CON | Issued          | FAST INTERNAL REFERENCE CELL TRIMMING FOR FLASH EEPROM MEMORY  | 2/9/1996   | 08/599,123 | 3/4/1997   | 5,608,669  | US      |
| NMXI-0168.00/US | Utility - ORG | Issued          | IC SOCKET PERMITTING CHECKING CONNECTED STATE BETWEEN IC SOCKET AND PRINTED WIRING BOARD   | 11/18/1994 | 08/342,379 | 9/23/1997  | 5,669,783  | US      |

## Exhibit A - US Applications and Issued Patents

| Pat. No.        | Mat. Type     | Mat. Status | Title  | Filed Date | Serial No. | Issue Date | Pat. No.  | Country |
|-----------------|---------------|-------------|--|------------|------------|------------|-----------|---------|
| NMXI-0168.03/US | Utility - DIV | Issued      | IC SOCKET AND METHOD OF CHECKING CONNECTED STATE BETWEEN IC SOCKET AND PRINTED WIRING BOARD  | 3/20/1996  | 08/618,686 | 12/23/1997 | 5,699,612 | US      |
| NMXI-0169.00/US | Utility - ORG | Issued      | DISPLAYING CALLER IDENTIFICATION INFORMATION IN A COMPUTER SYSTEM  | 6/29/1994  | 08/268,265 | 8/13/1996  | 5,546,447 | US      |
| NMXI-0170.00/US | Utility - ORG | Issued      | PARALLEL FLEXIBLE TRANSMISSION CABLE   | 6/27/1994  | 08/266,150 | 9/17/1996  | 5,557,075 | US      |
| NMXI-0171.00/US | Utility - CON | Issued      | METHOD AND APPARATUS FOR INTERACTIVE BUILT-IN-SELF-TESTING WITH USER - PROGRAMMABLE TEST PATTERNS  | 8/15/1996  | 08/698,465 | 5/30/2000  | 6,070,252 | US      |
| NMXI-0172.00/US | Utility - ORG | Issued      | PROGRAMMABLE HIGH WATERMARK IN STACK FRAME CACHE USING SECOND REGION AS A STORAGE IF FIRST REGION IS FULL AND AN EVENT HAVING A PREDETERMINED MINIMUM PRIORITY | 9/28/1994  | 08/314,418 | 6/3/1997   | 5,636,362 | US      |
| NMXI-0173.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR PROVIDING AN ULTRA LOW POWER REGULATED NEGATIVE CHARGE PUMP   | 3/23/1994  | 08/217,095 | 7/2/1996   | 5,532,915 | US      |
| NMXI-0175.00/US | Utility - ORG | Expired     | HEAD DRUM GROUND SYSTEM FOR USE IN A VIDEO CASSETTE RECORDER   | 1/5/1989   | 08/294,529 | 6/16/1992  | 5,123,097 | US      |
| NMXI-0176.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR COOPERATIVE MESSAGING   | 4/18/1995  | 08/423,306 | 4/15/1997  | 5,621,798 | US      |
| NMXI-0177.00/US | Utility - CON | Issued      | METHOD AND DATA STORAGE SYSTEM FOR STRING DATA IN BLOCKS WITHOUT FILE REALLOCATION BEFORE ERASURE  | 3/17/1997  | 08/819,688 | 9/15/1998  | 5,809,558 | US      |
| NMXI-0178.00/US | Utility - ORG | Issued      | DRAIN BIAS MULTIPLEXING FOR MULTIPLE BIT FLASH CELL  | 6/2/1994   | 08/252,684 | 1/16/1996  | 5,485,422 | US      |
| NMXI-0179.00/US | Utility - ORG | Issued      | I/O CONNECTOR FOR ADD IN PRINTED CIRCUIT CARDS FOR COMPUTER SYSTEMS  | 7/15/1994  | 08/275,599 | 12/19/1995 | 5,477,418 | US      |
| NMXI-0180.00/US | Utility - ORG | Issued      | POWER VALIDATION TOOL FOR MICROPROCESSOR SYSTEMS   | 10/19/1994 | 08/325,879 | 6/14/1996  | 5,525,913 | US      |
| NMXI-0181.00/US | Utility - ORG | Issued      | CLOCKING SCHEME FOR LATCHING OF A DOMINO OUTPUT  | 1/4/1995   | 08/368,335 | 9/26/1995  | 5,453,708 | US      |
| NMXI-0182.00/US | Utility - ORG | Issued      | ERROR MANAGEMENT PROCESSES FOR FLASH EEPROM MEMORY ARRAYS  | 12/27/1994 | 08/364,546 | 12/12/1995 | 5,475,693 | US      |
| NMXI-0183.00/US | Utility - CON | Issued      | POLYSILICON POLISH FOR PATTERNING IMPROVEMENT  | 9/2/1997   | 08/944,041 | 6/8/1999   | 5,911,111 | US      |
| NMXI-0184.00/US | Utility - CON | Issued      | COMPUTER SYSTEM WITH A MEMORY IDENTIFICATION SCHEME  | 5/7/1996   | 08/643,990 | 2/18/1997  | 5,604,880 | US      |
| NMXI-0185.00/US | Utility - CIP | Issued      | PROCESS, APPARATUS AND SYSTEM FOR SELECTING QUANTIZATION LEVELS FOR ENCODING VIDEO SIGNALS   | 4/28/1994  | 08/234,978 | 7/2/1996   | 5,532,940 | US      |
| NMXI-0186.00/US | Utility - CIP | Issued      | PROCESS, APPARATUS AND SYSTEM FOR TRANSFORMING SIGNALS USING PSEUDO SIMD PROCESSING  | 4/28/1994  | 08/234,976 | 9/24/1996  | 5,559,722 | US      |
| NMXI-0187.00/US | Utility - ORG | Issued      | TEST FLOW ASSURANCE USING MEMORY IMPRINTING  | 9/27/1994  | 08/312,831 | 7/23/1996  | 5,538,141 | US      |
| NMXI-0187.01/US | Utility - DIV | Issued      | APPARATUS AND SEMICONDUCTOR COMPONENT FOR ASSURING TEST FLOW COMPLIANCE  | 2/2/1996   | 08/595,796 | 2/18/1997  | 5,603,412 | US      |
| NMXI-0188.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR ROUTING IN REDUCED SWITCH MATRICES TO PROVIDE ONE HUNDRED PERCENT COVERAGE  | 7/25/1994  | 08/279,954 | 11/5/1996  | 5,572,198 | US      |
| NMXI-0189.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR PREVENTING OVER-ERASURE OF FLASH EEPROM MEMORY DEVICES  | 6/28/1994  | 08/267,472 | 2/6/1996   | 5,490,109 | US      |
| NMXI-0190.00/US | Utility - ORG | Issued      | HI-JACK HINGED CONNECTION ADAPTER FOR INPUT/OUTPUT CARDS   | 11/1/1994  | 08/333,139 | 6/10/1997  | 5,637,018 | US      |
| NMXI-0190.01/US | Utility - CON | Issued      | HI-JACK HINGED CONNECTION ADAPTER FOR INPUT/OUTPUT CARDS   | 9/30/1996  | 08/724,625 | 7/7/1998   | 5,775,951 | US      |
| NMXI-0191.00/US | Utility - ORG | Issued      | RENDERING AN INTER VERSUS INTRA VIDEO ENCODING DECISION BASED UPON A VERTICAL GRADIENT MEASURE OF TARGET VIDEO FRAMES  | 9/8/1994   | 08/302,595 | 2/4/1997   | 5,600,375 | US      |
| NMXI-0192.00/US | Utility - CON | Issued      | DATA CONSISTENCY ACROSS A BUS TRANSACTIONS THAT IMPOSE ORDERING CONSTRAINTS  | 6/19/1997  | 08/878,660 | 11/3/1998  | 5,832,241 | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| MTI DocNo       | Matter Type   | Matte Status | Title  | Filed Date | Serial No  | Issue Date | Patent No | Country |
|-----------------|---------------|--------------|--|------------|------------|------------|-----------|---------|
| NMXI-0193.00/US | Utility - ORG | Issued       | PHOTOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL  | 6/30/1995  | 08/497,321 | 9/1/1998   | 5,801,821 | US      |
| NMXI-0193.01/US | Utility - DIV | Issued       | PHOTOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL  | 10/31/1997 | 08/962,138 | 8/31/1999  | 5,946,079 | US      |
| NMXI-0193.02/US | Utility - DIV | Abandoned    | PHOTOGRAPHY METHOD USING COHERENCE DISTANCE CONTROL  | 9/16/1998  | 09/156,272 |            |           | US      |
| NMXI-0194.00/US | Utility - ORG | Issued       | LOW LOAD HOST/PCI BUS BRIDGE   | 12/19/1994 | 08/358,359 | 4/14/1998  | 5,740,385 | US      |
| NMXI-0195.00/US | Utility - CON | Issued       | METHOD AND APPARATUS FOR PROVIDING PROCESSOR FIELD UPGRADABILITY TO A MOTHERBOARD                      | 7/31/1996  | 08/691,859 | 7/1/1997   | 5,643,000 | US      |
| NMXI-0196.00/US | Utility - ORG | Issued       | LOW POWER VOLTAGE DETECTOR CIRCUIT INCLUDING A FLASH MEMORY CELL                                       | 10/19/1994 | 08/326,689 | 2/27/1996  | 5,495,453 | US      |
| NMXI-0197.00/US | Utility - CON | Issued       | SELECTING AN INTEGRATED CIRCUIT FROM DIFFERENT INTEGRATED CIRCUIT ARRAY CONFIGURATIONS                 | 11/28/1997 | 08/978,998 | 2/2/1999   | 5,867,721 | US      |
| NMXI-0198.00/US | Utility - CON | Issued       | HORIZONTALLY SCALING IMAGE SIGNALS USING DIGITAL DIFFERENTIAL ACCUMULATOR PROCESSING                   | 6/3/1994   | 08/254,364 | 7/21/1998  | 5,784,046 | US      |
| NMXI-0199.00/US | Utility - CON | Issued       | VERTICALLY SCALING IMAGE SIGNALS USING DIGITAL DIFFERENTIAL ACCUMULATOR PROCESSING                     | 6/3/1994   | 08/253,737 | 12/2/1997  | 5,694,149 | US      |
| NMXI-0200.00/US | Utility - CON | Issued       | HORIZONTALLY SCALING A IMAGE SIGNALS USING SELECTED WEIGHT FACTORS                                     | 6/3/1994   | 08/253,374 | 5/19/1998  | 5,754,162 | US      |
| NMXI-0201.00/US | Utility - ORG | Issued       | RUN-LENGTH ENCODING SEQUENCE FOR VIDEO SIGNALS   | 10/26/1994 | 08/329,524 | 9/3/1996   | 5,552,832 | US      |
| NMXI-0202.00/US | Utility - ORG | Issued       | FOUR WIRE MODEM SIGNAL SWITCHING FOR VOICE AND DATA APPLICATIONS                                       | 3/24/1995  | 08/410,212 | 2/11/1997  | 5,602,902 | US      |
| NMXI-0203.01/US | Utility - CON | Issued       | PORTABLE DISK DRIVE CARRIER AND CHASSIS  | 12/29/1995 | 08/580,489 | 12/31/1996 | 5,588,728 | US      |
| NMXI-0204.00/US | Utility - ORG | Abandoned    | METHOD AND APPARATUS FOR DETECTING AND SELECTING VOLTAGE SUPPLIES FOR FLASH MEMORY                     | 10/19/1994 | 08/326,702 |            |           | US      |
| NMXI-0204.01/US | Utility - CON | Issued       | METHOD AND APPARATUS FOR DETECTING AND SELECTING VOLTAGE SUPPLIES FOR FLASH MEMORY                     | 12/19/1995 | 08/575,177 | 9/2/1997   | 5,663,918 | US      |
| NMXI-0205.00/US | Utility - CON | Abandoned    | CPU-CYCLE STEALING FOR MULTI-TASKING OPERATING SYSTEM  | 9/23/1997  | 08/933,742 | 1/4/2000   | 6,012,082 | US      |
| NMXI-0206.00/US | Utility - ORG | Issued       | APPARATUS AND METHOD FOR SELECTING A TAP RANGE IN A DIGITAL DELAY LINE                                 | 3/30/1995  | 08/413,951 | 7/16/1996  | 5,537,069 | US      |
| NMXI-0207.00/US | Utility - ORG | Issued       | ALGORITHMIC ARRAY MAPPING TO DECREASE DETECT SENSITIVITY OF MEMORY DEVICES                             | 3/29/1995  | 08/412,555 | 8/24/1999  | 5,943,693 | US      |
| NMXI-0208.00/US | Utility - ORG | Issued       | VARIABLE STAGE CHARGE PUMP   | 9/29/1995  | 08/537,233 | 2/11/1997  | 5,602,794 | US      |
| NMXI-0208.01/US | Utility - DIV | Issued       | VARIABLE STAGE CHARGE PUMP   | 10/3/1996  | 08/720,943 | 6/16/1998  | 5,767,735 | US      |
| NMXI-0208.02/US | Utility - DIV | Issued       | VARIABLE STAGE CHARGE PUMP   | 10/3/1996  | 08/720,876 | 3/24/1998  | 5,732,039 | US      |
| NMXI-0208.03/US | Utility - DIV | Issued       | VARIABLE STAGE CHARGE PUMP   | 10/4/1996  | 08/720,944 | 7/14/1998  | 5,781,473 | US      |
| NMXI-0209.00/US | Utility - ORG | Issued       | POWER SUPPLY CONFIGURED SENSING SCHEME FOR FLASH EEPROM  | 5/25/1995  | 08/451,037 | 11/5/1996  | 5,572,465 | US      |
| NMXI-0210.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR ENDPOINT DETECTION IN A CHEMICAL/MECHANICAL PROCESS FOR POLISHING A SUBSTRATE | 11/30/1994 | 08/347,813 | 1/21/1997  | 5,595,526 | US      |
| NMXI-0211.00/US | Utility - ORG | Issued       | DECODING VARIABLE-LENGTH ENCODED SIGNALS   | 10/26/1994 | 08/329,340 | 12/31/1996 | 5,589,829 | US      |
| NMXI-0212.00/US | Utility - ORG | Issued       | ENCODING VIDEO SIGNALS USING SELECTIVE PRE FILTERING   | 10/31/1994 | 08/332,583 | 9/17/1996  | 5,557,330 | US      |
| NMXI-0213.00/US | Utility - ORG | Issued       | ENCODING VIDEO SIGNALS USING LOCAL QUANTIZATION LEVELS   | 10/18/1994 | 08/324,923 | 9/1/1998   | 5,802,213 | US      |
| NMXI-0214.00/US | Utility - CON | Issued       | LOW CURRENT REDUCED AREA PROGRAMMING VOLTAGE DETECTOR FOR FLASH MEMORY                                 | 2/27/1996  | 08/607,521 | 1/14/1997  | 5,594,360 | US      |
| NMXI-0215.00/US | Utility - CON | Issued       | LOW POWER PULSE GENERATOR FOR SMART VOLTAGE FLASH EEPROM   | 12/11/1996 | 08/764,666 | 9/23/1997  | 5,671,179 | US      |

Exhibit A - US Applications and Issued Patents

| WI. DctNo       | Matentype     | Matentatus | Title  | Filedte    | Serialno   | Issuedate  | Patentno  | Country |
|-----------------|---------------|------------|--|------------|------------|------------|-----------|---------|
| NMXI-0216.00/US | Utility - ORG | Issued     | SMART BATTERY PROVIDING BATTERY LIFE AND RECHARGE TIME PREDICTION  | 12/15/1994 | 08/356,905 | 10/15/1996 | 5,565,759 | US      |
| NMXI-0217.00/US | Utility - CON | Abandoned  | DEBUGGER FOR DEBUGGING TASKS IN AN OPERATING SYSTEM VIRTUAL DEVICE DRIVER                                      | 11/8/1996  | 08/748,551 | 3/30/1999  | 5,889,988 | US      |
| NMXI-0218.00/US | Utility - ORG | Issued     | METHOD AND APPARATUS FOR COMPENSATING FOR SUBTHRESHOLD CURRENT IN A STATIC RAM                                 | 3/14/1995  | 08/403,407 | 5/14/1996  | 5,517,452 | US      |
| NMXI-0219.00/US | Utility - ORG | Issued     | MEMORY UNIT WITH BIT LINE DISCHARGER   | 5/5/1995   | 08/437,090 | 5/6/1997   | 5,627,788 | US      |
| NMXI-0220.00/US | Utility - ORG | Issued     | A BRANCH PREDICTOR USING MULTIPLE PREDICTION HEURISTICS AND A HEURISTIC IDENTIFIER IN THE BRANCH INSTRUCTION   | 4/28/1995  | 08/431,016 | 11/11/1997 | 5,687,360 | US      |
| NMXI-0221.00/US | Utility - CON | Issued     | INTEGRATED CIRCUIT ASSEMBLY PREVENTING INTRUSIONS INTO ELECTRONIC CIRCUITRY                                    | 8/25/1997  | 08/917,988 | 6/2/1998   | 5,751,054 | US      |
| NMXI-0221.01/US | Utility - CON | Issued     | A METHOD TO PREVENT INTRUSIONS INTO ELECTRONIC CIRCUITRY   | 1/20/1998  | 09/008,968 | 8/27/2002  | 6,438,825 | US      |
| NMXI-0222.00/US | Utility - ORG | Issued     | PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES  | 3/13/1995  | 08/402,933 | 2/17/1998  | 5,719,436 | US      |
| NMXI-0222.01/US | Utility - DIV | Issued     | PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES  | 1/21/1997  | 08/781,358 | 8/11/1998  | 5,793,101 | US      |
| NMXI-0223.00/US | Utility - ORG | Issued     | METHOD AND APPARATUS FOR ACCOMMODATING DIFFERENT ISSUE WIDTH IMPLEMENTATIONS OF VLIW ARCHITECTURES             | 9/20/1995  | 08/530,076 | 12/8/1998  | 5,848,288 | US      |
| NMXI-0224.01/US | Utility - CON | Issued     | NEGATIVE VOLTAGE SWITCHING CIRCUIT   | 9/30/1996  | 08/723,666 | 12/23/1997 | 5,701,272 | US      |
| NMXI-0225.00/US | Utility - CIP | Issued     | USE OF RS1 INDICATION FOR IMPROVED DATA TRANSMISSION OVER AMPS NETWORK   | 3/31/1995  | 08/414,914 | 3/18/1997  | 5,612,991 | US      |
| NMXI-0225.01/US | Utility - CON | Issued     | USE OF RS1 INDICATION FOR IMPROVED DATA TRANSMISSION OVER AMPS NETWORK   | 3/19/1997  | 08/820,129 | 6/6/2000   | 6,073,030 | US      |
| NMXI-0226.00/US | Utility - ORG | Issued     | METHOD AND APPARATUS FOR ELIMINATING VISUAL ARTIFACTS CAUSED BY DIFFUSING ERRORS IN A DECMATED VIDEO SIGNAL    | 1/2/1996   | 08/582,200 | 7/7/1998   | 5,777,624 | US      |
| NMXI-0227.00/US | Utility - CON | Issued     | INTELLIGENT START FOR MOTION ESTIMATION SEARCH   | 5/12/1997  | 08/854,770 | 10/6/1998  | 5,818,969 | US      |
| NMXI-0228.00/US | Utility - ORG | Issued     | INSTRUCTION DEPENDENCY CHAIN IDENTIFIER  | 9/6/1995   | 08/524,065 | 1/20/1998  | 5,710,902 | US      |
| NMXI-0229.00/US | Utility - DIV | Issued     | METHOD AND APPARATUS FOR SCRUBBING THE BOND PADS OF AN INTEGRATED CIRCUIT DURING WAFER SORT                    | 6/13/1997  | 08/874,821 | 6/1/1999   | 5,909,121 | US      |
| NMXI-0229.01/US | Utility - DIV | Issued     | METHOD AND APPARATUS FOR CLEANING A TEST PROBE   | 6/13/1997  | 08/874,388 | 10/5/1999  | 5,961,728 | US      |
| NMXI-0230.00/US | Utility - ORG | Issued     | METHOD AND APPARATUS FOR IMPROVING POWER UP TIME OF FLASH EEPROM MEMORY ARRAYS                                 | 6/2/1995   | 08/458,805 | 6/17/1997  | 5,640,083 | US      |
| NMXI-0231.00/US | Utility - CON | Issued     | MEMORY ARRAYS WITH INTEGRATED BIT LINE VOLTAGE STABILIZATION CIRCUITRY   | 8/8/1997   | 08/907,754 | 12/1/1998  | 5,844,852 | US      |
| NMXI-0232.00/US | Utility - ORG | Issued     | APPARATUS AND METHOD FOR COMPUTATION OF STICKY BIT IN A MULTI-STAGE SHIFTER USED FOR FLOATING POINT ARITHMETIC | 6/28/1996  | 08/672,678 | 6/23/1998  | 5,771,183 | US      |
| NMXI-0233.00/US | Utility - ORG | Issued     | LOW POWER PROGRAMMABLE LOGIC ARRAYS  | 8/15/1995  | 08/515,248 | 11/19/1996 | 5,576,636 | US      |
| NMXI-0234.00/US | Utility - ORG | Abandoned  | BLOCKING HOST INPUT WHILE A REMOTE GUEST HAS INPUT CONTROL TO A HOST-EXECUTED SHARED APPLICATION               | 12/1/1995  | 08/565,953 | 12/21/1999 | 6,006,282 | US      |
| NMXI-0235.00/US | Utility - ORG | Issued     | NO HANDLE ZIP SOCKET   | 11/13/1995 | 08/558,032 | 2/11/1997  | 5,602,719 | US      |
| NMXI-0236.00/US | Utility - CON | Issued     | NEGATIVE VOLTAGE SWITCH ARCHITECTURE FOR A NONVOLATILE MEMORY  | 7/18/1997  | 08/895,613 | 11/2/1999  | 5,978,263 | US      |
| NMXI-0237.00/US | Utility - ORG | Issued     | METHODS AND APPARATUS FOR CORRELATING STUCK-AT FAULT TEST COVERAGE AND CURRENT LEAKAGE FAULT TEST COVERAGE     | 9/29/1995  | 08/536,112 | 1/27/1998  | 5,712,857 | US      |
| NMXI-0238.00/US | Utility - CON | Issued     | METHOD AND APPARATUS FOR CONNECTING A TELEPHONE TO A VOICE CAPABLE MODEM                                       | 4/9/1997   | 08/831,578 | 6/16/1998  | 5,768,351 | US      |
| NMXI-0239.00/US | Utility - ORG | Issued     | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS  | 12/20/1995 | 08/575,295 | 10/20/1998 | 5,824,571 | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Patent No.      | Matter Type   | Matter Status | Title  | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|------------|---------|
| NMX1-0240.00/US | Utility - ORG | Issued        | CIRCUIT FOR DETECTING WHEN A SUPPLY OUTPUT VOLTAGE EXCEEDS A PREDETERMINED LEVEL.  | 9/29/1995  | 08/536,853 | 2/11/1997  | 5,602,502  | US      |
| NMX1-0241.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS TO IMPROVE LATENCY EXPERIENCED BY AN AGENT UNDER A ROUND ROBIN ARBITRATION SCHEME   | 9/15/1995  | 08/528,914 | 6/17/1997  | 5,640,519  | US      |
| NMX1-0242.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR CONVERTING A FIVE WIRE ARBITRATION/BUFFER MANAGEMENT PROTOCOL INTO A TWO WIRE PROTOCOL  | 12/31/1996 | 08/775,807 | 6/29/1999  | 5,918,025  | US      |
| NMX1-0243.00/US | Utility - ORG | Issued        | PLA ARCHITECTURE HAVING IMPROVED CLOCK SIGNAL TO OUTPUT TIMING USING A TYPE-I DOMINO AND PLANE   | 12/27/1995 | 08/579,095 | 1/27/1998  | 5,712,578  | US      |
| NMX1-0244.00/US | Utility - ORG | Issued        | MULTIPLE INTAKE DUCT MICROPROCESSOR COOLING SYSTEM   | 12/27/1995 | 08/578,984 | 11/25/1997 | 5,691,883  | US      |
| NMX1-0245.00/US | Utility - ORG | Issued        | FAST FLOATING-POINT TO INTEGER CONVERSION  | 9/29/1995  | 08/537,260 | 6/9/1998   | 5,754,548  | US      |
| NMX1-0246.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM  | 7/1/1996   | 08/672,983 | 9/10/2002  | 6,449,735  | US      |
| NMX1-0247.00/US | Utility - ORG | Issued        | LOCKING POWER CABLE INTERFACE GUARD  | 12/11/1995 | 08/570,239 | 4/7/1998   | 5,735,701  | US      |
| NMX1-0248.00/US | Utility - ORG | Issued        | SYSTEM AND METHOD FOR PROGRAMMING PROGRAMMABLE ELECTRONIC COMPONENTS USING BOARD-LEVEL AUTOMATED TEST EQUIPMENT  | 11/3/1995  | 08/553,017 | 8/11/1998  | 5,794,007  | US      |
| NMX1-0249.00/US | Utility - ORG | Issued        | MULTIPLEXER HAVING A PLURALITY OF INTERNAL DATA PATHS THAT OPERATE AT DIFFERENT SPEEDS   | 9/27/1995  | 08/534,485 | 4/29/1997  | 5,625,303  | US      |
| NMX1-0250.00/US | Utility - ORG | Issued        | CLEANING STEP WHICH IMPROVES ELECTROMIGRATION PERFORMANCE OF INTERLAYER CONNECTION IN INTEGRATED CIRCUITS  | 3/29/1996  | 08/623,672 | 5/18/1999  | 5,904,560  | US      |
| NMX1-0252.00/US | Utility - ORG | Issued        | REORDER BUFFER WITH INTERLEAVING MECHANISM FOR ACCESSING A MULTI-PORTER CIRCULAR MEMORY ARRAY  | 12/27/1995 | 08/578,964 | 7/28/1998  | 5,787,454  | US      |
| NMX1-0253.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR PROTECTING DATA STORED IN FLASH MEMORY  | 12/29/1995 | 08/578,175 | 1/14/1997  | 5,594,685  | US      |
| NMX1-0254.00/US | Utility - CON | Issued        | RECESSED OR RAISED CHARACTERS ON A CERAMIC LID   | 4/7/1997   | 08/835,228 | 6/29/1999  | 5,917,239  | US      |
| NMX1-0255.00/US | Utility - ORG | Issued        | CONNECTOR WITH ATTACHABLE DAUGHTER CARD RETENTION SYSTEM   | 1/8/1996   | 08/584,330 | 7/21/1998  | 5,784,263  | US      |
| NMX1-0256.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR FAST SELF-DESTRUCTION OF A CMOS INTEGRATED CIRCUIT  | 12/29/1995 | 08/581,436 | 4/7/1998   | 5,736,777  | US      |
| NMX1-0257.00/US | Utility - ORG | Issued        | ANCHOR PROVISIONS TO PREVENT MOLD DELAMINATION IN AN OVERMOLDED PLASTIC ARRAY PACKAGE  | 4/3/1996   | 08/627,058 | 6/30/1998  | 5,773,895  | US      |
| NMX1-0258.00/US | Utility - ORG | Issued        | SLOW SPEED DRIVER WITH CONTROLLED SLEW RATE  | 6/28/1996  | 08/672,412 | 5/26/1998  | 5,757,217  | US      |
| NMX1-0259.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR TESTING A MEMORY WHERE DATA IS PASSED THROUGH THE MEMORY FOR COMPARISON WITH DATE READ FROM THE MEMORY                                    | 12/29/1995 | 08/576,705 | 1/7/1997   | 5,592,425  | US      |
| NMX1-0259.01/US | Utility - DIV | Issued        | MEMORY BYPASS MODE   | 9/10/1996  | 08/707,925 | 3/18/1997  | 5,612,916  | US      |
| NMX1-0260.00/US | Utility - ORG | Issued        | METHOD FOR VERIFYING CONTIGUITY OF A BINARY TRANSLATED BLOCK OF INSTRUCTIONS BY ATTACHING A COMPARE AND/OR BRANCH INSTRUCTION TO PREDECESSOR BLOCK OF INSTRUCTIONS | 8/7/1996   | 08/689,357 | 2/24/1998  | 5,721,927  | US      |
| NMX1-0261.00/US | Utility - ORG | Issued        | RANDOM NUMBER GENERATOR  | 5/15/1996  | 08/648,553 | 1/6/1998   | 5,706,218  | US      |
| NMX1-0262.00/US | Utility - ORG | Issued        | TABLE-DRIVEN BI-DIRECTIONAL MOTION ESTIMATION USING SCRATCH AREA AND OFFSET VALUES   | 3/7/1996   | 08/610,582 | 7/7/1998   | 5,778,097  | US      |
| NMX1-0263.00/US | Utility - ORG | Issued        | MULTIVOLTAGE KEYED ELECTRICAL CONNECTOR  | 6/28/1996  | 08/672,643 | 6/27/2000  | 6,080,022  | US      |
| NMX1-0264.00/US | Utility - ORG | Issued        | ETCH PROCESS TO PRODUCE ROUNDED TOP CORNERS FOR SUB MICRON SILICON TRENCH APPLICATIONS   | 12/31/1996 | 08/775,573 | 12/1/1998  | 5,843,845  | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Patent No.      | Matter Type   | Matter Status | Title  | Filing Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|--|-------------|------------|------------|------------|---------|
| NMXI-0265.00/US | Utility - ORG | Issued        | CIRCUIT BOARD RETENTION SYSTEM   | 6/18/1996   | 08/655,405 | 3/10/1998  | 5,726,855  | US      |
| NMXI-0266.00/US | Utility - ORG | Issued        | APPARATUS AND METHOD FOR CONTROLLING THE TEMPERATURE OF AN INTEGRATED CIRCUIT UNDER TEST   | 6/18/1996   | 08/668,045 | 12/8/1998  | 5,847,356  | US      |
| NMXI-0267.00/US | Utility - ORG | Issued        | CLADDING OF AN INTERCONNECT FOR IMPROVED ELECTROMIGRATION PERFORMANCE  | 6/28/1996   | 08/673,277 | 6/1/1999   | 5,909,635  | US      |
| NMXI-0268.00/US | Utility - CON | Issued        | MEMORY TEST MODE FOR WORLDLINE RESISTIVE DEFECTS   | 2/23/1998   | 09/030,522 | 7/4/2000   | 6,085,341  | US      |
| NMXI-0269.00/US | Utility - ORG | Issued        | A SYSTEM AND METHOD FOR ENSURING INTEGRITY OF AUDIO  | 10/25/1996  | 08/738,086 | 8/31/1999  | 5,946,396  | US      |
| NMXI-0270.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR TRACKING ERASE CYCLES UTILIZING ACTIVE AND INACTIVE WEAR BAR BLOCKS HAVING FIRST AND SECOND COUNT FIELDS                              | 12/20/1996  | 08/770,958 | 10/5/1999  | 5,963,970  | US      |
| NMXI-0271.00/US | Utility - ORG | Issued        | COMPARATOR UTILIZING REDUNDANCY  | 7/17/1996   | 08/682,108 | 3/9/1999   | 5,881,076  | US      |
| NMXI-0272.00/US | Utility - ORG | Issued        | METHOD FOR EXTRACTING A RESISTOR NETWORK FROM AN INTEGRATED CIRCUIT POLYGON LAYOUT   | 7/12/1996   | 08/679,197 | 8/24/1999  | 5,943,487  | US      |
| NMXI-0273.00/US | Utility - ORG | Abandoned     | LEAKAGE TRACKING DEVICE SAMPLE FOR IDDQ MEASUREMENT AND DEFECT RESOLUTION  | 9/27/1996   | 08/721,685 | 3/30/1999  | 5,889,409  | US      |
| NMXI-0274.00/US | Utility - ORG | Abandoned     | DELTA IDDQ TESTING   | 6/27/1996   | 08/670,545 | 3/30/1999  | 5,889,408  | US      |
| NMXI-0274.01/US | Utility - DIV | Issued        | METHOD TO PERFORM IDDQ TESTING IN THE PRESENCE OF HIGH BACKGROUND LEAKAGE CURRENT  | 9/29/1998   | 09/162,912 | 5/29/2001  | 6,239,605  | US      |
| NMXI-0274.02/US | Utility - DIV | Issued        | METHOD TO PERFORM IDDQ TESTING IN THE PRESENCE OF HIGH BACKGROUND LEAKAGE CURRENT  | 9/29/1998   | 09/163,592 | 5/29/2001  | 6,239,606  | US      |
| NMXI-0275.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR DETECTING THE VOLTAGE ON THE VCC PIN  | 9/30/1996   | 08/723,270 | 10/13/1998 | 5,822,246  | US      |
| NMXI-0276.00/US | Utility - ORG | Issued        | METHOD FOR IMPROVED DATA RETENTION IN A NONVOLATILE WRITABLE MEMORY BY SENSING AND REPROGRAMMING CELL VOLTAGE LEVELS   | 12/20/1996  | 08/771,445 | 11/10/1998 | 5,835,413  | US      |
| NMXI-0277.00/US | Utility - ORG | Issued        | A SENSE AMPLIFIER COMPRISING A PREAMPLIFIER AND A DIFFERENTIAL INPUT LATCH FOR FLASH MEMORIES  | 9/16/1997   | 08/931,247 | 10/26/1999 | 5,973,957  | US      |
| NMXI-0278.00/US | Utility - ORG | Issued        | NEURAL-FLASH ANALOG-TO-DIGITAL CONVERTER USING WEIGHTED CURRENT SIMULATION   | 2/26/1999   | 09/259,650 | 3/6/2001   | 6,198,421  | US      |
| NMXI-0279.00/US | Utility - ORG | Issued        | DATA SPECULATABLE PROCESSOR HAVING REPLAY ARCHITECTURE   | 11/13/1996  | 08/746,547 | 10/12/1999 | 5,966,544  | US      |
| NMXI-0280.00/US | Utility - ORG | Issued        | APPARATUS FOR PROTECTING GATE ELECTRODES OF TARGET TRANSISTORS IN THE GATE ARRAY FROM GATE CHARGING BY EMPLOYING FREE TRANSISTORS IN THE GATE ARRAY            | 6/28/1996   | 08/672,411 | 8/11/1998  | 5,793,069  | US      |
| NMXI-0280.01/US | Utility - DIV | Issued        | METHOD AND APPARATUS FOR PROTECTING GATE ELECTRODES OF TARGET TRANSISTORS IN THE GATE ARRAY FROM GATE CHARGING BY EMPLOYING FREE TRANSISTORS IN THE GATE ARRAY | 2/17/1998   | 09/024,830 | 9/5/2000   | 6,113,648  | US      |
| NMXI-0281.00/US | Utility - ORG | Issued        | LATCHING MECHANISM FOR PULSED DOMINO LOGIC WITH INHERENT RACE MARGIN AND TIME BORROWING  | 8/12/1996   | 08/700,613 | 8/18/1998  | 5,796,282  | US      |
| NMXI-0282.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN  | 12/31/1996  | 08/775,796 | 4/27/1999  | 5,898,610  | US      |
| NMXI-0283.00/US | Utility - ORG | Issued        | ORGANIC SUBSTRATE (PCB) SLIP PLANE "STRESS DEFLECTOR" FOR FLIP CHIP DEVICES  | 9/26/1996   | 08/721,268 | 9/8/1998   | 5,804,771  | US      |
| NMXI-0283.01/US | Utility - DIV | Issued        | ORGANIC SUBSTRATE (PCB) SLIP PLANE "STRESS DEFLECTOR" FOR FLIP CHIP DEVICES  | 2/27/1998   | 09/032,628 | 1/16/2001  | 6,173,489  | US      |
| NMXI-0284.00/US | Utility - ORG | Issued        | PROCESS FOR FORMING AN INTEGRATED STRUCTURE COMPRISING A SELF-ALIGNED VIA/CONTACT AND INTERCONNECT   | 10/31/1996  | 08/739,879 | 3/30/1999  | 5,888,897  | US      |
| NMXI-0285.00/US | Utility - ORG | Issued        | CIRCUIT BOARD RETENTION SYSTEM   | 8/16/1996   | 08/689,698 | 6/24/1997  | 5,642,263  | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| Patent No.       | Material       | Material  | Material | Title  | Filed      | Serial No. | Issue Date | Patent No. | Country |
|------------------|----------------|-----------|----------|--|------------|------------|------------|------------|---------|
| NMXXI-0286.00/US | Utility - ORG  | Issued    |          | METHOD AND APPARATUS FOR THE NON-INVASIVE TESTING OF PRINTED CIRCUIT BOARD ASSEMBLIES  | 9/19/1996  | 08/715,857 | 9/1/1998   | 5,802,074  | US      |
| NMXXI-0287.00/US | Utility - ORG  | Issued    |          | METHOD AND APPARATUS FOR DYNAMIC LOCATION AND CONTROL OF PROCESSOR RESOURCES TO INCREASE RESOLUTION OF DATA DEPENDENCY STALLS                                  | 1/8/1997   | 08/780,675 | 6/29/1999  | 5,918,033  | US      |
| NMXXI-0288.00/US | Utility - ORG  | Issued    |          | PACKAGING MULTIPLE DIES ON A BALL GRID ARRAY SUBSTRATE   | 3/25/1997  | 08/822,743 | 9/29/1998  | 5,815,372  | US      |
| NMXXI-0289.00/US | Utility - ORG  | Issued    |          | KEY MANAGEMENT SYSTEM FOR DVD COPYRIGHT MANAGEMENT   | 11/5/1996  | 08/740,976 | 6/22/1999  | 5,915,018  | US      |
| NMXXI-0290.00/US | Utility - ORG  | Issued    |          | METHOD AND APPARATUS FOR GUIDED TOURING OF INTERNET/INTRANET WEBSITES  | 7/22/1996  | 08/685,952 | 9/15/1998  | 5,809,247  | US      |
| NMXXI-0291.00/US | Utility - ORG  | Issued    |          | INTEGRATED CIRCUIT PACKAGE   | 9/9/1996   | 08/708,857 | 2/29/2000  | 6,031,283  | US      |
| NMXXI-0292.00/US | Utility - ORG  | Issued    |          | METHOD AND APPARATUS FOR MOUNTING A VERY LARGE SCALE INTEGRATION (VLSI) CHIP TO A COMPUTER CHASSIS FOR COOLING   | 12/31/1996 | 08/777,604 | 11/2/1999  | 5,978,228  | US      |
| NMXXI-0293.00/US | Utility - ORG  | Issued    |          | METHOD FOR PERFORMING COMMON SUBEXPRESSION ELIMINATION ON A RACK-N-STATIC SINGLE ASSIGNMENT LANGUAGE   | 4/1/1997   | 08/829,856 | 12/14/1999 | 6,002,879  | US      |
| NMXXI-0294.00/US | Utility - ORG  | Issued    |          | METHOD FOR USING STATIC SINGLE ASSIGNMENT TO COLOR OUT ARTIFICIAL REGISTER DEPENDENCIES  | 4/1/1997   | 08/831,739 | 1/18/2000  | 6,016,398  | US      |
| NMXXI-0295.00/US | Utility - ORG  | Issued    |          | PICKUP CHUCK WITH AN INTEGRAL HEATSINK   | 12/30/1997 | 09/000,981 | 2/1/2000   | 6,019,165  | US      |
| NMXXI-0296.00/US | Utility - ORG  | Issued    |          | PRODUCTION PRINTED CIRCUIT BOARD (PCB) EDGE CONNECTOR TEST CONNECTOR   | 12/18/1996 | 08/768,961 | 8/18/1998  | 5,795,172  | US      |
| NMXXI-0297.00/US | Utility - ORG  | Abandoned |          | METHOD AND APPARATUS FOR PERFORMING BLOCK BASED FREQUENCY DOMAIN FILTERING   | 12/19/1996 | 08/772,016 |            |            | US      |
| NMXXI-0298.00/US | Utility - CIP  | Issued    |          | METHOD AND APPARATUS FOR VERIFYING THE INSTALLATION OF STRAPPING DEVICES ON A CIRCUIT BOARD ASSEMBLY   | 1/2/1997   | 08/778,305 | 8/18/1998  | 5,796,639  | US      |
| NMXXI-0299.00/US | Utility - ORG  | Issued    |          | METHOD AND APPARATUS FOR FACILITATING DETECTION OF SOLDER OPENS OF SMT COMPONENTS  | 10/30/1996 | 08/741,053 | 10/12/1999 | 5,966,020  | US      |
| NMXXI-0300.00/US | Utility - ORG  | Issued    |          | METHOD FOR CALIBRATING A DIGITAL CAMERA TO A PC MONITOR TO ENHANCE PICTURE QUALITY OF A PICTURE CAPTURED BY THE DIGITAL CAMERA AND DISPLAYED ON THE PC MONITOR | 1/23/1997  | 08/787,865 | 6/29/1999  | 5,918,192  | US      |
| NMXXI-0301.00/US | Utility - ORG  | Issued    |          | AN OUT-OF-ORDER SUPERSCALAR MICROPROCESSOR WITH A RENAMING DEVICE THAT MAPS INSTRUCTIONS FROM MEMORY TO REGISTERS  | 12/30/1996 | 08/774,659 | 11/17/1998 | 5,838,941  | US      |
| NMXXI-0301.01/US | Utility - CON  | Abandoned |          | ADVANCED REGISTER RENAMING SYSTEM FOR IMPROVED INSTRUCTION LEVEL PARALLELISM   | 7/24/1998  | 09/122,270 |            |            | US      |
| NMXXI-0302.00/US | Utility - ORG  | Abandoned |          | METHOD AND APPARATUS FOR SCALING IMAGE DATA  | 2/11/1997  | 08/799,654 |            |            | US      |
| NMXXI-0303.00/US | Utility - ORG  | Issued    |          | METHOD AND APPARATUS FOR CONTROLLING A CHARGE PUMP FOR RAPID INITIALIZATION  | 12/17/1996 | 08/768,297 | 3/9/1999   | 5,880,622  | US      |
| NMXXI-0304.00/US | Utility - ORG  | Issued    |          | APPARATUS FOR PERFORMING A USER REQUESTED RESET DURING ALGORITHM EXECUTION   | 12/31/1996 | 08/778,015 | 4/25/2000  | 6,055,614  | US      |
| NMXXI-0305.00/RE | Utility - REIS | Issued    |          | BLOCK LOCKING APPARATUS FOR FLASH MEMORY   | 2/3/1997   | 10/094,056 | 3/7/2000   | RE42,551   | US      |
| NMXXI-0305.00/US | Utility - ORG  | Issued    |          | BLOCK LOCKING APPARATUS FOR FLASH MEMORY   | 2/3/1997   | 08/794,283 | 3/7/2000   | 6,035,401  | US      |
| NMXXI-0306.00/US | Utility - ORG  | Issued    |          | METHOD AND APPARATUS FOR DISTRIBUTION OF BROADCAST DATA  | 12/20/1996 | 08/771,709 | 3/30/1999  | 5,889,950  | US      |
| NMXXI-0307.00/US | Utility - ORG  | Issued    |          | DYNAMIC COMMUNICATION PATH SELECTION FOR DATA TRANSMISSION BETWEEN COMPUTERS   | 4/28/1997  | 08/848,554 | 8/15/2000  | 6,104,720  | US      |
| NMXXI-0308.00/US | Utility - ORG  | Issued    |          | FIIDUCIAL FOR ALIGNING AN INTEGRATED CIRCUIT DIE   | 12/20/1996 | 08/771,275 | 8/24/1999  | 5,942,805  | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| MTI DefNo       | MatterType    | MatterStatus | Title   | Filedate   | SerialNo   | IssueDate  | PatentNo  | Country |
|-----------------|---------------|--------------|---|------------|------------|------------|-----------|---------|
| NMXI-0808.00/US | Utility - DIV | Issued       | A METHOD OF FORMING A FIDUCIAL FOR ALIGNING AN INTEGRATED CIRCUIT DIE   | 11/26/1997 | 08/978,535 | 12/14/1999 | 6,001,703 | US      |
| NMXI-0809.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR DELIVERING DATA  | 12/31/1996 | 08/801,508 | 11/16/1999 | 5,987,504 | US      |
| NMXI-0810.00/US | Utility - ORG | Issued       | DYNAMICALLY CONTROLLING THE NUMBER OF BOUNDARY-SCAN CELLS IN A BOUNDARY-SCAN PATH   | 5/22/1997  | 08/862,006 | 6/16/1998  | 5,758,289 | US      |
| NMXI-0811.00/US | Utility - ORG | Issued       | SELF-CONTAINING INPUT BUFFER ON FLASH MEMORIES  | 4/11/1997  | 08/834,029 | 9/30/2003  | 6,628,552 | US      |
| NMXI-0812.00/US | Utility - ORG | Issued       | SELF-CONFIGURING OUTPUT BUFFER ON FLASH MEMORIES  | 4/11/1997  | 08/834,032 | 5/11/1999  | 5,903,500 | US      |
| NMXI-0813.00/US | Utility - ORG | Issued       | SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES   | 4/11/1997  | 08/834,026 | 8/3/1999   | 5,933,026 | US      |
| NMXI-0814.00/US | Utility - ORG | Issued       | VARIABLE LOADING APPARATUS FOR OUTPUT LOADING OF INTEGRATED CIRCUITS  | 6/13/1997  | 08/874,496 | 5/4/1999   | 5,900,770 | US      |
| NMXI-0815.00/US | Utility - ORG | Issued       | COMPUTER PACKAGE WITH A POLYGONAL SHAPED MOTHERBOARD  | 9/19/1997  | 08/933,405 | 5/11/1999  | 5,903,432 | US      |
| NMXI-0816.00/US | Utility - ORG | Issued       | MERGING DUMMY STRUCTURE REPRESENTATIONS FOR IMPROVED DISTRIBUTION OF ARTIFACTS IN A SEMICONDUCTOR LAYER                             | 9/30/1997  | 08/944,599 | 6/27/2000  | 6,084,272 | US      |
| NMXI-0817.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR CONNECTING EXPANSION BUSES TO A PERIPHERAL COMPONENT INTERCONNECT BUS                                      | 1/2/1997   | 08/778,192 | 9/14/1999  | 5,951,667 | US      |
| NMXI-0818.00/US | Utility - ORG | Issued       | EFFICIENT SELF-TIMED MARKING OF LENGTHLY VARIABLE LENGTH INSTRUCTIONS   | 12/23/1997 | 08/997,461 | 8/24/1999  | 5,941,982 | US      |
| NMXI-0819.00/US | Utility - ORG | Issued       | APPARATUS AND METHOD FOR SELF-TIMED MARKING OF VARIABLE LENGTH INSTRUCTIONS HAVING LENGTH-AFFECTING PREFIX BYTES                    | 12/23/1997 | 08/997,462 | 9/7/1999   | 5,948,096 | US      |
| NMXI-0820.00/US | Utility - ORG | Issued       | BRANCH INSTRUCTION HANDLING IN A SELF-TIMED MARKING SYSTEM  | 12/23/1997 | 08/996,756 | 8/3/1999   | 5,931,944 | US      |
| NMXI-0821.00/US | Utility - ORG | Issued       | EMPLOYING A LOOK-UP SERVICE AND A CALLEE CONNECTION SERVICE TO ESTABLISH A NETWORK PHONE CALL BETWEEN A CALLER AND A CALLEE         | 3/31/1997  | 08/829,608 | 2/20/2001  | 6,192,044 | US      |
| NMXI-0822.00/US | Utility - ORG | Issued       | ANTI-THEFT MECHANISM FOR MOBILE COMPUTERS   | 9/9/1997   | 08/927,370 | 11/21/2000 | 6,151,678 | US      |
| NMXI-0823.00/US | Utility - ORG | Issued       | PERIPHERAL DEVICE PREVENTING POST-SCAN MODIFICATION   | 3/31/1997  | 08/829,594 | 7/20/1999  | 5,926,550 | US      |
| NMXI-0824.00/US | Utility - ORG | Issued       | UNIQUE CHEMICAL MECHANICAL PLANARIZATION APPROACH WHICH UTILIZES MAGNETIC SLURRY FOR POLISH AND MAGNETIC FIELDS FOR PROCESS CONTROL | 12/31/1997 | 09/001,509 | 7/4/2000   | 6,083,839 | US      |
| NMXI-0824.01/US | Utility - DIV | Issued       | UNIQUE CHEMICAL MECHANICAL PLANARIZATION APPROACH WHICH UTILIZES MAGNETIC SLURRY FOR POLISH AND MAGNETIC FIELDS FOR PROCESS CONTROL | 7/13/1999  | 09/352,634 | 9/4/2001   | 6,284,091 | US      |
| NMXI-0825.00/US | Utility - ORG | Issued       | MULTI-LAYER CODER/DECODER FOR PRODUCING QUANTIZATION ERROR SIGNAL SAMPLES   | 11/5/1997  | 08/965,637 | 9/21/2004  | 6,795,501 | US      |
| NMXI-0826.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR FINDING LOOP LEVER PARALLELISM IN A POINTER BASED APPLICATION  | 1/13/1998  | 09/006,276 | 8/7/2001   | 6,272,676 | US      |
| NMXI-0827.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR USING FUNCTION CONTEXT TO IMPROVE BRANCH PREDICTION  | 9/22/1997  | 08/934,864 | 10/12/1999 | 5,964,870 | US      |
| NMXI-0828.00/US | Utility - ORG | Issued       | FAST BICMOS ACTIVE-PIXEL SENSOR CELL WITH FAST NPN EMITTER-FOLLOWER READOUT   | 1/6/1998   | 09/003,477 | 10/2/2001  | 6,297,492 | US      |
| NMXI-0829.00/US | Utility - CIP | Issued       | STATIC, HIGH-SENSITIVITY, FUSE-BASED STORAGE CELL   | 12/31/1997 | 09/002,174 | 9/28/1999  | 5,959,445 | US      |
| NMXI-0830.00/US | Utility - CON | Issued       | CIRCUIT AND METHOD FOR CONTROLLING POWER AND PERFORMANCE BASED ON OPERATING ENVIRONMENT   | 5/7/1999   | 09/307,324 | 7/29/2003  | 6,601,179 | US      |

11/21/2011

## Title

| MTI-DETNO        | MasterType    | MasterState | Title   | FiledDate  | SerialNo   | IssuedDate | PatentNo  | Country |
|------------------|---------------|-------------|---|------------|------------|------------|-----------|---------|
| NMXXI-Q331.00/US | Utility - ORG | Abandoned   | METHOD AND APPARATUS FOR SEARCHING A MASS STORAGE MEDIUM  | 11/6/1997  | 08/965,592 |            |           | US      |
| NMXXI-Q332.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR AUTOMATICALLY DETERMINING AND DYNAMICALLY UPDATING USER PREFERENCES IN AN ENTERTAINMENT SYSTEM   | 5/30/1997  | 08/866,707 | 8/31/1999  | 5,945,988 | US      |
| NMXXI-Q333.00/US | Utility - ORG | Issued      | PLGA TEST HANDLER THROW MEASURING UNIT  | 2/9/1998   | 09/020,661 | 9/7/1999   | 5,948,960 | US      |
| NMXXI-Q334.00/US | Utility - ORG | Issued      | TECHNIQUE FOR PERFORMING DMA INCLUDING ARBITRATION BETWEEN A CHAINED LOW PRIORITY DMA AND HIGH PRIORITY DMA OCCURRING BETWEEN TWO LINKS IN THE CHAINED LOW PRIORITY | 2/10/1998  | 09/021,688 | 8/22/2000  | 6,108,743 | US      |
| NMXXI-Q335.00/US | Utility - ORG | Issued      | THERMAL INTERFACE ATTACH MECHANISM FOR ELECTRICAL PACKAGES  | 12/15/1997 | 08/990,856 | 10/12/1999 | 5,965,997 | US      |
| NMXXI-Q336.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS EMPLOYING A DYNAMIC ENCRYPTION INTERFACE BETWEEN A PROCESSOR AND A MEMORY  | 9/29/1997  | 08/937,874 | 11/16/1999 | 5,987,572 | US      |
| NMXXI-Q337.00/US | Utility - ORG | Issued      | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD  | 1/14/1998  | 09/006,649 | 11/30/1999 | 5,994,939 | US      |
| NMXXI-Q338.00/US | Utility - ORG | Issued      | ADVANCED NEAR IDEAL MIXER   | 12/4/1998  | 09/205,992 | 2/6/2001   | 6,184,799 | US      |
| NMXXI-Q339.00/US | Utility - ORG | Issued      | COMPUTER SYSTEM INCLUDING RIGHT ANGLE PROCESSOR AND ADD-ON CARD CONNECTORS  | 9/24/1997  | 08/936,857 | 11/2/1999  | 5,978,873 | US      |
| NMXXI-Q340.00/US | Utility - ORG | Issued      | SUPPLYING STANDBY VOLTAGE TO MEMORY AND WAKEUP CIRCUITRY TO WAKE A COMPUTER FROM A LOW POWER MODE   | 12/30/1997 | 09/001,102 | 10/23/2001 | 6,308,278 | US      |
| NMXXI-Q342.00/US | Utility - ORG | Issued      | SUPPLYING STANDBY VOLTAGE TO MEMORY AND WAKEUP CIRCUITRY TO WAKE A COMPUTER FROM A LOW POWER MODE   | 1/15/1999  | 09/231,725 | 6/13/2000  | 6,075,751 | US      |
| NMXXI-Q343.00/US | Utility - ORG | Issued      | LOW VOLTAGE PROGRAMMABLE COMPLEMENTARY INPUT STAGE SENSE AMP/BUFER  | 1/14/1998  | 09/006,770 | 4/18/2000  | 6,051,999 | US      |
| NMXXI-Q344.00/US | Utility - ORG | Issued      | SYSTEM FOR SPECULATIVE BRANCH TARGET PREDICTION HAVING A DYNAMIC PREDICTION HISTORY BUFER AND A STATIC PREDICTION HISTORY BUFER                                     | 11/26/1997 | 08/979,579 | 11/2/1999  | 5,978,909 | US      |
| NMXXI-Q345.00/US | Utility - ORG | Issued      | CIRCUIT AND METHOD FOR CONFIGURING AND REGISTERING A CRYPTOGRAPHIC DEVICE   | 9/30/1997  | 08/938,491 | 10/19/1999 | 5,970,147 | US      |
| NMXXI-Q346.00/US | Utility - ORG | Issued      | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE   | 12/4/1997  | 08/984,766 | 2/2/1999   | 5,867,367 | US      |
| NMXXI-Q346.00/US | Utility - CON | Issued      | AD FLAT PACK INTEGRATED CIRCUIT PACKAGE   | 10/26/1998 | 09/179,016 | 9/7/1999   | 5,949,651 | US      |
| NMXXI-Q346.02/US | Utility - DIV | Issued      | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE   | 10/26/1998 | 09/179,125 | 11/2/1999  | 5,978,224 | US      |
| NMXXI-Q346.03/US | Utility - DIV | Issued      | QUAD FLAT PACK INTEGRATED CIRCUIT PACKAGE   | 10/26/1998 | 09/179,014 | 7/3/2001   | 6,255,135 | US      |
| NMXXI-Q347.00/US | Utility - ORG | Abandoned   | METHOD AND APPARATUS FOR CHANGING THE FONT USED TO DISPLAY TEXT ENTERED INTO AN INFORMATION INPUT FIELD   | 12/30/1997 | 09/000,712 |            |           | US      |
| NMXXI-Q348.00/US | Utility - DIV | Issued      | EFFICIENT COMPANDING ALGORITHM SUITABLE FOR COLOR IMAGING   | 1/13/2000  | 09/482,551 | 2/26/2002  | 6,351,555 | US      |
| NMXXI-Q349.00/US | Utility - ORG | Issued      | A CHARGE PUMP AVOIDING GAIN DEGRADATION DUE TO THE BODY EFFECT  | 1/12/1998  | 09/005,560 | 5/15/2001  | 6,232,826 | US      |
| NMXXI-Q350.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR REDUCING THE POWER CONSUMPTION OF A VOLTAGE REGULATOR  | 9/3/1998   | 09/148,033 | 10/23/2001 | 6,307,355 | US      |
| NMXXI-Q351.00/US | Utility - ORG | Issued      | OVERLAY COUNTER FOR ACCELERATED GRAPHICS PORT   | 1/30/1998  | 09/016,790 | 3/6/2001   | 6,199,149 | US      |
| NMXXI-Q352.00/US | Utility - ORG | Abandoned   | PORTABLE DIGITAL PHOTOGRAPH VIEWER  | 1/7/1998   | 09/004,052 |            |           | US      |
| NMXXI-Q352.00/US | Utility - DIV | Abandoned   | PORTABLE DIGITAL PHOTOGRAPH VIEWER  | 12/19/2002 | 10/324,692 |            |           | US      |
| NMXXI-Q353.00/US | Utility - ORG | Issued      | PUMP SUPPLY SELF REGULATION FOR FLASH MEMORY CELL PAIR REFERENCE CIRCUIT  | 3/16/1998  | 09/040,523 | 8/31/1999  | 5,946,258 | US      |

Exhibit A - US Applications and Issued Patents

| Mini-Donno      | Matter Type   | Matter Status | Title  | File Date  | Serial No  | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|-----------|---------|
| NMXI-0354.00/US | Utility - ORG | Issued        | HOT SHEAR APPARATUS AND METHOD FOR REMOVING A SEMICONDUCTOR CHIP FROM AN EXISTING PACKAGE  | 12/30/1997 | 09/001,021 | 2/29/2000  | 6,020,730 | US      |
| NMXI-0355.00/US | Utility - ORG | Issued        | ELECTRONIC CARTRIDGE WHICH ALLOWS DIFFERENTIAL THERMAL EXPANSION BETWEEN COMPONENTS OF THE CARTRIDGE   | 2/17/1998  | 09/024,860 | 4/13/1999  | 5,894,408 | US      |
| NMXI-0356.00/US | Utility - ORG | Issued        | INTERGRADED PROCESSOR SUBSTRATE PROTECTIVE ENCLOSURE FOR USE IN DESKTOP PC SYSTEMS   | 2/17/1998  | 09/024,921 | 7/4/2000   | 6,084,773 | US      |
| NMXI-0357.00/US | Utility - ORG | Issued        | RETENTION MECHANISM ASSEMBLY FOR PROCESSOR CARTRIDGES WITH CAPTURED SCREW FASTENERS  | 6/16/1998  | 09/098,086 | 3/27/2001  | 6,208,527 | US      |
| NMXI-0358.00/US | Utility - ORG | Issued        | FAN DUCT MODULE  | 1/29/1998  | 09/015,423 | 10/10/2000 | 6,130,819 | US      |
| NMXI-0359.00/US | Utility - ORG | Issued        | REMOVELY MONITORING EXECUTION OF A PROGRAM   | 4/29/1998  | 09/069,527 | 7/25/2000  | 6,094,530 | US      |
| NMXI-0360.00/US | Utility - ORG | Issued        | CHASSIS FOR ELECTRONIC COMPONENTS  | 5/28/1998  | 09/085,779 | 10/12/1999 | 5,964,513 | US      |
| NMXI-0360.01/US | Utility - DIV | Issued        | CHASSIS FOR ELECTRONIC COMPONENTS  | 6/10/1999  | 09/329,425 | 2/15/2000  | 6,024,425 | US      |
| NMXI-0360.02/US | Utility - DIV | Issued        | CHASSIS FOR ELECTRONIC COMPONENTS  | 6/10/1999  | 09/329,426 | 2/22/2000  | 6,027,191 | US      |
| NMXI-0361.00/US | Utility - ORG | Issued        | CODEC WITH GENETIC ADAPTATION  | 4/30/1998  | 09/071,049 | 3/25/2003  | 6,539,366 | US      |
| NMXI-0362.00/US | Utility - ORG | Issued        | SELECTING DESIGN POINTS ON PARAMETER FUNCTIONS HAVING FIRST SUM OF CONSTRAINT SET AND SECOND SUM OF OPTIMIZING SET TO IMPROVE SECOND SUM WITHIN DESIGN CONSTRAINTS | 9/4/1998   | 09/148,392 | 3/18/2008  | 7,346,479 | US      |
| NMXI-0363.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR UPDATING A TIMER FROM MULTIPLE TIMING DOMAINS   | 6/26/1998  | 09/105,479 | 2/1/2000   | 6,021,505 | US      |
| NMXI-0364.00/US | Utility - ORG | Issued        | AUTOMATIC TRANSFER OF IMAGE INFORMATION BETWEEN IMAGING DEVICE AND HOST SYSTEM   | 1/7/1998   | 09/003,732 | 7/3/2001   | 6,256,059 | US      |
| NMXI-0365.00/US | Utility - ORG | Issued        | SILICON WAFER TESTING RIG AND A METHOD FOR TESTING A SILICON WAFER WHEREIN THE SILICON WAFER IS BENT INTO A DOME SHAPE   | 5/29/1998  | 09/087,753 | 8/8/2000   | 6,100,709 | US      |
| NMXI-0366.00/US | Utility - ORG | Issued        | THIN OXIDES OF SILICON   | 7/29/1998  | 09/124,562 | 4/24/2001  | 6,221,789 | US      |
| NMXI-0367.00/US | Utility - ORG | Issued        | COMPUTER PROCESSOR WITH A REPLAY SYSTEM  | 6/30/1998  | 09/106,857 | 12/19/2000 | 6,163,838 | US      |
| NMXI-0368.00/US | Utility - ORG | Issued        | INTERFACE TO A MEMORY SYSTEM FOR A PROCESSOR HAVING A REPLAY SYSTEM  | 12/30/1999 | 09/475,029 | 12/16/2003 | 6,665,792 | US      |
| NMXI-0368.01/US | Utility - CON | Issued        | INTERFACE TO A MEMORY SYSTEM FOR A PROCESSOR HAVING A REPLAY SYSTEM  | 10/23/2003 | 10/690,634 | 8/8/2006   | 7,089,409 | US      |
| NMXI-0369.00/US | Utility - ORG | Issued        | HIGH SPEED ANALOG COMPENSATED INPUT BUFFER   | 1/23/1998  | 09/012,201 | 11/7/2000  | 6,144,218 | US      |
| NMXI-0370.00/US | Utility - ORG | Abandoned     | POSITION SENSITIVE DISPLAY CONTROLLER  | 5/14/1998  | 09/079,065 |            |           | US      |
| NMXI-0371.00/US | Utility - ORG | Issued        | PARALLEL ACCESS TESTING OF A MEMORY ARRAY  | 5/28/1998  | 09/086,330 | 11/9/1999  | 5,982,684 | US      |
| NMXI-0372.00/US | Utility - ORG | Issued        | BUS PROTOCOL FOR ATOMIC TRANSACTIONS   | 1/26/1998  | 09/013,774 | 11/16/1999 | 5,987,552 | US      |
| NMXI-0373.00/US | Utility - CON | Abandoned     | METHOD AND SYSTEM FOR BRANCH TARGET PREDICTION USING PATH INFORMATION  | 3/31/2003  | 10/404,384 |            |           | US      |
| NMXI-0374.00/US | Utility - ORG | Issued        | UP-SAMPLING DECMATED COLOR PLANE DATA  | 12/7/1999  | 09/457,058 | 6/17/2003  | 6,580,837 | US      |
| NMXI-0375.00/US | Utility - CIP | Issued        | VOLTAGE BLOCKING METHOD AND APPARATUS FOR A CHARGE PUMP WITH DIODE CONNECTED PULL-UP AND PULL-DOWN ON BOOT NODES   | 12/30/1999 | 09/475,459 | 4/16/2002  | 6,373,324 | US      |
| NMXI-0376.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE   | 5/7/1998   | 09/074,556 | 2/20/2001  | 6,192,079 | US      |
| NMXI-0378.00/US | Utility - CON | Issued        | APPARATUS AND METHOD FOR IMAGER DEVICE COLOR CALIBRATION USING SPECTRAL LIGHT SOURCE   | 11/18/2005 | 11/283,375 | 6/3/2008   | 7,382,918 | US      |
| NMXI-0379.00/US | Utility - ORG | Issued        | MEMORY CARD WHICH IS THERMALLY CONTROLLED  | 1/5/1999   | 09/225,927 | 12/19/2000 | 6,163,474 | US      |
| NMXI-0380.00/US | Utility - CON | Abandoned     | INSTRUCTION PIPELINE INSTRUCTION QUEUE FOR INSTRUCTION REISSUE   | 6/19/2003  | 10/601,172 |            |           | US      |
| NMXI-0381.00/US | Utility - ORG | Issued        | CART FOR TRANSFERRING OBJECTS  | 6/26/1998  | 09/105,191 | 8/15/2000  | 6,102,647 | US      |

11/21/2011



Exhibit A - US Applications and Issued Patents

| Pat. No.        | Material      | Material Status | Title   | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|-----------------|---|------------|------------|------------|------------|---------|
| NMXI-0409.00/US | Utility - ORG | Issued          | FLIP-FLOP CIRCUIT METHOD AND APPARATUS FOR PERFORMING DISTRIBUTED SIMULATION UTILIZING A SIMULATION BACKPLANE   | 6/28/1999  | 09/240,417 | 1/30/2001  | 6,181,180  | US      |
| NMXI-0410.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS TO SELECTIVELY ADVANCE A WRITE POINTER FOR A QUEUE BASED ON THE INDICATED VALIDITY OR INVALIDITY OF AN INSTRUCTION STORED WITHIN THE QUEUE | 12/22/1999 | 09/470,875 | 1/15/2008  | 7,319,947  | US      |
| NMXI-0411.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR PROVIDING REDUNDANCY IN NON-VOLATILE MEMORY DEVICES  | 3/30/2000  | 09/539,734 | 12/12/2006 | 7,149,883  | US      |
| NMXI-0412.00/US | Utility - ORG | Issued          | METHOD FOR MOTION COMPENSATED FRAME RATE UPSAMPLING BASED ON PIECEWISE AFFINE WARPING   | 5/6/1999   | 09/306,322 | 6/6/2000   | 6,072,723  | US      |
| NMXI-0413.00/US | Utility - ORG | Issued          | FLOURINE DOPED SiO2 FILM AND METHOD OF FABRICATION  | 4/21/1999  | 09/296,451 | 8/20/2002  | 6,438,275  | US      |
| NMXI-0414.00/US | Utility - DIV | Abandoned       | PARTIAL UNDERFILL FOR FLIP-CHIP ELECTRONIC PACKAGES   | 3/24/2003  | 10/396,659 | 3/2/2004   | 6,700,209  | US      |
| NMXI-0415.00/US | Utility - ORG | Issued          | PARTIAL UNDERFILL FOR FLIP-CHIP ELECTRONIC PACKAGES   | 12/29/1999 | 09/474,746 | 4/2/2002   | 6,365,441  | US      |
| NMXI-0415.00/US | Utility - DIV | Issued          | CHARGE PUMP WITH GATED PUMPED OUTPUT DIODE AT INTERMEDIATE STAGE  | 10/23/2000 | 09/694,802 | 11/21/2000 | 6,451,229  | US      |
| NMXI-0416.00/US | Utility - ORG | Issued          | GATE ENHANCEMENT CHARGE PUMP FOR LOW VOLTAGE POWER SUPPLY   | 6/30/1999  | 09/346,483 | 9/18/2001  | 6,292,048  | US      |
| NMXI-0417.00/US | Utility - ORG | Issued          | MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT  | 11/11/1999 | 09/438,186 | 11/11/2003 | 6,646,991  | US      |
| NMXI-0418.00/US | Utility - ORG | Issued          | MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT  | 12/22/1999 | 09/470,189 | 2/3/2004   | 6,687,840  | US      |
| NMXI-0418.00/US | Utility - ORG | Issued          | MULTI-LINK EXTENSIONS AND BUNDLE SKEW MANAGEMENT  | 4/21/2000  | 09/557,099 | 7/1/2008   | 7,394,766  | US      |
| NMXI-0418.00/US | Utility - CON | Issued          | BUNDLE SKEW MANAGEMENT AND CELL SYNCHRONIZATION   | 9/2/2003   | 10/554,252 | 4/17/2007  | 7,206,955  | US      |
| NMXI-0419.00/US | Utility - CON | Issued          | OPTICALLY SWITCHABLE INFRARED DETECTOR  | 12/23/2003 | 10/745,903 | 4/20/2004  | 6,724,442  | US      |
| NMXI-0419.00/US | Utility - ORG | Issued          | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS  | 3/26/1999  | 09/277,637 |            |            | US      |
| NMXI-0420.00/US | Utility - ORG | Abandoned       | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS  | 3/3/1999   | 09/261,849 |            |            | US      |
| NMXI-0420.00/US | Utility - DIV | Issued          | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS  | 6/5/2001   | 09/874,666 | 11/28/2006 | 7,141,448  | US      |
| NMXI-0421.00/US | Utility - ORG | Issued          | TECHNIQUE FOR SYNCHRONIZING FAULTS IN A PROCESSOR HAVING A REPLAY SYSTEM  | 12/28/1999 | 09/472,839 | 9/30/2003  | 6,629,271  | US      |
| NMXI-0421.00/US | Utility - CON | Issued          | TECHNIQUE FOR SYNCHRONIZING FAULTS IN A PROCESSOR HAVING A REPLAY SYSTEM  | 7/8/2003   | 10/614,215 | 1/2/2007   | 7,159,154  | US      |
| NMXI-0422.00/US | Utility - CON | Issued          | ENHANCED VIRTUAL RENAMING SCHEME AND DEADLOCK PREVENTION THEREFOR   | 1/27/2003  | 10/351,444 | 5/26/2009  | 7,539,850  | US      |
| NMXI-0423.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR RECOVERY OF PHYSICAL REGISTERS DURING A MISPREDECTION CONDITION  | 12/29/2000 | 09/750,095 | 12/26/2006 | 7,455,599  | US      |
| NMXI-0424.00/US | Utility - ORG | Pending         | METHOD AND APPARATUS FOR PREDICTING BRANCHES USING A META PREDICTOR   | 12/28/2000 | 09/749,405 |            |            | US      |
| NMXI-0425.00/US | Utility - ORG | Issued          | FIBRE METERING ARRANGEMENT  | 11/13/1995 | 08/849,031 | 9/8/1998   | 5,802,674  | US      |
| NMXI-0426.00/US | Utility - ORG | Expired         | DATABIT TO CONSTELLATION SYMBOL MAPPER  | 4/9/1991   | 07/583,359 | 2/9/1993   | 5,185,763  | US      |
| NMXI-0427.00/US | Utility - ORG | Issued          | METHOD OF QUANTIZING SIGNAL SAMPLES OF AN IMAGE DURING SAME APPARATUS FOR FAST LOGIC TRANSFER OF DATA ACROSS ASYNCHRONOUS CLOCK DOMAINS                         | 2/18/2000  | 09/507,399 | 6/8/2004   | 6,748,118  | US      |
| NMXI-0428.00/US | Utility - ORG | Issued          | PLASMA INDUCED DEPLETION OF FLUORINE FROM SURFACES OF FLUORINATED LOW-K DIELECTRIC MATERIALS  | 8/16/1999  | 09/374,798 | 1/9/2001   | 6,172,540  | US      |
| NMXI-0429.00/US | Utility - ORG | Issued          |   | 8/15/2000  | 09/639,625 | 1/25/2005  | 6,846,737  | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Patent No.      | Matter Type   | Matter Status | Title  | Filing Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|--|-------------|------------|------------|------------|---------|
| NMXI-0429.01/US | Utility - DIV | Issued        | PLASMA INDUCED DEPLETION OF FLUORINE FROM SURFACES OF FLUORINATED LOW-K DIELECTRIC MATERIALS   | 1/15/2002   | 10/050,463 | 7/15/2003  | 6,593,650  | US      |
| NMXI-0430.00/US | Utility - ORG | Abandoned     | TRANSMITTING DATA AND COMMANDS OVER A BUS  | 7/6/1999    | 09/350,269 |            |            | US      |
| NMXI-0431.00/US | Utility - DIV | Issued        | A SYSTEM PROCESSING DATA PACKETS RECEIVED FROM REMOTE HOST TO CONTROL SYSTEM OPERATION ACCORDING TO ADJUSTABLE TIMER   | 2/9/2006    | 11/351,991 | 4/10/2007  | 7,203,767  | US      |
| NMXI-0431.01/US | Utility - DIV | Issued        | INTERUPTS BASED ON DATA FLOW RATE  |             |            |            |            |         |
|                 |               |               | A HOST COMPUTER USING BASIC INPUT AND OUTPUT SYSTEM TO PROCESS CONTROL COMMAND RECEIVED FROM A REMOTE COMPUTER ACCORDING TO TIMER INTERRUPTS   | 2/9/2006    | 11/351,994 | 3/25/2008  | 7,349,991  | US      |
| NMXI-0432.00/US | Utility - ORG | Issued        | ELASTIC BUFFER   | 11/1/1999   | 09/432,050 | 7/15/2003  | 6,594,329  | US      |
| NMXI-0432.01/US | Utility - CIP | Issued        | JABBER COUNTER MECHANISM FOR ELASTIC BUFFER OPERATION  | 2/9/2000    | 09/500,524 | 2/21/2006  | 7,003,059  | US      |
| NMXI-0433.00/US | Utility - ORG | Issued        | MICROSCOPE WITH INFRARED IMAGING   | 7/30/1999   | 09/364,142 | 4/25/2000  | 6,055,095  | US      |
| NMXI-0433.01/US | Utility - CON | Abandoned     | MICROSCOPE WITH INFRARED IMAGING   | 3/14/2000   | 09/524,701 |            |            | US      |
| NMXI-0434.00/US | Utility - ORG | Pending       | AUTOMATICALLY RECORDING VIDEO  | 9/30/1999   | 09/409,522 |            |            | US      |
| NMXI-0435.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR TRANSFERRING WATER CASSETTES IN MICROELECTRONIC MANUFACTURING ENVIRONMENT   | 10/1/1999   | 09/410,400 | 4/2/2002   | 6,364,331  | US      |
| NMXI-0436.00/US | Utility - ORG | Issued        | PIXEL SPAN DEPTH BUFFER  | 11/18/1999  | 09/442,663 | 12/24/2002 | 6,498,605  | US      |
| NMXI-0437.00/US | Utility - ORG | Issued        | FIELD FRAME MOTION DESIGN FOR DIGITAL VIDEO DECODER  | 12/29/1999  | 09/474,183 | 5/6/2003   | 6,560,285  | US      |
| NMXI-0438.00/US | Utility - ORG | Issued        | VOLTAGE TOLERANT HIGH DRIVE PULL-UP DRIVER FOR AN I/O BUFFER   | 12/29/1999  | 09/474,566 | 5/14/2002  | 6,388,475  | US      |
| NMXI-0439.00/US | Utility - ORG | Abandoned     | OBSCURING VIDEO SIGNALS FOR CONDITIONAL ACCESS   | 2/29/2000   | 09/515,556 |            |            | US      |
| NMXI-0440.00/US | Utility - ORG | Issued        | METHOD OF COMPRESSING A COLOR IMAGE  | 10/1/1999   | 09/411,697 | 9/28/2004  | 6,798,901  | US      |
| NMXI-0441.00/US | Utility - ORG | Issued        | STEREOSCOPIC IMAGE SENSOR  | 9/10/1999   | 09/393,757 | 8/22/2000  | 6,108,130  | US      |
| NMXI-0442.00/US | Utility - ORG | Abandoned     | CONTROLLING PROCESSOR-BASED SYSTEMS USING A DIGITAL CAMERA   | 10/29/1999  | 09/430,282 | 6/12/2007  | 7,231,083  | US      |
| NMXI-0443.00/US | Utility - ORG | Issued        | EXECUTING A TRANSLATED BLOCK OF INSTRUCTIONS AND BRANCHING TO CORRECTION CODE WHEN EXPECTED TOP OF STACK DOES NOT MATCH ACTUAL TOP OF STACK TO ADJUST STACK AT EXECUTION TIME TO CONTINUE EXECUTING WITHOUT RESTARTING TRANSLATING | 9/29/2000   | 09/676,175 | 3/4/2008   | 7,340,592  | US      |
| NMXI-0444.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR ADAPTIVE HIERARCHICAL VISIBILITY IN A TILED THREE-DIMENSIONAL GRAPHICS ARCHITECTURE   | 11/2/1999   | 09/432,134 | 2/25/2003  | 6,525,726  | US      |
| NMXI-0445.00/US | Utility - ORG | Issued        | SELF-TUNING AMPLIFIER  | 11/5/1999   | 09/434,250 | 7/31/2001  | 6,268,774  | US      |
| NMXI-0446.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR RAPID INITIALIZATION OF CHARGE PUMP CIRCUITS  | 9/27/1999   | 09/406,329 | 7/3/2001   | 6,255,896  | US      |
| NMXI-0447.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR UNLOCKING A COMPUTER SYSTEM HARD DRIVE  | 11/23/2001  | 10/002,541 | 2/14/2006  | 7,000,109  | US      |
| NMXI-0448.00/US | Utility - ORG | Issued        | REAL-TIME PERFORMANCE ASSESSMENT OF LARGE AREA NETWORK USER EXPERIENCE   | 12/23/1999  | 09/471,964 | 4/21/2009  | 7,523,190  | US      |
| NMXI-0449.00/US | Utility - ORG | Expired       | METHOD AND APPARATUS FOR IDENTIFYING SPEECH IN TELEPHONE SIGNALS   | 5/15/1989   | 07/351,785 | 12/18/1990 | 4,979,214  | US      |
| NMXI-0450.00/US | Utility - ORG | Expired       | METHOD AND APPARATUS FOR FREQUENCY ANALYSIS OF TELEPHONE SIGNALS   | 5/15/1989   | 07/351,393 | 6/5/1990   | 4,932,062  | US      |
| NMXI-0451.00/US | Utility - ORG | Expired       | CONVERTER FOR IN-BAND ROUTING AND/OR ORIGINATOR INFORMATION  | 10/31/1989  | 07/429,849 | 9/3/1991   | 5,046,088  | US      |
| NMXI-0452.00/US | Utility - ORG | Expired       | AUTOMATIC CALL CENTER OVERFLOW RETRIEVAL SYSTEM  | 11/13/1990  | 07/612,572 | 2/9/1993   | 5,185,786  | US      |

11/23/2011

| Title | File |
|-------|------|
|       |      |

| MTI-Defining     | Material Type | Material Status | Title   | Filed Date | Serial No  | Issue Date | Patent No | Country |
|------------------|---------------|-----------------|---|------------|------------|------------|-----------|---------|
| NMXXI-0453.00/US | Utility - ORG | Expired         | DIAL PULSE DIGIT DETECTOR   | 3/7/1991   | 07/665,970 | 6/8/1993   | 5,218,636 | US      |
| NMXXI-0454.00/US | Utility - ORG | Issued          | OUTCALLING APPARATUS  | 3/4/1993   | 08/024,402 | 4/4/1995   | 5,404,400 | US      |
| NMXXI-0455.00/US | Utility - ORG | Issued          | RING TONE DETECTION FOR A TELEPHONE SYSTEM  | 4/12/1994  | 08/126,678 | 8/15/1995  | 5,442,694 | US      |
| NMXXI-0456.00/US | Utility - CON | Issued          | VOICE DETECTION   | 8/22/1995  | 08/524,111 | 6/10/1997  | 5,638,436 | US      |
| NMXXI-0457.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR ASSIGNING MEMORY SPACE ADDRESSES TO MEMORY ON COMPUTER INTERFACE BOARDS    | 1/27/1994  | 08/188,555 | 4/28/1998  | 5,745,786 | US      |
| NMXXI-0458.00/US | Utility - ORG | Issued          | TECHNIQUE OF CONTROLLING TELEPHONE CALLS  | 1/31/1996  | 08/594,411 | 9/15/1998  | 5,809,110 | US      |
| NMXXI-0459.00/US | Utility - ORG | Issued          | CALL PROCESSING USING PREVIOUSLY OBTAINED LINE CHARACTERISTICS                                      | 10/6/1994  | 08/319,159 | 6/9/1998   | 5,764,759 | US      |
| NMXXI-0460.00/US | Utility - CON | Issued          | METHOD AND APPARATUS FOR DETERMINING DIGITS DIALED USING A SELECTED ALGORITHM                       | 6/4/1997   | 08/828,000 | 3/9/1999   | 5,881,146 | US      |
| NMXXI-0461.00/US | Utility - ORG | Issued          | TELEPHONE NETWORK WITH NORMALLY OFF HOOK TELEPHONES   | 1/30/1996  | 08/554,016 | 6/15/1999  | 5,912,950 | US      |
| NMXXI-0462.00/PR | Prov - ORG    | Expired         | ANTENNA INTERFACE   | 6/2/1999   | 60/135,467 |            |           | US      |
| NMXXI-0462.00/US | Utility - ORG | Issued          | ANTENNA INTERFACE   | 3/16/2000  | 09/527,314 | 5/11/2004  | 6,735,418 | US      |
| NMXXI-0463.00/US | Utility - ORG | Issued          | HASH CAM HAVING A REDUCED WIDTH COMPARISON CIRCUITRY AND ITS APPLICATION                            | 12/21/1999 | 09/468,477 | 12/30/2003 | 6,671,771 | US      |
| NMXXI-0464.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR MAINTAINING ORDER IN A PIPELINED PROCESS AND ITS APPLICATION               | 12/21/1999 | 09/468,965 | 6/22/2004  | 6,754,764 | US      |
| NMXXI-0465.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR SWITCHING AN OPTICAL BEAM  | 12/22/1999 | 09/470,574 | 11/18/2003 | 6,650,802 | US      |
| NMXXI-0466.00/US | Utility - CON | Issued          | PRESBYOPIC BRANCH TARGET PREFETCH METHOD AND APPARATUS  | 4/2/2004   | 10/817,263 | 4/7/2009   | 7,516,312 | US      |
| NMXXI-0467.00/US | Utility - ORG | Issued          | METHODS AND APPARATUS FOR FAST ARGUMENT REDUCTION IN A COMPUTING SYSTEM                             | 6/30/2000  | 09/609,496 | 4/29/2008  | 7,366,748 | US      |
| NMXXI-0468.00/US | Utility - ORG | Issued          | SHIFTING AN INPUT SIGNAL FROM A HIGH-SPEED DOMAIN TO A LOWER-SPEED DOMAIN                           | 12/29/1999 | 09/474,750 | 12/23/2003 | 6,668,298 | US      |
| NMXXI-0468.01/US | Utility - CON | Abandoned       | SHIFTING AN INPUT SIGNAL FROM A HIGH-SPEED DOMAIN TO A LOWER-SPEED DOMAIN                           | 12/23/2003 | 10/743,397 |            |           | US      |
| NMXXI-0469.00/US | Utility - ORG | Issued          | TRIMMING SURFACES   | 3/31/2000  | 09/539,343 | 2/20/2007  | 7,180,523 | US      |
| NMXXI-0470.00/US | Utility - ORG | Issued          | ACCESSING FILE DATA STORED IN NON-VOLATILE RE-PROGRAMMABLE SEMICONDUCTOR MEMORIES                   | 4/12/2000  | 09/547,624 | 5/25/2004  | 6,741,978 | US      |
| NMXXI-0471.00/US | Utility - ORG | Abandoned       | SYSTEM AND METHOD FOR STALLING A DISPLAY CONTROLLER UPON REQUEST                                    | 1/7/2000   | 09/478,994 |            |           | US      |
| NMXXI-0472.00/US | Utility - ORG | Issued          | USER TRANSPARENT CONTINUOUS COMPILATION   | 6/30/2000  | 09/608,616 | 7/3/2007   | 7,240,342 | US      |
| NMXXI-0473.00/US | Utility - ORG | Issued          | EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP                   | 3/4/1997   | 09/526,794 | 6/10/2003  | 6,577,695 | US      |
| NMXXI-0474.00/PR | Prov - ORG    | Expired         | A SINGLE-CHIP CMOS DIRECT-CONVERSION TRANSCIVER   | 9/10/1998  | 60/099,713 |            |           | US      |
| NMXXI-0474.00/US | Utility - ORG | Issued          | SINGLE-CHIP CMOS DIRECT-CONVERSION TRANSCIVER   | 11/20/2001 | 09/762,720 | 6/20/2006  | 7,065,327 | US      |
| NMXXI-0475.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS TO IMPROVE THE PROTECTION OF INFORMATION PRESENTED BY A COMPUTER               | 9/20/2000  | 09/665,826 | 2/28/2006  | 7,007,304 | US      |
| NMXXI-0476.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS FOR HARDWARE AND SOFTWARE CO-SIMULATION  | 1/31/2000  | 09/495,150 | 9/12/2006  | 7,107,202 | US      |
| NMXXI-0477.00/US | Utility - CON | Issued          | MEMORY CONTROLLER HUB INTERFACE   | 7/6/2005   | 11/174,662 | 2/5/2008   | 7,327,370 | US      |
| NMXXI-0478.00/US | Utility - ORG | Issued          | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS | 6/30/2000  | 09/607,483 | 10/22/2002 | 6,469,482 | US      |
| NMXXI-0478.01/US | Utility - DIV | Issued          | MEMORY CIRCUIT INCLUDING BOOSTER PUMP FOR PROGRAMMING VOLTAGE GENERATION                            | 6/17/2002  | 10/174,193 | 6/1/2004   | 6,744,669 | US      |
| NMXXI-0479.00/US | Utility - CON | Issued          | METHOD FOR REDUCING POWER CONSUMPTION IN WAIT-MODE  | 12/13/1999 | 09/458,715 | 4/29/2003  | 6,556,128 | US      |

Exhibit A - US Applications and Issued Patents

| Pat. No.        | Material      | Material  | Title  | File Date  | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|-----------|--|------------|------------|------------|------------|---------|
| NMXI-0480.00/US | Utility - ORG | Issued    | METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS  | 6/15/1998  | 09/103,683 | 4/4/2000   | 6,047,035  | US      |
| NMXI-0481.00/US | Utility - ORG | Issued    | METHOD AND APPARATUS FOR BRANCH TRACE MESSAGE SCHEME   | 2/14/2000  | 09/503,879 | 11/11/2003 | 6,647,545  | US      |
| NMXI-0482.00/US | Utility - ORG | Issued    | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK                                      | 6/26/2000  | 09/603,957 | 8/17/2004  | 6,778,548  | US      |
| NMXI-0482.00/US | Utility - DIV | Issued    | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK                                      | 1/20/2004  | 10/761,394 | 4/1/2008   | 7,352,763  | US      |
| NMXI-0482.02/US | Utility - DIV | Abandoned | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK                                      | 1/20/2004  | 10/761,395 |            |            | US      |
| NMXI-0482.09/US | Utility - CON | Issued    | DEVICE TO RECEIVE, BUFFER, AND TRANSMIT PACKETS OF DATA IN A PACKET SWITCHING NETWORK                                      | 5/10/2004  | 10/842,752 | 11/11/2008 | 7,450,583  | US      |
| NMXI-0483.00/US | Utility - ORG | Issued    | METHOD OF INVERSE QUANTIZED SIGNAL SAMPLES OF AN IMAGE DURING IMAGE DECOMPRESSION  | 2/18/2000  | 09/507,213 | 11/1/2005  | 6,961,472  | US      |
| NMXI-0484.00/US | Utility - ORG | Issued    | SHARED CREDIT ROUND ROBIN QUEUING  | 3/21/2000  | 09/541,048 | 1/27/2004  | 6,683,884  | US      |
| NMXI-0485.00/US | Utility - ORG | Issued    | VITERBI ACS UNIT WITH RENORMALIZATION  | 3/29/1993  | 08/038,323 | 9/20/1994  | 5,349,608  | US      |
| NMXI-0486.00/US | Utility - ORG | Issued    | CARRIER FREQUENCY ESTIMATOR FOR A SIGNAL RECEIVER  | 7/20/1998  | 09/119,479 | 12/14/1999 | 6,002,721  | US      |
| NMXI-0486.01/US | Utility - CON | Abandoned | CARRIER FREQUENCY ESTIMATOR FOR A SIGNAL RECEIVER  | 9/30/1999  | 09/409,791 |            |            | US      |
| NMXI-0487.00/US | Utility - ORG | Issued    | PROCESS FOR FORMING MICROELECTRONIC PACKAGES AND INTERMEDIATE STRUCTURES FORMED THEREWITH                                  | 3/1/2000   | 09/516,652 | 7/1/2003   | 6,586,836  | US      |
| NMXI-0488.00/US | Utility - DIV | Abandoned | DIE-IN-HEAT SPREADER MICROELECTRONIC PACKAGE   | 2/9/2004   | 10/774,952 |            |            | US      |
| NMXI-0489.00/US | Utility - ORG | Issued    | NOVEL SYSTEM AND METHOD OF HEAT EXTRACTION FROM AN INTEGRATED CIRCUIT DIE  | 6/29/2000  | 09/606,893 | 2/26/2002  | 6,351,387  | US      |
| NMXI-0489.01/US | Utility - DIV | Issued    | METHODS OF HEAT EXTRACTION FROM AN INTEGRATED CIRCUIT DIE  | 12/18/2001 | 10/025,434 | 6/22/2004  | 6,751,887  | US      |
| NMXI-0490.00/US | Utility - ORG | Issued    | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE  | 5/1/1992   | 07/877,409 | 5/30/1995  | 5,421,001  | US      |
| NMXI-0491.00/US | Utility - ORG | Issued    | METHOD FOR IMPROVING ACCESSIBILITY OF A COMPUTER DISPLAY   | 8/18/1997  | 09/912,380 | 7/6/1999   | 5,920,870  | US      |
| NMXI-0492.00/US | Utility - ORG | Issued    | EFFICIENT IMPLEMENTATION OF N-POINT DCT, N-POINT IDCT, SA-DCT AND SA-IDCT ALGORITHMS                                       | 9/30/2000  | 09/676,556 | 5/8/2007   | 7,216,140  | US      |
| NMXI-0493.00/US | Utility - ORG | Abandoned | COMBINED PROPAGATE/GENERATE/PARTIAL SUM CIRCUIT FOR SINGLE-RAIL ADDERS   | 6/26/2000  | 09/603,800 |            |            | US      |
| NMXI-0494.00/US | Utility - ORG | Issued    | METHOD AND APPARATUS FOR PROVIDING A SECURE-PRIVATE PARTITION ON A HARD DISK DRIVE OF A COMPUTER SYSTEM VIA IDE CONTROLLER | 6/30/2000  | 09/608,117 | 12/26/2006 | 7,155,615  | US      |
| NMXI-0495.00/US | Utility - ORG | Abandoned | METHOD AND APPARATUS TO PROVIDE CLIENT-SIDE FILTERING IN A BROADCAST SYSTEM  | 3/22/2000  | 09/533,024 |            |            | US      |
| NMXI-0496.00/US | Utility - ORG | Issued    | METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE   | 6/30/2000  | 09/507,592 | 5/16/2006  | 7,047,495  | US      |
| NMXI-0497.00/US | Utility - ORG | Issued    | REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE   | 5/31/2000  | 09/584,520 | 6/1/2010   | 7,729,943  | US      |
| NMXI-0498.00/US | Utility - ORG | Issued    | MEMORY SUBSYSTEM EMPLOYING POOL OF REFRESH CANDIDATES  | 12/29/2000 | 09/753,005 | 4/2/2002   | 6,366,516  | US      |
| NMXI-0499.00/US | Utility - ORG | Issued    | METHOD AND APPARATUS FOR ALLOWING CONTINUOUS APPLICATION OF HIGH VOLTAGE TO A FLASH MEMORY DEVICE POWER PIN                | 12/29/2000 | 09/751,514 | 2/4/2003   | 6,515,901  | US      |
| NMXI-0499.01/US | Utility - CON | Issued    | METHOD AND APPARATUS FOR ALLOWING CONTINUOUS APPLICATION OF HIGH VOLTAGE TO A FLASH MEMORY DEVICE POWER PIN                | 12/13/2002 | 10/318,464 | 4/6/2004   | 6,717,855  | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| WI. Dctno       | Material      | Material  | Title  | Filedate   | SerialNo   | Issuedate  | PatentNo  | Country |
|-----------------|---------------|-----------|--|------------|------------|------------|-----------|---------|
| NMXI-0500.00/US | Utility - DIV | Issued    | CIRCUIT BOARD WITH ORGANIC DIELECTRIC LAYER  | 12/3/2004  | 11/003,956 | 3/25/2008  | 7,348,496 | US      |
| NMXI-0501.00/US | Utility - DIV | Abandoned | PROTECTIVE FILM FOR THE FABRICATION OF DIRECT BUILD-UP LAYERS ON AN ENCAPSULATED DIE PACKAGE                                 | 11/19/2002 | 10/299,135 |            |           | US      |
| NMXI-0502.00/US | Utility - ORG | Abandoned | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS   | 12/8/2000  | 09/733,289 |            |           | US      |
| NMXI-0503.00/US | Utility - ORG | Issued    | DRIVING A VISUAL INDICATOR ARRAY IN AN ELECTRONIC SIGNALING SYSTEM   | 5/31/2001  | 09/872,490 | 11/26/2002 | 6,486,791 | US      |
| NMXI-0504.00/US | Utility - ORG | Issued    | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS                                 | 11/22/2000 | 09/721,785 | 12/11/2007 | 7,308,713 | US      |
| NMXI-0505.00/US | Utility - ORG | Issued    | METHOD OF ADAPTING PROCESSING PARAMETERS IN A CALL PROCESSING SYSTEM   | 8/11/1994  | 08/289,315 | 4/9/1996   | 5,506,885 | US      |
| NMXI-0506.00/US | Utility - ORG | Issued    | TELEPHONE CALL SETUP PROCEDURE   | 10/10/1995 | 08/541,678 | 4/23/2002  | 6,377,576 | US      |
| NMXI-0507.00/US | Utility - ORG | Issued    | USE OF CALLER ID INFORMATION   | 2/27/1996  | 08/607,796 | 7/28/1998  | 5,787,159 | US      |
| NMXI-0508.00/US | Utility - ORG | Issued    | DTMF DETECTION IN INTERNET TELEPHONY   | 12/2/1998  | 09/204,607 | 6/4/2002   | 6,400,709 | US      |
| NMXI-0509.00/US | Utility - ORG | Issued    | COMPUTER TELEPHONE SERVER WITH IMPROVED FLEXIBILITY  | 3/26/1999  | 09/277,286 | 10/17/2006 | 7,123,712 | US      |
| NMXI-0510.00/US | Utility - ORG | Issued    | TEST FIXTURE FOR TESTING A MULTI-PORT VOICE PROCESSING CARD AND THE LIKE   | 3/29/1999  | 09/280,171 | 7/22/2003  | 6,595,798 | US      |
| NMXI-0511.00/US | Utility - ORG | Issued    | BOARD ID DISPLAY SYSTEM  | 4/26/1999  | 09/299,162 | 6/12/2001  | 6,246,334 | US      |
| NMXI-0512.00/US | Utility - ORG | Issued    | DYNAMIC AND SCHEDULED COMPUTER TELEPHONE RESOURCE ALLOCATION   | 4/29/1999  | 09/201,855 | 11/4/2003  | 6,643,367 | US      |
| NMXI-0513.00/US | Utility - ORG | Issued    | DUAL PERSONALITY ANALOG PORT   | 7/28/1999  | 09/362,425 | 8/16/2005  | 6,931,121 | US      |
| NMXI-0514.00/US | Utility - ORG | Issued    | HIGH IMPEDANCE POLARITY DETECTOR   | 10/12/1999 | 09/417,161 | 2/8/2005   | 6,853,723 | US      |
| NMXI-0515.00/US | Utility - ORG | Issued    | MULTIPLE PLATFORM VOICE PROCESSING SYSTEM WITH OPTIMIZED RESOURCE ALLOCATION   | 2/22/2000  | 09/510,311 | 7/9/2002   | 6,418,201 | US      |
| NMXI-0516.00/US | Utility - ORG | Issued    | CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS  | 3/4/2000   | 09/518,966 | 10/16/2001 | 6,304,645 | US      |
| NMXI-0517.00/US | Utility - ORG | Issued    | METHOD OF PROCESSING AN INBOUND CALL IN A CALL CENTER  | 3/10/2000  | 09/523,935 | 12/13/2005 | 6,975,720 | US      |
| NMXI-0518.00/US | Utility - ORG | Abandoned | DYNAMICALLY LOCATING ICONS ON A VIDEO DISPLAY SCREEN   | 7/7/2000   | 09/611,938 |            |           | US      |
| NMXI-0519.00/US | Utility - CIP | Issued    | DIGITAL TELEPHONE SYSTEM WITH AUTOMATIC ROUTING BASED UPON CALLER ID   | 8/31/1998  | 09/144,511 | 11/27/2001 | 6,324,263 | US      |
| NMXI-0520.00/US | Utility - ORG | Issued    | APPARATUS AND METHOD FOR CONNECTING AN IMPLEMENT TO A TRACTION VEHICLE   | 10/29/1998 | 09/182,828 | 5/29/2001  | 6,240,168 | US      |
| NMXI-0520.01/US | Utility - CON | Issued    | METHOD AND APPARATUS FOR CONTROLLING A COMPUTER TO IMPLEMENT TELEPHONE FUNCTIONS WITH A DISPLAYED TELEPHONE OF VARIABLE SIZE | 2/4/2000   | 09/497,970 | 7/16/2002  | 6,421,432 | US      |
| NMXI-0521.00/US | Utility - ORG | Expired   | LOGIC SUPPORT CHIP FOR AT-TYPE COMPUTER WITH IMPROVED BUS ARCHITECTURE   | 11/13/1989 | 07/436,200 | 6/23/1992  | 5,125,080 | US      |
| NMXI-0521.01/US | Utility - CON | Expired   | LOGIC SUPPORT CHIP FOR AT-TYPE COMPUTER WITH IMPROVED BUS ARCHITECTURE   | 6/22/1992  | 07/902,317 | 1/18/1994  | 5,280,590 | US      |
| NMXI-0522.00/US | Utility - ORG | Expired   | DATACACHE MANAGEMENT SYSTEM WITH TEST MODE USING INDEX REGISTERS AND CAS DISABLE AND POSTED WRITE DISABLE                    | 7/2/1990   | 07/544,821 | 1/4/1994   | 5,276,833 | US      |
| NMXI-0523.00/US | Utility - ORG | Issued    | SYSTEM FOR PERFORMING INPUT AND OUTPUT OPERATIONS TO AND FROM A PROCESSOR  | 4/9/1993   | 08/046,109 | 9/26/2000  | 6,125,412 | US      |
| NMXI-0524.00/US | Utility - DIV | Expired   | DRAM REFRESH CONTROLLER WITH IMPROVED BUS ARBITRATION SCHEME   | 11/24/1992 | 07/981,329 | 9/6/1994   | 5,345,577 | US      |
| NMXI-0525.00/US | Utility - ORG | Issued    | UNIVERSAL PULSE SYNCHRONIZER   | 1/6/1999   | 09/226,924 | 1/9/2001   | 6,172,538 | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| MTI-DataNo       | MaterialType  | MaterialStatus | Title   | FiledDate  | SerialNo   | IssueDate  | PatentNo  | Country |
|------------------|---------------|----------------|---|------------|------------|------------|-----------|---------|
| NMXXI-0526.00/US | Utility - ORG | Issued         | METHOD AND APPARATUS FOR INTERLACED IMAGE ENHANCEMENT   | 9/13/1999  | 09/395,000 | 5/7/2002   | 6,384,872 | US      |
| NMXXI-0527.00/US | Utility - ORG | Issued         | CLOCK SPLITTER CIRCUIT TO GENERATE SYNCHRONIZED CLOCK AND INVERTED CLOCK  | 9/29/2000  | 09/676,313 | 5/7/2002   | 6,384,658 | US      |
| NMXXI-0528.00/US | Utility - ORG | Issued         | METHOD AND APPARATUS FOR A LOW LATENCY SOURCE-SYNCHRONOUS ADDRESS RECEIVER FOR A HOST SYSTEM BUS IN A MEMORY CONTROLLER | 9/20/2000  | 09/665,922 | 6/8/2004   | 6,748,513 | US      |
| NMXXI-0528.01/US | Utility - CON | Issued         | METHOD AND APPARATUS FOR A LOW LATENCY SOURCE-SYNCHRONOUS ADDRESS RECEIVER FOR A HOST SYSTEM BUS IN A MEMORY CONTROLLER | 3/30/2004  | 10/813,145 | 7/5/2005   | 6,915,407 | US      |
| NMXXI-0529.00/US | Utility - ORG | Issued         | RAY INTERSECTION REDUCTION USING DIRECTIONALLY CLASSIFIED TARGET LISTS  | 6/7/1999   | 09/327,015 | 12/3/2002  | 6,489,955 | US      |
| NMXXI-0530.00/US | Utility - ORG | Issued         | STOCHASTIC SAMPLING WITH CONSTANT DENSITY IN OBJECT SPACE FOR ANISOTROPIC TEXTURE MAPPING                               | 9/10/1999  | 09/393,477 | 6/4/2002   | 6,400,370 | US      |
| NMXXI-0531.00/US | Utility - ORG | Issued         | 3D RENDERING TEXTURE CACHING SCHEME   | 2/11/2000  | 09/502,994 | 5/23/2006  | 7,050,063 | US      |
| NMXXI-0532.00/US | Utility - ORG | Issued         | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM                           | 10/15/1996 | 08/731,635 | 12/29/1998 | 5,854,632 | US      |
| NMXXI-0533.00/US | Utility - ORG | Issued         | METHOD AND SYSTEM FOR SERVING CACHE LINE IN RESPONSE TO PARTIAL CACHE LINE REQUEST                                      | 12/29/2000 | 09/752,846 | 12/24/2002 | 6,499,085 | US      |
| NMXXI-0534.00/US | Utility - ORG | Abandoned      | METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN  | 6/30/2000  | 09/608,637 |            |           | US      |
| NMXXI-0534.01/US | Utility - CON | Abandoned      | METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN  | 5/5/2005   | 11/124,315 |            |           | US      |
| NMXXI-0534.02/US | Utility - DIV | Abandoned      | ANTECEDENT STRENGTHENING TO PERFORM GENERALIZED TRAJECTORY EVALUATION   | 10/27/2006 | 11/553,905 |            |           | US      |
| NMXXI-0535.00/US | Utility - ORG | Issued         | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS   | 9/27/2000  | 09/670,850 | 3/12/2002  | 6,356,062 | US      |
| NMXXI-0536.00/US | Utility - ORG | Issued         | EXTENSIBLE BIOS ERROR LOG   | 12/13/2000 | 09/737,140 | 10/17/2006 | 7,124,412 | US      |
| NMXXI-0537.00/US | Utility - ORG | Issued         | DIFFERENTIAL SIGNAL PATH FOR HIGH SPEED DATA TRANSMISSION IN FLASH MEMORY   | 12/29/2000 | 09/752,345 | 8/27/2002  | 6,442,069 | US      |
| NMXXI-0537.01/US | Utility - CIP | Issued         | DIFFERENTIAL REDUNDANCY MULTIPLEXOR FOR FLASH MEMORY DEVICES  | 10/16/2001 | 09/982,246 | 6/3/2003   | 6,574,141 | US      |
| NMXXI-0538.00/US | Utility - ORG | Issued         | GATE ENHANCED TRI-CHANNEL POSITIVE CHARGE PUMP  | 12/29/2000 | 09/753,045 | 12/17/2002 | 6,496,055 | US      |
| NMXXI-0538.01/US | Utility - CON | Issued         | GATE ENHANCED TRI-CHANNEL POSITIVE CHARGE PUMP  | 10/18/2002 | 10/273,689 | 2/3/2004   | 6,686,793 | US      |
| NMXXI-0539.00/US | Utility - ORG | Issued         | TRIPLE WELL NO BODY EFFECT NEGATIVE CHARGE PUMP   | 12/28/2000 | 09/751,695 | 9/17/2002  | 6,452,438 | US      |
| NMXXI-0540.00/US | Utility - ORG | Issued         | NOVEL BI-VOLTAGE LEVELS SWITCHES  | 12/21/2000 | 09/745,970 | 2/18/2003  | 6,522,180 | US      |
| NMXXI-0541.00/US | Utility - ORG | Issued         | SELF INITIALIZATION FOR CHARGE PUMPS  | 12/27/2000 | 09/752,247 | 4/7/2002   | 6,366,158 | US      |
| NMXXI-0541.01/US | Utility - CON | Issued         | SELF INITIALIZATION FOR CHARGE PUMPS  | 1/17/2002  | 10/051,696 | 8/27/2002  | 6,441,678 | US      |
| NMXXI-0542.00/US | Utility - ORG | Issued         | OUTPUT SWITCH FOR CHARGE PUMP RECONFIGURATION   | 12/26/2000 | 09/748,611 | 4/9/2002   | 6,369,642 | US      |
| NMXXI-0543.00/US | Utility - ORG | Issued         | NEGATIVE OUTPUT VOLTAGE CHARGE PUMP AND METHOD THEREFOR   | 12/29/2000 | 09/753,351 | 3/19/2002  | 6,359,814 | US      |
| NMXXI-0544.00/US | Utility - ORG | Issued         | METHOD AND APPARATUS FOR MATCHED-REFERENCE SENSING ARCHITECTURE FOR NON-VOLATILE MEMORIES                               | 12/28/2000 | 09/752,714 | 2/4/2003   | 6,515,906 | US      |
| NMXXI-0544.01/US | Utility - CON | Issued         | METHOD AND APPARATUS FOR MATCHED-REFERENCE SENSING ARCHITECTURE FOR NON-VOLATILE MEMORIES                               | 12/26/2002 | 10/330,469 | 12/14/2004 | 6,831,862 | US      |
| NMXXI-0545.00/US | Utility - ORG | Issued         | CHARGING A CAPACITANCE OF A MEMORY CELL AND CHARGER   | 6/22/2001  | 09/888,252 | 1/7/2003   | 6,504,760 | US      |
| NMXXI-0545.01/US | Utility - DIV | Issued         | CHARGING A CAPACITANCE OF A MEMORY CELL AND CHARGER   | 5/23/2002  | 10/154,019 | 7/22/2003  | 6,597,606 | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| PatNo           | MatType       | MatStatus | Title  | Filed      | SerNo      | Issued     | PatNo     | Country |
|-----------------|---------------|-----------|--|------------|------------|------------|-----------|---------|
| NMXI-0546.00/US | Utility - CON | Issued    | THREAD LIST EXPLICIT CACHING SYSTEM  | 11/7/1997  | 08/966,771 | 7/18/2000  | 6,092,154 | US      |
| NMXI-0547.00/US | Utility - ORG | Issued    | RUNTIME PREDICTION FRAMEWORK FOR CPU INTENSIVE APPLICATIONS  | 9/29/2000  | 09/675,286 | 1/23/2007  | 7,168,074 | US      |
| NMXI-0548.00/US | Utility - ORG | Issued    | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING  | 3/30/2001  | 09/823,463 | 11/5/2002  | 6,477,091 | US      |
| NMXI-0548.00/US | Utility - DIV | Issued    | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING  | 2/19/2002  | 10/078,106 | 9/7/2004   | 6,788,584 | US      |
| NMXI-0548.00/US | Utility - DIV | Issued    | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING  | 6/25/2004  | 10/877,634 | 9/13/2005  | 6,944,065 | US      |
| NMXI-0549.00/US | Utility - ORG | Issued    | SYSTEM AND METHOD OF TRANSMITTING DATA FRAMES IN A MESH OF DATA SWITCHES   | 11/15/2000 | 09/714,382 | 3/15/2005  | 6,868,071 | US      |
| NMXI-0550.00/US | Utility - ORG | Issued    | SYSTEM AND METHOD FOR AUTOMATIC PAX DETECTION  | 1/20/1998  | 09/009,561 | 12/7/1999  | 5,999,593 | US      |
| NMXI-0551.00/US | Utility - ORG | Issued    | CONTROLLING SNOOP ACTIVITIES USING TASK TABLE IN MULTIPROCESSOR SYSTEM   | 6/24/2002  | 10/179,790 | 5/5/2009   | 7,530,066 | US      |
| NMXI-0552.00/US | Utility - ORG | Issued    | SHARING CLASSES BETWEEN PROGRAMS   | 1/8/2001   | 09/756,579 | 7/17/2007  | 7,246,135 | US      |
| NMXI-0553.00/US | Utility - ORG | Abandoned | PROVIDING A VALID INITIAL FRAME  | 11/27/2000 | 09/723,210 |            |           | US      |
| NMXI-0554.00/US | Utility - ORG | Issued    | CIRCUIT AND METHOD FOR DIGITAL TO ANALOG SIGNAL CONVERSION   | 9/30/1993  | 08/129,899 | 11/21/1995 | 5,469,164 | US      |
| NMXI-0555.00/US | Utility - ORG | Issued    | VOLTAGE LEVEL SHIFTER HAVING ZERO DC CURRENT AND STATE RETENTION IN DROWSY MODE  | 12/29/2000 | 09/750,110 | 9/24/2002  | 6,456,110 | US      |
| NMXI-0556.00/US | Utility - CON | Abandoned | MECHANISM FOR HANDLING EXPLICIT WRITEBACK IN A CACHE COHERENT MULTI-NODE ARCHITECTURE  | 12/28/2005 | 11/321,632 |            |           | US      |
| NMXI-0557.00/US | Utility - ORG | Issued    | METHOD & APPARATUS FOR ADDING REAL-TIME PRIMITIVES   | 12/19/2000 | 09/741,599 | 10/17/2006 | 7,123,265 | US      |
| NMXI-0558.00/US | Utility - ORG | Issued    | METHOD AND DRIVER FOR DETECTING GLITCHES   | 12/19/2000 | 09/741,405 | 5/1/2007   | 7,212,497 | US      |
| NMXI-0559.00/US | Utility - ORG | Issued    | METHOD AND APPARATUS FOR DETERMINING AND DISPLAYING THE SERVICE LEVEL OF A DIGITAL TELEVISION BROADCAST SIGNAL                               | 11/21/2000 | 09/717,579 | 9/25/2007  | 7,275,254 | US      |
| NMXI-0560.00/US | Utility - ORG | Issued    | AUTO LATENCY TEST TOOL   | 12/28/2000 | 09/749,610 | 7/15/2003  | 6,594,344 | US      |
| NMXI-0561.00/US | Utility - ORG | Issued    | METHOD AND APPARATUS FOR RECOVERING FROM AN OVERHEATED MICROPROCESSOR  | 12/29/2000 | 09/751,601 | 3/21/2006  | 7,017,062 | US      |
| NMXI-0562.00/US | Utility - ORG | Issued    | ION IMPLANTATION SYSTEM  | 12/4/2000  | 09/730,206 | 9/9/2003   | 6,617,593 | US      |
| NMXI-0563.00/US | Utility - ORG | Issued    | METHOD OF FORMING A SEMICONDUCTOR DEVICE USING A CARBON DOPED OXIDE LAYER TO CONTROL THE CHEMICAL MECHANICAL POLISHING OF A DIELECTRIC LAYER | 11/20/2001 | 09/996,215 | 4/15/2003  | 6,548,399 | US      |
| NMXI-0563.00/US | Utility - DIV | Issued    | METHOD OF FORMING A SEMICONDUCTOR DEVICE USING A CARBON DOPED OXIDE LAYER TO CONTROL THE CHEMICAL MECHANICAL POLISHING OF A DIELECTRIC LAYER | 1/29/2003  | 10/354,361 | 10/7/2003  | 6,630,390 | US      |
| NMXI-0564.00/US | Utility - CON | Abandoned | COHERENT LIGHT DESPECKLING   | 1/29/2004  | 10/767,324 | 5/8/2007   | 7,215,383 | US      |
| NMXI-0565.00/US | Utility - ORG | Abandoned | APPARATUS FOR SHIELDING TRANSMISSION LINE EFFECTS ON A PRINTED CIRCUIT BOARD   | 12/7/2000  | 09/732,115 |            |           | US      |
| NMXI-0566.00/US | Utility - ORG | Issued    | I/O DEVICE TESTING METHOD AND APPARATUS  | 11/8/2000  | 09/709,000 | 12/30/2003 | 6,671,847 | US      |
| NMXI-0567.00/US | Utility - ORG | Issued    | SPINDLE SLEEVE FOR COATER/DEVELOPER  | 12/21/2000 | 09/746,435 | 4/20/2004  | 6,723,167 | US      |
| NMXI-0568.00/US | Utility - DIV | Abandoned | EXPOSED AND EMBEDDED OVERLAY STRUCTURE   | 9/11/2003  | 10/662,021 | 8/31/2004  | 6,784,004 | US      |
| NMXI-0569.00/US | Utility - ORG | Issued    | PURGING GAS FROM A PHOTOLITHOGRAPHY ENCLOSURE BETWEEN A MASK PROTECTIVE DEVICE AND A PATTERNED MASK  | 12/29/2000 | 09/752,938 | 3/23/2004  | 6,710,845 | US      |
| NMXI-0569.00/US | Utility - DIV | Issued    | PURGING GAS FROM A PHOTOLITHOGRAPHY ENCLOSURE BETWEEN A MASK PROTECTIVE DEVICE AND A PATTERNED MASK  | 1/16/2004  | 10/759,641 | 8/14/2007  | 7,256,872 | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| WIPO No.        | Water Type    | Water Status | Title  | File Date  | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|--------------|--|------------|------------|------------|------------|---------|
| NMXI-0570.00/US | Utility - ORG | Issued       | POST ETCH CLEAN SEQUENCE FOR MAKING A SEMICONDUCTOR DEVICE   | 10/6/2000  | 09/684,550 | 10/15/2002 | 6,455,358  | US      |
| NMXI-0571.00/US | Utility - ORG | Issued       | INTERCONNECT MECHANICS FOR ELECTROMAGNETIC COUPLER   | 11/15/2000 | 09/713,702 | 12/24/2002 | 6,498,305  | US      |
| NMXI-0572.00/US | Utility - ORG | Issued       | CLAMP TO SECURE CARRIER TO DEVICE FOR ELECTROMAGNETIC COUPLER  | 12/29/2000 | 09/751,442 | 6/10/2003  | 6,576,847  | US      |
| NMXI-0573.00/US | Utility - ORG | Issued       | ELECTROMAGNETIC COUPLER SOCKET   | 12/29/2000 | 09/751,527 | 3/18/2003  | 6,533,586  | US      |
| NMXI-0574.00/US | Utility - ORG | Issued       | SYSTEM AND METHOD FOR SPLIT AUTOMATIC GAIN CONTROL   | 9/14/2001  | 09/951,904 | 3/4/2008   | 7,340,069  | US      |
| NMXI-0575.00/US | Utility - ORG | Issued       | PROGRAMMABLE DIGITAL PHASE LOCK LOOP   | 12/29/2000 | 09/750,671 | 9/3/2002   | 6,445,230  | US      |
| NMXI-0576.00/US | Utility - ORG | Abandoned    | TIMING MULTIPLE EVENTS WITH A SINGLE TIMER   | 4/13/2001  | 09/835,034 |            |            | US      |
| NMXI-0577.00/US | Utility - ORG | Abandoned    | DATA PATH EVALUATION SYSTEM AND METHOD   | 4/12/2001  | 09/834,016 | 10/7/2003  | 6,631,339  | US      |
| NMXI-0578.00/PR | Prov - ORG    | Expired      | UNIVERSAL TELEPHONE INTERFACE POLARITY DETECTOR  | 8/29/2001  | 60/315,797 |            |            | US      |
| NMXI-0578.00/US | Utility - ORG | Issued       | UNIVERSAL TELEPHONE INTERFACE POLARITY DETECTOR  | 11/28/2001 | 09/996,255 | 1/9/2007   | 7,162,028  | US      |
| NMXI-0579.00/US | Utility - ORG | Issued       | KEYBOARD HOLDER  | 3/12/2001  | 09/804,489 | 10/29/2002 | 6,471,170  | US      |
| NMXI-0580.00/US | Utility - ORG | Abandoned    | METHOD AND APPARATUS FOR SYNCHRONIZING NETWORK POLICIES  | 11/17/2000 | 09/715,529 |            |            | US      |
| NMXI-0581.00/US | Utility - ORG | Abandoned    | METHOD AND APPARATUS FOR MAPPING IN ONE STAGE AND MERGING AND REMAPPING IN A SECOND STAGE A PLURALITY OF INSTRUCTION GROUPS TO A PLURALITY OF FUNCTIONAL UNITS | 12/28/2000 | 09/749,725 |            |            | US      |
| NMXI-0582.00/US | Utility - ORG | Issued       | METHOD FOR READING A STRUCTURAL PHASE-CHANGE MEMORY  | 8/2/2001   | 09/921,853 | 7/8/2003   | 6,590,807  | US      |
| NMXI-0582.01/US | Utility - CON | Issued       | METHOD FOR READING A STRUCTURAL PHASE-CHANGE MEMORY  | 6/10/2003  | 10/458,828 | 3/16/2004  | 6,707,712  | US      |
| NMXI-0584.00/PR | Prov - ORG    | Expired      | SINGLE-CHIP NETWORK PROCESSOR AND INTERFACE SYSTEM   | 6/27/1998  | 60/090,939 |            |            | US      |
| NMXI-0585.00/US | Utility - ORG | Abandoned    | NETWORK ACCELERATOR SUBSYSTEM BASED ON SINGLE-CHIP NETWORK   | 6/25/1999  | 09/344,453 |            |            | US      |
| NMXI-0586.00/US | Utility - ORG | Issued       | TWO-DIMENSIONAL QUEUEING/DE-QUEUEING METHODS AND SYSTEMS FOR IMPLEMENTING THE SAME   | 3/16/1999  | 09/271,061 | 4/20/2004  | 6,724,767  | US      |
| NMXI-0587.00/US | Utility - ORG | Issued       | SYSTEMS AND METHODS FOR IMPLEMENTING GABR WITH GUARANTEED MCR  | 6/17/1999  | 09/335,223 | 12/2/2003  | 6,657,959  | US      |
| NMXI-0588.00/US | Utility - ORG | Issued       | SYSTEMS AND METHODS FOR ON-CHIP STORAGE OF VIRTUAL CONNECTION DESCRIPTORS  | 3/16/1999  | 09/270,287 | 10/30/2001 | 6,311,212  | US      |
| NMXI-0589.00/US | Utility - ORG | Issued       | SYSTEM AND METHOD FOR PERFORMING CUT-THROUGH FORWARDING IN AN ATM NETWORK SUPPORTING LAN EMULATION   | 6/25/1999  | 09/344,608 | 4/27/2004  | 6,728,249  | US      |
| NMXI-0590.00/US | Utility - ORG | Issued       | CBR/ABR TRAFFIC SCHEDULER  | 6/25/1999  | 09/344,820 | 12/31/2002 | 6,501,731  | US      |
| NMXI-0591.00/US | Utility - ORG | Issued       | SYSTEM FOR MULTI-LAYER BROADBAND PROVISIONING IN COMPUTER NETWORKS   | 6/25/1999  | 09/340,068 | 9/23/2003  | 6,625,650  | US      |
| NMXI-0592.00/US | Utility - ORG | Issued       | MULTI-PROTOCOL CONVERSION ASSISTANCE METHOD AND SYSTEM FOR A NETWORK ACCELERATOR   | 6/25/1999  | 09/344,672 | 8/5/2003   | 6,603,768  | US      |
| NMXI-0593.00/US | Utility - ORG | Issued       | SYSTEMS AND METHODS FOR IMPLEMENTING POINTER MANAGEMENT  | 6/25/1999  | 09/340,282 | 7/23/2002  | 6,425,067  | US      |
| NMXI-0594.00/US | Utility - ORG | Issued       | APPLICATION PROGRAMMING INTERFACES AND METHODS ENABLING A HOST TO INTERFACE WITH A NETWORK PROCESSOR   | 6/25/1999  | 09/337,025 | 8/5/2003   | 6,604,136  | US      |
| NMXI-0595.00/US | Utility - ORG | Issued       | MATCHING ROUTED CALL TO AGENT WORKSTATION  | 12/29/2000 | 09/750,030 | 9/14/2004  | 6,792,102  | US      |
| NMXI-0596.00/US | Utility - ORG | Issued       | AUTOMATIC DETECTING WHEN AN AGENT IS AVAILABLE   | 12/29/2000 | 09/750,025 | 7/10/2007  | 7,242,760  | US      |
| NMXI-0597.00/US | Utility - CON | Abandoned    | RECOGNIZING SIGNALS IN DESIGN SIMULATION   | 11/21/2005 | 11/285,565 |            |            | US      |
| NMXI-0598.00/US | Utility - ORG | Issued       | SYSTEM AND METHOD FOR PROTECTED MESSAGING  | 12/28/2000 | 09/752,882 | 12/13/2005 | 6,976,172  | US      |
| NMXI-0599.00/US | Utility - ORG | Issued       | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL  | 12/28/2000 | 09/749,745 | 3/20/2007  | 7,194,071  | US      |

## Exhibit A - US Applications and Issued Patents

| MT-DoctoNo      | MatterType    | MatterStatus | Title  | FileDate   | SerialNo   | IssueDate  | PatentNo  | Country |
|-----------------|---------------|--------------|--|------------|------------|------------|-----------|---------|
| NMXI-0600.00/US | Utility - ORG | Issued       | DEVICE AND METHOD OF INTERFACING VOICE DATA BETWEEN A SWITCH AND A COMPUTER SYSTEM                       | 4/13/2001  | 09/834,713 | 3/1/2005   | 6,862,341 | US      |
| NMXI-0601.00/US | Utility - ORG | Issued       | COLOR FILTER ARRAY AND COLOR INTERPOLATION ALGORITHM   | 11/30/2000 | 09/727,038 | 7/12/2005  | 6,917,381 | US      |
| NMXI-0602.00/US | Utility - DIV | Abandoned    | PHYSICALLY INTERACTING WITH A PROCESSOR-BASED DISPLAY  | 12/7/2004  | 11/005,694 |            |           | US      |
| NMXI-0603.00/US | Utility - ORG | Abandoned    | LOCATING A POSITION ON A DISPLAY SCREEN  | 4/18/2001  | 09/836,978 |            |           | US      |
| NMXI-0604.00/US | Utility - ORG | Issued       | METAL STRUCTURE FOR A PHASE-CHANGE MEMORY DEVICE   | 12/21/2000 | 09/745,835 | 5/27/2003  | 6,569,705 | US      |
| NMXI-0604.01/US | Utility - DIV | Issued       | METAL STRUCTURE FOR A PHASE-CHANGE MEMORY DEVICE   | 4/17/2003  | 10/418,530 | 5/28/2004  | 6,797,979 | US      |
| NMXI-0605.00/US | Utility - ORG | Issued       | METHOD FOR SWITCHING DATA STREAMS  | 11/15/2000 | 09/712,185 | 1/11/2005  | 6,842,452 | US      |
| NMXI-0606.00/US | Utility - ORG | Issued       | REMOVING REDUNDANT INFORMATION IN HYBRID BRANCH PREDICTION   | 10/3/2001  | 09/970,485 | 4/10/2007  | 7,203,825 | US      |
| NMXI-0607.00/US | Utility - ORG | Issued       | METHOD OF CREATING A PHOTONIC VIA USING FIBER OPTIC  | 12/19/2000 | 09/741,520 | 11/18/2003 | 6,650,823 | US      |
| NMXI-0608.00/US | Utility - ORG | Issued       | METHOD OF CREATING AN ANGLED WAVEGUIDE USING LITHOGRAPHIC TECHNIQUES                                     | 12/19/2000 | 09/741,237 | 2/21/2006  | 7,000,434 | US      |
| NMXI-0609.00/US | Utility - ORG | Issued       | MARKINGS FOR ALIGNING FIBER OPTIC BUNDLE   | 12/19/2000 | 09/741,642 | 10/21/2003 | 6,636,671 | US      |
| NMXI-0609.01/US | Utility - DIV | Issued       | MARKINGS FOR ALIGNING FIBER OPTIC BUNDLE   | 9/26/2003  | 10/672,329 | 8/9/2005   | 6,928,216 | US      |
| NMXI-0610.00/US | Utility - ORG | Issued       | ALIGNMENT OF OPTICAL FIBERS TO AN ETCHED ARRAY WAVEGUIDE   | 12/15/2000 | 09/738,800 | 9/30/2003  | 6,628,865 | US      |
| NMXI-0611.00/US | Utility - ORG | Issued       | ALIGNMENT OF FIBER OPTIC BUNDLE TO ARRAY WAVEGUIDE USING AN EPOXY  | 12/15/2000 | 09/738,799 | 3/16/2004  | 6,707,970 | US      |
| NMXI-0612.00/US | Utility - ORG | Issued       | ALIGNMENT OF FIBER OPTIC BUNDLE TO ARRAY WAVEGUIDE USING PINS  | 12/15/2000 | 09/738,686 | 5/3/2005   | 6,886,989 | US      |
| NMXI-0613.00/US | Utility - ORG | Issued       | METHOD FOR CENTERING A CORE OF A WAVE GUIDE AMPLIFIER  | 3/30/2001  | 09/822,380 | 2/17/2004  | 6,694,076 | US      |
| NMXI-0614.00/US | Utility - ORG | Issued       | METHOD AND SYSTEM FOR DYNAMIC AND INTERACTIVE ROUTE FINDING  | 3/28/2001  | 09/818,993 | 11/19/2002 | 6,484,092 | US      |
| NMXI-0615.00/US | Utility - ORG | Issued       | CLOCK NETWORK  | 12/12/2000 | 09/735,358 | 12/16/2008 | 7,466,180 | US      |
| NMXI-0616.00/US | Utility - ORG | Issued       | PACKAGING AND ASSEMBLY METHOD FOR OPTICAL COUPLING   | 6/26/2001  | 09/892,921 | 1/28/2003  | 6,512,861 | US      |
| NMXI-0617.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR MULTIPLE ACCUMULATE TWO-DIMENSIONAL SEPARABLE SAMETRIC FILTERING                | 11/20/2000 | 09/718,877 | 6/15/2004  | 6,751,640 | US      |
| NMXI-0618.00/US | Utility - ORG | Issued       | HIGH-SPEED DIGITAL DISTRIBUTION SYSTEM   | 5/25/1999  | 09/318,287 | 9/10/2002  | 6,449,308 | US      |
| NMXI-0619.00/US | Utility - ORG | Issued       | DYNAMIC DELAYED TRANSACTION BUFFER CONFIGURATION BASED ON BUS FREQUENCY                                  | 8/20/2001  | 09/933,547 | 4/5/2005   | 6,877,060 | US      |
| NMXI-0620.00/US | Utility - ORG | Issued       | INITIALIZATION OF NEGATIVE CHARGE PUMP   | 3/15/2001  | 09/809,623 | 4/22/2003  | 6,552,600 | US      |
| NMXI-0621.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR SWITCHING AN OPTICAL BEAM IN A SEMICONDUCTOR SUBSTRATE                          | 3/27/2001  | 09/819,160 | 8/5/2003   | 6,603,893 | US      |
| NMXI-0622.00/US | Utility - ORG | Issued       | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (AWG) AND OPTICAL AMPLIFIER(S) | 10/17/2001 | 09/982,227 | 5/23/2006  | 7,050,663 | US      |
| NMXI-0623.00/US | Utility - DIV | Issued       | USE OF MEMBRANE PROPERTIES TO REDUCE RESIDUAL STRESS IN AN INTERLAYER REGION                             | 9/10/2002  | 10/738,081 | 9/2/2003   | 6,614,065 | US      |
| NMXI-0623.01/US | Utility - CON | Issued       | USE OF MEMBRANE PROPERTIES TO REDUCE RESIDUAL STRESS IN AN INTERLAYER REGION                             | 9/18/2002  | 10/246,273 | 7/22/2003  | 6,596,614 | US      |
| NMXI-0624.00/US | Utility - ORG | Issued       | APPARATUS AND METHOD FOR REMOVING PHOTOMASK CONTAMINATION AND CONTROLLING ELECTROSTATIC DISCHARGE        | 5/8/2001   | 09/850,766 | 5/11/2004  | 6,734,443 | US      |
| NMXI-0625.00/US | Utility - ORG | Issued       | APPARATUS FOR ENHANCED RATE CHEMICAL MECHANICAL POLISHING WITH ADJUSTABLE SELECTIVITY                    | 12/22/2000 | 09/746,470 | 12/3/2002  | 6,488,571 | US      |
| NMXI-0625.01/US | Utility - DIV | Issued       | APPARATUS FOR ENHANCED RATE CHEMICAL MECHANICAL POLISHING WITH ADJUSTABLE SELECTIVITY                    | 8/6/2002   | 10/233,776 | 6/14/2005  | 6,905,397 | US      |
| NMXI-0626.00/US | Utility - ORG | Issued       | VIA-IN-PAD WITH OFF-CENTER GEOMETRY  | 12/29/2000 | 09/751,614 | 12/21/2004 | 6,833,615 | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Mat. Doctm      | Mat. Type     | Mat. Status | Title   | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|-------------|---|------------|------------|------------|------------|---------|
| NMXI-0626.01/US | Utility - DIV | Issued      | METHODS OF FABRICATING A VIA-IN-PAD WITH OFF-CENTER GEOMETRY  | 11/22/2004 | 10/994,995 | 4/24/2007  | 7,208,348  | US      |
| NMXI-0627.00/US | Utility - ORG | Issued      | CONNECTOR ASSEMBLY WITH DECOUPLING CAPACITORS   | 5/15/2001  | 09/858,224 | 9/16/2003  | 6,621,287  | US      |
| NMXI-0627.01/US | Utility - DIV | Issued      | CONNECTOR ASSEMBLY WITH DECOUPLING CAPACITORS   | 8/25/2003  | 10/647,396 | 5/31/2005  | 6,888,852  | US      |
| NMXI-0628.00/US | Utility - ORG | Issued      | PORTABLE DOWNLOAD UNIT INCLUDING A MEMORY CHIP-TO-CD BURNER   | 10/23/2001 | 09/999,784 | 9/9/2008   | 7,424,206  | US      |
| NMXI-0629.00/US | Utility - ORG | Issued      | INTERCONNECTING CIRCUIT MODULES TO A MOTHERBOARD USING AN EDGE CONNECTOR WITH CONDUCTIVE POLYMER CONTACTS   | 5/4/2001   | 09/849,506 | 10/8/2002  | 6,461,169  | US      |
| NMXI-0630.00/PR | Prov - ORG    | Expired     | TIME VARYING FILTER WITH ZERO   | 9/7/2001   | 60/317,802 |            |            | US      |
| NMXI-0631.00/US | Utility - ORG | Abandoned   | METHOD OF SCHEDULING MODULES ON A CAROUSEL  | 6/29/2001  | 09/895,113 |            |            | US      |
| NMXI-0632.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR DETECTING AC REMOVAL   | 3/9/2001   | 09/802,451 | 8/3/2004   | 6,772,366  | US      |
| NMXI-0633.00/US | Utility - ORG | Issued      | MULTIPLIERLESS PYRAMID FILTER   | 1/3/2001   | 09/754,684 | 12/9/2003  | 6,662,200  | US      |
| NMXI-0634.00/US | Utility - ORG | Issued      | LOAD FOR NON-VOLATILE MEMORY DRAIN BIAS   | 12/29/2000 | 09/752,535 | 5/27/2003  | 6,570,789  | US      |
| NMXI-0635.00/US | Utility - ORG | Issued      | KICKER FOR NON-VOLATILE MEMORY DRAIN BIAS   | 12/29/2000 | 09/752,550 | 6/1/2004   | 6,744,671  | US      |
| NMXI-0636.00/US | Utility - ORG | Issued      | DUAL-SWIPING INTERCONNECTION CLIP, AND HOOK AND SLOT ARRANGEMENT FOR PRINTED CIRCUIT BOARD (PCB) ATTACHMENT | 3/30/2001  | 09/821,114 | 4/8/2003   | 6,544,047  | US      |
| NMXI-0637.00/US | Utility - ORG | Issued      | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION   | 1/2/2002   | 10/039,095 | 1/13/2004  | 6,677,233  | US      |
| NMXI-0638.00/US | Utility - ORG | Abandoned   | LARGE FORMAT EMISSIVE DISPLAY   | 5/2/2001   | 09/847,447 |            |            | US      |
| NMXI-0638.01/US | Utility - DIV | Issued      | LARGE FORMAT EMISSIVE DISPLAY   | 1/5/2006   | 11/325,961 | 2/2/2010   | 7,654,878  | US      |
| NMXI-0639.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR INTERSYSTEM CUT/COPY AND PASTE   | 3/30/2001  | 09/823,215 | 11/21/2006 | 7,139,842  | US      |
| NMXI-0640.00/US | Utility - ORG | Issued      | METHOD AND APPARATUS FOR COMPRESSING CALDRE BURN CALCULATION DATA USING POLYNOMIAL COEFFICIENTS             | 1/30/2001  | 09/773,174 | 2/10/2004  | 6,689,057  | US      |
| NMXI-0641.00/US | Utility - ORG | Issued      | ON-DIE CACHE MEMORY WITH REPEATERS  | 6/27/2001  | 09/893,779 | 11/11/2003 | 6,647,455  | US      |
| NMXI-0642.00/US | Utility - ORG | Issued      | AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT                                 | 4/19/2001  | 09/888,730 | 8/17/2004  | 6,777,970  | US      |
| NMXI-0642.01/US | Utility - DIV | Issued      | AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT                                 | 7/12/2004  | 10/889,417 | 11/22/2005 | 6,967,496  | US      |
| NMXI-0642.02/US | Utility - DIV | Issued      | AC TESTING OF LEAKAGE CURRENT IN INTEGRATED CIRCUITS USING RC TIME CONSTANT                                 | 10/19/2005 | 11/253,377 | 3/25/2008  | 7,348,790  | US      |
| NMXI-0643.00/US | Utility - ORG | Issued      | FASTENER INSTALLATION TOOL  | 3/29/2001  | 09/821,247 | 9/23/2003  | 6,622,802  | US      |
| NMXI-0643.01/US | Utility - CON | Issued      | FASTENER INSTALLATION TOOL  | 7/28/2003  | 10/628,597 | 8/5/2008   | 7,407,070  | US      |
| NMXI-0643.02/US | Utility - DIV | Issued      | FASTENER INSTALLATION TOOLS, SYSTEMS, AND METHODS OF USE  | 9/5/2003   | 10/655,822 | 5/23/2006  | 7,048,073  | US      |
| NMXI-0643.03/US | Utility - DIV | Abandoned   | FASTENER INSTALLATION TOOLS, SYSTEMS, AND METHODS OF USE  | 4/3/2006   | 11/396,769 |            |            | US      |
| NMXI-0644.00/US | Utility - ORG | Issued      | INTEGRATED CIRCUIT ANNEALING METHODS AND APPARATUS  | 4/25/2001  | 09/842,389 | 1/6/2004   | 6,675,057  | US      |
| NMXI-0645.00/US | Utility - ORG | Issued      | DIGITAL LEAKAGE COMPENSATION CIRCUIT  | 3/29/2001  | 09/823,191 | 5/28/2002  | 6,396,305  | US      |
| NMXI-0646.00/US | Utility - ORG | Issued      | A METHOD OF REDUCING RESIDUE DEPOSITION ONTO ASH CHAMBER BASE SURFACES                                      | 3/26/2001  | 09/818,177 | 5/20/2003  | 6,564,811  | US      |
| NMXI-0647.00/US | Utility - ORG | Abandoned   | NETWORK COMMUNICATION CASING  | 9/28/2001  | 09/964,502 |            |            | US      |
| NMXI-0648.00/US | Utility - ORG | Issued      | DYNAMIC LOCAL DRIVE AND PRINTER SHARING   | 7/27/2001  | 09/917,320 | 1/15/2008  | 7,320,033  | US      |
| NMXI-0649.00/US | Utility - ORG | Issued      | CACHE ARCHITECTURE FOR PIPELINED OPERATION WITH ON-DIE PROCESSOR  | 6/27/2001  | 09/894,513 | 10/7/2003  | 6,631,444  | US      |
| NMXI-0650.00/US | Utility - ORG | Issued      | CACHE ARCHITECTURE WITH REDUNDANT SUB ARRAY   | 6/27/2001  | 09/894,638 | 12/9/2003  | 6,662,271  | US      |
| NMXI-0651.00/US | Utility - ORG | Issued      | METHOD AND SYSTEM FOR SOFTWARE MODULARIZATION AND AUTOMATIC CODE GENERATION FOR EMBEDDED SYSTEMS            | 9/17/2001  | 09/953,277 | 3/7/2006   | 7,010,780  | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| Matter No       | Matter Type   | Matter Status | Title  | Filed Date | Serial No  | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|-----------|---------|
| NMXI-0552.00/US | Utility - ORG | Issued        | APPARATUS HAVING A FIRST CIRCUIT SUPPLYING A POWER POTENTIAL TO A SECOND CIRCUIT UNDER A FIRST OPERATING MODE OTHERWISE DECOUPLING THE POWER POTENTIAL | 4/13/2001  | 09/834,408 | 3/7/2006   | 7,010,706 | US      |
| NMXI-0553.00/US | Utility - ORG | Issued        | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY CHOPPER STABILIZED BANDGAP REFERENCE CIRCUIT TO CANCEL OFFSET VARIATION                           | 11/29/2001 | 09/997,809 | 11/18/2003 | 6,650,589 | US      |
| NMXI-0554.00/US | Utility - ORG | Issued        | NOISE DEPENDENT FILTER   | 6/28/2001  | 09/894,151 | 10/8/2002  | 6,462,612 | US      |
| NMXI-0555.00/US | Utility - ORG | Issued        | RELEASING FUNCTIONAL BLOCKS IN RESPONSE TO A DETERMINATION OF A SUPPLY VOLTAGE PREDETERMINED LEVEL AND A LOGIC PREDETERMINED INITIAL STATE             | 6/22/2001  | 09/887,595 | 1/10/2006  | 6,985,709 | US      |
| NMXI-0556.00/US | Utility - ORG | Issued        | GENERATING PULSES FOR RESETTNG INTEGRATED CIRCUITS   | 9/28/2001  | 09/967,180 | 12/9/2003  | 6,661,264 | US      |
| NMXI-0557.00/US | Utility - ORG | Issued        | PARALLEL FILTER  | 9/28/2001  | 09/966,481 | 10/4/2005  | 6,952,122 | US      |
| NMXI-0558.00/US | Utility - ORG | Issued        | ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED   | 3/28/2001  | 09/820,108 | 7/20/2004  | 6,766,286 | US      |
| NMXI-0559.00/US | Utility - ORG | Abandoned     | VOLTAGE-LEVEL CONVERTER  | 3/26/2001  | 09/817,719 |            |           | US      |
| NMXI-0560.00/US | Utility - DIV | Issued        | PARALLEL CACHELETS   | 4/26/2006  | 11/411,647 | 4/1/2008   | 7,352,209 | US      |
| NMXI-0561.00/PR | Prov - ORG    | Expired       | PARALLEL CACHELETS   | 5/12/2001  | 60/287,707 |            |           | US      |
| NMXI-0561.00/US | Utility - ORG | Issued        | PARALLEL CACHELETS   | 6/27/2001  | 09/891,523 | 9/9/2008   | 7,424,576 | US      |
| NMXI-0561.01/US | Utility - CON | Issued        | LOOK-AHEAD CARRY ADDER CIRCUIT   | 1/9/2006   | 11/327,454 | 5/8/2007   | 7,216,201 | US      |
| NMXI-0562.00/US | Utility - ORG | Issued        | SYMMETRIC CASCADED DOMINO CARRY GENERATE CIRCUIT   | 12/18/2001 | 10/020,447 | 1/29/2008  | 7,325,025 | US      |
| NMXI-0563.00/US | Utility - ORG | Issued        | CASCADED DOMINO FOUR-TO-TWO REDUCER CIRCUIT AND METHOD   | 9/21/2001  | 09/956,403 | 9/23/2008  | 7,428,568 | US      |
| NMXI-0564.00/US | Utility - ORG | Issued        | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  | 6/29/2001  | 09/893,868 | 6/24/2008  | 7,392,277 | US      |
| NMXI-0565.00/US | Utility - ORG | Abandoned     | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  | 3/26/2001  | 09/817,711 |            |           | US      |
| NMXI-0566.00/US | Utility - ORG | Issued        | ELECTRONIC ASSEMBLIES WITH HIGH CAPACITY CURVED FIN HEAT SINKS   | 3/30/2001  | 09/823,212 | 5/3/2005   | 6,889,237 | US      |
| NMXI-0567.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR UTILIZING STATIC QUEUES IN PROCESSOR STAGING  | 9/10/2001  | 09/950,100 | 12/30/2003 | 6,671,172 | US      |
| NMXI-0568.00/US | Utility - ORG | Issued        | DUAL-TARGET BLOCK REGISTER ALLOCATION  | 6/7/2001   | 09/878,062 | 10/14/2003 | 6,633,972 | US      |
| NMXI-0569.00/US | Utility - ORG | Issued        | DATA-FLOW METHOD OF ANALYZING DEFINITIONS AND USES OF L VALUES IN PROGRAMS   | 9/28/2001  | 09/966,278 | 12/11/2007 | 7,308,563 | US      |
| NMXI-0570.00/US | Utility - ORG | Issued        | DATA-FLOW METHOD FOR OPTIMIZING EXCEPTION-HANDLING INSTRUCTIONS IN PROGRAMS  | 1/6/1999   | 09/226,804 | 4/9/2002   | 6,370,685 | US      |
| NMXI-0571.00/US | Utility - ORG | Issued        | TRENCH SIDEWALL PROFILE FOR DEVICE ISOLATION   | 4/19/2000  | 09/552,292 | 10/10/2006 | 7,120,904 | US      |
| NMXI-0573.00/US | Utility - ORG | Issued        | BARRIER MATERIAL ENCAPSULATION OF PROGRAMMABLE MATERIAL  | 6/30/2001  | 09/896,532 | 2/4/2003   | 6,514,805 | US      |
| NMXI-0574.00/US | Utility - ORG | Issued        | BARRIER MATERIAL ENCAPSULATION OF PROGRAMMABLE MATERIAL  | 6/30/2001  | 09/896,530 | 11/4/2003  | 6,644,102 | US      |
| NMXI-0574.01/US | Utility - DIV | Issued        | ADHESIVE MATERIAL FOR PROGRAMMABLE DEVICE  | 6/30/2001  | 10/418,548 | 6/20/2006  | 7,064,344 | US      |
| NMXI-0575.00/US | Utility - ORG | Issued        | REDUCED AREA INTERSECTION BETWEEN ELECTRODE AND PROGRAMMING ELEMENT  | 6/30/2001  | 09/896,008 | 8/3/2004   | 6,770,531 | US      |
| NMXI-0576.00/US | Utility - ORG | Issued        | REDUCED AREA INTERSECTION BETWEEN ELECTRODE AND PROGRAMMING ELEMENT  | 6/30/2001  | 09/896,531 | 8/12/2003  | 6,605,527 | US      |
| NMXI-0576.01/US | Utility - CON | Issued        | ASSEMBLING DISPLAY MODULES   | 5/13/2003  | 10/438,146 | 8/11/2009  | 7,572,666 | US      |
| NMXI-0577.00/US | Utility - ORG | Abandoned     | PROVIDING OPTICAL ELEMENTS OVER EMISSIVE DISPLAYS  | 7/12/2001  | 09/904,269 |            |           | US      |
| NMXI-0578.00/US | Utility - DIV | Abandoned     | PROVIDING OPTICAL ELEMENTS OVER EMISSIVE DISPLAYS  | 7/12/2001  | 09/904,268 | 3/29/2005  | 6,873,380 | US      |
| NMXI-0579.00/US | Utility - ORG | Issued        | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  | 1/11/2005  | 11/032,828 |            |           | US      |
|                 |               |               |  | 4/30/2001  | 09/846,609 | 4/20/2004  | 6,725,247 | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| MTI-DefNo       | Material Type | Material Status | Title   | Filed Date | Serial No  | Issue Date | Patent No | Country |
|-----------------|---------------|-----------------|---|------------|------------|------------|-----------|---------|
| NMXI-0580.00/US | Utility - ORG | Issued          | SYSTEM FOR SHARING CHANNELS BY INTERLEAVING FILTS   | 6/29/2001  | 09/894,136 | 2/3/2009   | 7,486,685 | US      |
| NMXI-0581.00/US | Utility - CON | Issued          | METHOD AND APPARATUS FOR REORDERING MEMORY REQUESTS FOR PAGE COHERENCY                          | 5/24/2005  | 11,137,700 | 4/1/2008   | 7,353,349 | US      |
| NMXI-0582.00/US | Utility - CON | Issued          | METHOD AND SYSTEM FOR USING INTERNAL FIFO RAM TO IMPROVE SYSTEM BOOT TIMES                      | 7/29/2005  | 11,192,025 | 7/8/2008   | 7,396,383 | US      |
| NMXI-0583.00/US | Utility - ORG | Issued          | PULSE POSITION MODULATION WITH SPREAD SPECTRUM  | 11/1/1993  | 08/146,490 | 8/5/1997   | 5,654,978 | US      |
| NMXI-0583.01/US | Utility - CIP | Issued          | BEST-OF-M PULSE POSITION MODULATION DETECTOR  | 6/6/1995   | 08/468,802 | 9/9/1997   | 5,666,379 | US      |
| NMXI-0584.00/US | Utility - CON | Issued          | APPARATUS FOR RECEIVING AND CORRELATING A SPREAD SPECTRUM SIGNAL                                | 5/7/1999   | 09/307,646 | 11/13/2001 | 6,317,452 | US      |
| NMXI-0585.00/US | Utility - ORG | Issued          | MOBILE STATION LOCATING SYSTEM AND METHOD   | 7/15/1997  | 08/893,938 | 5/4/1999   | 5,901,358 | US      |
| NMXI-0585.01/US | Utility - CON | Issued          | MOBILE STATION LOCATING SYSTEM AND METHOD   | 3/31/1999  | 09/282,281 | 10/23/2001 | 6,308,073 | US      |
| NMXI-0586.00/US | Utility - ORG | Issued          | COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER                              | 3/20/1997  | 08/823,234 | 10/26/1999 | 5,974,310 | US      |
| NMXI-0586.01/US | Utility - CON | Issued          | COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER                              | 7/15/1999  | 09/354,926 | 7/3/2001   | 6,256,492 | US      |
| NMXI-0586.02/US | Utility - CON | Issued          | COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER                              | 3/27/2001  | 09/819,460 | 6/15/2004  | 6,751,456 | US      |
| NMXI-0587.00/US | Utility - ORG | Issued          | SPREAD SPECTRUM CODES FOR USE IN COMMUNICATION  | 3/20/1997  | 08/821,611 | 8/28/2001  | 6,282,228 | US      |
| NMXI-0588.00/US | Utility - ORG | Issued          | BYPASSABLE AMPLIFIER  | 5/18/1998  | 09/080,812 | 10/24/2000 | 6,137,354 | US      |
| NMXI-0589.00/US | Utility - ORG | Issued          | MOUNTING BRACKET FOR PCS AND OTHER ANTENNAS   | 1/14/2000  | 09/483,215 | 3/26/2002  | 6,361,007 | US      |
| NMXI-0590.00/US | Utility - ORG | Issued          | COMMON BOOT ENVIRONMENT FOR A MODULAR SERVER SYSTEM   | 11/20/2001 | 10/086,410 | 6/7/2005   | 6,904,482 | US      |
| NMXI-0591.00/US | Utility - ORG | Issued          | SUCCESSIVE TEMPLATE GENERATION USING MINIMAL RANDOM ACCESS MEMORY BANDWIDTH                     | 6/28/2001  | 09/894,755 | 7/22/2003  | 6,598,133 | US      |
| NMXI-0592.00/US | Utility - ORG | Issued          | METHOD AND APPARATUS TO IMPROVE THE PROTECTION OF INFORMATION PRESENTED BY A COMPUTER           | 6/29/2001  | 09/895,738 | 7/4/2006   | 7,073,070 | US      |
| NMXI-0593.00/US | Utility - ORG | Issued          | MECHANISM TO IMPROVE AUTHENTICATION FOR REMOTE MANAGEMENT OF A COMPUTER SYSTEM                  | 10/30/2001 | 10/002,337 | 8/15/2006  | 7,093,124 | US      |
| NMXI-0593.01/US | Utility - DIV | Abandoned       | MECHANISM TO IMPROVE AUTHENTICATION FOR REMOTE MANAGEMENT OF A COMPUTER SYSTEM                  | 7/25/2005  | 11,188,420 |            |           | US      |
| NMXI-0594.00/US | Utility - ORG | Issued          | VARIABLE LEVEL MEMORY   | 9/18/2001  | 09/955,282 | 11/4/2003  | 6,643,169 | US      |
| NMXI-0594.01/US | Utility - CON | Issued          | VARIABLE LEVEL MEMORY   | 9/18/2003  | 10/666,988 | 3/22/2005  | 6,870,767 | US      |
| NMXI-0595.00/US | Utility - ORG | Issued          | MEMBRANE-ACTUATED CHARGE CONTROLLED MIRROR (CCM) PROJECTION DISPLAY                             | 12/9/1998  | 09/208,041 | 2/29/2000  | 6,031,657 | US      |
| NMXI-0596.00/US | Utility - ORG | Issued          | BEAM-ADDRESSED MICROMIRROR DIRECT VIEW DISPLAY  | 10/28/1998 | 09/179,933 | 2/29/2000  | 6,031,656 | US      |
| NMXI-0597.00/US | Utility - ORG | Issued          | CHARGE CONTROLLED MIRROR WITH IMPROVED FRAME TIME UTILIZATION AND METHOD OF ADDRESSING THE SAME | 10/15/1998 | 09/172,615 | 2/22/2000  | 6,028,696 | US      |
| NMXI-0598.00/US | Utility - CON | Issued          | SAWC-PHASE DETECTION METHOD AND APPARATUS   | 11/4/1992  | 07/971,689 | 1/4/1994   | 5,276,704 | US      |
| NMXI-0599.00/US | Utility - ORG | Issued          | PCB-TO-CHASSIS MOUNTING SCHEMES   | 8/21/2001  | 09/934,871 | 12/10/2002 | 6,493,233 | US      |
| NMXI-0700.00/US | Utility - CIP | Abandoned       | METHOD AND APPARATUS TO EMULATE EXTERNAL IO INTERCONNECTION                                     | 10/16/2001 | 09/982,242 |            |           | US      |
| NMXI-0701.00/US | Utility - DIV | Abandoned       | METHOD AND APPARATUS FOR ACCESSING THREAD-PRIVATIZED GLOBAL STORAGE OBJECTS                     | 5/19/2006  | 11,143,352 |            |           | US      |
| NMXI-0702.00/US | Utility - ORG | Issued          | REPAIRABLE TILED DISPLAYS   | 7/16/2001  | 09/906,342 | 10/2/2007  | 7,277,066 | US      |
| NMXI-0702.01/US | Utility - CON | Abandoned       | REPAIRABLE TILED DISPLAYS   | 1/18/2006  | 11,334,729 |            |           | US      |
| NMXI-0703.00/US | Utility - ORG | Abandoned       | PARAMETRIC REPRESENTATION METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN          | 12/3/2002  | 10/309,472 |            |           | US      |

## Exhibit A - US Applications and Issued Patents

| Patent No.      | Matter Type   | Matter Status | Title   | Filed Date | Serial No. | Issue Date | Patenting | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|-----------|---------|
| NMXI-0704.00/US | Utility - ORG | Issued        | AUTOMATIC SYMBOLIC INDEXING METHODS FOR FORMAL VERIFICATION ON A SYMBOLIC LATTICE DOMAIN  | 12/3/2002  | 10/309,529 | 12/18/2007 | 7,310,790 | US      |
| NMXI-0705.00/US | Utility - ORG | Issued        | DIGITAL UPDATE SCHEME FOR ADAPTIVE IMPEDANCE CONTROL OF ON-DIE INPUT/OUTPUT CIRCUITS  | 9/28/2001  | 09/565,627 | 6/3/2003   | 6,573,747 | US      |
| NMXI-0706.00/US | Utility - ORG | Issued        | STOPPING REPLAY TORNADOES   | 12/31/2001 | 10/039,588 | 10/4/2005  | 6,952,764 | US      |
| NMXI-0707.00/US | Utility - ORG | Issued        | WRITE-ONCE POLYMER MEMORY WITH E-BEAM WRITING AND READING   | 9/27/2001  | 09/566,014 | 9/23/2003  | 6,625,052 | US      |
| NMXI-0707.01/US | Utility - DIV | Issued        | WRITE-ONCE POLYMER MEMORY WITH E-BEAM WRITING AND READING   | 10/28/2002 | 10/282,534 | 11/4/2003  | 6,643,161 | US      |
| NMXI-0708.00/US | Utility - ORG | Issued        | GLOBAL I/O TIMING ADJUSTMENT USING CALIBRATED DELAY ELEMENTS  | 9/28/2001  | 09/965,223 | 3/27/2007  | 7,197,659 | US      |
| NMXI-0709.00/US | Utility - ORG | Issued        | SLURRY FOR POLISHING A BARRIER LAYER  | 11/30/2001 | 10/002,855 | 4/13/2004  | 6,719,920 | US      |
| NMXI-0710.00/US | Utility - ORG | Issued        | ELECTRONIC ASSEMBLIES WITH FILLED NO-FLOW UNDERFILL   | 10/26/2001 | 10/003,238 | 1/29/2008  | 7,323,360 | US      |
| NMXI-0711.00/US | Utility - ORG | Issued        | A ZERO INSERTION FORCE CONNECTOR FOR SUBSTRATES WITH EDGE CONTACTS  | 9/28/2001  | 09/567,060 | 1/6/2004   | 6,672,891 | US      |
| NMXI-0712.00/US | Utility - ORG | Issued        | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS | 2/24/1998  | 09/028,367 | 11/2/1999  | 5,978,591 | US      |
| NMXI-0712.01/US | Utility - CIP | Issued        | A WALLET DETACHABLY RETAINING A PERSONAL INFORMATION DEVICE WHICH SUPPLIES A WAKE SIGNAL TO THE WALLET TO TURN ON AN INPUT DEVICE OF THE WALLET   | 11/11/1998 | 09/189,572 | 4/24/2001  | 6,223,233 | US      |
| NMXI-0712.02/US | Utility - CIP | Issued        | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS | 8/5/1999   | 09/368,878 | 3/13/2001  | 6,202,209 | US      |
| NMXI-0712.03/US | Utility - CON | Issued        | WALLET DETACHABLY RETAINING A PERSONAL INFORMATION DEVICE AND RECEIVING A WAKE SIGNAL AND A STAY AWAKE SIGNAL FROM THE PERSONAL INFORMATION DEVICE  | 4/12/2001  | 09/834,167 | 1/18/2005  | 6,845,455 | US      |
| NMXI-0713.00/US | Utility - CON | Issued        | RECOVERY OF BIT-ROTATED FRAMES DURING FACSIMILE TRANSMISSIONS IN A GLOBAL SYSTEM FOR MOBILE COMMUNICATIONS (GSM) NETWORK  | 7/19/2002  | 10/199,382 | 5/17/2005  | 6,894,801 | US      |
| NMXI-0714.00/US | Utility - ORG | Abandoned     | PROTOCOL DRIVER APPLICATION PROGRAMMING INTERFACE FOR OPERATING SYSTEMS   | 1/31/2002  | 10/062,863 |            |           | US      |
| NMXI-0715.00/US | Utility - ORG | Issued        | LOCKED CONTENT ADDRESSABLE MEMORY FOR EFFICIENT ACCESS METHODS AND APPARATUS USING A FIBER-FLEXURE INDUCTION SOLDERING STATION  | 1/9/2002   | 10/042,905 | 8/17/2004  | 6,779,089 | US      |
| NMXI-0716.00/US | Utility - ORG | Issued        | PROPERTY MANAGEMENT SYSTEM PROTOCOL AUTO-DETECTION INTELLIGENT FORWARDED TELEPHONE CALL HANDLING WITH A CALL ANSWERING SYSTEM   | 11/25/2002 | 10/304,195 | 3/20/2007  | 7,194,182 | US      |
| NMXI-0717.00/US | Utility - ORG | Issued        | PROPERTY MANAGEMENT SYSTEM PROTOCOL AUTO-DETECTION INTELLIGENT FORWARDED TELEPHONE CALL HANDLING WITH A CALL ANSWERING SYSTEM   | 1/3/2002   | 10/038,917 | 9/18/2007  | 7,272,211 | US      |
| NMXI-0718.00/US | Utility - ORG | Issued        | DIGITAL FRAME DETERMINATION METHOD AND APPARATUS  | 9/28/2001  | 09/967,108 | 2/27/2007  | 7,184,536 | US      |
| NMXI-0719.00/US | Utility - ORG | Issued        | SOFTWARE CALL CONTROL AGENT   | 1/4/2002   | 10/040,689 | 10/12/2004 | 6,804,317 | US      |
| NMXI-0720.00/US | Utility - ORG | Issued        | PROBABALISTIC NETWORKS FOR DETECTING SIGNAL CONTENT   | 9/28/2001  | 09/967,367 | 2/8/2005   | 6,853,720 | US      |
| NMXI-0721.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR MODIFYING GRAPHICS CONTENT PRIOR TO DISPLAY FOR COLOR BLIND USE  | 9/25/2001  | 09/963,177 | 11/14/2006 | 7,136,813 | US      |
| NMXI-0722.00/US | Utility - ORG | Issued        |   | 11/21/2001 | 09/991,629 | 8/16/2005  | 6,931,151 | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| MTS/DocNo       | MatterType    | MatterStatus | Title  | FiledDate  | SerialNo   | IssueDate  | PatentNo  | Country |
|-----------------|---------------|--------------|--|------------|------------|------------|-----------|---------|
| NMX1-0723.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR SOFTWARE SELECTION OF PROTECTED REGISTER SETTINGS                 | 11/14/2001 | 09/991,128 | 6/7/2005   | 6,904,504 | US      |
| NMX1-0724.00/US | Utility - ORG | Issued       | SYNCHRONIZING DATA OR SIGNAL TRANSFER ACROSS CLOCKED LOGIC DOMAINS                         | 1/2/2002   | 10/038,956 | 1/9/2007   | 7,161,999 | US      |
| NMX1-0725.00/US | Utility - ORG | Issued       | FLASH MEMORY PROGRAM AND ERASE OPERATIONS  | 11/13/2001 | 09/993,336 | 8/30/2005  | 6,937,948 | US      |
| NMX1-0726.00/US | Utility - ORG | Issued       | CHARGE PUMP RIPLE REDUCTION  | 1/2/2002   | 10/038,499 | 8/12/2003  | 6,605,984 | US      |
| NMX1-0726.01/US | Utility - CIP | Issued       | CHARGE PUMP RIPLE REDUCTION  | 6/27/2002  | 10/185,419 | 12/28/2004 | 6,836,176 | US      |
| NMX1-0727.00/US | Utility - ORG | Issued       | VOLTAGE DRIVER FOR A MEMORY  | 8/31/2001  | 09/945,021 | 9/10/2002  | 6,449,211 | US      |
| NMX1-0728.00/US | Utility - ORG | Issued       | PHASE CHANGE MATERIAL MEMORY DEVICE  | 9/7/2001   | 09/948,880 | 7/1/2003   | 6,586,761 | US      |
| NMX1-0728.01/US | Utility - DIV | Issued       | PHASE CHANGE MATERIAL MEMORY DEVICE  | 4/29/2003  | 10/426,380 | 6/21/2005  | 6,908,812 | US      |
| NMX1-0729.00/US | Utility - ORG | Issued       | REDUCING SHUNTS IN MEMORIES WITH PHASE-CHANGE MATERIAL                                     | 9/17/2001  | 09/953,833 | 3/1/2005   | 6,861,267 | US      |
| NMX1-0729.01/US | Utility - DIV | Issued       | REDUCING SHUNTS IN MEMORIES WITH PHASE-CHANGE MATERIAL                                     | 1/19/2005  | 11/038,336 | 1/9/2007   | 7,161,225 | US      |
| NMX1-0730.00/US | Utility - ORG | Expired      | DOUBLE DENSITY BACKWARD AND FORWARD COMPATIBLE CARD EDGE CONNECTOR SYSTEM                  | 8/14/1990  | 07/567,123 | 10/29/1991 | 5,061,190 | US      |
| NMX1-0731.00/US | Utility - ORG | Issued       | MULTIPLE LAYER PHASE-CHANGE MEMORY   | 8/31/2001  | 09/945,331 | 1/14/2003  | 6,507,061 | US      |
| NMX1-0731.01/US | Utility - DIV | Issued       | MULTIPLE LAYER PHASE-CHANGE MEMORY   | 11/21/2002 | 10/302,421 | 2/14/2006  | 6,998,289 | US      |
| NMX1-0731.02/US | Utility - CON | Issued       | MULTIPLE LAYER PHASE-CHANGE MEMORY   | 12/4/2002  | 10/309,633 | 1/6/2004   | 6,674,115 | US      |
| NMX1-0732.00/US | Utility - ORG | Issued       | SIGNAL TO NOISE RATIO OPTIMIZATION FOR VIDEO COMPRESSION BIT-RATE CONTROL                  | 9/5/2001   | 09/947,331 | 5/10/2005  | 6,891,889 | US      |
| NMX1-0733.00/US | Utility - ORG | Issued       | EMBEDDED PCB IDENTIFICATION  | 9/11/2001  | 09/954,402 | 1/8/2008   | 7,317,310 | US      |
| NMX1-0734.00/US | Utility - ORG | Issued       | OPTOELECTRICAL PACKAGE   | 3/28/2002  | 10/109,313 | 8/26/2008  | 7,418,163 | US      |
| NMX1-0735.00/US | Utility - ORG | Issued       | ULTRA-THIN POLARIZATION MODE CONVERTERS BASED ON LIQUID CRYSTAL MATERIALS                  | 10/7/2002  | 10/265,873 | 8/9/2005   | 6,926,200 | US      |
| NMX1-0735.01/US | Utility - CON | Issued       | ULTRA-THIN POLARIZATION MODE CONVERTERS BASED ON LIQUID CRYSTAL MATERIALS                  | 7/26/2005  | 11/189,543 | 2/6/2007   | 7,174,060 | US      |
| NMX1-0736.00/US | Utility - CIP | Issued       | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING                                | 3/28/2002  | 10/109,811 | 11/23/2004 | 6,822,535 | US      |
| NMX1-0736.01/US | Utility - DIV | Issued       | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING                                | 9/16/2004  | 10/942,147 | 12/26/2006 | 7,154,358 | US      |
| NMX1-0737.00/US | Utility - ORG | Issued       | 1 X N FANOUT WAVEGUIDE PHOTODETECTOR   | 12/7/2001  | 10/008,922 | 2/15/2005  | 6,856,733 | US      |
| NMX1-0737.01/US | Utility - DIV | Issued       | 1 X N FANOUT WAVEGUIDE PHOTODETECTOR   | 1/25/2005  | 11/042,537 | 6/19/2007  | 7,233,725 | US      |
| NMX1-0738.00/US | Utility - DIV | Issued       | METHOD FOR REDUCING SOCKET WARPAGE   | 1/15/2004  | 10/758,055 | 4/1/2008   | 7,350,299 | US      |
| NMX1-0738.01/US | Utility - DIV | Abandoned    | SOCKET WARPAGE REDUCTION APPARATUS   | 1/31/2008  | 12/023,521 |            |           | US      |
| NMX1-0739.00/US | Utility - ORG | Issued       | EXTENSION MECHANISM AND METHOD FOR ASSEMBLING OVERHANGING COMPONENTS                       | 9/28/2001  | 09/964,747 | 10/5/2004  | 6,801,436 | US      |
| NMX1-0739.01/US | Utility - DIV | Issued       | EXTENSION MECHANISM AND METHOD FOR ASSEMBLING OVERHANGING COMPONENTS                       | 8/12/2004  | 10/918,081 | 7/12/2005  | 6,917,524 | US      |
| NMX1-0740.00/US | Utility - ORG | Issued       | METHOD AND STRUCTURE FOR IDENTIFYING LEAD-FREE SOLDER                                      | 9/28/2001  | 09/964,746 | 8/7/2007   | 7,251,880 | US      |
| NMX1-0742.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR SYSTEM MANAGEMENT APPLICATIONS USING A LOCAL CONTROLLER           | 3/22/2002  | 10/104,341 | 9/19/2006  | 7,111,103 | US      |
| NMX1-0742.01/US | Utility - DIV | Issued       | METHOD AND APPARATUS FOR GENERATING TRAFFIC IN AN ELECTRONIC BRIDGE VIA A LOCAL CONTROLLER | 8/3/2006   | 11/462,264 | 3/18/2008  | 7,346,725 | US      |
| NMX1-0743.00/US | Utility - ORG | Issued       | SYSTEM AND METHOD TO SYNCHRONIZE TWO OR MORE OBJECT MANAGEMENT SYSTEMS                     | 1/23/2002  | 10/055,528 | 7/5/2005   | 6,915,522 | US      |
| NMX1-0744.00/US | Utility - ORG | Issued       | DIELECTRIC MATERIAL TREATMENT  | 9/23/2002  | 10/252,295 | 5/11/2004  | 6,734,118 | US      |
| NMX1-0745.00/US | Utility - ORG | Issued       | MEMORY SYSTEM ORGANIZED INTO BLOCKS OF DIFFERENT SIZES AND ALLOCATION METHOD THEREFOR      | 9/17/2001  | 09/953,356 | 2/1/2005   | 6,851,027 | US      |

| Title | File |
|-------|------|
|       |      |

| MIT DocId       | Matter Type   | Matter Status | Title  | Filed Date | Serial No  | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|-----------|---------|
| NMX1-0746.00/US | Utility - ORG | Issued        | SCALABLE RECEIVER STRUCTURE FOR EFFICIENT BIT SEQUENCE DECODING  | 6/30/1997  | 08/885,803 | 5/30/2000  | 6,069,928 | US      |
| NMX1-0747.00/PR | Prov - ORG    | Expired       | DIGITAL IMPAIRMENT LEARNING SEQUENCE   | 6/24/1999  | 60/140,705 |            |           | US      |
| NMX1-0748.00/US | Utility - ORG | Issued        | PAD AND CODEC DETECTION  | 6/22/2000  | 09/598,994 | 4/20/2004  | 6,724,814 | US      |
| NMX1-0749.00/US | Utility - DIV | Issued        | DEVICE AND METHOD FOR MAXIMIZING PERFORMANCE ON A MEMORY INTERFACE WITH A VARIABLE NUMBER OF CHANNELS                    | 5/24/2005  | 11/137,314 | 4/21/2009  | 7,523,230 | US      |
| NMX1-0750.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR HEADER UPDATING   | 1/7/2002   | 10/041,543 | 9/22/2009  | 7,593,318 | US      |
| NMX1-0751.00/US | Utility - CON | Issued        | METHOD AND APPARATUS FOR ANALOG COMPENSATION OF DRIVER OUTPUT SIGNAL SLEW RATE AGAINST DEVICE IMPEDANCE VARIATION        | 3/31/2004  | 10/814,398 | 4/22/2008  | 7,363,595 | US      |
| NMX1-0752.00/US | Utility - ORG | Issued        | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER   | 4/18/2002  | 10/126,274 | 2/14/2006  | 7,000,126 | US      |
| NMX1-0753.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES  | 12/19/2001 | 10/028,472 | 6/10/2003  | 6,577,274 | US      |
| NMX1-0753.01/US | Utility - DIV | Issued        | METHOD AND APPARATUS FOR CONTROLLING ACCESS TO MOBILE DEVICES  | 4/16/2003  | 10/417,540 | 6/8/2004   | 6,747,598 | US      |
| NMX1-0754.00/US | Utility - ORG | Issued        | PRACTICAL CODING AND METRIC CALCULATION FOR THE LATTICE INTERFERED CHANNEL   | 8/7/2002   | 10/214,630 | 4/25/2006  | 7,036,071 | US      |
| NMX1-0755.00/US | Utility - ORG | Issued        | SEARCH RECEIVER USING ADAPTIVE DETECTION THRESHOLDS  | 5/29/2002  | 10/154,948 | 9/16/2008  | 7,426,392 | US      |
| NMX1-0756.00/US | Utility - ORG | Issued        | TELECOMMUNICATIONS DEVICE FOR THE DEAF PROVIDING A CHAIN OF TOKENIZED ERROR AND STATE INFORMATION FOR A CALL STACK       | 1/3/2002   | 10/038,411 | 5/6/2003   | 6,560,317 | US      |
| NMX1-0757.00/US | Utility - ORG | Issued        | PROGRAMMABLE FRAME SPLITTER  | 4/29/2002  | 10/123,625 | 7/3/2007   | 7,240,247 | US      |
| NMX1-0758.00/US | Utility - ORG | Issued        | INVERSE MULTIPLEXING OF UNMANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK  | 1/10/2002  | 10/044,766 | 1/3/2006   | 6,982,988 | US      |
| NMX1-0759.00/US | Utility - ORG | Issued        | METHOD FOR PACKAGING A TAPE SUBSTRATE  | 2/21/2002  | 10/080,093 | 7/4/2006   | 7,072,352 | US      |
| NMX1-0760.00/US | Utility - ORG | Issued        | METHOD FOR PACKAGING A TAPE SUBSTRATE  | 8/26/2002  | 10/228,617 | 3/17/2009  | 7,503,155 | US      |
| NMX1-0760.01/US | Utility - DIV | Abandoned     | METHOD FOR PACKAGING A TAPE SUBSTRATE  | 1/29/2009  | 12/362,445 |            |           | US      |
| NMX1-0761.00/US | Utility - ORG | Issued        | COAXIAL RADIO FREQUENCY ADAPTER AND METHOD   | 12/10/2001 | 10/007,672 | 1/18/2005  | 6,844,738 | US      |
| NMX1-0761.01/US | Utility - DIV | Issued        | COAXIAL RADIO FREQUENCY ADAPTER AND METHOD   | 8/20/2003  | 10/645,229 | 3/8/2005   | 6,863,564 | US      |
| NMX1-0761.02/US | Utility - DIV | Issued        | COAXIAL RADIO FREQUENCY ADAPTER AND METHOD   | 4/7/2004   | 10/820,693 | 9/26/2006  | 7,112,970 | US      |
| NMX1-0762.00/US | Utility - ORG | Issued        | DEFERRED PROCEDURE CALL IN INTERFACE DESCRIPTION LANGUAGE  | 11/28/2001 | 09/997,669 | 8/23/2005  | 6,934,953 | US      |
| NMX1-0763.00/US | Utility - ORG | Issued        | MANUFACTURING INTEGRATED CIRCUITS AND TESTING ON-DIE POWER SUPPLIES USING DISTRIBUTED PROGRAMMABLE DIGITAL CURRENT SINKS | 3/12/2002  | 10/097,136 | 5/1/2007   | 7,212,021 | US      |
| NMX1-0764.00/US | Utility - ORG | Issued        | UNDERFILL PROCESS FOR FLIP-CHIP DEVICE   | 12/21/2001 | 10/032,115 | 3/9/2004   | 6,703,299 | US      |
| NMX1-0764.01/US | Utility - DIV | Abandoned     | UNDERFILL PROCESS FOR FLIP-CHIP DEVICE   | 2/9/2004   | 10/774,869 |            |           | US      |
| NMX1-0765.00/US | Utility - ORG | Issued        | ELECTRONIC PACKAGES AND COMPONENTS THEREOF FORMED BY SUBSTRATE-IMPRINTING  | 12/18/2002 | 10/322,902 | 5/13/2008  | 7,371,975 | US      |
| NMX1-0766.00/US | Utility - ORG | Issued        | MULTI-DIE SEMICONDUCTOR PACKAGE  | 8/22/2002  | 10/226,070 | 8/9/2005   | 6,927,497 | US      |
| NMX1-0766.01/US | Utility - DIV | Issued        | METHOD OF FORMING A MULTI-DIE SEMICONDUCTOR PACKAGE  | 6/27/2005  | 11/167,495 | 3/3/2009   | 7,498,201 | US      |
| NMX1-0767.00/US | Utility - ORG | Issued        | ELECTROSTATIC DISCHARGE PROTECTION UNIT INCLUDING EQUALIZATION   | 12/23/2002 | 10/329,058 | 5/15/2007  | 7,218,491 | US      |
| NMX1-0768.00/US | Utility - ORG | Issued        | TEST INTERFACE, SYSTEM, AND METHOD FOR TESTING COMMUNICATIONS DEVICES WITH NON-DETERMINISTIC LATENCY                     | 7/22/2002  | 10/202,624 | 8/26/2008  | 7,418,639 | US      |
| NMX1-0769.00/US | Utility - ORG | Abandoned     | USING MULTIPLE SIMULATION ENVIRONMENTS   | 5/23/2003  | 10/445,272 |            |           | US      |

Exhibit A - US Applications and Issued Patents

| MT-DoNo         | MatterType    | MatterStatus | Title   | FiledDate  | SerialNo   | IssueDate  | Patenting | Country |
|-----------------|---------------|--------------|---|------------|------------|------------|-----------|---------|
| NMXI-0770.00/US | Utility - ORG | Issued       | REFRESHING MEMORY CELLS OF A PHASE CHANGE MATERIAL MEMORY DEVICE  | 8/5/2002   | 10/212,630 | 7/27/2004  | 6,768,665 | US      |
| NMXI-0771.00/US | Utility - ORG | Issued       | TUNABLE OPTICAL DISPERSION COMPENSATOR  | 4/17/2002  | 10/126,106 | 8/17/2004  | 6,776,493 | US      |
| NMXI-0772.00/US | Utility - CON | Issued       | METHOD TO REALIZE FAST SILICON-ON-INSULATOR (SOI) OPTICAL DEVICE  | 4/25/2006  | 11/411,059 | 11/7/2006  | 7,133,586 | US      |
| NMXI-0772.01/US | Utility - DIV | Issued       | METHOD TO REALIZE FAST SILICON-ON-INSULATOR (SOI) OPTICAL DEVICE  | 4/25/2006  | 11/410,591 | 11/7/2006  | 7,133,585 | US      |
| NMXI-0773.00/US | Utility - CON | Issued       | METHOD AND APPARATUS FOR WAFER LEVEL TESTING OF INTEGRATED OPTICAL WAVEGUIDE CIRCUITS   | 11/19/2004 | 10/994,021 | 6/10/2008  | 7,386,197 | US      |
| NMXI-0774.00/US | Utility - ORG | Abandoned    | BIT ERROR RATE TEST SYSTEM FOR MULTI-SOURCE AGREEMENT COMPLIANT TRANSCEIVERS  | 12/20/2001 | 10/027,053 |            |           | US      |
| NMXI-0775.00/US | Utility - ORG | Issued       | ON-CHIP JITTER TESTING  | 12/26/2002 | 10/331,122 | 8/5/2008   | 7,409,621 | US      |
| NMXI-0776.00/US | Utility - ORG | Issued       | TESTING METHODOLOGY AND APPARATUS FOR INTERCONNECTS   | 12/16/2002 | 10/319,517 | 5/16/2006  | 7,047,458 | US      |
| NMXI-0777.00/US | Utility - ORG | Abandoned    | LCD CONTROLLER ARCHITECTURE FOR HANDLING FLUCTUATING BANDWIDTH CONDITIONS   | 1/31/2002  | 10/062,268 |            |           | US      |
| NMXI-0778.00/US | Utility - ORG | Issued       | INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK   | 2/21/2002  | 10/080,329 | 10/30/2007 | 7,289,525 | US      |
| NMXI-0779.00/US | Utility - ORG | Issued       | WAFER-LEVEL TEST STRUCTURE FOR EDGE-EMITTING SEMICONDUCTOR LASERS   | 4/18/2002  | 10/126,348 | 2/20/2007  | 7,180,929 | US      |
| NMXI-0779.01/US | Utility - DIV | Issued       | WAFER-LEVEL TEST STRUCTURE FOR EDGE-EMITTING SEMICONDUCTOR LASERS   | 9/11/2003  | 10/659,898 | 10/17/2006 | 7,122,391 | US      |
| NMXI-0780.00/US | Utility - ORG | Issued       | SMALLER AND LOWER POWER STATIC MUX CIRCUITRY IN GENERATING MULTIPLEXER PARTIAL PRODUCT SIGNALS  | 12/5/2003  | 10/728,395 | 12/11/2007 | 7,308,470 | US      |
| NMXI-0781.00/US | Utility - ORG | Issued       | ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMER CIRCUIT   | 4/24/2002  | 10/128,373 | 3/4/2008   | 7,339,770 | US      |
| NMXI-0782.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR GENERATING GRAY CODE FOR ANY EVEN COUNT VALUE TO ENABLE EFFICIENT POINTER EXCHANGE MECHANISMS IN ASYNCHRONOUS FIFO'S | 6/28/2002  | 10/186,564 | 10/5/2004  | 6,801,143 | US      |
| NMXI-0783.00/US | Utility - ORG | Issued       | RATE POLICING ALGORITHM FOR PACKET FLOWS  | 5/12/2002  | 10/137,753 | 12/26/2006 | 7,154,853 | US      |
| NMXI-0784.00/US | Utility - ORG | Issued       | FLIP-CHIP PACKAGE INTEGRATING OPTICAL AND ELECTRICAL DEVICES AND COUPLING TO A WAVEGUIDE ON A BOARD   | 5/30/2002  | 10/158,250 | 6/22/2004  | 6,754,407 | US      |
| NMXI-0784.01/US | Utility - CON | Issued       | FLIP-CHIP PACKAGE INTEGRATING OPTICAL AND ELECTRICAL DEVICES AND COUPLING TO A WAVEGUIDE ON A BOARD   | 5/6/2004   | 10/840,017 | 5/23/2006  | 7,049,704 | US      |
| NMXI-0785.00/US | Utility - ORG | Issued       | APPARATUS AND METHODS FOR INTERCONNECTING COMPONENTS TO VIA-IN-PAD INTERCONNECTS  | 10/8/2002  | 10/267,158 | 12/7/2004  | 6,828,512 | US      |
| NMXI-0787.00/US | Utility - ORG | Issued       | DYNAMIC COMPUTATION OF CHIPSET-SUPPORTED ACCELERATED GRAPHICS PORT APERTURE SIZES   | 3/21/2002  | 10/104,042 | 8/23/2005  | 6,933,944 | US      |
| NMXI-0787.01/US | Utility - CON | Issued       | DYNAMIC COMPUTATION OF CHIPSET-SUPPORTED ACCELERATED GRAPHICS PORT APERTURE SIZES   | 6/29/2005  | 11/172,342 | 2/27/2007  | 7,184,050 | US      |
| NMXI-0788.00/US | Utility - ORG | Issued       | SLAVE/I/O DRIVEN CALIBRATION USING ERROR-NULLING MASTER REFERENCE   | 8/29/2002  | 10/231,863 | 3/20/2007  | 7,194,559 | US      |
| NMXI-0789.00/US | Utility - ORG | Abandoned    | LOW LATENCY MEMORY ACCESS METHOD USING UNIFIED QUEUE MECHANISM  | 9/23/2002  | 10/253,229 |            |           | US      |
| NMXI-0790.00/US | Utility - ORG | Issued       | UPDATING STACK POINTER BASED ON INSTRUCTION BIT INDICATOR WITHOUT EXECUTING AN UPDATE MICROINSTRUCTION  | 9/30/2002  | 10/262,214 | 7/18/2006  | 7,080,236 | US      |
| NMXI-0791.00/US | Utility - ORG | Issued       | CODE-BASED OPTICAL NETWORKS, METHODS, AND APPARATUS   | 11/17/1999 | 09/454,164 | 6/20/2006  | 7,065,298 | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| MT-Dealing      | Mated type    | Mated Status | Title   | Filed Date | Serial No  | Issued Date | Patent No | Country |
|-----------------|---------------|--------------|---|------------|------------|-------------|-----------|---------|
| NMXI-0792.00/US | Utility - ORG | Issued       | METHOD, SYSTEM, AND PROGRAM FOR MEMORY BASED DATA TRANSFER  | 7/24/2002  | 10/205,546 | 10/19/2004  | 6,807,600 | US      |
| NMXI-0793.00/PR | Prov - ORG    | Expired      | OPTICAL PHASE AND AMPLITUDE MEASUREMENT DEVICE  | 4/10/2001  | 60/282,961 |             |           | US      |
| NMXI-0794.00/US | Utility - DIV | Abandoned    | SEGMENTED COMPLEX DIFFRACTION GRATINGS  | 6/3/2005   | 11/145,251 |             |           | US      |
| NMXI-0794.01/US | Utility - DIV | Abandoned    | SEGMENTED COMPLEX DIFFRACTION GRATINGS  | 6/3/2005   | 11/144,583 |             |           | US      |
| NMXI-0794.02/US | Utility - DIV | Abandoned    | SEGMENTED COMPLEX DIFFRACTION GRATINGS  | 6/3/2005   | 11/145,291 |             |           | US      |
| NMXI-0795.00/US | Utility - ORG | Issued       | PHASE-CONTROLLED FIBER BRAGG GRATINGS AND MANUFACTURING METHODS   | 5/9/2002   | 10/142,329 | 6/7/2005    | 6,904,201 | US      |
| NMXI-0796.00/US | Utility - ORG | Issued       | CODE-SWITCHED OPTICAL NETWORK   | 11/13/2000 | 09/712,822 | 3/8/2005    | 6,865,344 | US      |
| NMXI-0797.00/US | Utility - ORG | Issued       | DYNAMIC POWER LEVEL CONTROL BASED ON A BOARD LATCH STATE  | 8/1/2002   | 10/211,465 | 9/27/2005   | 6,950,952 | US      |
| NMXI-0798.00/US | Utility - ORG | Issued       | CONFIGURABLE MULTI-PORT MULTI-PROTOCOL NETWORK INTERFACE TO SUPPORT PACKET PROCESSING   | 7/9/2002   | 10/190,851 | 1/9/2007    | 7,162,564 | US      |
| NMXI-0799.00/US | Utility - ORG | Abandoned    | TELEPHONE CONFERENCE BRIDGE PROVIDED VIA A PLURALITY OF COMPUTER TELEPHONE RESOURCE ALGORITHMS                                  | 3/29/2002  | 10/112,194 |             |           | US      |
| NMXI-0800.00/US | Utility - ORG | Issued       | DIGITAL CAMERA WITH ISO PICKUP SENSITIVITY ADJUSTMENT   | 2/1/2002   | 10/061,907 | 11/25/2008  | 7,455,868 | US      |
| NMXI-0801.00/US | Utility - ORG | Issued       | MEASURING OPTICAL PHASE   | 4/10/2002  | 10/119,501 | 7/20/2004   | 6,765,681 | US      |
| NMXI-0802.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR INDEPENDENT CONTROL OF DEVICES UNDER TEST CONNECTED IN PARALLEL  | 10/24/2002 | 10/280,458 | 6/12/2007   | 7,231,552 | US      |
| NMXI-0803.00/US | Utility - ORG | Issued       | SLOW MOTION DETECTION SYSTEM  | 2/14/2002  | 10/076,817 | 2/20/2007   | 7,180,429 | US      |
| NMXI-0804.00/US | Utility - ORG | Issued       | ACCURATELY TUNING RESISTORS   | 4/19/2002  | 10/126,402 | 3/1/2005    | 6,862,714 | US      |
| NMXI-0805.00/US | Utility - ORG | Abandoned    | COMPACTING CIRCUIT RESPONSES  | 3/27/2002  | 10/107,628 | 2/27/2007   | 7,185,253 | US      |
| NMXI-0805.01/US | Utility - CON | Issued       | METHODS AND APPARATUS FOR DETERMINING A FLOATING-POINT EXPONENT ASSOCIATED WITH AN UNDERFLOW CONDITION OR AN OVERFLOW CONDITION | 10/17/2005 | 11/251,664 | 10/12/2010  | 7,814,388 | US      |
| NMXI-0806.00/US | Utility - ORG | Issued       |   | 4/8/2002   | 10/118,349 | 9/18/2007   | 7,272,623 | US      |
| NMXI-0807.00/US | Utility - ORG | Issued       | CLUSTER BASED REDUNDANCY SCHEME FOR SEMICONDUCTOR MEMORIES  | 7/16/2002  | 10/196,401 | 6/21/2005   | 6,909,645 | US      |
| NMXI-0808.00/US | Utility - ORG | Issued       | EXECUTING APPLICATIONS FROM A SEMICONDUCTOR NONVOLATILE MEMORY  | 8/8/2002   | 10/215,549 | 3/8/2011    | 7,904,897 | US      |
| NMXI-0808.01/US | Utility - CON | Pending      | EXECUTING APPLICATIONS FROM A SEMICONDUCTOR NONVOLATILE MEMORY  | 3/7/2011   | 13/041,541 |             |           | US      |
| NMXI-0809.00/US | Utility - ORG | Issued       | FORMING FERROELECTRIC POLYMER MEMORIES  | 5/31/2002  | 10/160,641 | 6/1/2010    | 7,727,777 | US      |
| NMXI-0810.00/US | Utility - ORG | Issued       | IN-SITU SEQUENTIAL HIGH DENSITY PLASMA DEPOSITION AND ETCH PROCESSING FOR GAP FILL  | 9/6/2002   | 10/236,203 | 5/29/2007   | 7,223,701 | US      |
| NMXI-0811.00/US | Utility - ORG | Issued       | PROMOTING ADHESION OF FLUOROPOLYMER FILMS TO SEMICONDUCTOR SUBSTRATES   | 4/24/2002  | 10/131,017 | 3/2/2004    | 6,699,798 | US      |
| NMXI-0812.00/US | Utility - ORG | Abandoned    | DATA LINK ANALYZER  | 9/10/2002  | 10/238,669 |             |           | US      |
| NMXI-0813.00/US | Utility - ORG | Issued       | SYSTEM AND METHOD TO ANALYZE VLSI DESIGNS   | 12/31/2002 | 10/334,113 | 4/10/2007   | 7,203,631 | US      |
| NMXI-0814.00/US | Utility - ORG | Issued       | ADAPTIVE EQUALIZATION USING A CONDITIONAL UPDATE SIGN-SIGN LEAST MEAN SQUARE ALGORITHM  | 9/10/2003  | 10/660,228 | 10/30/2007  | 7,289,557 | US      |
| NMXI-0815.00/US | Utility - ORG | Issued       | FUTURE ACTIVITY LIST FOR PERIPHERAL BUS HOST CONTROLLER   | 6/6/2003   | 10/456,185 | 6/12/2007   | 7,231,468 | US      |
| NMXI-0816.00/US | Utility - ORG | Issued       | SPEED CONTROL PLAYBACK OF PARAMETRIC SPEECH ENCODED DIGITAL AUDIO   | 7/23/2002  | 10/200,328 | 7/3/2007    | 7,239,999 | US      |
| NMXI-0817.00/US | Utility - ORG | Issued       | DATA PACKET HEADER CONVERSION   | 8/14/2002  | 10/217,644 | 1/29/2008   | 7,324,516 | US      |
| NMXI-0818.00/US | Utility - ORG | Issued       | DETERMINISTIC BANDWIDTH THROTTLING TO FACILITATE OPERATION OF A CONTROL UNIT  | 9/27/2002  | 10/256,475 | 1/9/2007    | 7,161,908 | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| File No         | Matter Type   | Matter Status | Title   | Filed Date | Serial No  | Issue Date | Patent No | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|-----------|---------|
| NMXI-0819.00/US | Utility - ORG | Issued        | OPTICAL THUMBTRACK  | 12/31/2002 | 10/334,749 | 6/15/2004  | 6,792,179 | US      |
| NMXI-0820.00/US | Utility - ORG | Issued        | ISOLATING PHASE CHANGE MEMORY DEVICES   | 12/13/2002 | 10/319,183 | 9/18/2007  | 7,271,403 | US      |
| NMXI-0821.00/US | Utility - ORG | Issued        | EFFICIENT ECHO CANCELLATION TECHNIQUES  | 10/30/2002 | 10/284,724 | 1/2/2007   | 7,158,631 | US      |
| NMXI-0822.00/US | Utility - ORG | Issued        | PHOTOACTIVE ADHESION PROMOTER   | 11/4/2002  | 10/288,021 | 3/10/2009  | 7,501,230 | US      |
| NMXI-0822.01/US | Utility - DIV | Abandoned     | PHOTOACTIVE ADHESION PROMOTER   | 8/12/2004  | 10/917,679 |            |           | US      |
| NMXI-0822.02/US | Utility - CON | Issued        | PHOTOACTIVE ADHESION PROMOTER   | 11/24/2008 | 12/277,114 | 11/3/2009  | 7,611,828 | US      |
| NMXI-0823.00/US | Utility - ORG | Issued        | FLASH ASSISTED ANNEALING  | 7/31/2002  | 10/210,865 | 5/29/2007  | 7,223,660 | US      |
| NMXI-0824.00/US | Utility - DIV | Abandoned     | FORMING PLANARIZED SEMICONDUCTOR STRUCTURES   | 12/16/2005 | 11/303,417 |            |           | US      |
| NMXI-0825.00/US | Utility - ORG | Abandoned     | DIGITAL PHASE DETECTION WITH JITTER FILTER  | 12/31/2002 | 10/335,012 |            |           | US      |
| NMXI-0826.00/US | Utility - ORG | Issued        | DIGITAL PHASE DETECTION WITH JITTER FILTER  | 12/31/2002 | 10/335,049 | 10/26/2004 | 6,810,443 | US      |
| NMXI-0827.00/US | Utility - ORG | Issued        | OPTICAL STORAGE TRANSFER PERFORMANCE  | 12/31/2002 |            |            |           | US      |
| NMXI-0827.00/US | Utility - ORG | Issued        | METHODS AND APPARATUS FOR CONFIGURING HARDWARE RESOURCES IN A PRE-BOOT ENVIRONMENT WITHOUT REQUIRING A SYSTEM RESET   | 10/30/2002 | 10/283,920 | 6/26/2007  | 7,237,102 | US      |
| NMXI-0828.00/US | Utility - ORG | Issued        | GENERATING ANIMATION FROM VISUAL AND AUDIO INPUT  | 10/7/2002  | 10/266,226 | 8/14/2007  | 7,257,538 | US      |
| NMXI-0829.00/US | Utility - ORG | Issued        | PHASE CHANGE MEMORY AND METHOD THEREFOR   | 12/13/2002 | 10/318,984 | 4/17/2007  | 7,205,562 | US      |
| NMXI-0830.00/US | Utility - ORG | Issued        | SURFACE MOUNT TECHNOLOGY TO VIA-IN-PAD INTERCONNECTIONS   | 12/20/2002 | 10/326,901 | 8/9/2005   | 6,927,346 | US      |
| NMXI-0831.00/US | Utility - DIV | Issued        | METHOD AND APPARATUS FOR REDUCING ELECTRICAL INTERCONNECTION FATIGUE  | 12/26/2006 | 11/616,164 | 1/5/2010   | 7,642,660 | US      |
| NMXI-0832.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR INTEGRATED CIRCUIT DATAPATH LAYOUT USING A VECTOR EDITOR   | 9/30/2003  | 10/676,893 | 5/20/2008  | 7,376,922 | US      |
| NMXI-0833.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR INTEGRATED CIRCUIT SELF-DESCRIPTION  | 6/30/2003  | 10/610,713 | 3/24/2009  | 7,509,548 | US      |
| NMXI-0834.00/US | Utility - ORG | Abandoned     | COMPACT INPUT/OUTPUT SIGNAL DRIVER FOR ELECTROSTATIC DISCHARGE PROTECTION   | 10/25/2002 | 10/280,926 |            |           | US      |
| NMXI-0835.00/US | Utility - ORG | Abandoned     | CODOPING OF SOURCE DRAINS USING CARBON OR FLUORINE ION IMPLANTS TO IMPROVE POLYSILICON DEPLETION  | 11/27/2002 | 10/306,320 |            |           | US      |
| NMXI-0836.00/US | Utility - DIV | Abandoned     | USING BENZOCYCLOBUTENE BASED POLYMERS AS UNDERFILL MATERIALS  | 9/10/2004  | 10/939,226 |            |           | US      |
| NMXI-0837.00/US | Utility - ORG | Issued        | ANALYZING INTERCONNECT STRUCTURES   | 10/28/2002 | 10/281,857 | 10/30/2007 | 7,289,945 | US      |
| NMXI-0838.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR LASER ANNEALING  | 12/12/2002 | 10/319,310 | 5/1/2007   | 7,211,501 | US      |
| NMXI-0839.00/US | Utility - ORG | Abandoned     | PRONUNCIATION NETWORK   | 12/30/2002 | 10/330,537 |            |           | US      |
| NMXI-0840.00/US | Utility - ORG | Issued        | PORTABLE COMPUTING DEVICE ADAPTED TO UPDATE DISPLAY INFORMATION WHILE IN A LOW POWER MODE   | 11/5/2002  | 10/289,081 | 7/17/2007  | 7,245,945 | US      |
| NMXI-0840.01/US | Utility - CON | Abandoned     | PORTABLE COMPUTING DEVICE ADAPTED TO UPDATE DISPLAY INFORMATION WHILE IN A LOW POWER MODE   | 1/24/2006  | 11/338,932 |            |           | US      |
| NMXI-0841.00/US | Utility - DIV | Abandoned     | HEATING OPTICAL DEVICES   | 6/13/2005  | 11/151,010 | 2/13/2007  | 7,177,502 | US      |
| NMXI-0842.00/US | Utility - ORG | Issued        | USING A TRANSVERSAL FILTER TO COMPENSATE FOR DISPERSION   | 4/8/2003   | 10/409,287 | 8/30/2005  | 6,997,794 | US      |
| NMXI-0843.00/US | Utility - ORG | Issued        | MECHANISM TO GUARANTEE FORWARD PROGRESS FOR INCOMING COHERENT INPUT/OUTPUT (I/O) TRANSACTIONS FOR CACHING I/O AGENT ON ADDRESS CONFLICT WITH PROCESSOR TRANSACTIONS | 12/19/2002 | 10/324,863 | 12/14/2004 | 6,832,268 | US      |
| NMXI-0843.01/US | Utility - CON | Issued        | MECHANISM TO GUARANTEE FORWARD PROGRESS FOR INCOMING COHERENT INPUT/OUTPUT (I/O) TRANSACTIONS FOR CACHING I/O AGENT ON ADDRESS CONFLICT WITH PROCESSOR TRANSACTIONS | 10/21/2004 | 10/970,015 | 6/10/2008  | 7,386,643 | US      |

## Exhibit A - US Applications and Issued Patents

| Patent No.      | Patent Title  | Patent Status | Patent Description  | Filed Date | Serial No.  | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|---|------------|-------------|------------|------------|---------|
| NMXI-0844.00/US | Utility - ORG | Issued        | SYSTEM AND METHOD FOR EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE    | 2/18/2003  | 10/369,933  | 11/18/2008 | 7,453,950  | US      |
| NMXI-0845.00/US | Utility - ORG | Abandoned     | REMOVING FLUORINE-BASED PLASMA ETCH RESIDUES  | 10/23/2002 | 10/278,535  |            |            | US      |
| NMXI-0846.00/US | Utility - ORG | Issued        | APPARATUS AND METHOD FOR TIME SYNCHRONIZATION OF A PLURALITY OF MULTIMEDIA STREAMS            | 11/14/2002 | 10/294,271  | 4/4/2006   | 7,024,575  | US      |
| NMXI-0847.00/US | Utility - ORG | Issued        | STIMULUS GENERATION   | 12/11/2002 | 10/317,605  | 7/3/2007   | 7,240,260  | US      |
| NMXI-0848.00/US | Utility - ORG | Abandoned     | SYSTEM AND PROCESS TO CONTROL ELECTROPLATING A METAL ONTO A SUBSTRATE                         | 6/20/2003  | 10/465,666  |            |            | US      |
| NMXI-0849.00/US | Utility - ORG | Issued        | METHODS TO PATTERN CONTACTS USING CHROMELESS PHASE SHIFT MASKS                                | 11/25/2002 | 10/304,303  | 5/20/2008  | 7,374,865  | US      |
| NMXI-0849.01/US | Utility - DIV | Abandoned     | METHODS TO PATTERN CONTACTS USING CHROMELESS PHASE SHIFT MASKS                                | 4/18/2008  | 12/106,211  |            |            | US      |
| NMXI-0850.00/US | Utility - ORG | Abandoned     | THERMAL MANAGEMENT FOR TELECOMMUNICATION DEVICES  | 1/3/2003   | 10/336,629  |            |            | US      |
| NMXI-0851.00/US | Utility - CIP | Issued        | INTERCONNECTION DESIGNS AND MATERIALS HAVING IMPROVED STRENGTH AND FATIGUE LIFE               | 4/30/2003  | 10/427,168  | 6/17/2008  | 7,387,827  | US      |
| NMXI-0851.01/US | Utility - DIV | Pending       | INTERCONNECTION DESIGNS AND MATERIALS HAVING IMPROVED STRENGTH AND FATIGUE LIFE               | 4/9/2008   | 12/100,380  |            |            | US      |
| NMXI-0852.00/US | Utility - ORG | Abandoned     | LOW POWER ADDER CIRCUIT UTILIZING BOTH STATIC AND DYNAMIC LOGIC                               | 4/30/2003  | 10/426,044  |            |            | US      |
| NMXI-0853.00/US | Utility - ORG | Abandoned     | LOW POWER ADDER   | 4/30/2003  | 10/425,987  |            |            | US      |
| NMXI-0854.00/US | Utility - ORG | Abandoned     | APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING                             | 5/9/2003   | 10/435,347  |            |            | US      |
| NMXI-0855.00/US | Utility - ORG | Abandoned     | METHOD AND APPARATUS FOR PREFETCHING BASED UPON TYPE IDENTIFIER TAGS                          | 6/2/2003   | 10/453,115  |            |            | US      |
| NMXI-0856.00/US | Utility - DIV | Issued        | METHODS OF FORMING A HIGH CONDUCTIVITY DIAMOND FILM AND STRUCTURES FORMED THEREBY             | 10/25/2004 | 10/973,161  | 8/26/2008  | 7,417,255  | US      |
| NMXI-0856.01/US | Utility - DIV | Issued        | METHODS OF FORMING A HIGH CONDUCTIVITY DIAMOND FILM AND STRUCTURES FORMED THEREBY             | 4/4/2005   | 11/1100,885 | 6/30/2009  | 7,553,694  | US      |
| NMXI-0857.00/US | Utility - ORG | Issued        | SYSTEM AND METHOD TO INITIALIZE REGISTERS WITH AN EEPROM STORED BOOT SEQUENCE                 | 4/22/2003  | 10/421,287  | 5/1/2007   | 7,213,142  | US      |
| NMXI-0858.00/US | Utility - ORG | Issued        | MEMORY DRIVER ARCHITECTURE AND ASSOCIATED METHODS   | 7/15/2003  | 10/620,469  | 4/3/2007   | 7,200,060  | US      |
| NMXI-0859.00/US | Utility - ORG | Abandoned     | SMART CONTROL POINTS  | 5/1/2003   | 10/427,377  |            |            | US      |
| NMXI-0860.00/US | Utility - ORG | Issued        | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR SIDE DIE AND METHOD | 3/5/2003   | 10/382,680  | 9/11/2007  | 7,268,425  | US      |
| NMXI-0861.00/US | Utility - ORG | Abandoned     | POST-DEPOSITION MODIFICATION OF INTERLAYER DIELECTRICS  | 6/30/2003  | 10/609,963  |            |            | US      |
| NMXI-0862.00/US | Utility - ORG | Issued        | VENTING OF PELLSIDE CAVITY FOR A MASK   | 12/31/2002 | 10/334,841  | 4/17/2007  | 7,205,074  | US      |
| NMXI-0863.00/US | Utility - ORG | Issued        | DIELECTRIC WITH SIDEWALL PASSIVATING LAYER  | 3/4/2003   | 10/379,061  | 2/13/2007  | 7,176,122  | US      |
| NMXI-0864.00/US | Utility - ORG | Abandoned     | METHOD AND APPARATUS TO CHARACTERIZE AN ELECTRONIC DEVICE                                     | 6/13/2003  | 10/461,217  |            |            | US      |
| NMXI-0865.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS TO CREATE BYPASS LOGIC IN A DIGITAL CIRCUIT DESIGN                       | 6/26/2003  | 10/606,868  | 11/20/2007 | 7,299,425  | US      |
| NMXI-0866.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR TRUSTED KEYBOARD SCANNING  | 9/30/2003  | 10/676,888  | 12/5/2006  | 7,145,481  | US      |
| NMXI-0867.00/US | Utility - ORG | Issued        | DYNAMIC BIOS EXECUTION AND CONCURRENT UPDATE FOR A BLADE SERVER                               | 5/29/2003  | 10/448,696  | 11/28/2006 | 7,143,279  | US      |
| NMXI-0868.00/US | Utility - ORG | Issued        | ELIMINATION OF POTENTIAL REMAINING STALLS DUE TO USE OF PARTIAL REGISTERS                     | 6/30/2003  | 10/608,121  | 1/9/2007   | 7,162,614  | US      |
| NMXI-0869.00/US | Utility - ORG | Issued        | PCB DESIGN AND METHOD FOR PROVIDING VENTED BLIND VIAS   | 5/20/2003  | 10/442,834  | 9/7/2004   | 6,787,443  | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Pat. No.        | Mat. No.      | Mat. Type | Mat. Status   | Title      | Filed      | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|-----------|---|------------|------------|------------|------------|------------|---------|
| NMXI-0869.01/US | Utility - DIV | Abandoned | PCB DESIGN AND METHOD FOR PROVIDING VENTED BLIND VIAS BOND FINGER ON VIA SUBSTRATE, PROCESS OF MAKING SAME, PACKAGE MADE THEREBY, AND METHOD OF ASSEMBLING SAME | 6/17/2004  | 10/870,506 | 12/4/2007  | 7,302,756  | US         |         |
| NMXI-0870.00/US | Utility - ORG | Issued    | CONNECTION APPARATUS, SYSTEMS, AND METHODS  | 6/30/2003  | 10/612,281 | 10/25/2005 | 6,958,450  | US         |         |
| NMXI-0871.00/US | Utility - ORG | Issued    | APPARATUS AND METHOD FOR COMBINING WRITES TO I/O TIMING ANALYSIS APPARATUS, SYSTEMS, AND METHODS  | 3/31/2003  | 10/405,098 | 4/17/2007  | 7,206,865  | US         |         |
| NMXI-0872.00/US | Utility - ORG | Issued    | SELECTIVE CONTROL OF TEST-ACCESS PORTS IN INTEGRATED CIRCUITS   | 3/28/2003  | 10/402,125 | 11/20/2007 | 7,299,433  | US         |         |
| NMXI-0873.00/US | Utility - ORG | Issued    | TEST SCAN CELLS   | 6/9/2003   | 10/458,537 | 7/29/2008  | 7,406,641  | US         |         |
| NMXI-0874.00/US | Utility - ORG | Issued    | LEAKAGE CONTROL IN INTEGRATED CIRCUITS  | 6/30/2003  | 10/612,293 | 10/14/2008 | 7,437,634  | US         |         |
| NMXI-0875.00/US | Utility - ORG | Issued    | MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A HIGH-ACID ELECTROLYTE SOLUTION   | 5/13/2003  | 10/436,775 | 11/27/2007 | 7,302,652  | US         |         |
| NMXI-0876.00/US | Utility - ORG | Issued    | MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A LOW-ACID ELECTROLYTE SOLUTION  | 3/31/2003  | 10/404,937 |            |            | US         |         |
| NMXI-0877.00/US | Utility - ORG | Abandoned | VARIABLE LATCH  | 10/8/2003  | 10/682,275 |            |            | US         |         |
| NMXI-0878.00/US | Utility - ORG | Issued    | MODIFIED ELECTROPLATING SOLUTION COMPONENTS IN A LOW-ACID ELECTROLYTE SOLUTION  | 10/8/2003  | 10/682,276 | 5/13/2008  | 7,371,311  | US         |         |
| NMXI-0878.01/US | Utility - DIV | Abandoned | VARIABLE LATCH  | 8/19/2005  | 11/207,305 |            |            | US         |         |
| NMXI-0879.00/US | Utility - ORG | Issued    | VARIABLE LATCH  | 3/18/2003  | 10/391,893 | 1/9/2007   | 7,160,127  | US         |         |
| NMXI-0879.01/US | Utility - DIV | Abandoned | METHOD, APPARATUS AND SYSTEM FOR CREATING EFFICIENT UPNP CONTROL POINTS   | 11/20/2006 | 11/561,684 |            |            | US         |         |
| NMXI-0880.00/US | Utility - ORG | Abandoned | INTEGRATED CIRCUIT WITH IMPROVED CHANNEL STRESS PROPERTIES AND A METHOD FOR MAKING IT   | 6/30/2003  | 10/611,617 |            |            | US         |         |
| NMXI-0881.00/US | Utility - ORG | Issued    | OSCILLATOR SYSTEM WITH SWITCHED-CAPACITOR NETWORK AND METHOD FOR GENERATING A PRECISION TIME REFERENCE  | 5/21/2003  | 10/443,152 | 5/16/2006  | 7,045,408  | US         |         |
| NMXI-0882.00/US | Utility - ORG | Issued    | TOPOLDOY BASED REPLACEMENT ROUTING OF SIGNAL PATHS  | 5/22/2003  | 10/444,311 | 3/1/2005   | 6,861,917  | US         |         |
| NMXI-0883.00/US | Utility - ORG | Abandoned | REUSABLE, BUILT-IN SELF-TEST METHODOLOGY FOR COMPUTER SYSTEMS   | 6/30/2003  | 10/610,855 |            |            | US         |         |
| NMXI-0884.00/US | Utility - ORG | Issued    | PUSH BUTTON MODE AUTOMATIC PATTERN SWITCHING FOR INTERCONNECT BUILT-IN SELF TEST  | 3/20/2003  | 10/393,223 | 12/26/2006 | 7,155,370  | US         |         |
| NMXI-0885.00/US | Utility - ORG | Issued    | METHOD AND PROCESS FOR DETERMINING A QUOTIENT TECHNIQUES TO PERFORM LINE QUALIFICATION  | 3/31/2003  | 10/404,405 | 11/30/2004 | 6,826,100  | US         |         |
| NMXI-0886.00/US | Utility - ORG | Issued    | ENHANCED GPRS MOBILITY MANAGEMENT   | 6/3/2003   | 10/454,799 | 7/1/2008   | 7,395,301  | US         |         |
| NMXI-0887.00/US | Utility - ORG | Abandoned | MANAGEMENT  | 6/6/2003   | 10/457,092 |            |            | US         |         |
| NMXI-0888.00/PR | Prov - ORG    | Expired   | CONTROLLING THE LOCATION OF CONDUCTION BREAKDOWN IN PHASE CHANGE MEMORIES   | 2/14/2003  | 60/447,665 |            |            | US         |         |
| NMXI-0888.00/US | Utility - ORG | Abandoned | DEVICE, SYSTEM AND METHOD FOR CHANNEL SCANNING  | 9/15/2003  | 10/663,165 |            |            | US         |         |
| NMXI-0889.00/US | Utility - ORG | Issued    | STROBE THROUGH DIFFERENTIAL SIGNALING   | 8/4/2003   | 10/633,869 | 4/18/2006  | 7,029,978  | US         |         |
| NMXI-0890.00/US | Utility - ORG | Abandoned | APPARATUS AND METHOD FOR ADAPTING A LEVEL SENSITIVE DEVICE TO PRODUCE EDGE-TRIGGERED BEHAVIOR   | 6/26/2003  | 10/603,859 |            |            | US         |         |
| NMXI-0891.00/US | Utility - ORG | Issued    | APPARATUS AND METHOD FOR ADAPTING A LEVEL SENSITIVE DEVICE TO PRODUCE EDGE-TRIGGERED BEHAVIOR   | 4/30/2003  | 10/427,163 | 1/11/2005  | 6,842,133  | US         |         |
| NMXI-0892.00/US | Utility - ORG | Issued    | APPARATUS AND METHOD FOR ADAPTING A LEVEL SENSITIVE DEVICE TO PRODUCE EDGE-TRIGGERED BEHAVIOR   | 5/30/2003  | 10/449,332 | 4/18/2006  | 7,032,149  | US         |         |
| NMXI-0892.01/US | Utility - CON | Issued    | METHOD AND AN APPARATUS FOR A HARD-CODED BIT VALUE CHANGEABLE IN ANY LAYER OF METAL   | 12/28/2005 | 11/321,012 | 4/24/2007  | 7,210,079  | US         |         |
| NMXI-0893.00/US | Utility - DIV | Issued    | METHOD AND AN APPARATUS FOR A HARD-CODED BIT VALUE CHANGEABLE IN ANY LAYER OF METAL   | 2/1/2005   | 11/049,435 | 2/5/2008   | 7,326,973  | US         |         |
| NMXI-0893.01/US | Utility - DIV | Issued    | COMMUNICATION REGISTERS FOR PROCESSING ELEMENTS   | 2/1/2005   | 11/049,524 | 12/11/2007 | 7,307,295  | US         |         |
| NMXI-0894.00/US | Utility - DIV | Abandoned |   | 8/31/2006  | 11/514,802 |            |            | US         |         |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Pat. No.        | Matter Type   | Matter Status | Title  | Filed Date | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|--|------------|------------|------------|------------|---------|
| NMXI-0835.00/US | Utility - ORG | Abandoned     | MANAGING A CACHE WITH PINNED DATA  | 7/28/2003  | 10/629,093 |            |            | US      |
| NMXI-0836.00/US | Utility - ORG | Abandoned     | MULTI-CONFERENCE STREAM MIXING   | 11/26/2003 | 10/723,413 |            |            | US      |
| NMXI-0897.00/US | Utility - ORG | Issued        | APPARATUS, SYSTEM AND METHOD FOR UPDATING A SORTED LIST  | 6/30/2003  | 10/609,954 | 3/27/2007  | 7,197,498  | US      |
| NMXI-0898.00/US | Utility - ORG | Abandoned     | MEDIA CENTER STORAGE DEVICE PROXY  | 5/12/2003  | 10/428,285 |            |            | US      |
| NMXI-0899.00/US | Utility - ORG | Abandoned     | PASS THROUGH MODE FOR A PERSONAL VIDEO RECORDER  | 6/17/2003  | 10/463,651 |            |            | US      |
| NMXI-0900.00/US | Utility - ORG | Abandoned     | APPARATUS, SYSTEM AND METHOD FOR PERFORMING TABLE MAINTENANCE  | 6/30/2003  | 10/610,104 |            |            | US      |
| NMXI-0901.00/US | Utility - ORG | Abandoned     | SECURE COMPUTER TELEPHONE INTEGRATION ACCESS   | 9/12/2003  | 10/660,876 |            |            | US      |
| NMXI-0902.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS TO PERFORM TASK SCHEDULING  | 9/23/2003  | 10/669,309 | 9/22/2009  | 7,594,228  | US      |
| NMXI-0903.00/US | Utility - ORG | Issued        | TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA  | 6/6/2003   | 10/466,076 | 7/18/2006  | 7,078,912  | US      |
| NMXI-0904.00/US | Utility - ORG | Abandoned     | MODULAR CONTENT ADDRESSABLE MEMORY   | 6/30/2003  | 10/609,714 |            |            | US      |
| NMXI-0905.00/US | Utility - ORG | Issued        | NETWORK STATISTICS   | 7/28/2003  | 10/628,997 | 10/30/2007 | 7,289,455  | US      |
| NMXI-0906.00/US | Utility - ORG | Issued        | SYSTEMS AND METHODS FOR USING HDLC CHANNEL CONTEXT TO SIMULTANEOUSLY PROCESS MULTIPLE HDLC CHANNELS                  | 9/4/2003   | 10/655,565 | 1/8/2008   | 7,317,737  | US      |
| NMXI-0907.00/US | Utility - ORG | Issued        | SYMBOLIC BUFFER ALLOCATION IN LOCAL CACHE AT A NETWORK PROCESSING ELEMENT  | 10/31/2003 | 10/699,638 | 11/21/2006 | 7,140,023  | US      |
| NMXI-0908.00/US | Utility - ORG | Issued        | WIRE-BONDED PACKAGE WITH ELECTRICALLY INSULATING WIRE ENCAPSULANT AND THERMALLY CONDUCTIVE OVERMOLD                  | 12/9/2003  | 10/731,177 | 11/28/2006 | 7,141,454  | US      |
| NMXI-0909.00/US | Utility - DIV | Issued        | APPARATUS FOR PRECISE ALIGNMENT OF PACKAGING CAPS ON A SUBSTRATE   | 6/30/2003  | 10/611,215 | 10/2/2007  | 7,275,312  | US      |
| NMXI-0910.00/US | Utility - ORG | Issued        | I/O ARCHITECTURE FOR INTEGRATED CIRCUIT PACKAGE  | 2/23/2005  | 11/063,963 | 2/12/2008  | 7,329,946  | US      |
| NMXI-0911.00/US | Utility - CON | Issued        | ELECTRICAL DEVICE CONNECTOR  | 6/20/2003  | 10/600,668 | 12/14/2004 | 6,830,470  | US      |
| NMXI-0912.00/US | Utility - ORG | Abandoned     | HIERARCHICAL REORDER BUFFERS FOR CONTROLLING SPECULATIVE EXECUTION IN A MULTI-CLUSTER SYSTEM                         | 6/30/2003  | 10/611,380 |            |            | US      |
| NMXI-0913.00/US | Utility - ORG | Abandoned     | METHOD AND APPARATUS FOR ENABLING AN ADAPTIVE REPLAY LOOP IN A PROCESSOR   | 12/30/2003 | 10/749,271 |            |            | US      |
| NMXI-0914.00/US | Utility - ORG | Issued        | MEMORY ARRAY WITH STAGED OUTPUT  | 12/19/2003 | 10/739,268 | 2/21/2006  | 7,002,873  | US      |
| NMXI-0915.00/US | Utility - ORG | Issued        | METHODS AND APPARATUSES FOR MANUFACTURING ULTRA THIN DEVICE LAYERS FOR INTEGRATED CIRCUIT DEVICES                    | 9/11/2003  | 10/661,738 | 8/15/2006  | 7,091,108  | US      |
| NMXI-0916.00/US | Utility - ORG | Abandoned     | SUBSTRATE CARRIER FOR SURFACE PLANARIZATION  | 6/30/2004  | 10/883,404 |            |            | US      |
| NMXI-0917.00/US | Utility - ORG | Abandoned     | SYSTEM AND METHOD FOR MULTIPLE ENGINE PROCESSING INSTRUCTIONS  | 8/15/2003  | 10/641,614 |            |            | US      |
| NMXI-0918.00/US | Utility - ORG | Issued        | PASSING PARAMETERS BY IMPLICIT REFERENCE   | 9/30/2003  | 10/677,081 | 4/8/2008   | 7,356,812  | US      |
| NMXI-0919.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR LIMITING PORTS IN A REGISTER ALIAS TABLE HAVING HIGH-BANDWIDTH AND LOW-BANDWIDTH STRUCTURES | 10/22/2003 | 10/692,436 | 9/18/2007  | 7,272,701  | US      |
| NMXI-0920.00/US | Utility - ORG | Issued        | PRIORITIZED ADDRESS DECODER  | 9/19/2003  | 10/666,077 | 10/27/2009 | 7,610,611  | US      |
| NMXI-0921.00/US | Utility - DIV | Abandoned     | ACTIVE PHASE CANCELLATION FOR INDUCTOR / CAPACITOR NETWORKS  | 8/4/2005   | 11/197,806 |            |            | US      |
| NMXI-0922.00/US | Utility - ORG | Issued        | DYNAMICALLY RECONFIGURABLE OPTICAL SWITCHING SYSTEM  | 4/20/2001  | 09/839,848 | 6/3/2003   | 6,574,386  | US      |
| NMXI-0923.00/US | Utility - ORG | Issued        | CONTROLLED BREAKDOWN PHASE CHANGE MEMORY DEVICE  | 9/10/2004  | 10/939,237 | 3/25/2008  | 7,348,268  | US      |
| NMXI-0925.00/US | Utility - ORG | Issued        | METHOD, SYSTEM, AND ARTICLE OF MANUFACTURE FOR RUNNING DIAGNOSTICS RELATED TO A DEVICE                               | 12/19/2003 | 10/742,218 | 4/18/2006  | 7,031,881  | US      |
| NMXI-0926.00/US | Utility - ORG | Issued        | SECURE BOOTING AND PROVISIONING  | 12/24/2003 | 10/746,575 | 4/17/2007  | 7,207,039  | US      |
| NMXI-0927.00/US | Utility - ORG | Abandoned     | USE OF ALTERNATIVE POLYMER MATERIALS FOR "SOFT" POLYMER PELLUCES   | 3/12/2004  | 10/799,928 |            |            | US      |
| NMXI-0928.00/US | Utility - ORG | Issued        | PREDICTIVE FILTERING OF REGISTER CACHE ENTRY   | 12/30/2003 | 10/747,625 | 1/30/2007  | 7,171,545  | US      |

11/21/2011

Exhibit A - US Applications and Issued Patents

| Patent No.      | App. No.      | App. Title | App. Status  | App. Date  | App. Inventor | App. Issue Date | App. Patent No. | App. Country |
|-----------------|---------------|------------|--|------------|---------------|-----------------|-----------------|--------------|
| NMXI-0929.00/US | Utility - ORG | Issued     | METHOD AND APPARATUS FOR PREDICTION FOR FORK AND JOIN INSTRUCTIONS IN SPECULATIVE EXECUTION  | 12/8/2003  | 10/773,567    | 2/20/2007       | 7,181,601       | US           |
| NMXI-0930.00/US | Utility - ORG | Issued     | FRONT SIDE HOT-SWAP CHASSIS MANAGEMENT MODULE FORMING PHASE CHANGE MEMORY ARRAYS   | 12/29/2003 | 10/748,385    | 2/6/2007        | 7,173,817       | US           |
| NMXI-0931.00/US | Utility - ORG | Issued     | MEMORY REUSE FOR MULTIPLE ENDPOINTS IN USB DEVICE  | 9/10/2004  | 10/999,145    | 8/21/2007       | 7,259,023       | US           |
| NMXI-0932.00/US | Utility - ORG | Issued     | USB ON-THE-GO IMPLEMENTATION   | 11/12/2004 | 10/988,417    | 10/20/2009      | 7,806,951       | US           |
| NMXI-0933.00/US | Utility - ORG | Abandoned  | REWRITABLE FUSE MEMORY   | 11/17/2004 | 10/992,250    |                 |                 | US           |
| NMXI-0934.00/US | Utility - ORG | Issued     | HYBRID BRANCH PREDICTION   | 12/23/2003 | 10/744,654    | 10/30/2007      | 7,289,382       | US           |
| NMXI-0935.00/US | Utility - ORG | Abandoned  | PRIORITIZATION OF NETWORK TRAFFIC  | 3/22/2004  | 10/805,947    |                 |                 | US           |
| NMXI-0936.00/US | Utility - ORG | Issued     | METHOD AND APPARATUS FOR DEDICATING CACHE ENTRIES TO CERTAIN STREAMS FOR PERFORMANCE OPTIMIZATION  | 7/7/2004   | 10/885,821    | 7/27/2010       | 7,764,709       | US           |
| NMXI-0937.00/US | Utility - ORG | Issued     | METHOD AND APPARATUS TO MANAGE EXCEPTIONS IN NETWORK PROCESSORS  | 2/20/2004  | 10/783,621    | 9/14/2010       | 7,797,492       | US           |
| NMXI-0938.00/US | Utility - ORG | Issued     | METHOD AND APPARATUS FOR DATA INTEGRITY OF STATE RETENTIVE ELEMENTS UNDER LOW POWER MODES  | 6/8/2004   | 10/863,661    | 10/21/2008      | 7,441,268       | US           |
| NMXI-0939.00/US | Utility - ORG | Abandoned  | PRINT STRIPPER FOR ESD CONTROL   | 9/30/2004  | 10/956,994    |                 |                 | US           |
| NMXI-0940.00/US | Utility - ORG | Issued     | GROUND PLANE FOR INTEGRATED CIRCUIT PACKAGE  | 8/31/2004  | 10/930,251    | 9/18/2007       | 7,270,056       | US           |
| NMXI-0941.00/US | Utility - ORG | Issued     | COMPUANT MULTI-COMPOSITION INTERCONNECTS   | 6/21/2004  | 10/873,817    | 12/26/2006      | 7,154,175       | US           |
| NMXI-0942.00/US | Utility - ORG | Issued     | TECHNIQUES FOR TRANSDUCTION OF A MATRIX ARRANGED IN A MEMORY AS MULTIPLE ITEMS PER WORD  | 4/26/2004  | 10/852,178    | 7/15/2008       | 7,400,041       | US           |
| NMXI-0943.00/US | Utility - ORG | Abandoned  |  | 2/2/2005   | 11/050,369    |                 |                 | US           |
| NMXI-0944.00/US | Utility - ORG | Issued     | METHODS AND APPARATUS FOR PROVIDING STACKED-DIE DEVICES  | 8/14/2004  | 10/917,142    | 3/6/2007        | 7,187,068       | US           |
| NMXI-0944.01/US | Utility - DIV | Issued     | METHODS AND APPARATUS FOR PROVIDING STACKED-DIE DEVICES  | 8/24/2005  | 11/212,015    | 1/11/2011       | 7,867,818       | US           |
| NMXI-0945.00/US | Utility - ORG | Issued     | METHOD AND APPARATUS TO WRITE BACK DATA  | 3/10/2004  | 10/799,555    | 8/19/2008       | 7,415,577       | US           |
| NMXI-0946.00/US | Utility - ORG | Abandoned  | APPARATUS AND METHOD OF DYNAMIC USAGE PROFILE ACQUISITION AND UTILIZATION IN A WIRELESS MOBILE DEVICE  | 6/15/2004  | 10/868,703    |                 |                 | US           |
| NMXI-0947.00/US | Utility - ORG | Abandoned  | A METHOD AND APPARATUS FOR MULTIPROCESSOR EMBEDDED DEBUGGING SUPPORT VIA ATTACHMENT OF BREAKPOINT BIT FIELDS TO INSTRUCTIONS AND DEBUG REGISTER FIELD MANIPULATION | 3/31/2004  | 10/815,904    |                 |                 | US           |
| NMXI-0948.00/US | Utility - ORG | Issued     | ELECTROMAGNETIC INTERFERENCE (EMI) FILTER WITH PASSIVE NOISE CANCELLATION  | 6/30/2004  | 10/883,610    | 2/27/2007       | 7,183,500       | US           |
| NMXI-0949.00/US | Utility - ORG | Issued     | QUEUE STRUCTURE WITH VALIDITY VECTOR AND ORDER ARRAY   | 6/23/2004  | 10/874,998    | 7/24/2007       | 7,249,230       | US           |
| NMXI-0950.00/US | Utility - ORG | Abandoned  | METHOD, SYSTEM, AND PROGRAM FOR OPTIMIZING CODE  | 3/19/2004  | 10/805,106    |                 |                 | US           |
| NMXI-0951.00/US | Utility - ORG | Abandoned  | PROGRAMMABLE CURRENT LOAD SYSTEMS AND METHODS  | 3/31/2005  | 11/095,950    |                 |                 | US           |
| NMXI-0952.00/US | Utility - ORG | Abandoned  | NON STROBE SENSING CIRCUIT   | 2/9/2005   | 11/053,786    |                 |                 | US           |
| NMXI-0953.00/US | Utility - ORG | Abandoned  | FLIP-CHIPS ON FLEX SUBSTRATES, FLIP-CHIP AND WIRE-BONDED CHIP STACKS, AND METHODS OF ASSEMBLING SAME   | 8/10/2004  | 10/915,293    |                 |                 | US           |
| NMXI-0954.00/US | Utility - ORG | Issued     | METHOD, SYSTEM, AND APPARATUS FOR TRACKING DEFECTIVE CACHE UNES  | 7/16/2004  | 10/893,015    | 5/27/2008       | 7,380,180       | US           |
| NMXI-0955.00/US | Utility - ORG | Abandoned  | DETERMINING AVAILABLE BANDWIDTH IN A NETWORK   | 6/30/2004  | 10/883,107    |                 |                 | US           |
| NMXI-0956.00/US | Utility - ORG | Issued     | INTERCONNECT STRUCTURE IN INTEGRATED CIRCUITS  | 6/30/2004  | 10/890,988    | 2/5/2008        | 7,326,972       | US           |
| NMXI-0957.00/US | Utility - ORG | Issued     | INITIALIZING PHASE CHANGE MEMORIES   | 6/30/2004  | 10/881,664    | 1/29/2008       | 7,323,707       | US           |
| NMXI-0958.00/US | Utility - ORG | Abandoned  | OVERHEAD PROCESSING AND GENERATION TECHNIQUES  | 9/29/2004  | 10/954,947    |                 |                 | US           |
| NMXI-0959.00/US | Utility - ORG | Abandoned  | METHOD AND APPARATUS FOR CHANNEL HEALTH ENCODING   | 12/27/2004 | 11/015,965    |                 |                 | US           |

## Exhibit A - US Applications and Issued Patents

| Patent No.      | Matter Type   | Matter Status | Title   | File Date  | Serial No. | Issue Date | Patent No. | Country |
|-----------------|---------------|---------------|---|------------|------------|------------|------------|---------|
| NMXI-0960.00/US | Utility - ORG | Issued        | CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM                                   | 12/17/2004 | 11,016,197 | 10/13/2009 | 7,602,777  | US      |
| NMXI-0961.00/US | Utility - ORG | Issued        | METHOD AND APPARATUS FOR PROVIDING A CONNECTION MATRIX  | 12/17/2004 | 11,015,967 | 3/31/2009  | 7,512,060  | US      |
| NMXI-0962.00/US | Utility - ORG | Abandoned     | REMOTE STATUS FORWARDING IN A DISTRIBUTED CROSS-CONNECTION SYSTEM                                       | 12/17/2004 | 11,015,964 |            |            | US      |
| NMXI-0963.00/US | Utility - ORG | Issued        | DEVICE AND METHODS FOR INCREASING WIRELESS CONNECTION SPEEDS  | 9/21/2004  | 10,945,797 | 7/1/2008   | 7,394,783  | US      |
| NMXI-0963.01/US | Utility - CON | Issued        | DEVICE AND METHODS FOR INCREASING WIRELESS CONNECTION SPEEDS  | 6/25/2008  | 12,145,723 | 2/8/2011   | 7,885,227  | US      |
| NMXI-0964.00/US | Utility - ORG | Abandoned     | CALIBRATION METHODS FOR TUNABLE LASERS  | 9/30/2004  | 10,955,394 |            |            | US      |
| NMXI-0965.00/US | Utility - ORG | Issued        | PROCESSING REPLIES TO REQUEST PACKETS IN AN ADVANCED SWITCHING CONTEXT                                  | 9/3/2004   | 10,934,640 | 8/21/2007  | 7,260,661  | US      |
| NMXI-0966.00/US | Utility - ORG | Issued        | PROVIDING CURRENT FOR PHASE CHANGE MEMORIES   | 6/30/2004  | 10,880,692 | 4/15/2008  | 7,359,231  | US      |
| NMXI-0967.00/US | Utility - ORG | Abandoned     | REMOTE USB NETWORK DEVICE CONTROL   | 12/29/2004 | 11,025,116 |            |            | US      |
| NMXI-0968.00/US | Utility - ORG | Issued        | PHASE CHANGE MEMORY BITS RESET THROUGH A SERIES OF PULSES OF INCREASING AMPLITUDE                       | 2/15/2005  | 11,058,797 | 8/29/2006  | 7,099,180  | US      |
| NMXI-0968.01/US | Utility - DIV | Issued        | METHOD OF RESETTNG PHASE CHANGE MEMORY BITS THROUGH A SERIES OF PULSES OF INCREASING AMPLITUDE          | 7/14/2006  | 11,486,872 | 12/25/2007 | 7,313,016  | US      |
| NMXI-0969.00/US | Utility - ORG | Issued        | PHASE CHANGE MEMORY WITH DAMASCENE MEMORY ELEMENT   | 9/24/2004  | 10,949,090 | 11/14/2006 | 7,135,696  | US      |
| NMXI-0969.01/US | Utility - DIV | Issued        | PHASE CHANGE MEMORY WITH DAMASCENE MEMORY ELEMENT   | 10/4/2006  | 11,542,712 | 5/19/2009  | 7,534,625  | US      |
| NMXI-0970.00/US | Utility - ORG | Issued        | PHASE CHANGE MEMORY WITH A SELECT DEVICE HAVING A BREADDOWN LAYER                                       | 9/24/2004  | 10,948,884 | 9/12/2006  | 7,105,408  | US      |
| NMXI-0971.00/US | Utility - ORG | Pending       | DEPOSITING TITANIUM SILICON NITRIDE FILMS FOR FORMING PHASE CHANGE MEMORIES                             | 10/28/2004 | 10,977,186 |            |            | US      |
| NMXI-0972.00/US | Utility - ORG | Issued        | COMPENSATING THE WORKFUNCTION OF A METAL GATE TRANSISTOR FOR ABSTRACTION BY THE GATE DIELECTRIC LAYER   | 7/28/2004  | 10,900,666 | 10/24/2006 | 7,125,762  | US      |
| NMXI-0973.00/US | Utility - ORG | Issued        | MULTI-WIRELESS NETWORK CONFIGURABLE BEHAVIOR  | 5/4/2000   | 09,565,215 | 3/20/2007  | 7,193,965  | US      |
| NMXI-0974.00/US | Utility - ORG | Issued        | WALKING WEAVER IMAGE REJECT MIXER FOR RADIO   | 1/8/2002   | 10,043,746 | 3/14/2006  | 7,013,120  | US      |
| NMXI-0975.00/US | Utility - ORG | Issued        | LOW-SWING LEVEL SHIFTER   | 1/31/2005  | 11,047,442 | 5/8/2007   | 7,215,173  | US      |
| NMXI-0976.00/US | Utility - ORG | Pending       | DC-DC CONVERTER SWITCHING TRANSISTORS CURRENT MEASUREMENT TECHNIQUE                                     | 6/30/2005  | 11,173,760 |            |            | US      |
| NMXI-0977.00/US | Utility - ORG | Issued        | ADAPTIVE COMMUNICATION INTERFACE  | 10/29/2004 | 10,976,572 | 3/3/2009   | 7,500,129  | US      |
| NMXI-0977.01/US | Utility - DIV | Pending       | ADAPTIVE COMMUNICATION INTERFACE  | 1/12/2009  | 12,352,499 |            |            | US      |
| NMXI-0978.00/US | Utility - ORG | Issued        | TECHNIQUES FOR TRANSMITTING AND RECEIVING TRAFFIC OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS     | 12/28/2004 | 11,025,420 | 9/1/2009   | 7,583,664  | US      |
| NMXI-0979.00/US | Utility - ORG | Abandoned     | TECHNIQUES FOR PROCESSING TRAFFIC TRANSMITTED OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS         | 12/28/2004 | 11,024,528 |            |            | US      |
| NMXI-0981.00/US | Utility - ORG | Issued        | POWER DISTRIBUTION WITHIN A FOLDED FLEX PACKAGE METHOD AND APPARATUS                                    | 10/28/2004 | 10,977,908 | 12/4/2007  | 7,304,373  | US      |
| NMXI-0982.00/US | Utility - ORG | Abandoned     | SORTING OF CARBON NANOTUBES THROUGH SELECTIVE DNA DELAMINATION OF DNA/CARBON NANOTUBE HYBRID STRUCTURES | 3/30/2005  | 11,095,414 |            |            | US      |
| NMXI-0983.00/US | Utility - ORG | Issued        | FLASH MEMORY CELL HAVING REDUCED FLOATING GATE TO FLOATING GATE COUPLING                                | 3/30/2005  | 11,095,330 | 3/25/2008  | 7,348,618  | US      |
| NMXI-0983.01/US | Utility - DIV | Issued        | FLASH MEMORY CELL HAVING REDUCED FLOATING GATE TO FLOATING GATE COUPLING                                | 10/17/2006 | 11,582,881 | 12/16/2008 | 7,465,625  | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Patent No.      | Material      | Material  | Title  | Filed Date | Serial No. | Issued Date | Patent No. | Country |
|-----------------|---------------|-----------|--|------------|------------|-------------|------------|---------|
| NMXI-0985.00/US | Utility - ORG | Issued    | TRAINING PATTERN BASED DE-SKEW MECHANISM AND FRAME ALIGNMENT   | 9/7/2004   | 10/935,902 | 3/3/2009    | 7,500,131  | US      |
| NMXI-0986.00/US | Utility - ORG | Pending   | HEATING PHASE CHANGE MATERIAL FORMING A CARBON LAYER BETWEEN PHASE CHANGE LAYERS OF A PHASE CHANGE MEMORY                                  | 4/11/2005  | 11/103,188 |             |            | US      |
| NMXI-0987.00/US | Utility - ORG | Issued    | READING PHASE CHANGE MEMORIES TO REDUCE READ DISTURBS PROGRAMMABLE POWER TRANSITION COUNTER TECHNIQUES TO MANAGE PARTITION PHYSICAL MEMORY | 1/18/2005  | 11/037,850 | 10/16/2007  | 7,282,730  | US      |
| NMXI-0988.00/US | Utility - ORG | Issued    | SCALABLE TRACEBACK TECHNIQUE FOR CHANNEL DECODER PROTECTED PROCESSING APPARATUS, SYSTEMS, AND METHODS                                      | 1/5/2005   | 11/029,981 | 8/21/2007   | 7,259,982  | US      |
| NMXI-0989.00/US | Utility - ORG | Issued    | SYSTEMS AND METHODS FOR EFFICIENT OPERATIONS OF COMPONENTS IN A WIRELESS COMMUNICATIONS DEVICE   | 11/15/2004 | 10/589,948 | 5/5/2009    | 7,529,958  | US      |
| NMXI-0990.00/US | Utility - ORG | Abandoned | PIXEL DATA COMPRESSION FROM CONTROLLER TO DISPLAY  | 12/23/2004 | 11/021,323 |             |            | US      |
| NMXI-0991.00/US | Utility - ORG | Issued    | SECURE MEMORY CONTROLLER   | 12/23/2004 | 11/022,212 | 3/17/2009   | 7,506,239  | US      |
| NMXI-0992.00/US | Utility - ORG | Abandoned | SECURE MEMORY WITH MODIFIABLE ADDRESS MAP  | 12/21/2004 | 11/018,958 |             |            | US      |
| NMXI-0993.00/US | Utility - ORG | Pending   | SECURE CONTROLLER FOR BLOCK ORIENTED STORAGE   | 12/29/2004 | 11/024,956 |             |            | US      |
| NMXI-0994.00/US | Utility - ORG | Abandoned | SECURE CONTROLLER FOR BLOCK ORIENTED STORAGE   | 6/29/2005  | 11/169,537 |             |            | US      |
| NMXI-0995.01/US | Utility - CIP | Abandoned | SECURE CONTROLLER FOR BLOCK ORIENTED STORAGE   | 12/30/2004 | 11/027,784 | 8/12/2008   | 7,412,579  | US      |
| NMXI-0996.01/US | Utility - CON | Issued    | COMMUNICATING AN ADDRESS TO A MEMORY DEVICE  | 12/30/2005 | 11/323,473 |             |            | US      |
| NMXI-0997.00/US | Utility - ORG | Abandoned | METHOD FOR FORMING A MEMORY DEVICE   | 12/28/2004 | 11/027,913 | 3/8/2011    | 7,904,943  | US      |
| NMXI-0998.00/US | Utility - ORG | Abandoned | MEMORY CELL WITH NANOCRYSTAL AS DISCRETE STORAGE ELEMENT   | 3/5/2011   | 13/041,379 |             |            | US      |
| NMXI-0998.01/US | Utility - DIV | Abandoned | MECHANISM FOR PERFORMING RUN-AHEAD EXECUTION IN A CENTRAL PROCESSING UNIT  | 12/15/2004 | 11/012,316 |             |            | US      |
| NMXI-0999.00/US | Utility - ORG | Abandoned | ADAPTIVE WIRELESS NETWORKS AND METHODS FOR COMMUNICATING MULTIMEDIA IN A PROACTIVE ENTERPRISE  | 12/22/2006 | 11/615,862 |             |            | US      |
| NMXI-1000.00/US | Utility - ORG | Abandoned | POWER LOSS RECOVERY IN NON-VOLATILE MEMORY   | 12/28/2004 | 11/024,164 |             |            | US      |
| NMXI-1002.00/US | Utility - ORG | Issued    | WIRELESS DEVICE ENCLOSURE USING PIEZOELECTRIC COOLING STRUCTURES   | 11/16/2004 | 10/989,539 |             |            | US      |
| NMXI-1003.00/US | Utility - ORG | Issued    | ERROR PROTECTION TECHNIQUES FOR FRAMES ON A WIRELESS NETWORK   | 9/2/2005   | 11/213,371 | 11/3/2009   | 7,613,894  | US      |
| NMXI-1004.00/US | Utility - ORG | Abandoned | PULSED DC AND RF PHYSICAL VAPOR DEPOSITION CLUSTER TOOL  | 11/1/2005  | 11/140,769 | 7/24/2007   | 7,248,475  | US      |
| NMXI-1005.00/US | Utility - ORG | Pending   | METHOD AND APPARATUS FOR MONITORING PATH STATISTICS  | 9/30/2005  | 11/241,339 |             |            | US      |
| NMXI-1006.00/US | Utility - ORG | Abandoned | FREQUENCY-DEPENDENT VOLTAGE CONTROL IN DIGITAL LOGIC   | 4/26/2005  | 11/114,261 |             |            | US      |
| NMXI-1007.00/US | Utility - ORG | Issued    | POWER MODE CHANGE VOLTAGE CONTROL IN COMPUTERIZED SYSTEM   | 3/25/2005  | 11/090,571 | 10/13/2009  | 7,603,575  | US      |
| NMXI-1008.00/US | Utility - ORG | Abandoned | LOCKING ENTRIES INTO TRANSLATION LOOKASIDE BUFFERS   | 6/30/2005  | 11/173,218 |             |            | US      |
| NMXI-1009.00/US | Utility - CON | Pending   | METHODS TO PERFORM CACHE COHERENCY IN MULTIPROCESSOR SYSTEM  | 3/1/2005   | 11/069,924 |             |            | US      |
| NMXI-1009.01/US | Utility - ORG | Issued    | LOW LOCALITY-OF-REFERENCE SUPPORT IN A MULTI-LEVEL CACHE HIERARCHY   | 8/3/2011   | 13/197,221 |             |            | US      |
| NMXI-1010.00/US | Utility - ORG | Issued    | MOVABLE LOCKED LINES IN A MULTI-LEVEL CACHE DEVICE, SYSTEM AND METHOD FOR WIRELESS COMMUNICATION AND CURSOR POINTING                       | 3/30/2005  | 11/094,887 | 2/5/2008    | 7,328,313  | US      |
| NMXI-1011.00/US | Utility - ORG | Issued    | DIFFERENTIAL DELAY COMPENSATION  | 4/7/2005   | 11/101,785 | 8/25/2009   | 7,581,065  | US      |
| NMXI-1012.00/US | Utility - ORG | Abandoned | DIFFERENTIAL DELAY COMPENSATION  | 2/24/2006  | 11/361,228 |             |            | US      |
| NMXI-1013.00/US | Utility - ORG | Abandoned | VOLTAGE CONTROLLED OSCILLATOR (VCO) TUNING   | 6/30/2005  | 11/170,110 |             |            | US      |
| NMXI-1014.00/US | Utility - CON | Issued    |  | 6/30/2005  | 11/170,110 |             |            | US      |
| NMXI-1014.01/US | Utility - CON | Pending   |  | 3/30/2005  | 11/093,907 | 9/13/2011   | 8,018,925  | US      |
| NMXI-1015.00/US | Utility - ORG | Issued    |  | 9/9/2011   | 13/229,455 |             |            | US      |
| NMXI-1015.00/US | Utility - ORG | Issued    |  | 6/30/2005  | 11/171,860 | 9/18/2007   | 7,271,673  | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| MN DocNo        | MatterType    | MatterStatus | Title  | FileDate   | SerialNo   | IssueDate  | PatentNo  | Country |
|-----------------|---------------|--------------|--|------------|------------|------------|-----------|---------|
| NMXI-1016.00/US | Utility - ORG | Abandoned    | METHOD AND SYSTEM FOR TRANSFERRING MESSAGES TO A MOBILE STATION ACCORDING TO SPECIFIC PARAMETERS                               | 6/27/2005  | 11/166,143 |            |           | US      |
| NMXI-1017.00/US | Utility - ORG | Issued       | PHASE CHANGE MEMORY WITH U-SHAPED CHALCOGENIDE CELL  | 7/20/2005  | 11/185,488 | 2/2/2010   | 7,655,938 | US      |
| NMXI-1018.00/US | Utility - ORG | Abandoned    | FABRICATING SUB-LITHOGRAPHIC CONTACTS  | 7/29/2005  | 11/193,952 |            |           | US      |
| NMXI-1018.01/US | Utility - DIV | Issued       | FABRICATING SUB-LITHOGRAPHIC CONTACTS  | 10/6/2008  | 12/287,073 | 7/13/2010  | 7,754,516 | US      |
| NMXI-1018.02/US | Utility - DIV | Allowed      | FABRICATING SUB-LITHOGRAPHIC CONTACTS  | 6/3/2010   | 12/792,992 |            |           | US      |
| NMXI-1019.00/US | Utility - ORG | Issued       | PERIPHERAL INTERFACE ALERT MESSAGE FOR DOWNSTREAM DEVICE   | 6/20/2005  | 11/156,838 | 6/28/2011  | 7,970,958 | US      |
| NMXI-1019.01/US | Utility - CON | Pending      | PERIPHERAL INTERFACE ALERT MESSAGE FOR DOWNSTREAM DEVICE   | 5/19/2011  | 13/111,673 |            |           | US      |
| NMXI-1020.00/US | Utility - ORG | Abandoned    | REDUCING OXIDATION OF PHASE CHANGE MEMORY ELECTRODES   | 6/28/2005  | 11/168,780 |            |           | US      |
| NMXI-1021.00/US | Utility - ORG | Issued       | UNIVERSAL NONVOLATILE MEMORY BOOT MODE   | 7/26/2005  | 11/189,448 | 2/24/2009  | 7,496,719 | US      |
| NMXI-1022.00/US | Utility - ORG | Issued       | APPARATUS, SYSTEM, AND METHOD FOR OSCILLATOR NETWORK WITH MULTIPLE PARALLEL OSCILLATOR CIRCUITS                                | 6/29/2005  | 11/172,133 | 6/26/2007  | 7,236,059 | US      |
| NMXI-1023.00/US | Utility - ORG | Issued       | STRUCTURED, ELECTRICALLY-FORMED FLOATING GATE FOR FLASH MEMORIES   | 11/14/2005 | 11/274,622 | 5/20/2008  | 7,374,996 | US      |
| NMXI-1023.01/US | Utility - DIV | Issued       | STRUCTURED, ELECTRICALLY-FORMED FLOATING GATE FOR FLASH MEMORIES   | 3/25/2008  | 12/055,216 | 12/7/2010  | 7,847,333 | US      |
| NMXI-1024.00/US | Utility - ORG | Issued       | METHOD AND APPARATUS FOR OPTIMIZING FLASH DEVICE ERASE DISTRIBUTION  | 9/30/2005  | 11/240,004 | 9/16/2008  | 7,426,605 | US      |
| NMXI-1025.00/US | Utility - ORG | Abandoned    | ALTERNATING BETWEEN UPLINK AND DOWNLINK WITHIN EACH CHANNEL OF A FREQUENCY DIVISION MULTIPLEXING SYSTEM                        | 9/12/2005  | 11/225,303 |            |           | US      |
| NMXI-1026.00/US | Utility - ORG | Abandoned    | COMPOSITE ORGANIC INORGANIC NANOCUSTERS AS CARRIERS AND IDENTIFIERS OF TESTER MOLECULES  | 9/26/2006  | 11/527,895 |            |           | US      |
| NMXI-1027.00/US | Utility - ORG | Abandoned    | FORMING HEATERS FOR PHASE CHANGE MEMORIES  | 10/12/2005 | 11/248,488 |            |           | US      |
| NMXI-1027.01/US | Utility - DIV | Abandoned    | FORMING HEATERS FOR PHASE CHANGE MEMORIES  | 3/6/2008   | 12/074,813 |            |           | US      |
| NMXI-1028.00/US | Utility - ORG | Abandoned    | METHOD AND APPARATUS FOR DISPLAYING ROTATED IMAGES   | 12/16/2005 | 11/303,117 |            |           | US      |
| NMXI-1029.00/US | Utility - ORG | Abandoned    | LOW-JITTER CLOCK DISTRIBUTION  | 11/30/2005 | 11/291,204 |            |           | US      |
| NMXI-1030.00/US | Utility - ORG | Issued       | PROCESSOR MULTI-PARTITION SECURITY ARCHITECTURE  | 11/10/2005 | 11/271,778 | 11/24/2009 | 7,624,249 | US      |
| NMXI-1031.00/US | Utility - ORG | Issued       | WIRELESS COMMUNICATIONS WITH AUXILIARY ANTENNAS FORMING OVONIC THRESHOLD SWITCHES WITH REDUCED DEPOSITION CHAMBER GAS PRESSURE | 3/30/2006  | 11/394,994 | 7/21/2009  | 7,564,919 | US      |
| NMXI-1032.00/US | Utility - ORG | Abandoned    | FORMING OVONIC THRESHOLD SWITCHES WITH REDUCED DEPOSITION CHAMBER GAS PRESSURE   | 3/29/2006  | 11/392,135 |            |           | US      |
| NMXI-1033.00/US | Utility - ORG | Abandoned    | ENCODING TECHNIQUES EMPLOYING NOISE-BASED ADAPTATION   | 3/31/2006  | 11/394,929 |            |           | US      |
| NMXI-1034.00/US | Utility - ORG | Abandoned    | LOW DELAY AND SMALL MEMORY FOOTPRINT PICTURE BUFFERING   | 12/2/2005  | 11/292,569 |            |           | US      |
| NMXI-1035.00/US | Utility - ORG | Abandoned    | SECURITY MODULE HAVING ACCESS LIMITED BASED UPON SECURITY LEVEL OF CODE SEEKING ACCESS   | 2/15/2006  | 11/354,676 |            |           | US      |
| NMXI-1036.00/US | Utility - ORG | Abandoned    | METHOD AND APPARATUS TO EFFICIENTLY CONFIGURE MULTI-ANTENNA EQUALIZERS   | 3/30/2006  | 11/394,533 |            |           | US      |
| NMXI-1037.00/US | Utility - ORG | Issued       | RADIO FREQUENCY AMPLIFIER  | 7/6/2000   | 09/611,720 | 6/18/2002  | 6,407,632 | US      |
| NMXI-1038.00/US | Utility - ORG | Pending      | ARCHITECTURE FOR VIRTUAL SECURITY MODULE   | 9/29/2006  | 11/529,987 |            |           | US      |
| NMXI-1039.00/US | Utility - ORG | Issued       | CURRENT MIRROR DRIVE CIRCUIT WITH HIGH BREAKDOWN VOLTAGE   | 11/2/1994  | 08/333,264 | 11/21/1995 | 5,469,093 | US      |
| NMXI-1040.00/US | Utility - ORG | Abandoned    | SYSTEM AND METHOD FOR REDUCING FALSE ALARM IN THE PRESENCE OF RANDOM SIGNALS   | 3/31/2006  | 11/396,329 |            |           | US      |
| NMXI-1041.00/US | Utility - ORG | Issued       | LOW VOLTAGE DOUBLE BALANCED MIXER  | 1/7/1998   | 09/003,908 | 4/3/2001   | 6,211,718 | US      |
| NMXI-1042.00/US | Utility - ORG | Issued       | FREQUENCY SYNTHESIZER  | 4/22/1998  | 09/064,276 | 5/9/2000   | 6,060,917 | US      |
| NMXI-1043.00/US | Utility - ORG | Issued       | TRANSDUCANCE AMPLIFIER ARRANGEMENT   | 11/2/2000  | 09/705,499 | 11/12/2002 | 6,480,066 | US      |

11/21/2011

## Exhibit A - US Applications and Issued Patents

| Patent No       | Matter Type     | Matter Status | Title   | Filed Date | Serial No  | Issue Date | Patent No | Country |
|-----------------|-----------------|---------------|---|------------|------------|------------|-----------|---------|
| NMXI-1044.00/US | Utility - ORG   | Issued        | RADIO FREQUENCY AMPLIFIER WITH IMPROVED INTERMODULATION PERFORMANCE | 1/24/2002  | 10/056,155 | 4/5/2005   | 6,876,843 | US      |
| NMXI-1045.00/US | Utility - ORG   | Issued        | SELF-ALIGNED BIPOLEAR JUNCTION TRANSISTORS                          | 4/26/2006  | 11/411,311 | 1/25/2011  | 7,875,513 | US      |
| NMXI-1045.01/US | Utility - DIV   | Pending       | SELF-ALIGNED BIPOLEAR JUNCTION TRANSISTORS                          | 12/16/2010 | 12/969,652 |            |           | US      |
| NMXI-1046.00/US | Utility - ORG   | Issued        | VOLTAGE CONTROLLED OSCILLATORS                                      | 8/20/2002  | 10/223,869 | 1/27/2004  | 6,683,509 | US      |
| NMXI-1047.00/US | Utility - ORG   | Issued        | SIGNAL PROCESSING STAGE AND RADIO FREQUENCY TUNER                   | 4/28/2003  | 10/424,899 | 9/14/2004  | 6,791,414 | US      |
| NMXI-1048.00/US | Utility - ORG   | Abandoned     | ASYMMETRIC CHALCOGENIDE DEVICE                                      | 10/10/2006 | 11/545,234 |            |           | US      |
| NMXI-1049.00/US | Utility - ORG   | Pending       | READ WINDOW IN CHALCOGENIDE SEMICONDUCTOR MEMORIES                  | 11/9/2006  | 11/595,055 |            |           | US      |
| NMXI-1050.00/US | Utility - ORG   | Abandoned     | AUTHENTICATING DATA RETURNED FROM NON-VOLATILE MEMORY COMMANDS      | 9/28/2006  | 11/529,022 |            |           | US      |
| NMXI-1051.00/US | Utility - ORG   | Abandoned     | A PHASE CHANGE MEMORY CELL HAVING A TAPERED MICROTRENCH             | 11/30/2006 | 11/606,800 |            |           | US      |
| NMXI-1052.00/US | Utility - ORG   | Issued        | HIGH DENSITY NOR FLASH ARRAY ARCHITECTURE                           | 6/28/2007  | 11/823,518 | 3/22/2011  | 7,910,976 | US      |
| NMXI-1052.01/US | Utility - CON   | Pending       | HIGH DENSITY NOR FLASH ARRAY ARCHITECTURE                           | 3/9/2011   | 13/043,677 |            |           | US      |
| NMXI-1053.00/US | Utility - NSPCT | Expired       | OSCILLATOR NETWORK FOR RADIO RECEIVER                               | 5/17/1989  | 07/335,087 | 8/7/1990   | 4,947,141 | US      |
| NMXI-1054.00/US | Utility - ORG   | Issued        | DIGITAL TELEPHONE SYSTEM WITH AUTOMATIC VOICE MAIL REDIRECTION      | 8/31/1998  | 09/144,186 | 4/1/2003   | 6,542,584 | US      |

Exhibit B - Foreign Applications and Patents

| Mfr. Dctg No.   | Mater. Type     | Mater. Status | Title  | Filed Date | Serial No.  | Issue Date | Patent No.    | Country |
|-----------------|-----------------|---------------|--|------------|-------------|------------|---------------|---------|
| NMXI-0490.00/AU | Utility - NSPCT | Abandoned     | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE  | 1/27/1993  | 35952/93    | 8/20/1996  | 668379        | AU      |
| NMXI-0546.00/AU | Utility - ORG   | Lapsed        | METHOD OF PRE-CACHING DATA UTILIZING THREAD LISTS AND MULTIMEDIA EDITING SYSTEM USING SUCH PRE-CACHING           | 9/12/1995  | 37233/95    |            |               | AU      |
| NMXI-0341.00/BE | Utility - ORG   | Expired       | PORTABLE COMMUNICATION TERMINAL FOR REMOTE DATABASE QUERY  | 6/30/1987  | 87109409.0  | 12/15/1993 | 0251296       | BE      |
| NMXI-0203.00/BR | Utility - ORG   | Abandoned     | UNIVERSAL CARRIER FOR HOT SWAPABLE DRIVER  | 11/28/1995 | P19509921-2 | 8/19/2003  | P19509921-2   | BR      |
| NMXI-0173.00/CA | Utility - ORG   | Abandoned     | LOW POWER CHARGE PUMP FOR FLASH MEMORY   | 3/16/1995  | 2,185,333   | 6/1/2004   | 2,185,333     | CA      |
| NMXI-0393.00/CA | Utility - ORG   | Abandoned     | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK  | 6/18/2001  | 2,355,881   |            |               | CA      |
| NMXI-0462.00/CA | Utility - ORG   | Abandoned     | ANTENNA INTERFACE  | 11/26/2001 | 2,371,945   |            |               | CA      |
| NMXI-0490.00/CA | Utility - NSPCT | Abandoned     | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE  | 1/27/1993  | 2,132,108   | 3/30/1999  | 2,132,108     | CA      |
| NMXI-0497.00/CA | Utility - NSPCT | Abandoned     | REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLIANCE   | 10/31/2002 | 2,408,276   |            |               | CA      |
| NMXI-0350.00/CA | Utility - ORG   | Abandoned     | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION  | 1/20/1999  | 2,318,450   | 6/5/2007   | 2,318,450     | CA      |
| NMXI-0599.00/CA | Utility - NSPCT | Abandoned     | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL  | 8/27/2003  | 2,432,906   |            |               | CA      |
| NMXI-0622.00/CA | Utility - NSPCT | Abandoned     | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIER(S)         | 10/17/2002 | 2,463,627   |            |               | CA      |
| NMXI-0683.00/CL | Utility - ORG   | Abandoned     | BEST-OF-M PULSE POSITION MODULATION DETECTOR   | 6/5/1996   | 1000-96     | 5/21/2003  | Z195196684.7  | CL      |
| NMXI-0204.00/CN | Utility - NSPCT | Issued        | ACCURATE VPP DETECTOR FOR FLASH MEMORY   | 10/18/1995 | 95196684.7  | 1/14/2004  | Z196199936.5  | CN      |
| NMXI-0239.00/CN | Utility - NSPCT | Issued        | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS  | 12/12/1996 | 96199936.5  |            |               | CN      |
| NMXI-0251.00/CN | Utility - NSPCT | Abandoned     | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER   | 9/29/1997  | 97181414.7  | 5/28/2003  | Z197181414.7  | CN      |
| NMXI-0377.00/CN | Utility - NSPCT | Issued        | METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING   | 10/25/2000 | 00819034.8  | 1/11/2006  | Z100819034.8  | CN      |
| NMXI-0392.00/CN | Utility - ORG   | Issued        | CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL  | 11/5/1999  | 99815589.6  | 8/4/2004   | Z1.99815589.6 | CN      |
| NMXI-0414.00/CN | Utility - NSPCT | Issued        | IMPROVED FLOURINE DOPED SiO2 FILM AND METHOD OF FABRICATION  | 10/11/2000 | 00816502.5  | 9/28/2005  | Z100816502.5  | CN      |
| NMXI-0420.00/CN | Utility - ORG   | Issued        | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | 2/9/2000   | 00804578.X  | 3/2/2005   | Z100804578.X  | CN      |
| NMXI-0478.00/CN | Utility - NSPCT | Issued        | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS              | 6/1/2001   | 01814712.7  | 8/5/2009   | Z101814712.7  | CN      |
| NMXI-0502.00/CN | Utility - NSPCT | Abandoned     | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS                                       | 6/10/2003  | 1820255.1   |            |               | CN      |
| NMXI-0504.00/CN | Utility - NSPCT | Issued        | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS                     | 11/20/2001 | 01819255.6  | 10/22/2008 | Z101819255.6  | CN      |
| NMXI-0516.00/CN | Utility - NSPCT | Abandoned     | CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS  | 1/24/2001  | 01806003.X  |            |               | CN      |
| NMXI-0535.00/CN | Utility - NSPCT | Issued        | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS  | 9/25/2001  | 01816419.6  | 12/7/2005  | Z101816419.6  | CN      |
| NMXI-0550.00/CN | Utility - ORG   | Issued        | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION  | 12/27/2000 | 99804084.3  | 2/25/2004  | Z199804084.3  | CN      |
| NMXI-0583.00/CN | Utility - NSPCT | Abandoned     | VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY   | 8/29/2003  | 01822965.1  | 8/27/2008  | Z101822965.1  | CN      |
| NMXI-0599.00/CN | Utility - NSPCT | Issued        | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL  | 12/21/2001 | 01822925.5  | 11/12/2008 | Z101822925.5  | CN      |

11/21/2011

Exhibit B - Foreign Applications and Patents

| Int. Defacto    | Material Type    | Material Status | Title   | Filed Date | Serial No.     | Issued Date | Patent No.       | Country |
|-----------------|------------------|-----------------|---|------------|----------------|-------------|------------------|---------|
| NMXI-0626.00/CN | Utility - NSPCT  | Issued          | VI-AIN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE                                    | 11/28/2001 | 1821582.3      | 11/8/2006   | ZI01821582.3     | CN      |
| NMXI-0637.00/CN | Utility - NSPCT  | Issued          | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION   | 12/30/2002 | 02813257.2     | 7/9/2008    | ZI02813257.2     | CN      |
| NMXI-0653.00/CN | Utility - NSPCT  | Issued          | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY  | 11/20/2002 | 02823658.0     | 11/12/2008  | ZI02823658.0     | CN      |
| NMXI-0685.00/CN | Utility - ORG    | Pending         | MOBILE STATION LOCATING SYSTEM AND METHOD   | 3/23/2000  | 98808744.8     |             |                  | CN      |
| NMXI-0694.00/CN | Utility - NSPCT  | Issued          | VARIABLE LEVEL MEMORY   | 8/6/2002   | 02822880.4     | 1/21/2010   | ZI02822880.4     | CN      |
| NMXI-0710.00/CN | Utility - NSPCT  | Issued          | ELECTRONIC ASSEMBLY WITH FILLED NO-FLOW UNDERFILL METHODS OF MANUFACTURE                          | 10/28/2002 | 2821250.9      | 3/28/2007   | ZI02821250.9     | CN      |
| NMXI-0719.00/CN | Utility - NSPCT  | Abandoned       | DIGITAL FRAME DETERMINATION AND APPARATUS   | 7/2/2004   | 02826724.9     |             |                  | CN      |
| NMXI-0721.00/CN | Utility - NSPCT  | Abandoned       | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT   | 3/25/2004  | 02818839.X     | 1/25/2006   | ZI02818839.X     | CN      |
| NMXI-0736.00/CN | Utility - NSPCT  | Issued          | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING                                       | 12/17/2002 | 02828164       | 6/22/2011   | ZI02828164.0     | CN      |
| NMXI-0741.00/CN | Utility - ORG    | Issued          | SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES                                 | 7/6/2005   | 200510098094.1 | 1/14/2009   | ZI200510098094.1 | CN      |
| NMXI-0752.00/CN | Utility - NSPCT  | Abandoned       | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER                            | 12/16/2004 | 03813976.6     |             |                  | CN      |
| NMXI-0778.00/CN | Utility - NSPCT  | Abandoned       | INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK                           | 2/5/2003   | 03808970.X     |             |                  | CN      |
| NMXI-0839.00/CN | Utility - NSPCT  | Abandoned       | PRONUNCIATION NETWORK   | 12/22/2003 | 200380107684.5 |             |                  | CN      |
| NMXI-0844.00/CN | Utility - NSPCT  | Pending         | SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE - CHINA | 1/8/2004   | 200480004269.1 |             |                  | CN      |
| NMXI-0860.00/CN | Utility - NSPCT  | Issued          | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD     | 2/9/2004   | 200480005928.3 | 1/7/2009    | ZI200480005928.3 | CN      |
| NMXI-0924.00/CN | Utility - NSPCT  | Abandoned       | ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION  | 7/3/2006   | 200480039698.2 |             |                  | CN      |
| NMXI-0984.00/CN | Utility - ORG    | Pending         | RESONANT TUNNELING DEVICE USING METAL OXIDE SEMICONDUCTOR PROCESSING                              | 10/28/2005 | 200510023017.X |             |                  | CN      |
| NMXI-1028.00/CN | Utility - ORG    | Abandoned       | METHOD AND APPARATUS FOR EFFICIENT AND ARTIFACTS FREE DISPLAYING OF ROTATING IMAGES               | 12/15/2006 | 200610064091.0 |             |                  | CN      |
| NMXI-0018.00/DE | Utility - ORG    | Abandoned       | A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY  | 8/6/1990   | PA024930.1-53  | 6/17/1993   | 4024930          | DE      |
| NMXI-0203.00/DE | Utility - EP/PAT | Issued          | PORTABLE DISK DRIVE CARRIER AND CHASSIS   | 11/28/1995 | 9594294.2      | 8/9/2004    | 595.33.487.5-08  | DE      |
| NMXI-0208.00/DE | Utility - EP/PAT | Issued          | VARIABLE STAGE CHARGE PUMP  | 9/30/1996  | 96933993.6     | 4/17/2002   | 69620774.5       | DE      |
| NMXI-0222.00/DE | Utility - EP/PAT | Issued          | PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES   | 3/12/1996  | 96908756.8     | 6/30/2004   | 0815615          | DE      |
| NMXI-0224.00/DE | Utility - EP/PAT | Issued          | NEGATIVE VOLTAGE SWITCHING CIRCUIT  | 6/7/1996   | 96918382.1     | 9/10/2003   | 69629925.9       | DE      |
| NMXI-0239.00/DE | Utility - NSPCT  | Issued          | MULTI-LAYERED CONTRACTING FOR SECURING INTEGRATED CIRCUITS  | 12/12/1996 | 19681689.0     | 5/10/2001   | 19681689         | DE      |
| NMXI-0246.00/DE | Utility - EP/PAT | Issued          | METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM             | 6/27/1997  | 97932317.7     | 8/25/2004   | 69730430.2       | DE      |
| NMXI-0251.00/DE | Utility - ORG    | Abandoned       | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER                                | 5/21/1999  | 197.82.133.2   | 8/19/2004   | 197.82.133       | DE      |
| NMXI-0291.00/DE | Utility - ORG    | Issued          | INTEGRATED CIRCUIT PACKAGE  | 7/9/1997   | 19781978.8     | 10/11/2007  | 19781978         | DE      |
| NMXI-0313.00/DE | Utility - ORG    | Issued          | SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES   | 10/11/1999 | 198.82.312.6   | 7/22/2010   | 198.82.312       | DE      |
| NMXI-0337.00/DE | Utility - EP/PAT | Issued          | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD  | 1/5/1999   | 99901331.1     | 4/23/2008   | 1048108          | DE      |
| NMXI-0341.00/DE | Utility - ORG    | Expired         | PORTABLE COMMUNICATION TERMINAL FOR REMOTE DATABASE QUERY   | 6/30/1987  | 87103170.0     | 12/15/1993  | 0251296          | DE      |

## Exhibit B - Foreign Applications and Patents

| MTD/CNO         | Material        | Material  | Title  | Filed      | Serial        | Issued     | Patent      | Country |
|-----------------|-----------------|-----------|--|------------|---------------|------------|-------------|---------|
| NMXI-0392.00/DE | Utility - ORG   | Issued    | METHOD AND CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL   | 5/14/2001  | 19983726.0-42 | 11/11/2010 | 19983726    | DE      |
| NMXI-0393.00/DE | Utility - EPPAT | Issued    | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK                                      | 12/30/1999 | 99967807.1    | 8/10/2005  | 1142202     | DE      |
| NMXI-0414.00/DE | Utility - NSPCT | Issued    | IMPROVED FLOURINE DOPED SIO2 FILM AND METHOD OF FABRICATION  | 10/11/2000 | 10085212.2    | 11/20/2008 | 10085212    | DE      |
| NMXI-0473.00/DE | Utility - EPPAT | Issued    | EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP                          | 3/4/1998   | 98911466.5    | 6/29/2005  | 968568      | DE      |
| NMXI-0478.00/DE | Utility - EPPAT | Issued    | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS        | 6/1/2001   | 01941818.5    | 12/15/2010 | 60143656.3  | DE      |
| NMXI-0496.00/DE | Utility - EPPAT | Abandoned | METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE                               | 12/27/2002 | 01948405.4    | 9/17/2008  | 1370935     | DE      |
| NMXI-0504.00/DE | Utility - EPPAT | Issued    | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS               | 11/20/2001 | 01995179.7    | 4/13/2011  | 1336286     | DE      |
| NMXI-0532.00/DE | Utility - EPPAT | Issued    | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM              | 10/15/1997 | 97308183.9    | 7/30/2003  | 69723798.2  | DE      |
| NMXI-0535.00/DE | Utility - EPPAT | Issued    | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS  | 9/25/2001  | 01975434.0    | 5/6/2009   | 1323226     | DE      |
| NMXI-0546.00/DE | Utility - EPPAT | Issued    | THREAD LIST EXPLICIT CACHING SYSTEM  | 9/12/1995  | 95935071.1    | 12/6/2006  | 0781432     | DE      |
| NMXI-0550.00/DE | Utility - EPPAT | Issued    | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION  | 1/20/1999  | 99904159.3    | 1/14/2006  | 699040302.2 | DE      |
| NMXI-0562.00/DE | Utility - EPPAT | Issued    | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRANGED WAVEGUIDE GRATINGS (AGC) AND OPTICAL AMPLIFIER(S) | 10/17/2002 | 02795533.5    | 12/21/2005 | 1436648     | DE      |
| NMXI-0565.00/DE | Utility - EPPAT | Issued    | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE   | 11/28/2001 | 01273027.1    | 5/11/2011  | 60144621.6  | DE      |
| NMXI-0572.00/DE | Utility - NSPCT | Issued    | DUAL-DAMASCENE INTERCONNECTS WITHOUT AN ETCH STOP LAYER BY ALTERNATING FILDS                               | 9/27/2002  | 02768930.6    | 12/7/2005  | 1430525     | DE      |
| NMXI-0579.00/DE | Utility - EPPAT | Issued    | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  | 11/27/2003 | 02725662.7    | 6/22/2005  | 60204778.1  | DE      |
| NMXI-0594.00/DE | Utility - EPPAT | Issued    | VARIABLE LEVEL MEMORY  | 8/6/2002   | 02757016.7    | 6/27/2007  | 1428221     | DE      |
| NMXI-0713.00/DE | Utility - EPPAT | Abandoned | DIGITAL FRAME DETERMINATION AND APPARATUS  | 6/24/2004  | 2798607.4     | 12/2/2009  | 60234644.4  | DE      |
| NMXI-0786.00/DE | Utility - EPPAT | Issued    | ESTABLISHING AN AD HOC NETWORK   | 4/24/2003  | 03724240.1    | 7/8/2009   | 1514386     | DE      |
| NMXI-0844.00/DE | Utility - EPPAT | Issued    | SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE - EPO            | 1/8/2004   | 04700900.6    | 12/12/2007 | 1595343     | DE      |
| NMXI-0174.00/EP | Utility - ORG   | Abandoned | MICROPROGRAMMED SYSTEMS SOFTWARE INSTRUCTION UNDO  | 5/11/1988  | 88107610.3    | 11/30/1994 | 292791      | EP      |
| NMXI-0203.00/EP | Utility - NSPCT | Issued    | UNIVERSAL CARRIER FOR HOT SWAPABLE DRIVER  | 11/28/1995 | 95942924.2    | 9/8/2004   | 0794717     | EP      |
| NMXI-0222.00/EP | Utility - NSPCT | Issued    | DUAL DIE PACKAGE FOR SERIAL APPLICATIONS   | 3/12/1996  | 96908756.8    | 6/30/2004  | 0815615     | EP      |
| NMXI-0325.00/EP | Utility - NSPCT | Abandoned | MULTI-LAYER CODER/DECODER FOR PRODUCING QUANTIZATION ERROR SIGNAL SAMPLES                                  | 6/2/2000   | 98946118.1    |            |             | EP      |
| NMXI-0337.00/EP | Utility - NSPCT | Issued    | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD   | 1/5/1999   | 99901331.1    | 4/23/2008  | 1048108     | EP      |
| NMXI-0377.00/EP | Utility - NSPCT | Abandoned | METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING   | 5/31/2002  | 00989714.1    |            |             | EP      |
| NMXI-0393.00/EP | Utility - NSPCT | Issued    | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK                                      | 12/30/1999 | 99967807.1    | 8/10/2005  | 1142202     | EP      |
| NMXI-0462.00/EP | Utility - NSPCT | Abandoned | ANTENNA INTERFACE  | 10/30/2001 | 00932547.3    |            |             | EP      |
| NMXI-0473.00/EP | Utility - NSPCT | Issued    | EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP                          | 3/4/1998   | 98911466.5    | 6/29/2005  | 968568      | EP      |
| NMXI-0474.00/EP | Utility - NSPCT | Lapsed    | SINGLE-CHIP CMOS DIRECT-CONVERSION TRANSCIVER  | 3/16/2001  | 99946859.8    | 11/15/2006 | 1110330     | EP      |
| NMXI-0478.00/EP | Utility - NSPCT | Issued    | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS        | 12/29/2002 | 01941818.5    | 12/15/2010 | 1297530     | EP      |

11/21/2011

## Exhibit B - Foreign Applications and Patents

| Pat. No.        | Mat. Type       | Mat. Status | Title   | Filed      | Ser. No.   | Issued     | Pat. No. | Country |
|-----------------|-----------------|-------------|---|------------|------------|------------|----------|---------|
| NMXI-0490.00/EP | Utility - NSPCT | Abandoned   | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE   | 1/27/1993  | 93904673.6 | 12/20/1995 | 638188   | EP      |
| NMXI-0496.00/EP | Utility - NSPCT | Abandoned   | METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE                            | 12/27/2002 | 01948405.4 | 9/17/2008  | 1370935  | EP      |
| NMXI-0497.00/EP | Utility - NSPCT | Abandoned   | REMOTEY MANAGING AND CONTROLLING A CONSUMER APPLIANCE   | 5/17/2001  | 01935694.8 |            |          | EP      |
| NMXI-0502.00/EP | Utility - NSPCT | Abandoned   | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS                              | 6/9/2003   | 1992286.3  |            |          | EP      |
| NMXI-0504.00/EP | Utility - NSPCT | Issued      | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS            | 11/20/2001 | 01995179.7 | 4/13/2011  | 1336286  | EP      |
| NMXI-0532.00/EP | Utility - ORG   | Issued      | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM           | 10/15/1997 | 97308183.9 | 7/30/2003  | 0837429  | EP      |
| NMXI-0533.00/EP | Utility - NSPCT | Issued      | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS   | 9/25/2001  | 01975434.0 | 5/6/2009   | 1323226  | EP      |
| NMXI-0546.00/EP | Utility - NSPCT | Issued      | THREAD LIST EXPLICIT CACHING SYSTEM   | 9/12/1995  | 95935071.1 | 12/6/2006  | 0781432  | EP      |
| NMXI-0550.00/EP | Utility - NSPCT | Issued      | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION   | 1/20/1999  | 99904159.3 | 1/14/2009  | 1053621  | EP      |
| NMXI-0583.00/EP | Utility - NSPCT | Abandoned   | SYSTEM AND METHOD FOR PROVIDING AUTHENTICATION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY   | 8/29/2003  | 1273026.3  |            |          | EP      |
| NMXI-0622.00/EP | Utility - NSPCT | Issued      | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIERS) | 10/17/2002 | 02795533.5 | 12/21/2005 | 1436648  | EP      |
| NMXI-0626.00/EP | Utility - NSPCT | Issued      | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE  | 11/28/2001 | 01273027.1 | 5/11/2011  | 1346613  | EP      |
| NMXI-0637.00/EP | Utility - NSPCT | Allowed     | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION   | 12/30/2002 | 02792542.9 |            |          | EP      |
| NMXI-0679.00/EP | Utility - NSPCT | Issued      | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE   | 11/27/2003 | 02725662.7 | 6/22/2005  | 1396081  | EP      |
| NMXI-0694.00/EP | Utility - NSPCT | Issued      | VARIABLE LEVEL MEMORY   | 8/6/2002   | 02757016.7 | 6/27/2007  | 1428221  | EP      |
| NMXI-0710.00/EP | Utility - NSPCT | Abandoned   | ELECTRONIC ASSEMBLY WITH FILLED NO-FLOW UNDERFILL METHODS OF MANUFACTURE                                | 4/8/2004   | 02802225.9 |            |          | EP      |
| NMXI-0719.00/EP | Utility - NSPCT | Abandoned   | DIGITAL FRAME DETERMINATION AND APPARATUS   | 6/24/2004  | 02798607.4 | 12/2/2009  | 1468514  | EP      |
| NMXI-0721.00/EP | Utility - NSPCT | Abandoned   | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT   | 4/22/2004  | 02757625.5 |            |          | EP      |
| NMXI-0736.00/EP | Utility - NSPCT | Pending     | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF PRODUCING IT                                       | 12/17/2002 | 02805206.6 |            |          | EP      |
| NMXI-0741.00/EP | Utility - NSPCT | Pending     | SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES                                       | 6/30/2005  | 05768522.4 |            |          | EP      |
| NMXI-0778.00/EP | Utility - NSPCT | Abandoned   | INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK                                 | 2/5/2003   | 03717887.8 | 4/11/2007  | 1476994  | EP      |
| NMXI-0786.00/EP | Utility - NSPCT | Issued      | ESTABLISHING AN AD HOC NETWORK  | 4/24/2003  | 03724240.1 | 7/8/2009   | 1514386  | EP      |
| NMXI-0844.00/EP | Utility - NSPCT | Issued      | SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE - EPO         | 1/8/2004   | 04709900.6 | 12/12/2007 | 1595343  | EP      |
| NMXI-0860.00/EP | Utility - NSPCT | Pending     | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD           | 2/9/2004   | 04709509.6 |            |          | EP      |
| NMXI-0924.00/EP | Utility - NSPCT | Abandoned   | ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION  | 10/27/2004 | 04796692.4 |            |          | EP      |
| NMXI-0393.00/FI | Utility - EPPAT | Abandoned   | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK                                   | 12/30/1999 | 99967807.1 | 8/10/2005  | 1142202  | FI      |
| NMXI-0018.00/FR | Utility - ORG   | Abandoned   | A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY  | 5/30/1990  | 9006713    | 1/27/1995  | 9006713  | FR      |
| NMXI-0050.00/FR | Utility - ORG   | Abandoned   | CONFORMING POLISHING PAD  | 7/7/1992   | 9208358    | 4/29/1994  | 9208358  | FR      |
| NMXI-0124.00/FR | Utility - EPPAT | Abandoned   | NEGATIVE VOLTAGE SWITCHING CIRCUIT  | 6/7/1996   | 96918582.1 | 9/10/2003  | 0830686  | FR      |

11/21/2011

## Exhibit B - Foreign Applications and Patents

| Pub. No.        | Matter Type     | Matter Status | Title  | Filed Date | Serial No.  | Issue Date | Patent No. | Country |
|-----------------|-----------------|---------------|--|------------|-------------|------------|------------|---------|
| NMXI-0393.00/FR | Utility - EPAT  | Abandoned     | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK                                    | 12/30/1999 | 99967807.1  | 8/10/2005  | 1142202    | FR      |
| NMXI-0532.00/FR | Utility - EPAT  | Lapsed        | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM            | 10/05/1997 | 97308183.9  | 7/30/2003  | 0837429    | FR      |
| NMXI-0622.00/FR | Utility - EPAT  | Abandoned     | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (AGW) AND OPTICAL AMPLIFIER(S) | 10/17/2002 | 02795533.5  | 12/21/2005 | 1436648    | FR      |
| NMXI-0679.00/FR | Utility - EPAT  | Abandoned     | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  | 11/27/2003 | 02725662.7  | 6/22/2005  | 1396081    | FR      |
| NMXI-0804.00/GB | Utility - ORG   | Abandoned     | LOAD LINE FOR EPROM  | 8/24/1988  | 8820080.3   | 11/13/1991 | 2214580    | GB      |
| NMXI-0812.00/GB | Utility - ORG   | Abandoned     | PORT EXPANDER ARCHITECTURE FOR EPROM   | 11/2/1989  | 8924745.6   | 11/4/1992  | 2227582    | GB      |
| NMXI-0818.00/GB | Utility - Div   | Abandoned     | METHOD OF ACCESSING AN ELECTRICALLY PROGRAMMABLE MEMORY DEVICE   | 3/5/1990   | 9314259.4   | 1/26/1994  | 226796     | GB      |
| NMXI-0818.01/GB | Utility - ORG   | Abandoned     | A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY   | 3/5/1990   | 9004859.6   | 1/5/1994   | 2234834    | GB      |
| NMXI-0850.00/GB | Utility - ORG   | Abandoned     | CONFORMING POLISHING PAD   | 2/19/1992  | 9203649.0   | 11/30/1994 | 2257382    | GB      |
| NMXI-0174.00/GB | Utility - ORG   | Abandoned     | M&A FOR BACKING OUT OF A SOFTWARE INSTRUCT. AFTER EXEC. HAS BEGUN  | 5/11/1988  | 881077610.3 | 11/30/1994 | 292791     | GB      |
| NMXI-0203.00/GB | Utility - EPAT  | Abandoned     | PORTABLE DISK DRIVE CARRIER AND CHASSIS  | 11/28/1995 | 9594294.2   | 9/8/2004   | 0794717    | GB      |
| NMXI-0222.00/GB | Utility - EPAT  | Abandoned     | PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES  | 3/12/1996  | 969087568   | 6/30/2004  | 0815615    | GB      |
| NMXI-0239.00/GB | Utility - ORG   | Abandoned     | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS  | 12/12/1996 | 9811429.1   | 12/13/2000 | 2323212    | GB      |
| NMXI-0251.00/GB | Utility - NSPCT | Abandoned     | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER                                       | 9/29/1997  | 9911695.6   | 9/12/2001  | 2340973    | GB      |
| NMXI-0291.00/GB | Utility - ORG   | Abandoned     | INTEGRATED CIRCUIT PACKAGE   | 7/9/1997   | 9905399.3   | 8/15/2001  | 2333182    | GB      |
| NMXI-0341.00/GB | Utility - ORG   | Abandoned     | PORTABLE COMMUNICATION TERMINAL FOR REMOTE DATABASE QUERY  | 6/30/1987  | 87109409.0  | 12/15/1993 | 0251296    | GB      |
| NMXI-0376.00/GB | Utility - ORG   | Lapsed        | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE   | 11/1/2000  | 0026721.1   | 12/4/2002  | 2353912    | GB      |
| NMXI-0414.00/GB | Utility - NSPCT | Abandoned     | IMPROVED FLOUSINE DOPED SiO2 FILM AND METHOD OF FABRICATION  | 10/11/2000 | 0212404.8   | 4/28/2004  | 2373572    | GB      |
| NMXI-0490.00/GB | Utility - NSPCT | Abandoned     | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE  | 1/27/1993  | 93904673.6  | 12/20/1995 | 638188     | GB      |
| NMXI-0532.00/GB | Utility - EPAT  | Issued        | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM            | 10/05/1997 | 97308183.9  | 7/30/2003  | 0837429    | GB      |
| NMXI-0546.00/GB | Utility - EPAT  | Lapsed        | THREAD LIST EXPLICIT CACHING SYSTEM  | 9/12/1995  | 95935071.1  | 12/6/2006  | 0781432    | GB      |
| NMXI-0622.00/GB | Utility - EPAT  | Abandoned     | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (AGW) AND OPTICAL AMPLIFIER(S) | 10/17/2002 | 02795533.5  | 12/21/2005 | 1436648    | GB      |
| NMXI-0679.00/GB | Utility - EPAT  | Abandoned     | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  | 11/27/2003 | 02725662.7  | 6/22/2005  | 1396081    | GB      |
| NMXI-0694.00/GB | Utility - EPAT  | Issued        | VARIABLE LEVEL MEMORY  | 8/6/2002   | 02757016.7  | 6/27/2007  | 1428221    | GB      |
| NMXI-0712.00/GB | Utility - ORG   | Abandoned     | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER                | 11/26/1999 | 0202551.8   | 3/31/2004  | 2369702    | GB      |
| NMXI-0812.00/GB | Utility - ORG   | Abandoned     | PORT EXPANDER ARCHITECTURE FOR EPROM   | 11/2/1989  | 97102060.5  | 3/27/1998  | 1000475    | HK      |
| NMXI-0804.00/GB | Utility - ORG   | Issued        | PORTABLE DISK DRIVE CARRIER AND CHASSIS  | 3/17/1998  | 98102243.4  | 5/13/2005  | HK1003252  | HK      |
| NMXI-0204.00/GB | Utility - ORG   | Issued        | METHOD AND APPARATUS FOR DETECTING AND SELECTING VOLTAGE SUPPLIES FOR FLASH MEMORY                       | 2/26/1998  | 981015020.2 | 7/19/2002  | HK1002790  | HK      |
| NMXI-0205.00/GB | Utility - ORG   | Issued        | VARIABLE STAGE CHARGE PUMP   | 1/15/1999  | 99100215.1  | 2/14/2003  | HK1015520  | HK      |
| NMXI-0224.00/GB | Utility - ORG   | Issued        | NEGATIVE VOLTAGE SWITCHING CIRCUIT   | 9/21/1998  | 98110789.7  | 7/30/2004  | HK10098978 | HK      |
| NMXI-0239.00/GB | Utility - ORG   | Issued        | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS  | 12/12/1996 | 99101098.1  | 8/17/2001  | 1016341    | HK      |

11/21/2011

## Exhibit B - Foreign Applications and Patents

| MT-Docking      | Material        | Material Status | The  | Filed      | Serial No           | Issued Date | Patent No | Country |
|-----------------|-----------------|-----------------|--|------------|---------------------|-------------|-----------|---------|
| NMX1-0246.00/HK | Utility - ORG   | Issued          | METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM                            | 5/7/1999   | 99102071            | 5/6/2005    | HK1017451 | HK      |
| NMX1-0251.00/HK | Utility - ORG   | Abandoned       | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER   | 9/29/1997  | 00105429.0          | 5/31/2002   | 1027403   | HK      |
| NMX1-0376.00/HK | Utility - ORG   | Issued          | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE   | 4/4/2001   | 01102410.7          | 5/2/2003    | HK1031968 | HK      |
| NMX1-0377.00/HK | Utility - ORG   | Abandoned       | METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING   | 5/31/2002  | 02108033.0          |             |           | HK      |
| NMX1-0414.00/HK | Utility - ORG   | Issued          | IMPROVED FLOUTINE DOPED SiO2 FILM AND METHOD OF FABRICATION  | 10/11/2000 | 02107701.3          | 12/10/2004  | HK1046331 | HK      |
| NMX1-0478.00/HK | Utility - ORG   | Abandoned       | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS              | 12/23/2002 | 03104815.2          |             |           | HK      |
| NMX1-0502.00/HK | Utility - ORG   | Abandoned       | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS                                       | 11/5/2003  | 03107859.2          |             |           | HK      |
| NMX1-0504.00/HK | Utility - ORG   | Abandoned       | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS                     | 11/20/2001 | 03106719.4          |             |           | HK      |
| NMX1-0516.00/HK | Utility - ORG   | Abandoned       | CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS  | 5/23/2003  | 03103974.1          |             |           | HK      |
| NMX1-0532.00/HK | Utility - ORG   | Issued          | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM                    | 5/25/1998  | 98194475.9          | 2/20/2004   | 1005524   | HK      |
| NMX1-0550.00/HK | Utility - ORG   | Issued          | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION  | 5/22/2001  | 01103490.8          | 9/11/2009   | 1034154   | HK      |
| NMX1-0626.00/HK | Utility - ORG   | Abandoned       | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE   | 11/28/2001 | 3109116.7           |             |           | HK      |
| NMX1-0719.00/HK | Utility - ORG   | Abandoned       | DIGITAL FRAME DETERMINATION AND APPARATUS  | 10/27/2004 | 04108409.4          |             |           | HK      |
| NMX1-0721.00/HK | Utility - ORG   | Abandoned       | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT  | 8/6/2004   | 04105864.8          |             |           | HK      |
| NMX1-0736.00/HK | Utility - ORG   | Abandoned       | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING  | 11/3/2004  | 04108659.1          |             |           | HK      |
| NMX1-0786.00/HK | Utility - ORG   | Abandoned       | ESTABLISHING AN AD HOC NETWORK   | 4/24/2003  | 05102495.1          | 7/8/2009    | 1069269   | HK      |
| NMX1-0844.00/HK | Utility - ORG   | Abandoned       | SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE                        | 12/30/2005 | 05112185.5          |             |           | HK      |
| NMX1-0860.00/HK | Utility - ORG   | Abandoned       | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD                    | 12/19/2005 | 05111716.5          |             |           | HK      |
| NMX1-0950.00/IE | Utility - ORG   | Abandoned       | CONFORMING POLISHING PAD   | 4/7/1992   | 921103              | 11/21/1995  | 66126     | IE      |
| NMX1-0282.00/IL | Utility - ORG   | Abandoned       | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN                                | 12/11/1997 | 130563              | 2/1/2004    | 130563    | IL      |
| NMX1-0420.00/IN | Utility - ORG   | Abandoned       | CONTROLLED COLLAPSE CHIP CONNECTION (CA) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS | 8/10/2001  | IN/PCT/200101042/MU | 4/17/2006   | 200061    | IN      |
| NMX1-0497.00/IN | Utility - NSPCT | Abandoned       | A METHOD AND SYSTEM FOR PROVIDING INTERNET SERVICES TO A CONSUMER THROUGH REMOTE MANAGEMENT AND CONTROL          | 5/17/2001  | IN/PCT/2002/01358-C | 6/9/2006    | 199364    | IN      |
| NMX1-0502.00/IN | Utility - NSPCT | Abandoned       | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS                                       | 4/29/2003  | 540/KOLNP/2003      |             |           | IN      |
| NMX1-0626.00/IN | Utility - NSPCT | Abandoned       | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE   | 5/27/2003  | 675/KOLNP/2003      | 10/14/2005  | 195131    | IN      |
| NMX1-0018.00/IT | Utility - ORG   | Abandoned       | A NOVEL ARCHITECTURE FOR FLASH VIRTUAL GROUND MEMORY ARRAY   | 7/23/1990  | 21015 A/90          | 5/16/1994   | 1242497   | IT      |
| NMX1-0174.00/IT | Utility - ORG   | Abandoned       | M&A FOR BACKING OUT OF A SOFTWARE INSTRUCTION AFTER EXECUTION HAS BEGUN  | 5/11/1988  | 88107610.3          | 11/30/1994  | 292791    | IT      |

11/21/2011

## Exhibit B - Foreign Applications and Patents

| Pat. No.        | Mat. Type       | Mat. Status | Title  | Filed Date | Serial No.  | Issue Date | Patent No. | Country |
|-----------------|-----------------|-------------|--|------------|-------------|------------|------------|---------|
| NMXI-0532.00/IT | Utility - EPPAT | Lapsed      | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM                    | 10/27/2003 | 97308183.9  |            |            | IT      |
| NMXI-0550.00/IT | Utility - EPPAT | Issued      | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION  | 1/20/1999  | 999041593   | 1/14/2009  | 1053621    | IT      |
| NMXI-0023.00/JP | Utility - ORG   | Issued      | TUNGSTEN ETCH PROCESS  | 4/17/1992  | 122983/92   | 7/27/2001  | 3213820    | JP      |
| NMXI-0034.00/JP | Utility - ORG   | Issued      | UPROM DEVICE   | 11/13/1991 | 324108/91   | 4/6/2001   | 317963     | JP      |
| NMXI-0050.00/JP | Utility - ORG   | Abandoned   | CONFORMING POLISHING PAD   | 5/22/1992  | 154196/92   | 8/18/2000  | 3099209    | JP      |
| NMXI-0052.00/JP | Utility - Div   | Abandoned   | ADVANCED MULTILAYER MOLDED PLASTIC PACKAGE USING MESIC TECHNOLOGY  | 2/18/1993  | 514358/93   |            |            | JP      |
| NMXI-0064.00/JP | Utility - ORG   | Issued      | SURFACE SWEEPING METHOD FOR 3-D TOPOGRAPHY SIMULATION  | 5/24/1993  | 121723/1993 | 4/11/2003  | 3416894    | JP      |
| NMXI-0065.00/JP | Utility - ORG   | Issued      | CALCULATION METHOD FOR PARTICLE FLUX SHADOWING   | 5/24/1993  | 121605/1993 | 11/4/2002  | 3365565    | JP      |
| NMXI-0168.00/JP | Utility - ORG   | Issued      | BUILD UP TYPE SOCKETABLE BGH   | 3/17/1994  | 71270/94    | 4/15/2005  | 3667786    | JP      |
| NMXI-0173.00/JP | Utility - ORG   | Issued      | METHOD AND APPARATUS FOR PROVIDING AN ULTRA LOW POWER REGULATED NEGATIVE CHARGE PUMP                             | 3/16/1995  | 524718/95   | 6/17/2005  | 3688710    | JP      |
| NMXI-0174.00/JP | Utility - ORG   | Abandoned   | METHOD AND APPARATUS FOR BACKING OUT OF A SOFTWARE INSTRUCTION AFTER EXECUTION HAS BEGUN                         | 5/18/1988  | 121653/88   | 8/26/1994  | 1865189    | JP      |
| NMXI-0204.00/JP | Utility - NSPCT | Pending     | ACCURATE VPP DETECTOR FOR FLASH MEMORY   | 10/18/1995 | 514005/96   |            |            | JP      |
| NMXI-0208.00/JP | Utility - NSPCT | Issued      | VARIABLE STAGE CHARGE PUMP   | 9/30/1996  | 513766/97   | 2/3/2006   | 3766101    | JP      |
| NMXI-0222.00/JP | Utility - NSPCT | Issued      | DUAL DIE PACKAGE FOR SERIAL APPLICATIONS   | 3/12/1996  | 522776/96   | 9/9/2005   | 3717937    | JP      |
| NMXI-0224.00/JP | Utility - NSPCT | Abandoned   | NEGATIVE VOLTAGE SWITCHING CIRCUIT   | 6/7/1996   | 501941/97   |            |            | JP      |
| NMXI-0251.00/JP | Utility - ORG   | Abandoned   | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER   | 9/29/1997  | 523636/98   | 2/26/2010  | 4463879    | JP      |
| NMXI-0282.00/JP | Utility - ORG   | Abandoned   | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN                                | 12/11/1997 | 530079/98   |            |            | JP      |
| NMXI-0323.00/JP | Utility - ORG   | Abandoned   | PERIPHERAL DEVICE PREVENTING POST-SCAN MODIFICATION  | 3/12/1998  | 541666/98   |            |            | JP      |
| NMXI-0325.00/JP | Utility - ORG   | Abandoned   | MULTI-LAYER CODER/DECODER FOR PRODUCING QUANTIZATION ERROR SIGNAL SAMPLES  | 9/21/1998  | 2000-519556 |            |            | JP      |
| NMXI-0337.00/JP | Utility - NSPCT | Issued      | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD   | 1/5/1999   | 2000-540619 | 9/19/2008  | 4188562    | JP      |
| NMXI-0376.00/JP | Utility - NSPCT | Abandoned   | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE   | 3/12/1999  | 2000-547785 |            |            | JP      |
| NMXI-0392.00/JP | Utility - ORG   | Issued      | METHOD AND CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL   | 4/19/2001  | 2000-583167 | 7/24/2009  | 4348015    | JP      |
| NMXI-0420.00/JP | Utility - ORG   | Abandoned   | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIPILAR UNDERFILL MATERIALS | 8/31/2001  | 2000-603092 |            |            | JP      |
| NMXI-0490.00/JP | Utility - NSPCT | Issued      | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE  | 1/27/1993  | 519231/93   | 3/31/2006  | 3786419    | JP      |
| NMXI-0502.00/JP | Utility - NSPCT | Abandoned   | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS                                       | 6/9/2003   | 2002-548782 |            |            | JP      |
| NMXI-0516.00/JP | Utility - NSPCT | Issued      | CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORMS  | 5/23/2003  | 2001-565629 | 6/9/2006   | 3813873    | JP      |
| NMXI-0532.00/JP | Utility - ORG   | Abandoned   | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM                    | 7/4/1997   | 135011/1997 |            |            | JP      |
| NMXI-0583.00/JP | Utility - NSPCT | Abandoned   | SYSTEM AND METHOD FOR PROVIDING AUTHENTICATION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY            | 6/27/2003  | 2002-554833 | 3/6/2009   | 4272429    | JP      |
| NMXI-0599.00/JP | Utility - NSPCT | Abandoned   | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL  | 8/27/2003  | 2002-555069 |            |            | JP      |
| NMXI-0599.01/JP | Utility - NSPCT | Abandoned   | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL  | 12/21/2001 | 2007-290121 |            |            | JP      |
| NMXI-0622.00/JP | Utility - NSPCT | Issued      | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYS WAVEGUIDE GRATING (AGW) AND OPTICAL AMPLIFIER(S)          | 10/17/2002 | 2003-536786 | 3/13/2009  | 4276946    | JP      |

11/21/2011

## Exhibit B - Foreign Applications and Patents

| WI-PatNo        | MatrType        | MatrStatus | Title   | FileDate   | SerialNo        | IssueDate  | PatentNo | Country |
|-----------------|-----------------|------------|---|------------|-----------------|------------|----------|---------|
| NMXI-0626.00/JP | Utility - NSPCT | Abandoned  | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE  | 6/30/2003  | 2002-555593     |            |          | JP      |
| NMXI-0653.00/JP | Utility - NSPCT | Abandoned  | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY  | 11/20/2002 | P 2003-55031    |            |          | JP      |
| NMXI-0659.00/JP | Utility - NSPCT | Abandoned  | ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED  | 9/26/2003  | 2002-575882     |            |          | JP      |
| NMXI-0679.00/JP | Utility - NSPCT | Issued     | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE   | 4/12/2002  | 2002-586286     | 3/28/2008  | 4102198  | JP      |
| NMXI-0712.00/JP | Utility - ORG   | Abandoned  | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS | 1/30/2002  | 2001-516051     |            |          | JP      |
| NMXI-0736.00/JP | Utility - NSPCT | Issued     | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING   | 12/17/2002 | 2003-553708     | 7/11/2008  | 4152892  | JP      |
| NMXI-0752.00/JP | Utility - NSPCT | Abandoned  | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER  | 10/14/2004 | 586733/2003     |            |          | JP      |
| NMXI-0752.01/JP | Utility - DIV   | Abandoned  | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER  | 12/25/2008 | 2008-329618     |            |          | JP      |
| NMXI-0786.00/JP | Utility - NSPCT | Abandoned  | ESTABLISHING AN AD HOC NETWORK  | 4/24/2003  | 2004-500440     |            |          | JP      |
| NMXI-0854.00/JP | Utility - NSPCT | Abandoned  | APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING   | 4/16/2004  | 2004-501283     |            |          | JP      |
| NMXI-0924.00/JP | Utility - NSPCT | Abandoned  | ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION  | 10/27/2004 | 2006-538272     |            |          | JP      |
| NMXI-0950.00/KR | Utility - ORG   | Issued     | CONFORMING POLISHING PAD  | 3/17/1992  | 92-4336         | 5/18/1999  | 214163   | KR      |
| NMXI-0173.00/KR | Utility - ORG   | Issued     | LOW POWER CHARGE PUMP FOR FLASH MEMORY  | 9/23/1996  | 1996-705254     | 5/15/2000  | 263275   | KR      |
| NMXI-0224.00/KR | Utility - NSPCT | Issued     | NEGATIVE VOLTAGE SWITCHING CIRCUIT  | 6/7/1996   | 1997-709025     | 3/27/2001  | 292832   | KR      |
| NMXI-0239.00/KR | Utility - NSPCT | Issued     | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS   | 6/20/1998  | 1998-704709     | 8/24/2001  | 307895   | KR      |
| NMXI-0251.00/KR | Utility - NSPCT | Issued     | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER  | 9/29/1997  | 1999-700461     | 5/30/2003  | 387336   | KR      |
| NMXI-0282.00/KR | Utility - ORG   | Issued     | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN   | 6/29/1999  | 1999-7005947    | 6/21/2002  | 343029   | KR      |
| NMXI-0291.00/KR | Utility - ORG   | Issued     | INTEGRATED CIRCUIT PACKAGE  | 3/9/1999   | 1999-7001953    | 4/29/2005  | 488324   | KR      |
| NMXI-0313.00/KR | Utility - ORG   | Issued     | SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES   | 10/9/1999  | 1999-7009316    | 5/30/2003  | 387338   | KR      |
| NMXI-0337.00/KR | Utility - NSPCT | Issued     | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD  | 7/13/2000  | 10-2000-7007725 | 12/20/2002 | 0367110  | KR      |
| NMXI-0370.00/KR | Utility - ORG   | Abandoned  | POSITION SENSITIVE DISPLAY CONTROLLER   | 11/7/2000  | 2000-7012436    | 4/22/2004  | 430139   | KR      |
| NMXI-0392.00/KR | Utility - ORG   | Issued     | METHOD AND CIRCUIT FOR DATA DEPENDENT VOLTAGE BIAS LEVEL  | 4/19/2001  | 10-2001-7006012 | 4/4/2006   | 0569749  | KR      |
| NMXI-0420.00/KR | Utility - ORG   | Issued     | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS  | 9/3/2001   | 2001-7011227    | 10/11/2005 | 0522383  | KR      |
| NMXI-0420.01/KR | Utility - DIV   | Issued     | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS  | 1/21/2005  | 2005-7001212    | 6/22/2005  | 0498675  | KR      |
| NMXI-0502.00/KR | Utility - NSPCT | Abandoned  | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS  | 6/5/2003   | 10-2003-7007506 |            |          | KR      |
| NMXI-0535.00/KR | Utility - NSPCT | Issued     | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS   | 3/5/2003   | 2003-7004397    | 1/25/2006  | 0548910  | KR      |

11/21/2011

## Exhibit B - Foreign Applications and Patents

| WI-DataNo       | MatterType       | MatterStatus | Title   | FilingDate | SerialNo         | IssueDate  | PatentNo       | Country |
|-----------------|------------------|--------------|---|------------|------------------|------------|----------------|---------|
| NMXI-0622.00/KR | Utility - NSPCT  | Issued       | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIER(S) VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE | 10/17/2002 | 2004-7005595     | 4/3/2007   | 0705865        | KR      |
| NMXI-0626.00/KR | Utility - NSPCT  | Issued       | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  | 6/28/2003  | 2003-7008832     | 7/14/2006  | 10-0603879     | KR      |
| NMXI-0653.00/KR | Utility - NSPCT  | Issued       | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  | 11/20/2002 | 2004-7008174     | 9/28/2006  | 10-0632138     | KR      |
| NMXI-0679.00/KR | Utility - NSPCT  | Issued       | VARIABLE LEVEL MEMORY   | 10/29/2003 | 10-203-7014161   | 2/2/2006   | 0550676        | KR      |
| NMXI-0694.00/KR | Utility - NSPCT  | Pending      | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING   | 3/17/2004  | 2004-7003885     |            |                | KR      |
| NMXI-0736.00/KR | Utility - NSPCT  | Issued       | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER  | 6/17/2004  | 2004-7009478     | 1/9/2007   | 669594         | KR      |
| NMXI-0752.00/KR | Utility - NSPCT  | Abandoned    | APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING   | 10/15/2004 | 10-2004-7016496  |            |                | KR      |
| NMXI-0854.00/KR | Utility - NSPCT  | Abandoned    | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS  | 4/16/2004  | 2005-7021223     |            |                | KR      |
| NMXI-0420.00/MX | Utility - ORG    | Abandoned    | DUAL DIE PACKAGE FOR SECAL APPLICATIONS   | 8/10/2001  | PA/A/2001/008580 |            |                | MX      |
| NMXI-0222.00/MY | Utility - ORG    | Abandoned    | AN INTEGRATED CIRCUIT PACKAGE   | 3/8/1996   | 9600858          | 6/30/2006  | 124460         | MY      |
| NMXI-0291.00/MY | Utility - ORG    | Abandoned    | SELF-CONFIGURING INTERFACE ARCHITECTURE ON FLASH MEMORIES   | 9/9/1997   | 9704164          | 5/31/2005  | MY-116966-A    | MY      |
| NMXI-0313.00/MY | Utility - ORG    | Abandoned    | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS  | 4/29/1998  | 9801913          | 4/29/2006  | MY-122261-A    | MY      |
| NMXI-0502.00/MY | Utility - ORG    | Abandoned    | VIA-IN-PAD WITH OFF CENTER GEOMETRY AND METHODS OF MANUFACTURE  | 11/12/2001 | PI20015198       |            |                | MY      |
| NMXI-0626.00/MY | Utility - ORG    | Abandoned    | DIGITAL LEAKAGE COMPENSATION CIRCUIT  | 12/10/2001 | PI20015604       | 6/29/2007  | MY-130278-A    | MY      |
| NMXI-0645.00/MY | Utility - ORG    | Abandoned    | NOISE DEPENDENT FILTER  | 1/18/2002  | PI 20020207      | 5/31/2006  | MY -122780 - A | MY      |
| NMXI-0655.00/MY | Utility - ORG    | Abandoned    | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING   | 6/21/2002  | PI 2022322       | 12/31/2007 | MY-134599-A    | MY      |
| NMXI-0736.00/MY | Utility - ORG    | Abandoned    | APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING   | 12/17/2002 | PI20024718       | 11/28/2008 | MY-136656-A    | MY      |
| NMXI-0854.00/MY | Utility - ORG    | Abandoned    | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK   | 5/7/2004   | PI 20041084      |            |                | MY      |
| NMXI-0393.00/NL | Utility - EP/PAT | Abandoned    | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE   | 12/30/1999 | 99967807.1       | 8/10/2005  | 1142202        | NL      |
| NMXI-0679.00/NL | Utility - EP/PAT | Abandoned    | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD  | 4/12/2002  | 02725662.7       | 6/22/2005  | 1396081        | NL      |
| NMXI-0337.00/PH | Utility - ORG    | Abandoned    | VARIABLE STAGE CHARGE PUMP  | 1/12/1999  | 1-1999-00064     | 2/3/2004   | 1-1999-00064   | PH      |
| NMXI-0208.00/RU | Utility - NSPCT  | Abandoned    | INTEGRATED OPTICAL CIRCUIT HAVING AN INTEGRATED ARRAYED WAVEGUIDE GRATING (ACW) AND OPTICAL AMPLIFIER(S)  | 9/30/1996  | 98108548         | 8/20/2001  | 2172525        | RU      |
| NMXI-0622.00/RU | Utility - NSPCT  | Abandoned    | ARRANGEMENT AND METHOD FOR PROVIDING FLEXIBLE MANAGEMENT OF A NETWORK   | 10/17/2002 | 2004114867       | 3/20/2006  | 2272308        | RU      |
| NMXI-0393.00/SE | Utility - EP/PAT | Abandoned    | CONFORMING POLISHING PAD  | 12/30/1999 | 99967807.1       | 8/10/2005  | 1142202        | SE      |
| NMXI-0050.00/SG | Utility - ORG    | Issued       | METHOD AND APPARATUS FOR PROGRAMMING AND ERASING FLASH EEPROM/MEMORY ARRAYS UTILIZING A CHARGE PUMP CIRCUIT   | 2/19/1992  | 9590755-6        | 7/23/1995  | 30657          | SG      |
| NMXI-0123.00/SG | Utility - ORG    | Issued       | UNIVERSAL CARRIER FOR HOT SWAPABLE DRIVER   | 9/5/1994   | 9605497-8        | 5/7/2002   | 63591          | SG      |
| NMXI-0203.00/SG | Utility - NSPCT  | Issued       | ACCURATE VPP DETECTOR FOR FLASH MEMORY  | 11/28/1995 | 9702174-5        | 9/28/1998  | 41086          | SG      |
| NMXI-0204.00/SG | Utility - NSPCT  | Issued       | VARIABLE STAGE CHARGE PUMP  | 10/18/1995 | 9701544-0        | 9/28/1998  | 39320          | SG      |
| NMXI-0208.00/SG | Utility - NSPCT  | Issued       | METHOD TO PREVENT INTRUSIONS INTO ELECTRONIC CIRCUITRY  | 9/30/1995  | 9800988-9        | 1/25/2000  | 49445          | SG      |
| NMXI-0221.00/SG | Utility - ORG    | Issued       | DUAL DIE PACKAGE FOR SECAL APPLICATIONS   | 2/15/1996  | 9704832-4        | 5/25/1999  | 45876          | SG      |
| NMXI-0222.00/SG | Utility - NSPCT  | Issued       |   | 3/12/1996  | 9704782-3        | 3/30/1999  | 45806          | SG      |

11/21/2011

## Exhibit B - Foreign Applications and Patents

| Patent No.      | Patent Type     | Patent Status | Title   | Filed      | Serial No.  | Issue Date | Patent No. | Country |
|-----------------|-----------------|---------------|---|------------|-------------|------------|------------|---------|
| NMXI-0246.00/SG | Utility - NSPCT | Issued        | METHOD AND APPARATUS FOR PROVIDING IMPROVED DIAGNOSTIC FUNCTIONS IN A COMPUTER SYSTEM               | 6/27/1997  | 9806364-7   | 3/20/2001  | 61079      | SG      |
| NMXI-0251.00/SG | Utility - NSPCT | Abandoned     | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER                                  | 9/29/1997  | 9902175-0   | 10/16/2001 | 55368      | SG      |
| NMXI-0282.00/SG | Utility - ORG   | Issued        | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN                   | 12/11/1997 | 9902870-6   | 2/15/2002  | 66110      | SG      |
| NMXI-0291.00/SG | Utility - ORG   | Issued        | INTEGRATED CIRCUIT PACKAGE  | 7/9/1997   | 9900858-3   | 4/17/2001  | 64145      | SG      |
| NMXI-0414.00/SG | Utility - NSPCT | Issued        | IMPROVED FLOUORINE DOPED SiO2 FILM AND METHOD OF FABRICATION  | 10/11/2000 | 200202998-7 | 4/29/2005  | 89071      | SG      |
| NMXI-0462.00/SG | Utility - ORG   | Abandoned     | ANTENNA INTERFACE   | 11/26/2001 | 200107090-3 | 7/29/2005  | 84929      | SG      |
| NMXI-0473.00/SG | Utility - NSPCT | Issued        | EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP                   | 3/4/1998   | 9904271.5   | 10/31/2007 | 67712      | SG      |
| NMXI-0478.00/SG | Utility - NSPCT | Issued        | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS | 6/1/2001   | 200207831-9 | 10/31/2005 | 94050      | SG      |
| NMXI-0502.00/SG | Utility - NSPCT | Abandoned     | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS                          | 5/29/2003  | 200303042-6 | 8/31/2006  | 97371      | SG      |
| NMXI-0532.00/SG | Utility - ORG   | Issued        | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM       | 10/13/1997 | 9703726-1   | 4/21/2001  | 74026      | SG      |
| NMXI-0535.00/SG | Utility - NSPCT | Issued        | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS   | 9/25/2001  | 200301211-9 | 10/31/2005 | 95375      | SG      |
| NMXI-0626.00/SG | Utility - NSPCT | Issued        | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE                                      | 11/28/2001 | 200302974-1 | 11/30/2005 | 96977      | SG      |
| NMXI-0637.00/SG | Utility - NSPCT | Issued        | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION   | 12/30/2002 | 200307522-3 | 8/31/2006  | 101681     | SG      |
| NMXI-0653.00/SG | Utility - NSPCT | Issued        | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY  | 11/20/2002 | 200402809-8 | 8/31/2006  | 104206     | SG      |
| NMXI-0694.00/SG | Utility - NSPCT | Issued        | VARIABLE LEVEL MEMORY   | 8/6/2002   | 200400973-4 | 5/31/2006  | 103418     | SG      |
| NMXI-0719.00/SG | Utility - NSPCT | Abandoned     | DIGITAL FRAME DETERMINATION AND APPARATUS   | 6/28/2004  | 200403602-6 | 7/31/2006  | 105264     | SG      |
| NMXI-0721.00/SG | Utility - NSPCT | Abandoned     | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT   | 3/10/2004  | 200401398-3 | 5/31/2006  | 103034     | SG      |
| NMXI-0786.00/SG | Utility - NSPCT | Issued        | ESTABLISHING AN AD HOC NETWORK  | 4/24/2003  | 200406094-3 | 10/31/2006 | 117627     | SG      |
| NMXI-0844.00/SG | Utility - NSPCT | Pending       | SYSTEM AND METHOD OF EFFICIENTLY MODULATING DATA USING SYMBOLS HAVING MORE THAN ONE PULSE           | 1/8/2004   | 200504905-1 |            |            | SG      |
| NMXI-0502.00/TH | Utility - ORG   | Abandoned     | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS                          | 12/3/2001  | 70155       |            |            | TH      |
| NMXI-0626.00/TH | Utility - ORG   | Abandoned     | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE                                      | 12/27/2001 | 70821       |            |            | TH      |
| NMXI-0710.00/TH | Utility - ORG   | Abandoned     | ELECTRONIC ASSEMBLY WITH FILLED NO-FLOW UNDERFILL METHODS OF MANUFACTURE                            | 10/24/2002 | 77522       |            |            | TH      |
| NMXI-0050.00/TW | Utility - ORG   | Issued        | CONFORMING POLISHING PAD- UTILITY   | 4/1/1992   | 81102503    | 2/1/1994   | 065102     | TW      |
| NMXI-0168.00/TW | Utility - ORG   | Issued        | BUILD UP TYPE SOCKETABLE BGH  | 2/28/1996  | 85102552    | 10/1/1999  | 107586     | TW      |
| NMXI-0221.00/TW | Utility - ORG   | Issued        | A METHOD TO PREVENT INTRUSION INTO ELECTRONIC CIRCUITRY   | 12/28/1995 | 84114065    | 4/23/2002  | NI 147686  | TW      |
| NMXI-0239.00/TW | Utility - ORG   | Issued        | IIC BASED VBI DECODER   | 1/30/1997  | 86101070    | 4/1/1998   | 093868     | TW      |
| NMXI-0253.00/TW | Utility - ORG   | Issued        | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER                                  | 10/15/1997 | 86115173    | 7/11/1999  | 105201     | TW      |
| NMXI-0282.00/TW | Utility - ORG   | Issued        | METHOD AND APPARATUS FOR BIT CELL GROUND CHOKING FOR IMPROVED MEMORY WRITE MARGIN                   | 12/26/1997 | 86119800    | 2/21/1999  | NI-101313  | TW      |
| NMXI-0330.00/TW | Utility - ORG   | Issued        | CIRCUIT AND METHOD FOR CONTROLLING POWER AND PERFORMANCE BASED ON OPERATING ENVIRONMENT             | 5/2/1998   | 87106806    | 8/21/2001  | NI-139833  | TW      |
| NMXI-0337.00/TW | Utility - ORG   | Issued        | VARIABLE DELAY CELL WITH A SELF-BIASING LOAD  | 1/13/1999  | 88100466    | 5/28/2001  | NI-132638  | TW      |
| NMXI-0376.00/TW | Utility - ORG   | Abandoned     | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE  | 8/4/1999   | 88107420    | 8/1/2001   | NI-137397  | TW      |

11/21/2011

## Exhibit B - Foreign Applications and Patents

| MTI DocNo       | Matentype     | MatentStatus | Title  | Filedate   | SerialNo  | Issuedate  | Patentno  | Country |
|-----------------|---------------|--------------|--|------------|-----------|------------|-----------|---------|
| NMXI-0377.00/TW | Utility - ORG | Issued       | METHOD AND APPARATUS FOR PERFORMING VIDEO IMAGE DECODING   | 12/5/2000  | 089125882 | 12/27/2002 | NI-162716 | TW      |
| NMXI-0411.00/TW | Utility - ORG | Issued       | METHOD AND APPARATUS FOR WRITING UOPS INTO A BUFFER QUEUE INCLUDING OVERWRITING ENTRIES THAT ARE INDICATED TO CONTAIN INVALID UOPS                     | 2/21/2001  | 090103910 | 7/1/2003   | NI-172931 | TW      |
| NMXI-0414.00/TW | Utility - ORG | Issued       | IMPROVED FLOURINE DOPED SIO2 FILM AND METHOD OF FABRICATION  | 11/16/2000 | 089124269 | 1/1/2005   | 1226100   | TW      |
| NMXI-0462.00/TW | Utility - ORG | Issued       | ANTENNA INTERFACE  | 5/23/2000  | 89109929  | 4/17/2002  | 147310    | TW      |
| NMXI-0474.00/TW | Utility - ORG | Issued       | SINGLE-CHIP CMOS DIRECT-CONVERSION TRANSCIVER  | 12/7/1999  | 88115667  | 12/8/2001  | 138262    | TW      |
| NMXI-0478.00/TW | Utility - ORG | Issued       | INDUCTIVE CHARGE PUMP CIRCUIT FOR PROVIDING VOLTAGES USEFUL FOR FLASH MEMORY AND OTHER APPLICATIONS  | 5/24/2001  | 90112533  | 2/1/2003   | 519657    | TW      |
| NMXI-0497.00/TW | Utility - ORG | Abandoned    | REMOTELY MANAGING AND CONTROLLING A CONSUMER APPLANCE  | 5/30/2001  | 90113072  | 11/1/2005  | 1242733   | TW      |
| NMXI-0504.00/TW | Utility - ORG | Issued       | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS   | 11/21/2001 | 090128857 | 11/11/2003 | NI-189923 | TW      |
| NMXI-0532.00/TW | Utility - ORG | Abandoned    | APPARATUS AND METHOD FOR SIMULATING SPECULAR REFLECTION IN A COMPUTER GRAPHICS/IMAGING SYSTEM  | 6/27/1997  | 86109133  | 12/16/1998 | 096578    | TW      |
| NMXI-0535.00/TW | Utility - ORG | Abandoned    | DEGENERATIVE LOAD TEMPERATURE CORRECTION FOR CHARGE PUMPS  | 9/27/2001  | 090123932 | 10/16/2003 | NI-179952 | TW      |
| NMXI-0548.00/TW | Utility - ORG | Issued       | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING  | 3/5/2002   | 091104064 | 7/27/2004  | NI-199496 | TW      |
| NMXI-0537.00/TW | Utility - ORG | Issued       | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION  | 12/31/2002 | 91138032  | 10/11/2005 | 1241355   | TW      |
| NMXI-0545.00/TW | Utility - ORG | Issued       | DIGITAL LEAKAGE COMPENSATION CIRCUIT   | 3/26/2002  | 091105913 | 10/27/2003 | NI-180769 | TW      |
| NMXI-0552.00/TW | Utility - ORG | Issued       | APPARATUS HAVING A FIRST CIRCUIT SUPPLYING A POWER POTENTIAL TO A SECOND CIRCUIT UNDER A FIRST OPERATING MODE OTHERWISE DECOUPLING THE POWER POTENTIAL | 3/7/2002   | 91104263  | 7/11/2005  | 1235909   | TW      |
| NMXI-0553.00/TW | Utility - ORG | Issued       | LOW VOLTAGE OPERATION OF STATIC RANDOM ACCESS MEMORY   | 11/20/2002 | 091132888 | 3/21/2007  | 1 277100  | TW      |
| NMXI-0559.00/TW | Utility - ORG | Abandoned    | ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED   | 1/15/2002  | 91100508  | 9/29/2004  | 203147    | TW      |
| NMXI-0579.00/TW | Utility - ORG | Issued       | TWO-DIMENSIONAL PYRAMID FILTER ARCHITECTURE  | 3/12/2002  | 091104602 | 10/1/2003  | NI-187516 | TW      |
| NMXI-0594.00/TW | Utility - ORG | Issued       | VARIABLE LEVEL MEMORY  | 8/28/2002  | 91119529  | 12/11/2006 | 1268511   | TW      |
| NMXI-0713.00/TW | Utility - ORG | Abandoned    | DIGITAL FRAME DETERMINATION AND APPARATUS  | 1/3/2003   | 92100083  | 11/11/2006 | 1 266510  | TW      |
| NMXI-0721.00/TW | Utility - ORG | Abandoned    | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT  | 9/23/2002  | 091121741 | 1/21/2008  | 1292902   | TW      |
| NMXI-0734.00/TW | Utility - ORG | Abandoned    | INTEGRATED OPTOELECTRICAL CIRCUIT PACKAGE WITH OPTICAL WAVEGUIDE INTERCONNECTS   | 3/11/2003  | 92105182  |            |           | TW      |
| NMXI-0736.00/TW | Utility - ORG | Issued       | FILM BULK ACOUSTIC RESONATOR STRUCTURE AND METHOD OF MAKING  | 12/13/2002 | 91136074  | 6/4/2004   | 196797    | TW      |
| NMXI-0741.00/TW | Utility - ORG | Issued       | SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES  | 7/4/2005   | 94122531  | 12/1/2009  | 1 317868  | TW      |
| NMXI-0752.00/TW | Utility - ORG | Abandoned    | METHOD, MACHINE-READABLE MEDIUM, APPARATUS AND SYSTEM FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER                                  | 4/17/2003  | 92108929  |            |           | TW      |
| NMXI-0778.00/TW | Utility - ORG | Abandoned    | INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK  | 2/21/2003  | 92103629  | 12/1/2009  | 1318066   | TW      |
| NMXI-0781.00/TW | Utility - ORG | Abandoned    | ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMER CIRCUIT  | 2/24/2003  | 092103764 |            |           | TW      |
| NMXI-0814.00/TW | Utility - ORG | Issued       | ADAPTIVE EQUALIZATION USING A CONDITIONAL UPDATE SIGN-SIGN LEAST MEAN SQUARE ALGORITHM   | 9/9/2004   | 93127272  | 12/1/2005  | 1244835   | TW      |

11/21/2011

Exhibit B - Foreign Applications and Patents

| AppNo           | MatType       | MatStatus | Title  | Filed      | SerNo          | IssueDate  | PatentNo | Country |
|-----------------|---------------|-----------|--|------------|----------------|------------|----------|---------|
| NMXI-0860.00/TW | Utility - ORG | Issued    | TERNALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD                                       | 2/11/2004  | 93103221       | 8/11/2009  | 131504   | TW      |
| NMXI-0887.00/TW | Utility - ORG | Issued    | TECHNIQUES TO PERFORM LINE QUALIFICATION   | 5/27/2004  | 93115087       | 12/1/2005  | 1244854  | TW      |
| NMXI-0891.00/TW | Utility - ORG | Issued    | STROBE THROUGH DIFFERENTIAL SIGNALING IN MSL   | 4/19/2004  | 93110830       | 1/21/2006  | 1248255  | TW      |
| NMXI-0903.00/TW | Utility - ORG | Issued    | TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA  | 5/27/2004  | 93115086       | 12/11/2005 | 1245503  | TW      |
| NMXI-0924.00/TW | Utility - ORG | Abandoned | ANNOTATING MEDIA CONTENT WITH USER-SPECIFIED INFORMATION   | 10/28/2004 | 93132733       | 11/1/2009  | 1316670  | TW      |
| NMXI-0944.00/TW | Utility - ORG | Issued    | NOVEL STACKED PACKAGE THROUGH SOLDER BALL WITH STENCIL-PRINTED ENCAPSULANT   | 7/27/2005  | 94125491       | 4/21/2008  | 1296151  | TW      |
| NMXI-0960.00/TW | Utility - ORG | Abandoned | CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM  | 12/16/2005 | 94144718       |            |          | TW      |
| NMXI-0962.00/TW | Utility - ORG | Abandoned | REMOTE STATUS FORWARDING IN A DISTRIBUTED CROSS-CONNECTION SYSTEM  | 12/16/2005 | 94144719       |            |          | TW      |
| NMXI-0978.00/TW | Utility - ORG | Issued    | TECHNIQUES FOR TRANSMITTING AND RECEIVING TRAFFIC OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS                                | 12/28/2005 | 94147137       | 3/11/2010  | 1321931  | TW      |
| NMXI-0984.00/TW | Utility - ORG | Issued    | RESONANT TUNNELING TRANSISTOR USING CONVENTIONAL MOSFET PROCESSING   | 10/28/2005 | 094137898      | 12/1/2010  | 1334224  | TW      |
| NMXI-1028.00/TW | Utility - ORG | Pending   | METHOD AND APPARATUS FOR EFFICIENT AND ARTIFACT-FREE DISPLAYING OF ROTATING IMAGES   | 12/12/2006 | 95146490       |            |          | TW      |
| NMXI-1034.00/TW | Utility - ORG | Pending   | LOW DELAY AND SMALL MEMORY FOOTPRINT PICTURE BUFFERING   | 12/1/2006  | 95146473       |            |          | TW      |
| NMXI-0037.00/PC | Utility - ORG | Expired   | ANNEAL TO DECREASE MOISTURE ABSORANCE OF INTERMETAL DIELECTRICS  | 5/18/1992  | PCT/US92/04145 |            |          | WO      |
| NMXI-0203.00/PC | Utility - ORG | Completed | PORTABLE DISK DRIVE CARRIER AND CHASSIS  | 11/28/1995 | PCT/US95/15443 |            |          | WO      |
| NMXI-0222.00/PC | Utility - ORG | Completed | PACKAGE HOUSING MULTIPLE SEMICONDUCTOR DIES  | 3/12/1996  | PCT/US96/03331 |            |          | WO      |
| NMXI-0239.00/PC | Utility - ORG | Completed | MULTI-LAYERED CONTACTING FOR SECURING INTEGRATED CIRCUITS  | 12/12/1996 | PCT/US96/19808 |            |          | WO      |
| NMXI-0251.00/PC | Utility - ORG | Completed | METHOD AND APPARATUS FOR DOCKING AND UNDOCKING A NOTEBOOK COMPUTER   | 9/29/1997  | PCT/US97/17491 |            |          | WO      |
| NMXI-0256.00/PC | Utility - ORG | Abandoned | METHOD AND APPARATUS FOR FAST SELF DESTRUCTION OF A CMOS INTEGRATED CIRCUIT  | 12/27/1996 | PCT/US96/20808 |            |          | WO      |
| NMXI-0276.00/PC | Utility - ORG | Expired   | METHOD FOR IMPROVED DATA RETENTION IN A NONVOLATILE WRITABLE MEMORY BY SENSING AND REPROGRAMMING CELL VOLTAGE LEVELS               | 10/10/1997 | PCT/US97/18694 |            |          | WO      |
| NMXI-0291.00/PC | Utility - ORG | Expired   | INTEGRATED CIRCUIT PACKAGE   | 7/9/1997   | PCT/US97/11890 |            |          | WO      |
| NMXI-0308.00/PC | Utility - ORG | Expired   | FIDUCIAL FOR ALIGNING AN INTEGRATED CIRCUIT DIE  | 12/11/1997 | PCT/US97/22690 |            |          | WO      |
| NMXI-0323.00/PC | Utility - ORG | Expired   | PERIPHERAL DEVICE PREVENTING POST-SCAN MODIFICATION  | 3/12/1998  | PCT/US98/05010 |            |          | WO      |
| NMXI-0325.00/PC | Utility - ORG | Expired   | MULTI-LAYER CODEX/DECODEX  | 9/21/1998  | PCT/US98/19536 |            |          | WO      |
| NMXI-0330.01/PC | Utility - ORG | Expired   | CIRCUIT AND METHOD FOR CONTROLLING POWER AND PERFORMANCE BASED ON OPERATING ENVIRONMENT  | 4/17/1998  | PCT/US98/07759 |            |          | WO      |
| NMXI-0370.00/PC | Utility - ORG | Expired   | POSITION SENSITIVE DISPLAY CONTROLLER  | 4/13/1999  | PCT/US99/08034 |            |          | WO      |
| NMXI-0376.00/PC | Utility - ORG | Expired   | METHOD AND APPARATUS FOR INCREASING VIDEO FRAME RATE   | 3/12/1999  | PCT/US99/05436 |            |          | WO      |
| NMXI-0411.00/PC | Utility - ORG | Expired   | METHOD AND APPARATUS FOR WRITING UOPS INTO A BUFFER QUEUE INCLUDING OVERWRITING ENTRIES THAT ARE INDICATED TO CONTAIN INVALID UOPS | 3/13/2001  | PCT/US01/08104 |            |          | WO      |
| NMXI-0414.00/PC | Utility - ORG | Completed | IMPROVED FLOURINE DOPED SiO2 FILM AND METHOD OF FABRICATION  | 10/11/2000 | PCT/US00/28164 |            |          | WO      |

## Exhibit B - Foreign Applications and Patents

| MT-Device        | Material      | Material  | Title  | File       | Serial         | Issue | Patent | Country |
|------------------|---------------|-----------|--|------------|----------------|-------|--------|---------|
| NMXXI-0420.00/PC | Utility - ORG | Expired   | CONTROLLED COLLAPSE CHIP CONNECTION (C4) INTEGRATED CIRCUIT PACKAGE WHICH HAS TWO DISSIMILAR UNDERFILL MATERIALS                                       | 2/8/2000   | PCT/US00/03242 |       |        | WO      |
| NMXXI-0462.00/PC | Utility - ORG | Expired   | ANTENNA INTERFACE  | 5/18/2000  | PCT/US00/13606 |       |        | WO      |
| NMXXI-0473.00/PC | Utility - ORG | Completed | EMULATING NARROW BAND PHASE-LOCKED LOOP BEHAVIOR ON A WIDE BAND PHASE-LOCKED LOOP  | 3/4/1998   | PCT/US98/04175 |       |        | WO      |
| NMXXI-0474.00/PC | Utility - ORG | Expired   | SINGLE-CHIP CMOS DIRECT-CONVERSION TRANSCIVER  | 9/10/1999  | PCT/US99/20792 |       |        | WO      |
| NMXXI-0485.00/PC | Utility - ORG | Expired   | VITERBI ACS UNIT WITH RENORMALIZATION  | 7/15/1994  | PCT/US94/07957 |       |        | WO      |
| NMXXI-0486.00/PC | Utility - ORG | Expired   | CARRIER FREQUENCY ESTIMATOR FOR A SIGNAL RECEIVER  | 5/26/1999  | PCT/US99/10464 |       |        | WO      |
| NMXXI-0490.00/PC | Utility - ORG | Expired   | COMPUTER METHOD AND APPARATUS FOR A TABLE DRIVEN FILE INTERFACE  | 1/27/1993  | PCT/US93/00732 |       |        | WO      |
| NMXXI-0496.00/PC | Utility - ORG | Completed | METHOD AND APPARATUS FOR GRAPHICAL DEVICE MANAGEMENT USING A VIRTUAL CONSOLE   | 6/15/2001  | PCT/US01/19280 |       |        | WO      |
| NMXXI-0497.00/PC | Utility - ORG | Expired   | REMOVELY MANAGING AND CONTROLLING A CONSUMER APPLANCE  | 5/17/2001  | PCT/US01/16135 |       |        | WO      |
| NMXXI-0502.00/PC | Utility - ORG | Expired   | MICROELECTRONIC PACKAGE HAVING AN INTEGRATED HEAT SINK AND BUILD-UP LAYERS   | 11/9/2001  | PCT/US01/48898 |       |        | WO      |
| NMXXI-0504.00/PC | Utility - ORG | Completed | LINK-LOCK DEVICE AND METHOD OF MONITORING AND CONTROLLING A LINK FOR FAILURES AND INTRUSIONS   | 11/20/2001 | PCT/US01/43615 |       |        | WO      |
| NMXXI-0516.00/PC | Utility - ORG | Completed | CALL PROCESSING SYSTEM WITH RESOURCES ON MULTIPLE PLATFORM   | 1/24/2001  | PCT/US01/02323 |       |        | WO      |
| NMXXI-0548.00/PC | Utility - ORG | Expired   | METHOD, APPARATUS, AND SYSTEM TO ENHANCE NEGATIVE VOLTAGE SWITCHING  | 3/21/2002  | PCT/US02/08751 |       |        | WO      |
| NMXXI-0550.00/PC | Utility - ORG | Completed | SYSTEM AND METHOD FOR AUTOMATIC PBX DETECTION  | 1/20/1999  | PCT/US99/01202 |       |        | WO      |
| NMXXI-0583.00/PC | Utility - ORG | Expired   | SYSTEM AND METHOD FOR PROVIDING AUTHENTICATION AND VERIFICATION SERVICES IN AN ENHANCED MEDIA GATEWAY  | 11/20/2001 | PCT/US01/44675 |       |        | WO      |
| NMXXI-0587.00/PC | Utility - ORG | Expired   | SYSTEMS AND METHODS FOR IMPLEMENTING WITH GUARANTEED MCR   | 6/25/1999  | PCT/US99/14500 |       |        | WO      |
| NMXXI-0590.00/PC | Utility - ORG | Expired   | CBR/VBR TRAFFIC SCHEDULER  | 6/25/1999  | PCT/US99/14268 |       |        | WO      |
| NMXXI-0599.00/PC | Utility - ORG | Completed | ENHANCED MEDIA GATEWAY CONTROL PROTOCOL  | 12/21/2001 | PCT/US01/49779 |       |        | WO      |
| NMXXI-0626.00/PC | Utility - ORG | Completed | VIA-IN-PAD WITH OFF-CENTER GEOMETRY AND METHODS OF MANUFACTURE   | 11/28/2001 | PCT/US01/44721 |       |        | WO      |
| NMXXI-0637.00/PC | Utility - ORG | Completed | MATERIAL DEPOSITION FROM A LIQUEFIED GAS SOLUTION  | 12/30/2002 | PCT/US02/41705 |       |        | WO      |
| NMXXI-0645.00/PC | Utility - ORG | Expired   | DIGITAL LEAKAGE COMPENSATION CIRCUIT   | 2/7/2002   | PCT/US02/03633 |       |        | WO      |
| NMXXI-0652.00/PC | Utility - ORG | Expired   | APPARATUS HAVING A FIRST CIRCUIT SUPPLYING A POWER POTENTIAL TO A SECOND CIRCUIT UNDER A FIRST OPERATING MODE OTHERWISE DECOUPLING THE POWER POTENTIAL | 4/5/2002   | PCT/US02/10816 |       |        | WO      |
| NMXXI-0659.00/PC | Utility - ORG | Expired   | ENABLING MANUAL ADJUSTMENT OF POINTING DEVICE CURSOR SPEED   | 1/25/2002  | PCT/US02/02110 |       |        | WO      |
| NMXXI-0683.00/PC | Utility - ORG | Completed | BEST-OF-M PULSE POSITION MODULATION DETECTOR   | 5/13/1996  | PCT/US96/06811 |       |        | WO      |
| NMXXI-0686.00/PC | Utility - ORG | Abandoned | COMMUNICATION CONTROL FOR A USER OF A CENTRAL COMMUNICATION CENTER   | 3/13/1998  | PCT/US98/04864 |       |        | WO      |
| NMXXI-0694.00/PC | Utility - ORG | Completed | VARIABLE LEVEL MEMORY  | 8/6/2002   | PCT/US02/25092 |       |        | WO      |
| NMXXI-0710.00/PC | Utility - ORG | Completed | ELECTRONIC ASSEMBLY WITH FILLED NO-FLOW UNDERFILL METHODS OF MANUFACTURE   | 10/28/2002 | PCT/US02/34516 |       |        | WO      |
| NMXXI-0712.00/PC | Utility - ORG | Abandoned | WALLET FOR PERSONAL INFORMATION DEVICE   | 11/11/1999 | PCT/US99/26626 |       |        | WO      |

11/21/2011

## Exhibit B - Foreign Applications and Patents

| MT-Device       | Material Type   | Material Status | Title   | Filed Date | Serial No.        | Issued Date | Patent No. | Country |
|-----------------|-----------------|-----------------|---|------------|-------------------|-------------|------------|---------|
| NMXI-0712.00/PC | Utility - ORG   | Expired         | PERSONAL INFORMATION DEVICE AND METHOD FOR DOWNLOADING REPROGRAMMING DATA FROM A COMPUTER TO THE PERSONAL INFORMATION DEVICE VIA THE PCMCIA PORT OR THROUGH A DOCKING STATION WITH BAUD RATE CONVERSION MEANS | 11/16/1999 | PCT/US99/27132    |             |            | WO      |
| NMXI-0719.00/PC | Utility - ORG   | Expired         | DIGITAL FRAME DETERMINATION AND APPARATUS   | 12/27/2002 | PCT/US02/41548    |             |            | WO      |
| NMXI-0721.00/PC | Utility - ORG   | Expired         | PROBABILISTIC NETWORKS FOR DETECTING SIGNAL CONTENT   | 9/5/2002   | PCT/US02/28358    |             |            | WO      |
| NMXI-0734.00/PC | Utility - ORG   | Expired         | INTEGRATED OPTOELECTRICAL CIRCUIT PACKAGE WITH OPTICAL WAVEGUIDE INTERCONNECTS  | 2/13/2003  | PCT/US03/04584    |             |            | WO      |
| NMXI-0741.00/PC | Utility - ORG   | Completed       | SYSTEM AND METHOD TO DETECT ERRORS AND PREDICT POTENTIAL FAILURES   | 6/30/2005  | PCT/US05/23733    |             |            | WO      |
| NMXI-0752.00/PC | Utility - ORG   | Expired         | METHOD FOR MEDIA CONTENT PRESENTATION IN CONSIDERATION OF SYSTEM POWER  | 4/10/2003  | PCT/US03/11345    |             |            | WO      |
| NMXI-0778.00/PC | Utility - ORG   | Completed       | INVERSE MULTIPLEXING OF MANAGED TRAFFIC FLOWS OVER A MULTI-STAR NETWORK   | 2/5/2003   | PCT/US03/03691    |             |            | WO      |
| NMXI-0781.00/PC | Utility - ORG   | Expired         | ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT HAVING A RING OSCILLATOR TIMER CIRCUIT   | 3/28/2003  | PCT/US03/09535    |             |            | WO      |
| NMXI-0786.00/PC | Utility - ORG   | Completed       | ESTABLISHING AN AD HOC NETWORK  | 4/24/2003  | PCT/US03/12902    |             |            | WO      |
| NMXI-0814.00/PC | Utility - ORG   | Expired         | ADAPTIVE EQUALIZATION USING A CONDITIONAL UPDATE SIGN-SIGN LEAST MEAN SQUARE ALGORITHM  | 9/8/2004   | PCT/US04/29175    |             |            | WO      |
| NMXI-0839.00/PC | Utility - ORG   | Expired         | PROMUNCIATION NETWORK   | 12/22/2003 | PCT/US03/39108    |             |            | WO      |
| NMXI-0854.00/PC | Utility - ORG   | Expired         | APPARATUS AND METHOD TO PROVIDE MULTITHREADED COMPUTER PROCESSING   | 4/16/2004  | PCT/US2004/012020 |             |            | WO      |
| NMXI-0860.00/PC | Utility - ORG   | Completed       | THERMALLY ENHANCED ELECTRONIC FLIP-CHIP PACKAGING WITH EXTERNAL-CONNECTOR-SIDE DIE AND METHOD   | 2/9/2004   | PCT/US04/03817    |             |            | WO      |
| NMXI-0887.00/PC | Utility - ORG   | Expired         | TECHNIQUES TO PERFORM LINE QUALIFICATION  | 5/26/2004  | PCT/US2004/016583 |             |            | WO      |
| NMXI-0891.00/PC | Utility - ORG   | Expired         | STROBE THROUGH DIFFERENTIAL SIGNALING   | 4/14/2004  | PCT/US2004/011645 |             |            | WO      |
| NMXI-0903.00/PC | Utility - ORG   | Expired         | TECHNIQUES TO TEST SIGNAL PROPAGATION MEDIA   | 5/26/2004  | PCT/US2004/016587 |             |            | WO      |
| NMXI-0944.00/PC | Utility - ORG   | Abandoned       | METHODS AND APPARATUS FOR PROVIDING STACKED-DIE DEVICES   | 7/29/2005  | PCT/US2005/027103 |             |            | WO      |
| NMXI-0960.00/PC | Utility - ORG   | Expired         | CASCADED CONNECTION MATRICES IN A DISTRIBUTED CROSS-CONNECTION SYSTEM   | 12/15/2005 | PCT/US05/146571   |             |            | WO      |
| NMXI-0962.00/PC | Utility - ORG   | Pending         | REMOTE STATUS FORWARDING IN A DISTRIBUTED CROSS-CONNECTION SYSTEM   | 12/15/2005 | PCT/US05/45850    |             |            | WO      |
| NMXI-0978.00/PC | Utility - ORG   | Abandoned       | TECHNIQUES FOR TRANSMITTING AND RECEIVING TRAFFIC OVER ADVANCED SWITCHING COMPATIBLE SWITCH FABRICS   | 12/28/2005 | PCT/US2005/047581 |             |            | WO      |
| NMXI-0980.00/PC | Utility - ORG   | Abandoned       | AIRFLOW CONTROL SYSTEM  | 11/17/2005 | PCT/US05/42254    |             |            | WO      |
| NMXI-0984.00/PC | Utility - ORG   | Expired         | RESONANT TUNNELING DEVICE USING METAL OXIDE SEMICONDUCTOR PROCESSING  | 10/27/2005 | PCT/US2005/039310 |             |            | WO      |
| NMXI-1001.00/PC | Utility - ORG   | Abandoned       | ADVANCED TCA CHASSIS WITH A MOVABLE FAN TRAY  | 1/17/2006  | PCT/US06/01918    |             |            | WO      |
| NMXI-1028.00/PC | Utility - ORG   | Pending         | METHOD AND APPARATUS FOR EFFICIENT AND ARTIFACTS FREE DISPLAYING OF ROTATING IMAGES   | 12/6/2006  | PCT/US2006/046778 |             |            | WO      |
| NMXI-1034.00/PC | Utility - ORG   | Pending         | LOW DELAY AND SMALL MEMORY FOOTPRINT PICTURE BUFFERING  | 11/30/2006 | PCT/US2006/046260 |             |            | WO      |
| NMXI-1038.00/PC | Utility - ORG   | Pending         | ARCHITECTURE FOR VIRTUAL SECURITY MODULE  | 9/26/2007  | PCT/US2007/020737 |             |            | WO      |
| NMXI-0203.00/ZA | Utility - NSPCT | Abandoned       | UNIVERSAL CARRIER FOR HOT SWAPABLE DRIVER   | 11/29/1995 | 95/10128          | 8/28/1996   | 95/10128   | ZA      |

11/21/2011

PATENT

RECORDED: 09/10/2013

REEL: 031305 FRAME: 0853