502549312 10/29/2013

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2

EPAS ID: PAT2594869

Stylesneet Version v								
SUBMISSION TYPE:			NEW ASSIGNMENT					
NATURE OF CONVE	YANCE:		ASSIGNMENT					
CONVEYING PARTY	DATA							
		N	lame	Execution Date				
ARTERIS SAS			10/11/2013					
RECEIVING PARTY [)ATA							
Name:	QUALCOMM	I TECH	NOLOGIES, INC.					
Street Address:	5775 MORE	HOUSE	DRIVE					
City:	SAN DIEGO							
State/Country:	CALIFORNIA	١						
Postal Code:	92121							
PROPERTY NUMBER	RS Total: 1							
Property T	уре		Number					
Application Number:		13969	559					
CORRESPONDENCE	DATA							
Fax Number:								
Phone: Email:		387 00						
			yahoo.com hen the email attempt is unsuccessful.					
Correspondent Name			AL SERVICES		ķ			
Address Line 1:	325	SHAR	ON PARK DRIVE					
Address Line 2:	ST 2							
Address Line 4:	MEN	NLO PA	ARK, CALIFORNIA 94025					
ATTORNEY DOCKET	NUMBER:		ART-024US3					
NAME OF SUBMITTE	R:		JUBIN DANA					
Signature: /JUBIN DANA/								

10/29/2013

PATENT REEL: 031498 FRAME: 0564

502549312

Date:

Total Attachments: 15

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PATENT ASSIGNMENT

This PATENT ASSIGNMENT (this "Assignment") is made and entered into as of October 11, 2013 (the "Effective Date") by and between Arteris Holdings, Inc., a Delaware corporation ("Target Holdings"), Arteris, Inc., a Delaware corporation and wholly owned subsidiary of Target Holdings ("Target USA Sub"), Arteris, SAS a French société par actions simplifiée and wholly owned subsidiary of Target Holdings ("Target France Sub", and together with Target Holdings and Target USA Sub, the "Assignors", and each an "Assignor") and Qualcomm Technologies, Inc., a Delaware corporation ("Assignee").

WHEREAS, pursuant to that certain Asset Purchase Agreement (the "Purchase Agreement"), dated October 9, 2013, among Target Holdings, Target USA Sub, Target France Sub, Assignee and Qualcomm France SARL, each Assignor has agreed to sell, assign, transfer, convey, and deliver to Assignee all of such Assignor's right, title, and interest in and to certain assets, including, without limitation, the Assigned Patents (defined below); and

WHEREAS, pursuant to the Purchase Agreement, the Assignors and Assignee have agreed to enter into this Assignment.

NOW, THEREFORE, in consideration of the promises and covenants set forth in the Purchase Agreement and for other good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, the parties hereto agree as follows:

- Assignee all of such Assignor's right, title and interest in, to and under the issued patents and patent applications listed on Schedule A, including all reissues, divisionals, continuations, continuations-in-part, revisions, reexaminations, extensions, and counterparts (whether foreign or domestic) claiming priority to or based on any of the foregoing items, together with all patents issuing therefrom, all inventions and improvements claimed or described in any of the foregoing, including without limitation all rights of priority under International Conventions, Treaties, or Agreements, all rights to collect royalties and proceeds in connection with any of the foregoing (collectively, the "Assigned Patents"), and all rights to sue and bring other claims for past, present and future infringement, misappropriation or other violation of any of the foregoing and all rights to recover and retain damages (including attorney's fees and expenses) or lost profits in connection therewith.
- 2. <u>Recordation</u>. Each Assignor hereby requests the United States Patent and Trademark Office Commissioner for Patents and any other applicable governmental entity or registrar (including any applicable foreign or international office or registrar), to record Assignee as the assignee and owner of the Assigned Patents. Each Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents or certificates of invention which may be granted upon any of the Assigned Patents in the name of Assignee, as the assignee to the entire interest therein.

3. Information and Assistance.

3.1 Upon Assignee's reasonable request and without further compensation, each Assignor shall execute, acknowledge and deliver all such other instruments and documents and

shall take all such other actions reasonably necessary or required by law to consummate and make fully effective the transaction contemplated by this Assignment.

- If Assignee is unable for any reason to secure an Assignor's signature to any document required to file, prosecute, register or memorialize the assignment of any rights under any Assigned Patents as provided under this Assignment, each Assignor hereby irrevocably designates and appoints Assignee and Assignee's duly authorized officers and agents as such Assignor's agents and attorneys-in-fact to act for and on such Assignor's behalf and instead of such Assignor to take all lawfully permitted acts to further the filing, prosecution, registration, memorialization of assignment, issuance and enforcement of rights under such Assigned Patents, all with the same legal force and effect as if executed by such Assignor. The foregoing is deemed a power coupled with an interest and is irrevocable.
- 4. Successors and Assigns. This Assignment and all the provisions hereof shall be binding upon and shall inure to the benefit of the parties hereto and their respective successors and permitted assigns and nothing herein express or implied shall give or be construed to give to any person, other than the parties hereto and such permitted assigns, any legal or equitable rights hereunder.
- Counterparts. This Assignment may be executed in two or more consecutive counterparts (including by facsimile), each of which shall be an original, with the same effect as if the signatures thereto and hereto were upon the same instrument, and shall become effective when one or more counterparts have been signed by each party and delivered (by facsimile or otherwise) to the other party.
- Section Headings. The section headings contained in this Assignment are for reference 6. purposes only, and shall not in any way affect the meaning or interpretation of this Assignment.
- Purchase Agreement Controls. This Assignment is provided pursuant to the Purchase 7. Agreement, to which reference is made for a further statement of the rights and obligations of the Assignors and Assignee with respect to the Assigned Patents. Nothing contained in this Assignment shall be deemed to modify, supersede, enlarge or affect the rights of any person under the Purchase Agreement. If any provision of this Assignment is inconsistent or conflicts with the Purchase Agreement, the Purchase Agreement shall control.
- Governing Law. This Assignment and all claims or causes of action (whether in contract, 8. tort or otherwise) that may be based upon, arise out of or relate to this Assignment or the negotiation, execution or performance of this Assignment shall be governed by and construed in accordance with the laws of the State of Delaware, without giving effect to any choice or conflict of law provision or rule.

[Signature Page Follows]

- 2 -

IN WITNESS WHEREOF, the undersigned have caused this Patent Assignment to be executed, effective as of the Effective Date.

ASSIGNORS:

ARTERIS HOLDINGS, INC.

By: 12. Carled Janes
Name: 12. Charled Janes
Title: 12. sident and co

ARTERIS, INC.

By: 1. Charles Janes.
Name: K. Charles Janes.
Title: president and CEO

ARTERIS, SAS

By: Dr. Charles Janae
Name: 16. Charles Dange
Title: Directent General

CALIFORNIA ALL-PURPOSE CERTIFICATE OF ACKNOWLEDGMENT

CERTIFICATE OF	ACMIOWEEDGMENT
State of California County of Janu Clarda	
On October 11.20/Before me,	Inda Dukett, Notary Public, (Here insert name and title of the officer) UN Janac
personally appeared Char	une Janac
the within instrument and acknowledged to me the capacity (ies), and that by his/her/their signature (s) which the person(s) acted, executed the instrument	
I certify under PENALTY OF PERJURY under th is true and correct.	e laws of the State of California that the foregoing paragraph
WITNESS my hand and official seal.	LINDA DUKETT COMM. # 2003850 NOTARY PUBLIC - CALIFORNIA & SANTA CLARA COUNTY My Comm. Exp. Feb. 9, 2017 (Notary Seal)
Signature of Notary Public	(1-0-taly Seat)
ADDITIONAL O	PTIONAL INFORMATION
DESCRIPTION OF THE ATTACHED DOCUMENT Control of Attached document	INSTRUCTIONS FOR COMPLETING THIS FORM Any acknowledgment completed in California must contain verbiage exactly as appears above in the notary section or a separate acknowledgment form must be properly completed and attached to that document. The only exception is if a document is to be recorded outside of California. In such instances, any alternative acknowledgment verbiage as may be printed on such a document so long as the verbiage does not require the notary to do something that is illegal for a notary in California (i.e. certifying the authorized capacity of the signer). Please check the document carefully for proper notarial wording and attach this form if required. State and County information must be the State and County where the document signer(s) personally appeared before the notary public for acknowledgment. Date of notarization must be the date that the signer(s) personally appeared which must also be the same date the acknowledgment is completed. The notary public must print his or her name as it appears within his or her commission followed by a comma and then your title (notary public). Print the name(s) of document signer(s) who personally appear at the time of
CAPACITY CLAIMED BY THE SIGNER	notarization. • Indicate the correct singular or plural forms by crossing off incorrect forms (i.e. he/she/they, is /are) or circling the correct forms. Failure to correctly indicate this

- ☐ Individual (s)
- ☐ Corporate Officer

(Title)

- ☐ Partner(s)
- ☐ Attorney-in-Fact
- ☐ Trustee(s)
- ☐ Other

- information may lead to rejection of document recording.
- The notary seal impression must be clear and photographically reproducible. Impression must not cover text or lines. If seal impression smudges, re-seal if a sufficient area permits, otherwise complete a different acknowledgment form.
- Signature of the notary public must match the signature on file with the office of the county clerk.
 - Additional information is not required but could help to ensure this acknowledgment is not misused or attached to a different document.
 - Indicate title or type of attached document, number of pages and date.
 - Indicate the capacity claimed by the signer. If the claimed capacity is a corporate officer, indicate the title (i.e. CEO, CFO, Secretary).
- · Securely attach this document to the signed document

IN WITNESS WHEREOF, the undersigned have caused this Patent Assignment to be executed, effective as of the Effective Date.

Acknowledged and Accepted:

ASSIGNEE:

QUALCOMM TECHNOLOGIES, INC.

By:

Name: Cristiano Amon

Title: Executive Vice President

[Signature Page to Patent Assignment]

SCHEDULE A TO PATENT ASSIGNMENT

WEST\241933068.2 336110-000261

Owner	Country	inventor(s)	Application Title	Sørcharge Date at USPTO	Application Number	Application Status	Priority Date	National/Act ual Filed	Filed Date	Patent Number	Issued/Grant Date
				Date at GUT TO	(Aller COR)	Janus		Date		19331114293	LONE
			Method And Device For								
		Philippo Poucard	Managing Priority In The Transmission Of A	Next renewal due							
ARTERIS	FR	Philippe Boucard César Douady	Message	8/31/2013	FR0309918	Granted		8/13/2003	8/13/2003	FR2858895	5/5/2006
			System And Method For								
			Transmission Of A								
		Philippe Boucard	Sequence Of Messages In A Network	Next renewal due							
ARTERIS	FR	César Douady	Interconnection	11/30/2012	FR0313298	Abandoned		11/13/2003	11/13/2003	FR2862457	2/24/2006
ARTERIS	FR	Philippe Boucard César Douady	Method And Device For Switching Between Agents	Next renewal	FR0402149	Granted		3/2/2004	3/2/2004	FR2867338	8/10/2007
Autens		cesar bodday	System And Method For	duc 5/51/2014	1110402143	Grantea		3/ 2/ 2004	3/2/2004	1112007330	0/10/2007
		César Douady	Transmission Of A Message Sequence In An								
ARTERIS	EP	Philippe Boucard	Interconnection Network		04291339.2	Expired	11/13/2003	5/28/2004	5/28/2004	EP1531589	4/13/2011
			Sustana And Dragges For								
		César Douady	System And Process For Packet Switching Between								
ARTERIS	EP	Philippe Boucard	Agents		04291341.8	Granted	3/2/2004	5/28/2004	5/28/2004	EP1575222	10/15/2008
			Method And Device For Priority Management								
		César Douady	During A Message				0 /4 2 /2002	0/5/2004	0 /5 /000 4		2 (4 2 (2222
ARTERIS	EP	Philippe Boucard	Transmission		04292011.6	Expired	8/13/2003	8/6/2004	8/6/2004	EP1507374	3/19/2008
			Method And Device For								
		César Douady	Managing Priority During The Transmission Of A	Pay Maintenance							
ARTERIS	US	Philippe Boucard	Message		10/915,950	Granted	8/13/2003	8/11/2004	8/11/2004	7,769,027	8/3/2010
			System And Method For Transmitting A Sequence								
		César Douady	Of Messages In An								
ARTERIS	US	Philippe Boucard	Interconnection Network	04/04/2015	10/988,831	Granted	11/13/2003	11/15/2004	11/15/2004	8,031,730	10/4/2011
ARTERIS	US	César Douady Philippe Boucard	Method And Device For Switching Between Agents	2/14/2017	11/054,179	Granted	3/2/2004	2/9/2005	2/9/2005	7,574,629	8/11/2009
				, , , , , , , , , , , , , , , , , , , ,	,,		-,-,	-, -,	-,-,	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
			System And Method Of								
			Conveying Data In An				s (1 o (5	= /a = /c			110000
ARTERIS	EP	Luc Montperrus	Electronic Circuit		05291515.4	Granted	6/10/2005	7/13/2005	7/13/2005	EP1732241	11/26/2008
		Philippe Boucard	Switching System	Next renewal							
ARTERIS	FR	Luc Montperrus	Message Interconnection System	due 3/31/2014	FR0602810	Granted		3/31/2006	3/31/2006	FR2899413	8/8/2008
			On Chip Blocks Outside								
		Philippe Boucard	With A Single Communication Protocol	Next renewal							
ARTERIS	FR	César Douady	Parameterizable	Next renewal due 4/30/2014	FR0603248	Granted		4/12/2006	4/12/2006	FR2900017	10/31/2008
	•	. ,	•		•	•	•			•	

				1			_		_		_
		César Douady	System And Method For Transmission Of A Message Sequence In An				5/28/2004				
ARTERIS	BG	Philippe Boucard	Interconnection Network		04291339.2	Lapsed	11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS	EP	Cesar Douady Philippe Boucard	Method Of Managing A Storage Organised As Fifo, And Corresponding Apparatus		04292872.1	Granted	12/3/2004 12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
		Cesar Douady	Method Of Managing A Storage Organised As Fifo, And Corresponding	Last fee paid			12/3/2004				
ARTERIS	IT IT	Philippe Boucard	Apparatus System Of Interconnections For External Functional Blocks	0FFICE ACTION	04292872.1	Lapsed	12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
ARTERIS	us	César Douady Philippe Boucard	On A Chip Provided With A Single Configurable	6/29/2013	11/482,175	Application	4/12/2006	7/6/2006	7/6/2006		
ARTERIS	US	Philippe Boucard Luc Montperrus	Message Switching System	6/29/2013	11/484,837	Granted	3/31/2006	7/11/2006	7/11/2006	7,639,704	12/29/2009
		Philippe Boucard	Managing Messages Transmitted In An								
ARTERIS	US	Vincent Vacquerie	Interconnect Network	2/28/2016	11/516,811	Granted	6/23/2006	9/6/2006	9/6/2006	8,254,380	8/28/2012
		César Douady	System And Method For Transmission Of A Message Sequence In An				5/28/2004				
ARTERIS	LU	Philippe Boucard	Interconnection Network		04291339.2	Lapsed	11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
		Philippe Boucard	Message Switching	Fee due							
ARTERIS	EP	Luc Montperrus	System	2/28/2014	07102227.1	Application	3/31/2006	2/13/2007	2/13/2007		
ARTERIS	EP	Philippe Boucard César Douady	On Chip System For Interconnecting External Functional Blocks Comprising A Single Parametrizable		07300863.3	Granted	4/12/2006	3/14/2007	3/14/2007	EP1845456	4/8/2009
ARTERIS	GB	Philippe Boucard César Douady	On Chip System For Interconnecting External Functional Blocks	Last fee paid 3/15/2013 Fee due	07300863.3	Granted	3/14/2007 4/12/2006	3/14/2007	3/14/2007	EP1845456	4/8/2009
		Philippe Boucard	On Chip System For Interconnecting External Functional Blocks	Last fee paid			3/14/2007				
ARTERIS	IT	César Douady	Comprising A Single	6/29/2012	07300863.3	Granted	4/12/2006	3/14/2007	3/14/2007	EP1845456	4/8/2009
ARTERIS	US	César Douady Philippe Boucard	Method And Device For Switching Between Agents On Chip System For		12/539,554	Abandoned	2/9/2005 3/2/2004	8/11/2009	8/11/2009		
ARTERIS	NL	Philippe Boucard César Douady	Interconnecting External Functional Blocks System And Method For	Last fee paid 2/22/2012	07300863.3	Granted	3/14/2007 4/12/2006	3/14/2007	3/14/2007	EP1845456	4/8/2009
ARTERIS	EP	Philippe Boucard Vincent Vacquerie	The Management Of Messages Transported In		07108875.1	Granted	6/23/2006	5/24/2007	5/24/2007	EP1871058	12/30/2009
, with the same of		Philippe Boucard Pascal Godet	Messages mansported in	Next renewal fee due	07100075.1	Grantea	0,23,2000	3,21,2007	3/2 1/2007	1.10/1030	12, 30, 2003
ARTERIS	FR	Luc Montperrus	Electronic Memory Device Method And Device For		FR0706740	Granted		9/26/2007	9/26/2007	FR2921507	4/15/2011
ARTERIS	FR	César Douady Philippe Boucard	Priority Management During A Message	Last paid 6/26/2012	04292011.6	Granted	8/6/2004 8/13/2003	3/19/2008	8/6/2004	EP1507374	3/19/2008
		César Douady	Method And Device For Priority Management	Last paid 8/24/2012			8/6/2004				
ARTERIS	GB	Philippe Boucard	During A Message	Pay	04292011.6	Granted	8/13/2003	3/19/2008	8/6/2004	EP1507374	3/19/2008
		César Douady	Method And Device For Priority Management During A Message	Last paid			8/6/2004				
ARTERIS	IT	Philippe Boucard	Transmission Method And Device For	8/21/2012	04292011.6	Granted	8/13/2003	3/19/2008	8/6/2004	EP1507374	3/19/2008
ARTERIS	NL	César Douady Philippe Boucard	Priority Management During A Message	Last paid 7/26/2012	04292011.6	Granted	8/6/2004 8/13/2003	3/19/2008	8/6/2004	EP1507374	3/19/2008

		Philippe Boucard	Message Switching			1	7/11/2006	T			Τ
ARTERIS	us	Luc Montperrus	System		12/616,694	Abandoned	3/31/2006	11/11/2009	11/11/2009		
		Philippe Boucard		Next renewal				- / /	. / /		
ARTERIS	EP	Pascal Godet	Electronic Memory Device	fee due	08290870.8	Application	9/26/2007	9/16/2008	9/16/2008		
		Philippe Boucard									
		Pascal Godet				L	0 /0 6 /000	. / /	0 (0.5 (0.00)		
ARTERIS	US	Luc Montperrus	Electronic Memory Device	1/13/2014	12/284,820	Granted	9/26/2007	9/25/2008	9/25/2008	7,755,920	7/13/2010
		a, a .	System And Process For				E (20 (200 t				
ARTERIS	FR	César Douady Philippe Boucard	Packet Switching Between	1/10/2013	04291341.8	Grantod	5/28/2004 3/2/2004	10/15/2008	E /29/2004	EP1575222	10/15/2008
ARTERIS	FK	Fillippe Boucard	Agents	1/10/2013	04231341.8	Granted	3/2/2004	10/13/2008	3/28/2004	EF13/3222	10/13/2008
				Last fee paid							
		S. 5 .	System And Process For	5/21/2012			E (20 (200 f				
ARTERIS	GB	César Douady Philippe Boucard	Packet Switching Between Agents	Fee Due 5/28/2014	04291341.8	Granted	5/28/2004 3/2/2004	10/15/2008	5/28/2004	EP1575222	10/15/2008
ARTERIS	GB	Fillippe Boucard	Agents	3/28/2014	04291341.8	Granted	3/2/2004	10/13/2008	3/28/2004	EF13/3222	10/13/2008
		S. 5 .	System And Process For				F /20 /2004				
ARTERIS	IT	César Douady Philippe Boucard	Packet Switching Between Agents	5/22/2012	04291341.8	Granted	5/28/2004 3/2/2004	10/15/2008	5/28/2004	EP1575222	10/15/2008
ANTENIS		Fillippe Boucaru	Agents	3/22/2012	04231341.8	Granteu	3/2/2004	10/13/2008	3/28/2004	LF13/3222	10/13/2008
			System And Process For								
ADTEDIC	N.I.	César Douady	Packet Switching Between		04204244 B	Cuantad	5/28/2004	10/15/2000	E /20/2004	ED4E7E333	10/15 /2009
ARTERIS	NL	Philippe Boucard	Agents	4/25/2012	04291341.8	Granted	3/2/2004	10/15/2008	5/28/2004	EP1575222	10/15/2008
				Next renewal							
ADTEDIC C A C	ED.	Philippe Boucard	Network On Chip Zero	due	FD0057437			10/12/2000	10/13/3000		
ARTERIS S.A.S.	FR	Jean-Jacques Lecler	Latency	10/31/2013 Final rejection	FR0957137	Application	1	10/13/2009	10/13/2009		+
		Jean-Jacques Lecler	Zero-Latency Network On	of all claims							
ARTERIS S.A.S.	us	Philippe Boucard	Chip (Noc)	dated	12/579,346	Application	10/13/2009	10/14/2009	10/14/2009		
			System And Method For	Lapsed							
		Philippe Pausered	Management Of Messages	1 ' '							
ARTERIS	FR	Philippe Boucard Vincent Vacquerie	Sent In A Network Interconnection	Last fee paid was 4/8/2010	FR0605646	Lapsed		6/23/2006	6/23/2006	FR2902957	9/12/2008
	- 11	sent vacquerie	c. coiicciioii	1/0/2010				5, 25, 2000	5, 25, 2000		5, 12, 2000
			System And Method For								
			The Management Of								
			Messages Transported In								
ADTEDIS	ED.	Philippe Boucard	An Interconnection	Last fee paid	07109975 1	Grantad	5/24/2007	13/30/3000	E /24/2007	ED1971050	12/20/2000
ARTERIS	FR	Vincent Vacquerie	Network	3/27/2012	07108875.1	Granted	6/23/2006	12/30/2009	5/24/200/	EP1871058	12/30/2009
			System And Method For								
			The Management Of	Last fee paid							
			Messages Transported In	5/21/2012							
		Philippe Boucard	An Interconnection	Fee due			5/24/2007	l			
ARTERIS	GB	Vincent Vacquerie	Network	5/24/2014	07108875.1	Granted	6/23/2006	12/30/2009	5/24/2007	EP1871058	12/30/2009
			System And Method For								
			The Management Of								
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			Messages Transported In								
		Philippe Boucard	Messages Transported In An Interconnection	Last fee paid			5/24/2007				

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			System And Method For The Management Of								
			Messages Transported In								
		Philippe Boucard	An Interconnection	Last fee paid			5/24/2007				
ARTERIS	NL	Vincent Vacquerie	Network	4/25/2012	07108875.1	Granted	6/23/2006	12/30/2009	5/24/2007	EP1871058	12/30/2009
/ III ENIS	112	Timeent vacquerie	Herron	1,23,2012	07100075.1	Granica	0, 23, 2000	12,00,2005	3,21,2007	2.72030	22,50,2005
		Dhilinno Bousard	Notwork With On Chin	2012 foo poids							
		Philippe Boucard Philippe Martin	Network With On-Chip Features Quality-Of-	2013 fee paid; Renewal fee							
ARTERIS S.A.S	FR	Jean-Jacques Lecler	Service	due 6/30/14	FR1054339	Granted		6/3/2010	6/3/2010	FR2961048	4/26/2013
ARTERIO S.A.S	110	Philippe Boucard	JCI VICC	ddc 0/30/14	111034333	Granted	+	0/3/2010	0,3,2010	1112301048	4/20/2013
		Philippe Martin	Network On Chip (Noc)								
ARTERIS S.A.	US	Jean-Jacques Lecler	With Qos Features	5/20/2016	12/835,623	Granted	6/3/2010	7/13/2010	7/13/2010	8,316,171	11/20/2012
			Method And Device For	Pay							
			Managing Priority During	Maintenance							
		César Douady	The Transmission Of A	Fee			8/11/2004	1	1		
ARTERIS	US	Philippe Boucard	Message	11/14/2016	12/848,938	Granted	8/13/2003	8/2/2010	8/2/2010	8,441,931	5/14/2013
			System And Method For	Last paid							
		Céan Dauadu	Transmission Of A	5/14/2012			E /28/2004				
ARTERIS	DE	César Douady Philippe Boucard	Message Sequence In An Interconnection Network	Due 5/31/2014	602004032202.4	Granted	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ANTENIS	DE	Рипрре войсаги	System And Method For	3/31/2014	602004032202.4	Granted	11/13/2003	4/13/2011	3/28/2004	EL1221293	4/13/2011
		César Douady	Transmission Of A	Last paid			5/28/2004				
ARTERIS	FR	Philippe Boucard	Message Sequence In An	3/27/2012	04291339.2	Granted	11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
			System And Method For	,		1		,,	,,	1	1,,
		César Douady	Transmission Of A	Last paid			5/28/2004				
ARTERIS	GB	Philippe Boucard	Message Sequence In An	5/21/2012	04291339.2	Granted	11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
1			System And Method For								
		César Douady	Transmission Of A				5/28/2004				
ARTERIS	HU	Philippe Boucard	Message Sequence In An		04291339.2	Granted	11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
		Philippe Boucard	Network-On-A-Chip With								
l		Philippe Martin	Quality-Of-Service			L			_ , ,		
ARTERIS S.A.S.	wo	Jean-Jacques Lecler	Features		PCT/EP2011/05857	Expired	6/3/2010	5/25/2011	5/25/2011		
		César Douady	System And Method For	l ant maid			5/28/2004				
ARTERIS	IT	Philippe Boucard	Transmission Of A Message Sequence In An	Last paid 5/22/2012	04291339.2	Granted	11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
AKILKIS		Fillippe boucaru	System And Method For	3/22/2012	04231333.2	Granteu	11/13/2003	4/13/2011	3/28/2004	FL1221263	4/13/2011
		César Douady	Transmission Of A	Last paid			5/28/2004				
ARTERIS	NL	Philippe Boucard	Message Sequence In An	4/25/2012	04291339.2	Granted	11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
		i ii		Lapsed							
			Emulation Of One Or	1/31/08.							
		Philippe Boucard	More Chip Device	Last fee paid							
ARTERIS	FR	César Douady	Integrated Circuits	3/9/06	FR0305538	Lapsed		5/7/2003	5/7/2003	FR2854703	6/24/2005
		C B t .	Device For Emulating One								
ARTERIS	us	Cesar Douady Philippe Boucard	Or More Integrated- Circuit Chips		10/703,491	Abandonod	5/7/2003	11/10/2003	11/10/2002		
AKTERIS	03	Cesar Douady	Device For Emulating One	Last fee paid	10/705,491	Abandoned	5/7/2003	11/10/2003	11/10/2005		
ARTERIS	EP	Philippe Boucard	Or More Ic Chips	3/31/06	04291088.5	Withdrawn	5/7/2003	4/27/2004	4/27/2004		
741121113		Timppe bodedra	or whore to emps	Lapsed	0.1232000.5	TTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTT	3,7,2003	1,27,2001	1,27,2001		
			System And Method For	3/31/08.							
		Philippe Boucard	Communication Between	Last fee paid							
ARTERIS	FR	César Douady	Modules	7/5/06.	FR0308225	Lapsed		7/4/2003	7/4/2003	FR2857114	9/30/2005
			System And Method For				T				
		Cesar Douady	Communicating Between								
ARTERIS	US	Philippe Boucard	Modules		10/884,794	Abandoned	7/4/2003	7/2/2004	7/2/2004		1
		6	Communication System								
ADTERIC		Cesar Douady	Between Different	Last fee paid	04301345.0	Mith do	7/4/2002	E /20 /2004	E /30/3004		
ARTERIS	EP	Philippe Boucard	Modules And Method	3/31/06 Lapsed	04291345.9	Withdrawn	7/4/2003	5/28/2004	5/28/2004	1	1
				3/31/08.		1					
		Cesar Douady	Device And Method For	Last fee paid							
ARTERIS	FR	Jean-Jacques Lecler	Transmitting A Message	7/5/06	FR0308905	Lapsed		7/22/2003	7/22/2003	FR2858148	9/30/2005
		Cesar Douady	Device And Method For			Γ'	\vdash	T	' ' '	<u> </u>	1, ,
ARTERIS	us	Jean-Jacques Lecler	Forwarding A Message		10/892,815	Abandoned	7/22/2003	7/16/2004	7/16/2004		
			_								
		Cesar Douady	Device And Method For	Last fee paid		1					
ARTERIS	EP	Jean-Jacques Lecler	Transmitting A Message	3/31/06	04291344.2	Withdrawn	7/22/2003	5/28/2004	5/28/2004		
			Device Digital Delay	Lapsed							
		Philippe Boucard	Oscillator Clock Generator	6/30/10.		1					
ADTERIC		Jean-Jacques Lecler	Digital Signal And Memory	Last fee paid	ED0214547	Lamacid		10/1/2002	10/1/2022	EDDOCOCCO	0/1/2006
ARTERIS	FR	Luc Montperrus	Interface	10/1/08	FR0311517	Lapsed	+	10/1/2003	10/1/2003	FR2860663	9/1/2006
		Luc Montperrus	Digital Delay Device, Digital Oscillator Clock			1					
		Philippe Boucard	Signal Generator And	1st Fee never							
			Io-Dual Octionator Ana	Lack of Head	ı	1	1	1	1	1	1
ARTERIS	us	Jean-Jacques Lecler	Memory Interface	paid	10/957,211	Expired	10/1/2003	10/1/2004	10/1/2004	7,148,728	12/12/2006

			Digital Time Delay								
			Apparatus, Digital								
		Luc Montperrus	Oscillator Generating								
ARTERIS	ED	Philippe Boucard	Clock Signal And Memory	Last fee paid 6/27/08	04303030 7	NA Ciala al mana a con	10/1/2002	0/11/2004	D/11/2004		
AKTEKIS	EP	Jean-Jacques Lecler	Interface Method For Managing A	Lapsed	04292039.7	Withdrawn	10/1/2003	8/11/2004	8/11/2004		
			Data Storage Device Held	8/31/09.							
			In Queue And Related	Last fee paid							
ARTERIS	FR	Cesar Douady	Device	9/19/07	FR0314376	Lapsed		12/9/2003	12/9/2003	FR2863377	2/17/2006
			Method Of Managing A	_,,				,_,			
			Device For Memorizing								
			Data Organized In A								
		Cesar Douady	Queue, And Associated								
ARTERIS	US	Philippe Boucard	Device		11/008,410	Abandoned	12/9/2003	12/9/2004	12/9/2004		
			System And Method For								
			Transmission Of A								
		César Douady	Message Sequence In An				5/28/2004				
ARTERIS	TR	Philippe Boucard	Interconnection Network		04291339.2	Granted	11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
			Method Of Managing A								
		Casan Daviadu	Storage Organised As Fifo,				12/2/2004				
ARTERIS	СН	Cesar Douady	And Corresponding Apparatus		04292872.1	Lamand	12/3/2004	6/7/2006	17/7/7004	EP1542131	6 /7 /2006
ANTENIS	СП	Philippe Boucard	Apparatus		04232672.1	Lapsed	12/9/2003	6/ // 2006	12/3/2004	EF1342131	6/7/2006
			Method Of Managing A								
			Storage Organised As Fifo,					1		1	
		Cesar Douady	And Corresponding	Last fee paid			12/3/2004				
ARTERIS	FR	Philippe Boucard	Apparatus	4/30/08	04292872.1	Lapsed	12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
		1,	1.,	T	1	T ' -		1			T
			Method Of Managing A								
			Storage Organised As Fifo,								
		Cesar Douady	And Corresponding				12/3/2004				
ARTERIS	GB	Philippe Boucard	Apparatus		04292872.1	Lapsed	12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
		Philippe Martin									
ARTERIS S.A.	US	Luc Montperrus	Smart Aging Retry Buffer		13/304,153	Granted	11/24/2010	11/23/2011	11/23/2011	8,522,104	8/27/2013
			Method Of Managing A								
		Cesar Douady	Storage Organised As Fifo, And Corresponding				12/3/2004				
ARTERIS	LI	Philippe Boucard	Apparatus		04292872.1	Lapsed	12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
ARTERIS		Timppe bodeard	Method And System For	Lapsed	04232072.1	Lapseu	12/5/2005	0,7,2000	12/3/2004	LI 1542151	0,772000
			Transmission Of Messages	10/29/08.							
		Cesar Douady	In A Network	Last fee paid							
ARTERIS	FR	Philippe Boucard	Interconnection	12/28/06	FR0400554	Lapsed		1/21/2004	1/21/2004	FR2865334	3/3/2006
						T .		· ·	1		
			Method And System For								
			Transmitting Messages In								
		Cesar Douady	An Interconnection								
ARTERIS	US	Philippe Boucard	Network	1	11/039,112	Abandoned	1/21/2004	1/19/2005	1/19/2005		
						1		1		1	
			Method And System For			1		1		1	
			Transmitting Messages In	l		1		1		1	
ADTEDIC	E2	Cesar Douady	An Interconnection	Last fee paid	04304340.0	LA CALL J.	1 /24 /2004	E /30 /3004	F /20/2004	1	
ARTERIS	EP	Philippe Boucard	Network	3/31/06	04291340.0	Withdrawn	1/21/2004	5/28/2004	5/28/2004	-	1
			System And Mother J.F.	Lapsed							
			System And Method For Data Transmission In An	2/26/10. Last fee paid		1		1		1	
ARTERIS	FR	Luc Montperrus	Electronic Circuit	4/2/08	FR0505896	Lapsed		6/10/2005	6/10/2005	FR2887093	8/31/2007
/ MTEM3	T IV	Luc Montperrus	System And Method Of	1 2 1 30	110303030	Labsen	+	3/ 10/ 2003	0/ 10/ 2003	1112007033	0/31/200/
			Transmitting Data In An			1		1		1	
ARTERIS	us	Luc Montperrus	Electronic Circuit		11/179,014	Abandoned	6/10/2005	7/11/2005	7/11/2005	1	
	1	Philippe Boucard	Transport Of Pci-Ordered				-,, 2000	,,	,,,		
		Jean-Jacques Lecler	Traffic Over Independent								
ARTERIS SAS	US	Philippe Martin	Networks		13/337,280	Application		12/26/2011	12/26/2011	<u> </u>	
			System And Method For								
			Communicating On	Lapsed		1		1		1	
		Cesar Douady	Asynchronous Circuit	5/31/11.		1		1		1	
		Philippe Boucard	Between Sub-Circuits	Last fee paid							
ARTERIS	FR	Luc Montperrus	Synchronous	7/1/09	FR0509279	Lapsed		9/12/2005	9/12/2005	FR2890766	11/30/2007
						1		1		1	
			System And Method Of On	†		1		1		1	
		Cesar Douady	Circuit Asynchronous			1		1		1	
		Philippe Boucard	Communication, Between		İ	1	1	I		I	
ARTERIS	us	Luc Montperrus	Synchronous Subcircuits		11/399,293	Abandoned	9/12/2005	4/6/2006	4/6/2006		

					ı		1				
			Asynchronous								
		Company to	Communication System								
		Cesar Douady	And Method Between	Last foo maid							
ARTERIS	EP	Philippe Boucard Luc Montperrus	Synchronous Sub-Circuits On A Chip	Last fee paid 3/24/10	06290549.2	Withdrawn	9/12/2005	4/5/2006	4/5/2006		
ANTENIS	Lr	Luc Montperrus	Off A Crip	3/24/10	00230343.2	Withdrawn	3/12/2003	4/ 3/ 2000	4/3/2000		
			System And Method For	Lapsed							
			Static Routing Flow In A	7/31/09.							
		Philippe Boucard	Packet Data Network	Last fee paid							
ARTERIS	FR	Sophana Kok	Interconnection	10/10/07	FR0511591	Lapsed		11/16/2005	11/16/2005	FR2893471	1/25/2008
			System And Method For								
		Sophana Kok	The Static Routing Of Data Packet Streams In An								
ARTERIS	us	Philippe Boucard	Interconnect Network		11/407,391	Abandoned	11/16/2005	4/19/2006	4/19/2006		
ACCENTS	03	Timppe bodeard	Interconnect Network		11/407,331	Abditabiled	11/10/2003	1,15,2000	1/15/2000		
			System And Method For								
			Static Packet Data Flow								
		Sophana Kok	Routing In An	Last fee paid							
ARTERIS	EP	Philippe Boucard	Interconnection Network	2/22/08	06290646.6	Withdrawn	11/16/2005	4/20/2006	4/20/2006		
			Method Of Producing A								
			Circuit Timing Of Way Asynchronous Data								
			Exchanged Between Two	Lapsed							
			Blocks Synchronous And	1/29/10.							
		César Douady	Timing Circuit Drawn From								
ARTERIS	FR	Luc Montperrus	Such Method	3/12/2008	FR0604348	Lapsed		5/16/2006	5/16/2006	FR2901437	8/8/2008
			Process For Designing A								
			Circuit For Synchronizing								
			Data Asychronously								
			Exchanged Between Two								
		César Douady	Synchronous Blocks, And Synchronization Circuit								
ARTERIS	us	Luc Montperrus	Fabricated By Same		11/483,419	Abandoned	5/16/2006	7/7/2006	7/7/2006		
7.11.12.11.0			Tablicated by came		11, 100, 115	7.5011001100	5, 15, 2555	1,1,1,2000	1,1,2000		
			Method Of Creating A								
			Circuit For Synchronising								
			Data Exchanged								
			Asynchronously Between								
			Two Synchronous Blocks								
			As Well As Synchronisation Circuit								
		César Douady	Made According To Such A	Last fee paid							
ARTERIS	EP	Luc Montperrus	Method	3/31/2009	07105144.5	Withdrawn	5/16/2006	3/28/2007	3/28/2007		
		·	Management System	Lapsed							
			Messages Sent In A	3/31/10.							
		Philippe Boucard	Network-On-Chip	Last fee paid							
ARTERIS	FR	Sophana Kok	Interconnection	7/11/2008	FR0606833	Lapsed		7/26/2006	7/26/2006	FR2904445	10/10/2008
			System For Managing								
		Sophana Kok	Messages Transmitted In An On-Chip Interconnect								
ARTERIS	us	Philippe Boucard	Network		11/518,384	Abandoned	7/26/2006	9/8/2006	9/8/2006		
			System For Managing				,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	-, -,			
			Messages Transmitted In								
		Sophana Kok	An Interconnection								
ARTERIS	EP	Philippe Boucard	Network On A Chip		07112718.7	Withdrawn	7/26/2006	7/18/2007	7/18/2007		
				Refusal							
			System And Method For	6/3/2009		I				1	
		Dhilinna Daward	Determining Performance	Last for maid		1					
ARTERIS	FR	Philippe Boucard Alain Fawaz	Network Interconnection On Chip	Last fee paid 9/19/07	FR0655979	Abandoned		12/27/2006	12/27/2006	ER 2911027	
PICIENIS	1.17	Alain Lawaz	System And Method For	10/51/6/	110033373	Abandoned		12/2//2000	12/2//2006	11/2311027	+
			Determining The			I				1	
			Performance Of An On-			I				1	
		Philippe Boucard	Chip Interconnection			1					
ARTERIS	US	Alain Fawaz	Network		11/749,908	Abandoned	12/27/2006	5/17/2007	5/17/2007		
			System And Method For								
			Determining The			1					
		Distillance P	Performance Of An			I				1	
ADTEDIC	E.D.	Philippe Boucard	Interconnection Network		07301507.9	Mithdra	13/37/3006	12/21/2007	12/21/2007		
ARTERIS	EP	Alain Fawaz	On A Chip Link Between Chips Using		07291597.8	Withdrawn	12/27/2006	12/21/2007	12/21/2007	 	1
			Virtual Channels And			1					
ARTERIS SAS	us	Philippe Boucard	Credit Based Flow Control		13/340,690	Application		12/30/2011	12/30/2011	1	
	1	,			,,	1					1

			T	T	T	Г	T	1		I
		District Processed	Distributed Bearing							
ARTERIS SAS	us	Philippe Boucard Jean-Jacques Lecler	Distributed Reorder Buffers		13/436,944	Application		3/31/2012	3/31/2012	
		·								
Arteris S.A.	US	Jonah Proujansky-Bell	Packet-Based On-Chip And Off-Chip Interconnects		61/409,054	Expired		11/1/2010	11/1/2010	
Arteris S.A.	03	Philippe Boucard	Parallel Synchronous		01/409,034	Lxpired		11/1/2010	11/1/2010	
Arteris S.A.	US	Xavier Leloup	Interface		61/411,430	Expired		11/8/2010	11/8/2010	
Arteris S.A.	us	Philippe Martin	Smart Aging Retry Buffer		61/417,136	Expired		11/24/2010	11/24/2010	
		Jean-Jacques Lecler	Memory Access Latency		,					
ARTERIS S.A.	US	Philippe Boucard Philippe Martin	Metering		13/450,342 PCT/IB2011/00309	Application	4/18/2011	4/18/2012	4/18/2012	
ARTERIS S.A.S.	wo	Luc Montperrus	Smart Aging Retry Buffer		5	Expired	11/24/2010	11/23/2011	11/23/2011	
A DEEDIC C. A. C.	14/0	Philippe Martin	Court Asia - Batan Buffer		PCT/US2011/0621	Ei	11/34/3010	11 /22 /2011	11/22/2011	
ARTERIS S.A.S.	wo	Luc Montperrus	Smart Aging Retry Buffer Memory Access Latency		00	Expired	11/24/2010	11/23/2011	11/23/2011	
		Jean-Jacques Lecler	Metering For Network							
Arteris S.A.	US	Philippe Boucard	Interconnect		61/476,674	Expired		4/18/2011	4/18/2011	
		Daniel Michel Xavier Van Ruymbeke	Display And Automatic Improvement Of Timing	OA response						
ARTERIS S.A.	us	Pascal Godet	And Area In A Network-On		13/487,087	Application	6/2/2011	6/1/2012	6/1/2012	
		Daniel Michel Xavier Van Ruymbeke	Display And Automatic Improvement Of Timing							
		Pascal Godet	And Area In A Network-On							
ARTERIS S.A.	us	Xavier Leloup	Chip		61/492,709	Expired		6/2/2011	6/2/2011	
		Alain Fawaz								
		Philippe Boucard								
ARTERIS S.A.	US	Philippe Martin	Latency Probe		13/528,780	Application	6/22/2011	6/20/2012	6/20/2012	
			Asynchronous Clock							
ARTERIS S.A.	US	Philippe Boucard	Adapter		13/539,185	Application	7/1/2011	6/29/2012	6/29/2012	
ARTERIS S.A.	us	Alain Fawaz Philippe Boucard	Latency Probe		61/500,078	Expired		6/22/2011	6/22/2011	
, INTERNO SIA		Timppe boddard	Latericy 1100C		01/300,070	Expired		0, 22, 2011	0,22,2011	
			Asynchronous Power				1			
ARTERIS S.A.	US	Philippe Boucard Philippe Boucard	Disconnect		13/543,728	Application	7/6/2011	7/6/2012	7/6/2012	
		Jean-Jacques Lecler	Network On a Chip Socket							
ARTERIS SAS	US	Boris Boutillier	Protocol		13/626,766	Application		9/25/2012	9/25/2012	
ARTERIS S.A.	us	Philippe Boucard	Gals Asynchronous Clock Adapter		61/504,135	Expired		7/1/2011	7/1/2011	
		Philippe Boucard	Network On a Chip Socket							
ARTERIS SAS	US	Jean-Jacques Lecler	Protocol Power		13/626,758	Application		9/25/2012	9/25/2012	
ARTERIS S.A.	us	Philippe Boucard	Asynchronous Power Disconnect		61/504,948	Expired		7/6/2011	7/6/2011	
		Laurent Moll, Jean-	System Translation Look-	8/17/2013						
ARTERIS SAS	US	Jacques Lecler, Philippe Laurent Rene Moll	Aside Buffer With Request- Integrated Circuits With	Conversion	61/684,705	Application		8/18/2012	8/18/2012	
ARTERIS SAS	us	Jean-Jacques Lecler	Cache-Coherency		61/551,922	Expired		10/26/2011	10/26/2011	
				- / /						
		Philippe Martin	System And Method For Automatically Inserting	9/27/2013 Conversion						
ARTERIS SAS	us	Boris Boutillier	Safety Logic	Deadline	61/707,659	Application		9/28/2012	9/28/2012	
A PTERIO CAR	116		Simplified Controller With	9/27/2013		A		0 /20 /2212	0 /20 /22 : 2	
ARTERIS SAS	US	Laurent Rene Moll	Partial Coherency A Three Channel Cache-	Conversion	61/707,627	Application	+	9/28/2012	9/28/2012	
			Coherency Socket							
ARTERIS SAS	US	Jean-Jacques Lecler	Protocol		61/551,917	Expired		10/26/2011	10/26/2011	
		Lourent Dor - Ma-II	Integrated Circuits 14/21							
ARTERIS SAS	us	Laurent Rene Moll Jean-Jacques Lecler	Integrated Circuits With Cache-Coherency		13/659,850	Application	10/26/2011	10/24/2012	10/24/2012	
		,	A Three Channel Cache-		, ,	11	, ,	, ,	, ,	
	luc.	laan laanus tastaa	Coherency Socket		12/650 704	Applies #1	10/26/2011	10/24/2012	10/24/2012	
A DEEDIC CAC	US	Jean-Jacques Lecler	Protocol		13/659,781	Application	10/26/2011	10/24/2012	10/24/2012	
ARTERIS SAS							i .	1	1	1
ARTERIS SAS			Differential Formatting							
		Dhiling No. 1	Between Normal And		64/630 304	Eminad		13 (0 /3 31 1	13/0/2011	
ARTERIS SAS Arteris SAS	US	Philippe Martin	_		61/630,391 PCT/US2012/0619	Expired	10/24/2012	12/8/2011	12/8/2011	

							•	1	
			A Thurs Channel Cash						
			A Three Channel Cache- Coherency Socket	PCT/US2012/0619		10/24/2012			
ARTERIS SAS	wo	Jean-Jacques Lecler	Protocol	23	Application	10/26/2011	10/25/2012	10/25/2012	
7	11.5				, 10 p. 11 c. 11 c. 1	10, 20, 2022	,,	20, 20, 2022	
		Philippe Boucard	Network-On-A-Chip With						
		Philippe Martin	Quality-Of-Service			7/13/2010		l	
ARTERIS S.A.	US	Jean-Jacques Lecler	Features	13/680,965	Application	6/3/2010	11/19/2012	11/19/2012	
			Automatic Pipeline Stage						
ARTERIS SAS	us	Jean-Jacques Lecler	Insertion	13/680,399	Application		11/19/2012	11/19/2012	
		'		· ·					
		Jean-Jacques Lecler	Efficient Data Encoding On						
Arteris	US	Jonah Proujansky-Bell	A Variable Width Link	61/582,005	Abandoned		12/30/2011	12/30/2011	
			Mathad And Annavatus						
		Laurent Rene Moll	Method And Apparatus For Supporting Target-						
		Jean-Jacques Lecler	Side Security In A Cache						
ARTERIS SAS	US	Philippe Boucard	Coherent System	13/686,604	Application		11/27/2012	11/27/2012	
0tt - 5 05	luc.	Jean-Jacques Lecler	Elecenter Estimates	C4 (504 C30	Francisco d		42/20/2044	42/20/2044	
Arteris SAS	US	Boris Boutillier	Floorplan Estimator	61/581,639	Expired		12/30/2011	12/30/2011	
				ĺ					
		Philippe Boucard	Network-On-A-Chip With	ĺ					
		Philippe Martin	Quality-Of-Service			5/25/2011			
ARTERIS S.A.S.	GB	Jean-Jacques Lecler	Features	GB1221555.4	Application	6/3/2010	11/30/2012	5/25/2011	
		Philippe Boucard	Network-On-A-Chip With						
ARTERIS S.A.S.	CN	Philippe Martin Jean-Jacques Lecler	Quality-Of-Service Features	201180027348.4	Application	6/3/2010	12/3/2012	5/25/2011	
ARTERIS S.A.S.	CIV	Jean-Jacques Leciel	reatures	201180027348.4	Application	0/3/2010	12/3/2012	3/23/2011	
		Jean-Jacques Lecler	Differential Formatting						
		Philippe Martin	Between Normal And						
Arteris SAS	US	Jonah Probell	Retry Data Transmission	13/708,715	Application	12/8/2011	12/7/2012	12/7/2012	
		Jean-Jacques Lecler	Differential Formatting	PCT/US2012/0686				, . ,	
ARTERIS SAS	wo	Philippe Martin	Between Normal And	25	Application	12/8/2011	12/7/2012	12/7/2012	-
ARTERIS SAS	us	Jean-Jacques Lecler Boris Boutillier	Floorplan Estimation	13/723,186	Application	12/30/2011	12/20/2012	12/20/2012	
, III EIII 37 IS	0.5	Philippe Boucard	Transport Of Pci-Ordered	PCT/US2012/0715	дриссион	12,30,2011	ILICOLLOIL	ILICOLUIL	
ARTERIS SAS	wo	Jean-Jacques Lecler	Traffic Over Independent	09	Application	12/26/2011	12/21/2012	12/21/2012	
		Jean-Jacques Lecler		PCT/US2012/0715					
ARTERIS SAS	wo	Boris Boutillier	Floorplan Estimation	02	Application	12/30/2011	12/21/2012	12/21/2012	
			Link Between Chips Using				,,	,,	
		Philippe Boucard	Virtual Channels And	PCT/US2012/0715					
ARTERIS SAS	wo	Monica Tang	Credit Based Flow Control	98	Application	12/30/2011	12/25/2012	12/25/2012	
		Alain Fawaz							
		Philippe Boucard		PCT/IB2012/05314		6/20/2012			
ARTERIS S.A.S.	wo	Philippe Martin	Latency Probe	8	Application	6/22/2011	12/27/2012	12/27/2012	
		Jean-Jacques Lecler							
ADTEDIC CAC	LIC.	Laurent Rene Moll	Last Laval Casha	61/753 033	Appliantin		1/17/2012	1/17/2012	
ARTERIS SAS	US	Jonah Proujansky-Bell	Last Level Cache Description and	61/753,922	Application	+	1/17/2013	1/17/2013	
		Boris Boutillier	Composition of Networks			1			
ARTERIS SAS	US	Philippe Martin	on Chips	61/784,078	Application	1	3/14/2013	3/14/2013	
		Philippe Boucard	Tunneling Within A				·		
ARTERIS SAS	US	Philippe Martin	Network-On-Chip	61/800,830	Application		3/15/2013	3/15/2013	
			Method And Device For			1			
			Managing Priority During			1			
		Cesar Douady	The Transmission Of A			8/11/2004			
ARTERIS S.A.	us	Philippe Boucard	Message	13/868,614	Application	8/13/2003	4/23/2013	4/23/2013	
		Daniel Michel	Display And Automatic			T .	·	<u> </u>	
		The second of the second	Improvement Of Timing	1		6/1/2012			
		Xavier Van Ruymbeke			l				
Arteris S.A.S.	US	Pascal Godet	And Area In A Network-On-	13/898,540	Application	6/2/2011	5/24/2013	5/24/2013	
Arteris S.A.S.	US	Pascal Godet		13/898,540	Application	6/2/2011	5/24/2013	5/24/2013	
Arteris S.A.S. Arteris S.A.S.	US				Application Application	6/2/2011	5/24/2013 6/3/2013	5/24/2013	

		Philippe Boucard	DISTRIBUTED REORDER						
ARTERIS SAS	GB	Jean-Jacques Lecler	BUFFERS	GB1305241.0	Application		3/21/2013	3/21/2013	
								, , , , , , , ,	
		Philippe Boucard	Distributed Reorder						
ARTERIS SAS	CN	Jean-Jacques Lecler	Buffers	201310104136.2	Application		3/28/2013	3/28/2013	
		Laurent Moll	Adaptive Tuning of Snoop		T		, ,		
ARTERIS SAS	us	Jean-Jacques Lecler	Requests	13/938,651	Application	9/28/2012	7/10/2013	7/10/2013	
		Laurent Moll	Adaptive Tuning of Snoop		† · ·			, , ,	
ARTERIS SAS	us	Jean-Jacques Lecler	Requests	13/938,675	Application	9/28/2012	7/10/2013	7/10/2013	
		·	·		1				
		Laurent Moll	Coherency Controller with						
ARTERIS SAS	US	Jean-Jacques Lecler	Reduced Data Buffer	13/941,483	Application	9/28/2012	7/13/2013	7/13/2013	
		Kurt Shuler							
		Jonah Probell	Power Aware Thread						
ARTERIS SAS	US	Monica Tang	Scheduling	61/845,991	Application	7/13/2013	7/13/2013	7/13/2013	
		Boris Boutillier	Verification Using Generic						
ARTERIS SAS	US	Jean-Jacques Lecler	Protocol Adapters	13/941,535	Application	7/14/2013	7/14/2013	7/14/2013	
		Philippe Boucard	Selective Change of						
		Jean-Jacques Lecler	Pending Transaction						
ARTERIS SAS	US	Jonah Proujansky-Bell	Urgency	13/941,537	Application	7/14/2013	7/14/2013	7/14/2013	
			System Translation Look-						
		Laurent Moll	aside Buffer with Request-						
		Jean-Jacques Lecler	Based Allocation and						
ARTERIS SAS	US	Philippe Boucard	Prefetching	PCT/US13/55451	Application	8/18/2012	8/16/2013	8/16/2013	
			System Translation Look-						
		Laurent Moll	aside Buffer with Request-						
		Jean-Jacques Lecler	based Allocation and						
ARTERIS SAS	US	Philippe Boucard	Prefetching	13/969,425	Application	8/18/2012	8/16/2013	8/16/2013	
		Laurent Moll	System Translation Look-						
		Jean-Jacques Lecler	aside Buffer Integrated in						
ARTERIS SAS	US	Philippe Boucard	an Interconnect	13/969,451	Application	8/18/2012	8/16/2013	8/16/2013	
		Laurent Moll	DMA Engine with STLB			1			
		Jean-Jacques Lecler	Prefetch Capabilities and			1			
ARTERIS SAS	US	Philippe Boucard	Tethered Prefetching	13/969,559	Application	8/18/2012	8/17/2013	8/17/2013	
			STLB Prefetching for a						
ARTERIS SAS	US	Laurent Moll	Rotation Engine	13/969,562	Application	8/18/2012	8/17/2013	8/17/2013	
		Laurent Moll	Adaptive Tuning of Snoop						
ARTERIS SAS	GB	Jean-Jacques Lecler	Requests	GB1315339.0	Application	9/28/2012	8/29/2013	8/29/2013	
		Laurent Moll	Coherency Controller with			1			
ARTERIS SAS	GB	Jean-Jacques Lecler	Reduced Data Buffer	GB1315343.2	Application	9/28/2012	8/29/2013	8/29/2013	
		Jean-Jacques Lecler				1			
		Philippe Boucard	Network on a Chip Socket			1			
ARTERIS SAS	PCT	Boris Boutilier	Protocol	PCT/US13/61295	Application	9/25/2012	9/24/2013	9/24/2013	
			Simplified Controller with			1			
ARTERIS SAS	US	Laurent Moll	Partial Coherency	14/040,643	Application	9/28/2012	9/28/2013	9/28/2013	

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