PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT2594869

SUBMISSION TYPE:	NEW ASSIGNMENT

NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
ARTERIS SAS	10/11/2013

RECEIVING PARTY DATA

Name:	QUALCOMM TECHNOLOGIES, INC.
Street Address:	5775 MOREHOUSE DRIVE
City:	SAN DIEGO
State/Country:	CALIFORNIA
Postal Code:	92121

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	13969559

CORRESPONDENCE DATA

Fax Number:
Phone: 650 387 0003
Email: docketdls@yahoo.com
Correspondence will be sent via US Mail when the email attempt is unsuccessful.
Correspondent Name: DANA LEGAL SERVICES
Address Line 1:   325 SHARON PARK DRIVE
Address Line 2:   ST 217
Address Line 4:   MENLO PARK, CALIFORNIA 94025

ATTORNEY DOCKET NUMBER:	ART-024US3

NAME OF SUBMITTER:	JUBIN DANA

Signature:	/JUBIN DANA/

Date:	10/29/2013

OP $40.00 13969559

PATENT

Total Attachments: 15
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PATENT ASSIGNMENT

This PATENT ASSIGNMENT (this "**Assignment**") is made and entered into as of October 11, 2013 (the "**Effective Date**") by and between Arteris Holdings, Inc., a Delaware corporation ("**Target Holdings**"), Arteris, Inc., a Delaware corporation and wholly owned subsidiary of Target Holdings ("**Target USA Sub**"), Arteris, SAS a French société par actions simplifiée and wholly owned subsidiary of Target Holdings ("**Target France Sub**", and together with Target Holdings and Target USA Sub, the "**Assignors**", and each an "**Assignor**") and Qualcomm Technologies, Inc., a Delaware corporation ("**Assignee**").

WHEREAS, pursuant to that certain Asset Purchase Agreement (the "**Purchase Agreement**"), dated October 9, 2013, among Target Holdings, Target USA Sub, Target France Sub, Assignee and Qualcomm France SARL, each Assignor has agreed to sell, assign, transfer, convey, and deliver to Assignee all of such Assignor's right, title, and interest in and to certain assets, including, without limitation, the Assigned Patents (defined below); and

WHEREAS, pursuant to the Purchase Agreement, the Assignors and Assignee have agreed to enter into this Assignment.

NOW, THEREFORE, in consideration of the promises and covenants set forth in the Purchase Agreement and for other good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, the parties hereto agree as follows:

1.   Conveyance. Each Assignor hereby sells, assigns, transfers, conveys, and delivers to Assignee all of such Assignor's right, title and interest in, to and under the issued patents and patent applications listed on Schedule A, including all reissues, divisionals, continuations, continuations-in-part, revisions, reexaminations, extensions, and counterparts (whether foreign or domestic) claiming priority to or based on any of the foregoing items, together with all patents issuing therefrom, all inventions and improvements claimed or described in any of the foregoing, including without limitation all rights of priority under International Conventions, Treaties, or Agreements, all rights to collect royalties and proceeds in connection with any of the foregoing (collectively, the "**Assigned Patents**"), and all rights to sue and bring other claims for past, present and future infringement, misappropriation or other violation of any of the foregoing and all rights to recover and retain damages (including attorney's fees and expenses) or lost profits in connection therewith.

2.   Recordation. Each Assignor hereby requests the United States Patent and Trademark Office Commissioner for Patents and any other applicable governmental entity or registrar (including any applicable foreign or international office or registrar), to record Assignee as the assignee and owner of the Assigned Patents. Each Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents or certificates of invention which may be granted upon any of the Assigned Patents in the name of Assignee, as the assignee to the entire interest therein.

3.   Information and Assistance.

3.1   Upon Assignee's reasonable request and without further compensation, each Assignor shall execute, acknowledge and deliver all such other instruments and documents and

shall take all such other actions reasonably necessary or required by law to consummate and make fully effective the transaction contemplated by this Assignment.

3.2   If Assignee is unable for any reason to secure an Assignor's signature to any document required to file, prosecute, register or memorialize the assignment of any rights under any Assigned Patents as provided under this Assignment, each Assignor hereby irrevocably designates and appoints Assignee and Assignee's duly authorized officers and agents as such Assignor's agents and attorneys-in-fact to act for and on such Assignor's behalf and instead of such Assignor to take all lawfully permitted acts to further the filing, prosecution, registration, memorialization of assignment, issuance and enforcement of rights under such Assigned Patents, all with the same legal force and effect as if executed by such Assignor. The foregoing is deemed a power coupled with an interest and is irrevocable.

4.   Successors and Assigns. This Assignment and all the provisions hereof shall be binding upon and shall inure to the benefit of the parties hereto and their respective successors and permitted assigns and nothing herein express or implied shall give or be construed to give to any person, other than the parties hereto and such permitted assigns, any legal or equitable rights hereunder.

5.   Counterparts. This Assignment may be executed in two or more consecutive counterparts (including by facsimile), each of which shall be an original, with the same effect as if the signatures thereto and hereto were upon the same instrument, and shall become effective when one or more counterparts have been signed by each party and delivered (by facsimile or otherwise) to the other party.

6.   Section Headings. The section headings contained in this Assignment are for reference purposes only, and shall not in any way affect the meaning or interpretation of this Assignment.

7.   Purchase Agreement Controls. This Assignment is provided pursuant to the Purchase Agreement, to which reference is made for a further statement of the rights and obligations of the Assignors and Assignee with respect to the Assigned Patents. Nothing contained in this Assignment shall be deemed to modify, supersede, enlarge or affect the rights of any person under the Purchase Agreement. If any provision of this Assignment is inconsistent or conflicts with the Purchase Agreement, the Purchase Agreement shall control.

8.   Governing Law. This Assignment and all claims or causes of action (whether in contract, tort or otherwise) that may be based upon, arise out of or relate to this Assignment or the negotiation, execution or performance of this Assignment shall be governed by and construed in accordance with the laws of the State of Delaware, without giving effect to any choice or conflict of law provision or rule.

[Signature Page Follows]

IN WITNESS WHEREOF, the undersigned have caused this Patent Assignment to be executed, effective as of the Effective Date.
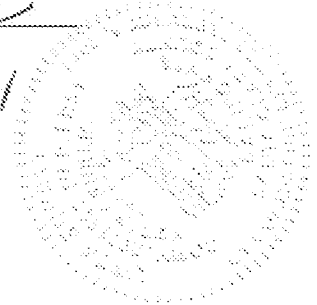
ASSIGNORS:

ARTERIS HOLDINGS, INC.

By: _____
Name: K. Charles Janac
Title: President and CEO

ARTERIS, INC.

By: _____
Name: K. Charles Janac
Title: President and CEO

ARTERIS, SAS

By: _____
Name: K. Charles Janac
Title: Directeur General

CALIFORNIA ALL-PURPOSE
CERTIFICATE OF ACKNOWLEDGMENT

State of California

County of _Santa Clara_

On _October 11, 2013_ before me, _Linda Dukett, Notary Public_ ,
<div align="center">(Here insert name and title of the officer)</div>
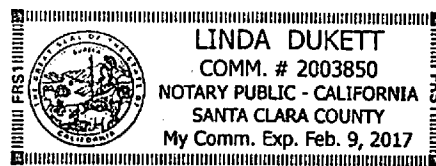
personally appeared _Charles Janac_ ,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

_____
Signature of Notary Public

LINDA DUKETT
COMM. # 2003850
NOTARY PUBLIC - CALIFORNIA
SANTA CLARA COUNTY
My Comm. Exp. Feb. 9, 2017

(Notary Seal)

ADDITIONAL OPTIONAL INFORMATION

DESCRIPTION OF THE ATTACHED DOCUMENT

Patent Agreement
(Title or description of attached document)

_____
(Title or description of attached document continued)

Number of Pages _____ Document Date _10/11/13_

Signature page Imbedded
(Additional information)

CAPACITY CLAIMED BY THE SIGNER
☐ Individual (s)
☐ Corporate Officer

_____
(Title)
☐ Partner(s)
☐ Attorney-in-Fact
☐ Trustee(s)
☐ Other _____

INSTRUCTIONS FOR COMPLETING THIS FORM
Any acknowledgment completed in California must contain verbiage exactly as appears above in the notary section or a separate acknowledgment form must be properly completed and attached to that document. The only exception is if a document is to be recorded outside of California. In such instances, any alternative acknowledgment verbiage as may be printed on such a document so long as the verbiage does not require the notary to do something that is illegal for a notary in California (i.e. certifying the authorized capacity of the signer). Please check the document carefully for proper notarial wording and attach this form if required.

- State and County information must be the State and County where the document signer(s) personally appeared before the notary public for acknowledgment.
- Date of notarization must be the date that the signer(s) personally appeared which must also be the same date the acknowledgment is completed.
- The notary public must print his or her name as it appears within his or her commission followed by a comma and then your title (notary public).
- Print the name(s) of document signer(s) who personally appear at the time of notarization.
- Indicate the correct singular or plural forms by crossing off incorrect forms (i.e. he/she/they, is /are) or circling the correct forms. Failure to correctly indicate this information may lead to rejection of document recording.
- The notary seal impression must be clear and photographically reproducible. Impression must not cover text or lines. If seal impression smudges, re-seal if a sufficient area permits, otherwise complete a different acknowledgment form.
- Signature of the notary public must match the signature on file with the office of the county clerk.
  - ❖ Additional information is not required but could help to ensure this acknowledgment is not misused or attached to a different document.
  - ❖ Indicate title or type of attached document, number of pages and date.
  - ❖ Indicate the capacity claimed by the signer. If the claimed capacity is a corporate officer, indicate the title (i.e. CEO, CFO, Secretary).
- Securely attach this document to the signed document

IN WITNESS WHEREOF, the undersigned have caused this Patent Assignment to be executed, effective as of the Effective Date.

Acknowledged and Accepted:

ASSIGNEE:

QUALCOMM TECHNOLOGIES, INC.

By:

Name: Cristiano Amon

Title: Executive Vice President

[Signature Page to Patent Assignment]

SCHEDULE A TO PATENT ASSIGNMENT

Owner	Country	Inventor(s)	Application Title	Surcharge Date at USPTO	Application Number	Application Status	Priority Date	National/Actual Filed Date	Filed Date	Patent Number	Issued/Grant Date
ARTERIS	FR	Philippe Boucard César Douady	Method And Device For Managing Priority In The Transmission Of A Message	Next renewal due 8/31/2013	FR0309918	Granted		8/13/2003	8/13/2003	FR2858895	5/5/2006
ARTERIS	FR	Philippe Boucard César Douady	System And Method For Transmission Of A Sequence Of Messages In A Network Interconnection	Next renewal due 11/30/2012	FR0313298	Abandoned		11/13/2003	11/13/2003	FR2862457	2/24/2006
ARTERIS	FR	Philippe Boucard César Douady	Method And Device For Switching Between Agents	Next renewal due 3/31/2014	FR0402149	Granted		3/2/2004	3/2/2004	FR2867338	8/10/2007
ARTERIS	EP	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An Interconnection Network		04291339.2	Expired	11/13/2003	5/28/2004	5/28/2004	EP1531589	4/13/2011
ARTERIS	EP	César Douady Philippe Boucard	System And Process For Packet Switching Between Agents		04291341.8	Granted	3/2/2004	5/28/2004	5/28/2004	EP1575222	10/15/2008
ARTERIS	EP	César Douady Philippe Boucard	Method And Device For Priority Management During A Message Transmission		04292011.6	Expired	8/13/2003	8/6/2004	8/6/2004	EP1507374	3/19/2008
ARTERIS	US	César Douady Philippe Boucard	Method And Device For Managing Priority During The Transmission Of A Message	Pay Maintenance Fee 2/3/2014	10/915,950	Granted	8/13/2003	8/11/2004	8/11/2004	7,769,027	8/3/2010
ARTERIS	US	César Douady Philippe Boucard	System And Method For Transmitting A Sequence Of Messages In An Interconnection Network	04/04/2015	10/988,831	Granted	11/13/2003	11/15/2004	11/15/2004	8,031,730	10/4/2011
ARTERIS	US	César Douady Philippe Boucard	Method And Device For Switching Between Agents	2/14/2017	11/054,179	Granted	3/2/2004	2/9/2005	2/9/2005	7,574,629	8/11/2009
ARTERIS	EP	Luc Montperrus	System And Method Of Conveying Data In An Electronic Circuit		05291515.4	Granted	6/10/2005	7/13/2005	7/13/2005	EP1732241	11/26/2008
ARTERIS	FR	Philippe Boucard Luc Montperrus	Switching System Message	Next renewal due 3/31/2014	FR0602810	Granted		3/31/2006	3/31/2006	FR2899413	8/8/2008
ARTERIS	FR	Philippe Boucard César Douady	Interconnection System On Chip Blocks Outside With A Single Communication Protocol Parameterizable	Next renewal due 4/30/2014	FR0603248	Granted		4/12/2006	4/12/2006	FR2900017	10/31/2008

Company	Country	Inventors	Title	Notes	App. No.	Status	Date 1	Date 2	Date 3	Patent No.	Date 4
ARTERIS	BG	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An Interconnection Network		04291339.2	Lapsed	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS	EP	Cesar Douady Philippe Boucard	Method Of Managing A Storage Organised As Fifo, And Corresponding Apparatus		04292872.1	Granted	12/3/2004 12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
ARTERIS	IT	Cesar Douady Philippe Boucard	Method Of Managing A Storage Organised As Fifo, And Corresponding Apparatus	Last fee paid 12/31/2007	04292872.1	Lapsed	12/3/2004 12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
ARTERIS	US	César Douady Philippe Boucard	System Of Interconnections For External Functional Blocks On A Chip Provided With A Single Configurable	OFFICE ACTION RESPONSE 6/29/2013	11/482,175	Application	4/12/2006	7/6/2006	7/6/2006		
ARTERIS	US	Philippe Boucard Luc Montperrus	Message Switching System	6/29/2013	11/484,837	Granted	3/31/2006	7/11/2006	7/11/2006	7,639,704	12/29/2009
ARTERIS	US	Philippe Boucard Vincent Vacquerie	Managing Messages Transmitted In An Interconnect Network	2/28/2016	11/516,811	Granted	6/23/2006	9/6/2006	9/6/2006	8,254,380	8/28/2012
ARTERIS	LU	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An Interconnection Network		04291339.2	Lapsed	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS	EP	Philippe Boucard Luc Montperrus	Message Switching System	Fee due 2/28/2014	07102227.1	Application	3/31/2006	2/13/2007	2/13/2007		
ARTERIS	EP	Philippe Boucard César Douady	On Chip System For Interconnecting External Functional Blocks Comprising A Single Parametrizable		07300863.3	Granted	4/12/2006	3/14/2007	3/14/2007	EP1845456	4/8/2009
ARTERIS	GB	Philippe Boucard César Douady	On Chip System For Interconnecting External Functional Blocks	Last fee paid 3/15/2013 Fee due	07300863.3	Granted	3/14/2007 4/12/2006	3/14/2007	3/14/2007	EP1845456	4/8/2009
ARTERIS	IT	Philippe Boucard César Douady	On Chip System For Interconnecting External Functional Blocks Comprising A Single	Last fee paid 6/29/2012	07300863.3	Granted	3/14/2007 4/12/2006	3/14/2007	3/14/2007	EP1845456	4/8/2009
ARTERIS	US	César Douady Philippe Boucard	Method And Device For Switching Between Agents		12/539,554	Abandoned	2/9/2005 3/2/2004	8/11/2009	8/11/2009		
ARTERIS	NL	Philippe Boucard César Douady	On Chip System For Interconnecting External Functional Blocks	Last fee paid 2/22/2012	07300863.3	Granted	3/14/2007 4/12/2006	3/14/2007	3/14/2007	EP1845456	4/8/2009
ARTERIS	EP	Philippe Boucard Vincent Vacquerie	System And Method For The Management Of Messages Transported In		07108875.1	Granted	6/23/2006	5/24/2007	5/24/2007	EP1871058	12/30/2009
ARTERIS	FR	Philippe Boucard Pascal Godet Luc Montperrus	Electronic Memory Device	Next renewal fee due 09/30/2013	FR0706740	Granted		9/26/2007	9/26/2007	FR2921507	4/15/2011
ARTERIS	FR	César Douady Philippe Boucard	Method And Device For Priority Management During A Message	Last paid 6/26/2012	04292011.6	Granted	8/6/2004 8/13/2003	3/19/2008	8/6/2004	EP1507374	3/19/2008
ARTERIS	GB	César Douady Philippe Boucard	Method And Device For Priority Management During A Message	Last paid 8/24/2012 Pay	04292011.6	Granted	8/6/2004 8/13/2003	3/19/2008	8/6/2004	EP1507374	3/19/2008
ARTERIS	IT	César Douady Philippe Boucard	Method And Device For Priority Management During A Message Transmission	Last paid 8/21/2012	04292011.6	Granted	8/6/2004 8/13/2003	3/19/2008	8/6/2004	EP1507374	3/19/2008
ARTERIS	NL	César Douady Philippe Boucard	Method And Device For Priority Management During A Message	Last paid 7/26/2012	04292011.6	Granted	8/6/2004 8/13/2003	3/19/2008	8/6/2004	EP1507374	3/19/2008

Company	Country	Inventors	Title	Fee Note	App. No.	Status	Date	Date	Date	Patent No.	Date
ARTERIS	US	Philippe Boucard, Luc Montperrus	Message Switching System		12/616,694	Abandoned	7/11/2006 3/31/2006	11/11/2009	11/11/2009		
ARTERIS	EP	Philippe Boucard, Pascal Godet	Electronic Memory Device	Next renewal fee due	08290870.8	Application	9/26/2007	9/16/2008	9/16/2008		
ARTERIS	US	Philippe Boucard, Pascal Godet, Luc Montperrus	Electronic Memory Device	1/13/2014	12/284,820	Granted	9/26/2007	9/25/2008	9/25/2008	7,755,920	7/13/2010
ARTERIS	FR	César Douady, Philippe Boucard	System And Process For Packet Switching Between Agents	Last fee paid 1/10/2013	04291341.8	Granted	5/28/2004 3/2/2004	10/15/2008	5/28/2004	EP1575222	10/15/2008
ARTERIS	GB	César Douady, Philippe Boucard	System And Process For Packet Switching Between Agents	Last fee paid 5/21/2012 Fee Due 5/28/2014	04291341.8	Granted	5/28/2004 3/2/2004	10/15/2008	5/28/2004	EP1575222	10/15/2008
ARTERIS	IT	César Douady, Philippe Boucard	System And Process For Packet Switching Between Agents	Last fee paid 5/22/2012	04291341.8	Granted	5/28/2004 3/2/2004	10/15/2008	5/28/2004	EP1575222	10/15/2008
ARTERIS	NL	César Douady, Philippe Boucard	System And Process For Packet Switching Between Agents	Last fee paid 4/25/2012	04291341.8	Granted	5/28/2004 3/2/2004	10/15/2008	5/28/2004	EP1575222	10/15/2008
ARTERIS S.A.S.	FR	Philippe Boucard, Jean-Jacques Lecler	Network On Chip Zero Latency	Next renewal due 10/31/2013	FR0957137	Application		10/13/2009	10/13/2009		
ARTERIS S.A.S.	US	Jean-Jacques Lecler, Philippe Boucard	Zero-Latency Network On Chip (Noc)	Final rejection of all claims dated	12/579,346	Application	10/13/2009	10/14/2009	10/14/2009		
ARTERIS	FR	Philippe Boucard, Vincent Vacquerie	System And Method For Management Of Messages Sent In A Network Interconnection	Lapsed 2/29/2012. Last fee paid was 4/8/2010	FR0605646	Lapsed		6/23/2006	6/23/2006	FR2902957	9/12/2008
ARTERIS	FR	Philippe Boucard, Vincent Vacquerie	System And Method For The Management Of Messages Transported In An Interconnection Network	Last fee paid 3/27/2012	07108875.1	Granted	5/24/2007 6/23/2006	12/30/2009	5/24/2007	EP1871058	12/30/2009
ARTERIS	GB	Philippe Boucard, Vincent Vacquerie	System And Method For The Management Of Messages Transported In An Interconnection Network	Last fee paid 5/21/2012 Fee due 5/24/2014	07108875.1	Granted	5/24/2007 6/23/2006	12/30/2009	5/24/2007	EP1871058	12/30/2009
ARTERIS	IT	Philippe Boucard, Vincent Vacquerie	System And Method For The Management Of Messages Transported In An Interconnection Network	Last fee paid 5/23/2012	07108875.1	Granted	5/24/2007 6/23/2006	12/30/2009	5/24/2007	EP1871058	12/30/2009

ARTERIS	NL	Philippe Boucard Vincent Vacquerie	System And Method For The Management Of Messages Transported In An Interconnection Network	Last fee paid 4/25/2012	07108875.1	Granted	5/24/2007 6/23/2006	12/30/2009	5/24/2007	EP1871058	12/30/2009
ARTERIS S.A.S	FR	Philippe Boucard Philippe Martin Jean-Jacques Lecler	Network With On-Chip Features Quality-Of-Service	2013 fee paid; Renewal fee due 6/30/14	FR1054339	Granted		6/3/2010	6/3/2010	FR2961048	4/26/2013
ARTERIS S.A.	US	Philippe Boucard Philippe Martin Jean-Jacques Lecler	Network On Chip (Noc) With Qos Features	5/20/2016	12/835,623	Granted	6/3/2010	7/13/2010	7/13/2010	8,316,171	11/20/2012
ARTERIS	US	César Douady Philippe Boucard	Method And Device For Managing Priority During The Transmission Of A Message	Pay Maintenance Fee 11/14/2016	12/848,938	Granted	8/11/2004 8/13/2003	8/2/2010	8/2/2010	8,441,931	5/14/2013
ARTERIS	DE	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An Interconnection Network	Last paid 5/14/2012 Due 5/31/2014	602004032202.4	Granted	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS	FR	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An	Last paid 3/27/2012	04291339.2	Granted	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS	GB	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An	Last paid 5/21/2012	04291339.2	Granted	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS	HU	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An		04291339.2	Granted	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS S.A.S.	WO	Philippe Boucard Philippe Martin Jean-Jacques Lecler	Network-On-A-Chip With Quality-Of-Service Features		PCT/EP2011/05857	Expired	6/3/2010	5/25/2011	5/25/2011		
ARTERIS	IT	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An	Last paid 5/22/2012	04291339.2	Granted	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS	NL	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An	Last paid 4/25/2012	04291339.2	Granted	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS	FR	Philippe Boucard César Douady	Emulation Of One Or More Chip Device Integrated Circuits	Lapsed 1/31/08. Last fee paid 3/9/06	FR0305538	Lapsed		5/7/2003	5/7/2003	FR2854703	6/24/2005
ARTERIS	US	Cesar Douady Philippe Boucard	Device For Emulating One Or More Integrated-Circuit Chips		10/703,491	Abandoned	5/7/2003	11/10/2003	11/10/2003		
ARTERIS	EP	Cesar Douady Philippe Boucard	Device For Emulating One Or More Ic Chips	Last fee paid 3/31/06	04291088.5	Withdrawn	5/7/2003	4/27/2004	4/27/2004		
ARTERIS	FR	Philippe Boucard César Douady	System And Method For Communication Between Modules	Lapsed 3/31/08. Last fee paid 7/5/06.	FR0308225	Lapsed		7/4/2003	7/4/2003	FR2857114	9/30/2005
ARTERIS	US	Cesar Douady Philippe Boucard	System And Method For Communicating Between Modules		10/884,794	Abandoned	7/4/2003	7/2/2004	7/2/2004		
ARTERIS	EP	Cesar Douady Philippe Boucard	Communication System Between Different Modules And Method	Last fee paid 3/31/06	04291345.9	Withdrawn	7/4/2003	5/28/2004	5/28/2004		
ARTERIS	FR	Cesar Douady Jean-Jacques Lecler	Device And Method For Transmitting A Message	Lapsed 3/31/08. Last fee paid 7/5/06	FR0308905	Lapsed		7/22/2003	7/22/2003	FR2858148	9/30/2005
ARTERIS	US	Cesar Douady Jean-Jacques Lecler	Device And Method For Forwarding A Message		10/892,815	Abandoned	7/22/2003	7/16/2004	7/16/2004		
ARTERIS	EP	Cesar Douady Jean-Jacques Lecler	Device And Method For Transmitting A Message	Last fee paid 3/31/06	04291344.2	Withdrawn	7/22/2003	5/28/2004	5/28/2004		
ARTERIS	FR	Philippe Boucard Jean-Jacques Lecler Luc Montperrus	Device Digital Delay Oscillator Clock Generator Digital Signal And Memory Interface	Lapsed 6/30/10. Last fee paid 10/1/08	FR0311517	Lapsed		10/1/2003	10/1/2003	FR2860663	9/1/2006
ARTERIS	US	Luc Montperrus Philippe Boucard Jean-Jacques Lecler	Digital Delay Device, Digital Oscillator Clock Signal Generator And Memory Interface	1st Fee never paid	10/957,211	Expired	10/1/2003	10/1/2004	10/1/2004	7,148,728	12/12/2006

Applicant	Country	Inventors	Title	Fee Status	Application No.	Status	Priority Date	Date	Date	Patent No.	Date
ARTERIS	EP	Luc Montperrus Philippe Boucard Jean-Jacques Lecler	Digital Time Delay Apparatus, Digital Oscillator Generating Clock Signal And Memory Interface	Last fee paid 6/27/08	04292039.7	Withdrawn	10/1/2003	8/11/2004	8/11/2004		
ARTERIS	FR	Cesar Douady	Method For Managing A Data Storage Device Held In Queue And Related Device	Lapsed 8/31/09. Last fee paid 9/19/07	FR0314376	Lapsed		12/9/2003	12/9/2003	FR2863377	2/17/2006
ARTERIS	US	Cesar Douady Philippe Boucard	Method Of Managing A Device For Memorizing Data Organized In A Queue, And Associated Device		11/008,410	Abandoned	12/9/2003	12/9/2004	12/9/2004		
ARTERIS	TR	César Douady Philippe Boucard	System And Method For Transmission Of A Message Sequence In An Interconnection Network		04291339.2	Granted	5/28/2004 11/13/2003	4/13/2011	5/28/2004	EP1531589	4/13/2011
ARTERIS	CH	Cesar Douady Philippe Boucard	Method Of Managing A Storage Organised As Fifo, And Corresponding Apparatus		04292872.1	Lapsed	12/3/2004 12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
ARTERIS	FR	Cesar Douady Philippe Boucard	Method Of Managing A Storage Organised As Fifo, And Corresponding Apparatus	Last fee paid 4/30/08	04292872.1	Lapsed	12/3/2004 12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
ARTERIS	GB	Cesar Douady Philippe Boucard	Method Of Managing A Storage Organised As Fifo, And Corresponding Apparatus		04292872.1	Lapsed	12/3/2004 12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
ARTERIS S.A.	US	Philippe Martin Luc Montperrus	Smart Aging Retry Buffer		13/304,153	Granted	11/24/2010	11/23/2011	11/23/2011	8,522,104	8/27/2013
ARTERIS	LI	Cesar Douady Philippe Boucard	Method Of Managing A Storage Organised As Fifo, And Corresponding Apparatus		04292872.1	Lapsed	12/3/2004 12/9/2003	6/7/2006	12/3/2004	EP1542131	6/7/2006
ARTERIS	FR	Cesar Douady Philippe Boucard	Method And System For Transmission Of Messages In A Network Interconnection	Lapsed 10/29/08. Last fee paid 12/28/06	FR0400554	Lapsed		1/21/2004	1/21/2004	FR2865334	3/3/2006
ARTERIS	US	Cesar Douady Philippe Boucard	Method And System For Transmitting Messages In An Interconnection Network		11/039,112	Abandoned	1/21/2004	1/19/2005	1/19/2005		
ARTERIS	EP	Cesar Douady Philippe Boucard	Method And System For Transmitting Messages In An Interconnection Network	Last fee paid 3/31/06	04291340.0	Withdrawn	1/21/2004	5/28/2004	5/28/2004		
ARTERIS	FR	Luc Montperrus	System And Method For Data Transmission In An Electronic Circuit	Lapsed 2/26/10. Last fee paid 4/2/08	FR0505896	Lapsed		6/10/2005	6/10/2005	FR2887093	8/31/2007
ARTERIS	US	Luc Montperrus	System And Method Of Transmitting Data In An Electronic Circuit		11/179,014	Abandoned	6/10/2005	7/11/2005	7/11/2005		
ARTERIS SAS	US	Philippe Boucard Jean-Jacques Lecler Philippe Martin	Transport Of Pci-Ordered Traffic Over Independent Networks		13/337,280	Application		12/26/2011	12/26/2011		
ARTERIS	FR	Cesar Douady Philippe Boucard Luc Montperrus	System And Method For Communicating On Asynchronous Circuit Between Sub-Circuits Synchronous	Lapsed 5/31/11. Last fee paid 7/1/09	FR0509279	Lapsed		9/12/2005	9/12/2005	FR2890766	11/30/2007
ARTERIS	US	Cesar Douady Philippe Boucard Luc Montperrus	System And Method Of On-Circuit Asynchronous Communication, Between Synchronous Subcircuits		11/399,293	Abandoned	9/12/2005	4/6/2006	4/6/2006		

Owner	Country	Inventors	Title	Fee Note	App Number	Status	Priority	Filing	Publication	Patent No.	Date
ARTERIS	EP	Cesar Douady Philippe Boucard Luc Montperrus	Asynchronous Communication System And Method Between Synchronous Sub-Circuits On A Chip	Last fee paid 3/24/10	06290549.2	Withdrawn	9/12/2005	4/5/2006	4/5/2006		
ARTERIS	FR	Philippe Boucard Sophana Kok	System And Method For Static Routing Flow In A Packet Data Network Interconnection	Lapsed 7/31/09. Last fee paid 10/10/07	FR0511591	Lapsed		11/16/2005	11/16/2005	FR2893471	1/25/2008
ARTERIS	US	Sophana Kok Philippe Boucard	System And Method For The Static Routing Of Data Packet Streams In An Interconnect Network		11/407,391	Abandoned	11/16/2005	4/19/2006	4/19/2006		
ARTERIS	EP	Sophana Kok Philippe Boucard	System And Method For Static Packet Data Flow Routing In An Interconnection Network	Last fee paid 2/22/08	06290646.6	Withdrawn	11/16/2005	4/20/2006	4/20/2006		
ARTERIS	FR	César Douady Luc Montperrus	Method Of Producing A Circuit Timing Of Way Asynchronous Data Exchanged Between Two Blocks Synchronous And Timing Circuit Drawn From Such Method	Lapsed 1/29/10. Last fee paid 3/12/2008	FR0604348	Lapsed		5/16/2006	5/16/2006	FR2901437	8/8/2008
ARTERIS	US	César Douady Luc Montperrus	Process For Designing A Circuit For Synchronizing Data Asychronously Exchanged Between Two Synchronous Blocks, And Synchronization Circuit Fabricated By Same		11/483,419	Abandoned	5/16/2006	7/7/2006	7/7/2006		
ARTERIS	EP	César Douady Luc Montperrus	Method Of Creating A Circuit For Synchronising Data Exchanged Asynchronously Between Two Synchronous Blocks As Well As Synchronisation Circuit Made According To Such A Method	Last fee paid 3/31/2009	07105144.5	Withdrawn	5/16/2006	3/28/2007	3/28/2007		
ARTERIS	FR	Philippe Boucard Sophana Kok	Management System Messages Sent In A Network-On-Chip Interconnection	Lapsed 3/31/10. Last fee paid 7/11/2008	FR0606833	Lapsed		7/26/2006	7/26/2006	FR2904445	10/10/2008
ARTERIS	US	Sophana Kok Philippe Boucard	System For Managing Messages Transmitted In An On-Chip Interconnect Network		11/518,384	Abandoned	7/26/2006	9/8/2006	9/8/2006		
ARTERIS	EP	Sophana Kok Philippe Boucard	System For Managing Messages Transmitted In An Interconnection Network On A Chip		07112718.7	Withdrawn	7/26/2006	7/18/2007	7/18/2007		
ARTERIS	FR	Philippe Boucard Alain Fawaz	System And Method For Determining Performance Network Interconnection On Chip	Refusal 6/3/2009 Last fee paid 9/19/07	FR0655979	Abandoned		12/27/2006	12/27/2006	FR2911027	
ARTERIS	US	Philippe Boucard Alain Fawaz	System And Method For Determining The Performance Of An On-Chip Interconnection Network		11/749,908	Abandoned	12/27/2006	5/17/2007	5/17/2007		
ARTERIS	EP	Philippe Boucard Alain Fawaz	System And Method For Determining The Performance Of An Interconnection Network On A Chip		07291597.8	Withdrawn	12/27/2006	12/21/2007	12/21/2007		
ARTERIS SAS	US	Philippe Boucard	Link Between Chips Using Virtual Channels And Credit Based Flow Control		13/340,690	Application		12/30/2011	12/30/2011		

ARTERIS SAS	US	Philippe Boucard Jean-Jacques Lecler	Distributed Reorder Buffers		13/436,944	Application			3/31/2012	3/31/2012	
Arteris S.A.	US	Jonah Proujansky-Bell	Packet-Based On-Chip And Off-Chip Interconnects		61/409,054	Expired			11/1/2010	11/1/2010	
Arteris S.A.	US	Philippe Boucard Xavier Leloup	Parallel Synchronous Interface		61/411,430	Expired			11/8/2010	11/8/2010	
Arteris S.A.	US	Philippe Martin	Smart Aging Retry Buffer		61/417,136	Expired			11/24/2010	11/24/2010	
ARTERIS S.A.	US	Jean-Jacques Lecler Philippe Boucard	Memory Access Latency Metering		13/450,342	Application	4/18/2011	4/18/2012	4/18/2012	
ARTERIS S.A.S.	WO	Philippe Martin Luc Montperrus	Smart Aging Retry Buffer		PCT/IB2011/003095	Expired	11/24/2010	11/23/2011	11/23/2011	
ARTERIS S.A.S.	WO	Philippe Martin Luc Montperrus	Smart Aging Retry Buffer		PCT/US2011/062100	Expired	11/24/2010	11/23/2011	11/23/2011	
Arteris S.A.	US	Jean-Jacques Lecler Philippe Boucard	Memory Access Latency Metering For Network Interconnect		61/476,674	Expired		4/18/2011	4/18/2011	
ARTERIS S.A.	US	Daniel Michel Xavier Van Ruymbeke Pascal Godet	Display And Automatic Improvement Of Timing And Area In A Network-On-	OA response was filed.	13/487,087	Application	6/2/2011	6/1/2012	6/1/2012	
ARTERIS S.A.	US	Daniel Michel Xavier Van Ruymbeke Pascal Godet Xavier Leloup	Display And Automatic Improvement Of Timing And Area In A Network-On-Chip		61/492,709	Expired		6/2/2011	6/2/2011	
ARTERIS S.A.	US	Alain Fawaz Philippe Boucard Philippe Martin	Latency Probe		13/528,780	Application	6/22/2011	6/20/2012	6/20/2012	
ARTERIS S.A.	US	Philippe Boucard	Asynchronous Clock Adapter		13/539,185	Application	7/1/2011	6/29/2012	6/29/2012	
ARTERIS S.A.	US	Alain Fawaz Philippe Boucard	Latency Probe		61/500,078	Expired		6/22/2011	6/22/2011	
ARTERIS S.A.	US	Philippe Boucard	Asynchronous Power Disconnect		13/543,728	Application	7/6/2011	7/6/2012	7/6/2012	
ARTERIS SAS	US	Philippe Boucard Jean-Jacques Lecler Boris Boutillier	Network On a Chip Socket Protocol		13/626,766	Application		9/25/2012	9/25/2012	
ARTERIS S.A.	US	Philippe Boucard	Gals Asynchronous Clock Adapter		61/504,135	Expired		7/1/2011	7/1/2011	
ARTERIS SAS	US	Philippe Boucard Jean-Jacques Lecler	Network On a Chip Socket Protocol		13/626,758	Application		9/25/2012	9/25/2012	
ARTERIS S.A.	US	Philippe Boucard	Asynchronous Power Disconnect		61/504,948	Expired		7/6/2011	7/6/2011	
ARTERIS SAS	US	Laurent Moll, Jean-Jacques Lecler, Philippe	System Translation Look-Aside Buffer With Request-	8/17/2013 Conversion	61/684,705	Application		8/18/2012	8/18/2012	
ARTERIS SAS	US	Laurent Rene Moll Jean-Jacques Lecler	Integrated Circuits With Cache-Coherency		61/551,922	Expired		10/26/2011	10/26/2011	
ARTERIS SAS	US	Philippe Martin Boris Boutillier	System And Method For Automatically Inserting Safety Logic	9/27/2013 Conversion Deadline	61/707,659	Application		9/28/2012	9/28/2012	
ARTERIS SAS	US	Laurent Rene Moll	Simplified Controller With Partial Coherency	9/27/2013 Conversion	61/707,627	Application		9/28/2012	9/28/2012	
ARTERIS SAS	US	Jean-Jacques Lecler	A Three Channel Cache-Coherency Socket Protocol		61/551,917	Expired		10/26/2011	10/26/2011	
ARTERIS SAS	US	Laurent Rene Moll Jean-Jacques Lecler	Integrated Circuits With Cache-Coherency		13/659,850	Application	10/26/2011	10/24/2012	10/24/2012	
ARTERIS SAS	US	Jean-Jacques Lecler	A Three Channel Cache-Coherency Socket Protocol		13/659,781	Application	10/26/2011	10/24/2012	10/24/2012	
Arteris SAS	US	Philippe Martin	Differential Formatting Between Normal And Retry Data Transmission		61/630,391	Expired		12/8/2011	12/8/2011	
ARTERIS SAS	WO	Laurent Rene Moll Jean-Jacques Lecler	Integrated Circuits With Cache-Coherency		PCT/US2012/061981	Application	10/24/2012 10/26/2011	10/25/2012	10/25/2012	

ARTERIS SAS	WO	Jean-Jacques Lecler	A Three Channel Cache-Coherency Socket Protocol		PCT/US2012/0619 23	Application	10/24/2012 10/26/2011	10/25/2012	10/25/2012		
ARTERIS S.A.	US	Philippe Boucard Philippe Martin Jean-Jacques Lecler	Network-On-A-Chip With Quality-Of-Service Features		13/680,965	Application	7/13/2010 6/3/2010	11/19/2012	11/19/2012		
ARTERIS SAS	US	Jean-Jacques Lecler	Automatic Pipeline Stage Insertion		13/680,399	Application		11/19/2012	11/19/2012		
Arteris	US	Jean-Jacques Lecler Jonah Proujansky-Bell	Efficient Data Encoding On A Variable Width Link		61/582,005	Abandoned		12/30/2011	12/30/2011		
ARTERIS SAS	US	Laurent Rene Moll Jean-Jacques Lecler Philippe Boucard	Method And Apparatus For Supporting Target-Side Security In A Cache Coherent System		13/686,604	Application		11/27/2012	11/27/2012		
Arteris SAS	US	Jean-Jacques Lecler Boris Boutillier	Floorplan Estimator		61/581,639	Expired		12/30/2011	12/30/2011		
ARTERIS S.A.S.	GB	Philippe Boucard Philippe Martin Jean-Jacques Lecler	Network-On-A-Chip With Quality-Of-Service Features		GB1221555.4	Application	5/25/2011 6/3/2010	11/30/2012	5/25/2011		
ARTERIS S.A.S.	CN	Philippe Boucard Philippe Martin Jean-Jacques Lecler	Network-On-A-Chip With Quality-Of-Service Features		201180027348.4	Application	6/3/2010	12/3/2012	5/25/2011		
Arteris SAS	US	Jean-Jacques Lecler Philippe Martin Jonah Probell	Differential Formatting Between Normal And Retry Data Transmission		13/708,715	Application	12/8/2011	12/7/2012	12/7/2012		
ARTERIS SAS	WO	Jean-Jacques Lecler Philippe Martin	Differential Formatting Between Normal And		PCT/US2012/0686 25	Application	12/8/2011	12/7/2012	12/7/2012		
ARTERIS SAS	US	Jean-Jacques Lecler Boris Boutillier	Floorplan Estimation		13/723,186	Application	12/30/2011	12/20/2012	12/20/2012		
ARTERIS SAS	WO	Philippe Boucard Jean-Jacques Lecler	Transport Of Pci-Ordered Traffic Over Independent		PCT/US2012/0715 09	Application	12/26/2011	12/21/2012	12/21/2012		
ARTERIS SAS	WO	Jean-Jacques Lecler Boris Boutillier	Floorplan Estimation		PCT/US2012/0715 02	Application	12/30/2011	12/21/2012	12/21/2012		
ARTERIS SAS	WO	Philippe Boucard Monica Tang	Link Between Chips Using Virtual Channels And Credit Based Flow Control		PCT/US2012/0715 98	Application	12/30/2011	12/25/2012	12/25/2012		
ARTERIS S.A.S.	WO	Alain Fawaz Philippe Boucard Philippe Martin	Latency Probe		PCT/IB2012/05314 8	Application	6/20/2012 6/22/2011	12/27/2012	12/27/2012		
ARTERIS SAS	US	Jean-Jacques Lecler Laurent Rene Moll Jonah Proujansky-Bell	Last Level Cache		61/753,922	Application		1/17/2013	1/17/2013		
ARTERIS SAS	US	Boris Boutillier Philippe Martin	Description and Composition of Networks on Chips		61/784,078	Application		3/14/2013	3/14/2013		
ARTERIS SAS	US	Philippe Boucard Philippe Martin	Tunneling Within A Network-On-Chip		61/800,830	Application		3/15/2013	3/15/2013		
ARTERIS S.A.	US	Cesar Douady Philippe Boucard	Method And Device For Managing Priority During The Transmission Of A Message		13/868,614	Application	8/11/2004 8/13/2003	4/23/2013	4/23/2013		
Arteris S.A.S.	US	Daniel Michel Xavier Van Ruymbeke Pascal Godet	Display And Automatic Improvement Of Timing And Area In A Network-On-		13/898,540	Application	6/1/2012 6/2/2011	5/24/2013	5/24/2013		
Arteris S.A.S.	EP	Philippe Martin Luc Montperrus	Smart Aging Retry Buffer		11813552.4	Application		6/3/2013	11/23/2011		

ARTERIS SAS	GB	Philippe Boucard Jean-Jacques Lecler	DISTRIBUTED REORDER BUFFERS		GB1305241.0	Application		3/21/2013	3/21/2013	
ARTERIS SAS	CN	Philippe Boucard Jean-Jacques Lecler	Distributed Reorder Buffers		201310104136.2	Application		3/28/2013	3/28/2013	
ARTERIS SAS	US	Laurent Moll Jean-Jacques Lecler	Adaptive Tuning of Snoop Requests		13/938,651	Application	9/28/2012	7/10/2013	7/10/2013	
ARTERIS SAS	US	Laurent Moll Jean-Jacques Lecler	Adaptive Tuning of Snoop Requests		13/938,675	Application	9/28/2012	7/10/2013	7/10/2013	
ARTERIS SAS	US	Laurent Moll Jean-Jacques Lecler	Coherency Controller with Reduced Data Buffer		13/941,483	Application	9/28/2012	7/13/2013	7/13/2013	
ARTERIS SAS	US	Kurt Shuler Jonah Probell Monica Tang	Power Aware Thread Scheduling		61/845,991	Application	7/13/2013	7/13/2013	7/13/2013	
ARTERIS SAS	US	Boris Boutillier Jean-Jacques Lecler	Verification Using Generic Protocol Adapters		13/941,535	Application	7/14/2013	7/14/2013	7/14/2013	
ARTERIS SAS	US	Philippe Boucard Jean-Jacques Lecler Jonah Proujansky-Bell	Selective Change of Pending Transaction Urgency		13/941,537	Application	7/14/2013	7/14/2013	7/14/2013	
ARTERIS SAS	US	Laurent Moll Jean-Jacques Lecler Philippe Boucard	System Translation Look-aside Buffer with Request-Based Allocation and Prefetching		PCT/US13/55451	Application	8/18/2012	8/16/2013	8/16/2013	
ARTERIS SAS	US	Laurent Moll Jean-Jacques Lecler Philippe Boucard	System Translation Look-aside Buffer with Request-based Allocation and Prefetching		13/969,425	Application	8/18/2012	8/16/2013	8/16/2013	
ARTERIS SAS	US	Laurent Moll Jean-Jacques Lecler Philippe Boucard	System Translation Look-aside Buffer Integrated in an Interconnect		13/969,451	Application	8/18/2012	8/16/2013	8/16/2013	
ARTERIS SAS	US	Laurent Moll Jean-Jacques Lecler Philippe Boucard	DMA Engine with STLB Prefetch Capabilities and Tethered Prefetching		13/969,559	Application	8/18/2012	8/17/2013	8/17/2013	
ARTERIS SAS	US	Laurent Moll	STLB Prefetching for a Rotation Engine		13/969,562	Application	8/18/2012	8/17/2013	8/17/2013	
ARTERIS SAS	GB	Laurent Moll Jean-Jacques Lecler	Adaptive Tuning of Snoop Requests		GB1315339.0	Application	9/28/2012	8/29/2013	8/29/2013	
ARTERIS SAS	GB	Laurent Moll Jean-Jacques Lecler	Coherency Controller with Reduced Data Buffer		GB1315343.2	Application	9/28/2012	8/29/2013	8/29/2013	
ARTERIS SAS	PCT	Jean-Jacques Lecler Philippe Boucard Boris Boutilier	Network on a Chip Socket Protocol		PCT/US13/61295	Application	9/25/2012	9/24/2013	9/24/2013	
ARTERIS SAS	US	Laurent Moll	Simplified Controller with Partial Coherency		14/040,643	Application	9/28/2012	9/28/2013	9/28/2013	