

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT2610164

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
METAGENCE TECHNOLOGIES LIMITED	06/20/2012
RECEIVING PARTY DATA	
Name:	IMAGINATION TECHNOLOGIES LIMITED
Street Address:	IMAGINATION HOUSE
Internal Address:	HOME PARK ESTATE, KINGS LANGLEY
City:	HERTFORDSHIRE
State/Country:	UNITED KINGDOM
Postal Code:	WD4 8LZ
PROPERTY NUMBERS Total: 14	
Property Type	Number
Patent Number:	7206870
Patent Number:	7865637
Patent Number:	7139985
Patent Number:	7409533
Patent Number:	7673275
Patent Number:	7406584
Patent Number:	7801033
Patent Number:	7945803
Patent Number:	7577874
Patent Number:	7805638
Patent Number:	8103866
Application Number:	12477012
Application Number:	12502927
Application Number:	12699799

OP \$560.00 7206870

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	4999-0001
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NAME OF SUBMITTER:	KEVIN S. ROSS
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Signature:	/Kevin S. Ross/
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Date:	11/11/2013
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Total Attachments: 7

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source=Assignment2_Metagence-to-Imagination#page4.tif
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THIS PATENT ASSIGNMENT (the "Patent Assignment") is dated 20 June 2012 (the "PA Effective Date")

BETWEEN

- (1) METAGENCE TECHNOLOGIES LIMITED incorporated and registered in England and Wales whose registered office is at Imagination House, Home Park Estate, Kings Langley, WD4 8LZ, United Kingdom (**Assignor**).
- (2) IMAGINATION TECHNOLOGIES LIMITED incorporated and registered in England and Wales whose registered office is at Imagination House, Home Park Estate, Kings Langley, WD4 8LZ, United Kingdom (**Assignee**).

BACKGROUND

- (A) The Assignor is the proprietor of the Assigned Patents (as defined below).
- (B) By the Intra-Group Assignment of Intellectual Property Rights dated 20 June 2012 (the "Main Agreement") the Assignor has agreed to assign the Assigned Patents to the Assignee on the terms set out in this Patent Assignment.
- (C) Now therefore, in consideration of the promises and covenants set forth in the Main Agreement and for other good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, the Parties agree as follows:

AGREED TERMS

1. CONVEYANCE

Pursuant to and for the consideration set out in the Main Agreement, Assignor hereby sells, assigns, transfers, conveys, and delivers to Assignee all of Assignor's right, title and interest in, to and under the issued patents and patent applications listed in Exhibit 1, including all reissues, divisionals, continuations, continuations-in-part, revisions, re-examinations, extensions, and counterparts (whether foreign or domestic) claiming priority to or based on any of the foregoing items, together with all patents issuing therefrom, all inventions and improvements claimed or described in any of the foregoing, all rights to collect royalties and proceeds in connection with any of the foregoing (collectively, the "Assigned Patents"), and all rights to sue and bring other claims for past, present and future infringement, misappropriation or other violation of any of the foregoing and all rights to recover damages (including attorney's fees and expenses) or lost profits in connection therewith.

2. RECORDATION

Assignor hereby requests the United States Patent and Trademark Office Commissioner for Patents and any other applicable governmental entity or registrar (including any applicable foreign or international office or registrar), to record Assignee as the assignee and owner of the Assigned Patents.

3. INFORMATION AND ASSISTANCE

- 3.1 Upon Assignee's reasonable request and without further compensation, Assignor shall execute, acknowledge and deliver all such other instruments and documents and shall take all such other actions reasonably necessary or required by law to consummate and make fully effective the transaction contemplated by this Patent Assignment.

- 3.2 If Assignee is unable for any reason to secure Assignor's signature to any document required to file, prosecute, register or memorialize the assignment of any rights under any Assigned Patents as provided under this Patent Assignment, Assignor hereby irrevocably designates and appoints Assignee and Assignee's duly authorized officers and agents as Assignor's agents and attorneys-in-fact to act for and on Assignor's behalf and instead of Assignor to take all lawfully permitted acts to further the filing, prosecution, registration, memorialization of assignment, issuance and enforcement of rights under such Assigned Patents, all with the same legal force and effect as if executed by Assignor. The foregoing is deemed a power coupled with an interest and is irrevocable.

4. ENTIRE AGREEMENT

- 4.1 This Patent Assignment constitutes the whole agreement between the parties and supersedes all previous agreements between the parties relating to its subject matter.
- 4.2 Each party acknowledges that, in entering into this Patent Assignment, it has not relied on, and shall have no right or remedy in respect of, any statement, representation, assurance or warranty (whether made negligently or innocently) other than as expressly set out in this Assignment.
- 4.3 Nothing in this clause shall limit or exclude any liability for fraud.

5. VARIATION

No variation of this Patent Assignment shall be effective unless it is in writing and signed by the parties (or their authorised representatives).

6. SEVERANCE

- 6.1 If any court or competent authority finds that any provision of this Patent Assignment (or part of any provision) is invalid, illegal or unenforceable, that provision or part-provision shall, to the extent required, be deemed to be deleted, and the validity and enforceability of the other provisions of this Patent Assignment shall not be affected.
- 6.2 If any invalid, unenforceable or illegal provision of this Patent Assignment would be valid, enforceable and legal if some part of it were deleted, the parties shall negotiate in good faith to amend such provision such that, as amended, it is legal, valid and enforceable, and, to the greatest extent possible, achieves the parties' original commercial intention.

7. THIRD PARTY RIGHTS

No person other than a party to this Patent Assignment shall have any rights to enforce any term of this Patent Assignment.

8. GOVERNING LAW AND JURISDICTION

- 8.1 This Patent Assignment and any dispute or claim arising out of or in connection with it or its subject matter or formation (including non-contractual disputes or claims) shall be governed by and construed in accordance with the law of England and Wales.
- 8.2 The parties irrevocably agree that the courts of England and Wales shall have exclusive jurisdiction to settle any dispute or claim that arises out of or in connection with this Patent Assignment or its subject matter or formation (including non-contractual disputes or claims).

[SIGNATURE PAGE FOLLOWS]

IN WITNESS whereof the parties have entered into this Patent Assignment as of the PA Effective Date.

METAGENCE TECHNOLOGIES LIMITED

Signed: 

Authorised Representative

Name: R. SMITH

Date: 20 JUNE 2012

IMAGINATION TECHNOLOGIES LIMITED

Signed: 

Authorised Representative

Name: R. SMITH

Date: 20 JUNE 2012

Exhibit 1 – Assigned Patents

Title	Jurisdiction	Application No.	Date of Application	Publication No.	Publication Date	Patent No.
AUTOMATION COLOR CALIBRATION OF AN IMAGE SENSOR	Europe	EP520070717456	26 Jan 2007	EP1977615	8 Oct 2008	N/A
METHOD AND APPARATUS FOR SCALING SOWN A BAYER DOMAIN IMAGE	USA	USSH 11/341,712	26 Jan 2006	US2007172117	26 Jul 2007	7,742,636
IMAGE STABILIZATION METHOD	USA	USSH 11/407,450	19 Apr 2006	US2007248166	25 Oct 2007	7,746,934
IMAGE STABILIZATION IN A DIGITAL CAMERA	USA	USSH 12/843,746	26 Jul 2010	US2012019677	26 Jan 2012	N/A
ONE-DIMENSIONAL LENS SHADING CORRECTION	USA	USSH 11/407,449	19 Apr 2006	US2007247672	25 Oct 2007	7,561,306
METHOD AND SYSTEM TO REDUCE FLICKER ARTIFACTS IN CAPTURED IMAGES	USA	USSH 11/407,455	19 Apr 2006	US2007247531	25 Oct 2007	7,561,190
TWO-DIMENSIONAL LENS SHADING CORRECTION	USA	USSH 12/496,677	2 Jul 2009	US2011001848	6 Jan 2011	N/A
LENS SHADING CORRECTION FOR AUTOFOCUS AND ZOOM LENSES	USA	USSH 12/496,687	2 Jul 2009	US2011001854	6 Jan 2011	8,223,229
AUTOMATIC COLOR CALIBRATION OF AN IMAGE SENSOR	USA	USSH 11/341,323	27 Jan 2006	US2007177032	2 Aug 2007	7,586,521
METHOD AND APPARATUS FOR SCALING DOWN A BAYER DOMAIN IMAGE	Europe	EP20070717457	26 Jan 2007	EP1977372	8 Oct 2008	N/A

Title	Jurisdiction	Application No.	Date of Application	Publication No.	Publication Date	Patent No.
CORRECTION FOR GEOMETRIC DISTORTION IN IMAGES IN PIPELINED HARDWARE	USA	USSN 11/866,342	2 Oct 2007	US2009087115	2 Apr 2009	N/A
METHOD AND APPARATUS FOR SHARPENING DIGITAL IMAGES	USA	USSN 12/769,153	28 Apr 2010	US20111267484	3 Nov 2011	8,203,620
SKIN TONE DETECTION IN A DIGITAL CAMERA	USA	USSN 12/634,078	9 Dec 2009	US2011134275	9 Jun 2011	N/A
DIRECTIONAL CROSS HAIR SEARCH SYSTEM AND METHOD FOR DETERMINING A PREFERRED MOTION VECTOR	USA	USSN 12/477,012	2 Jun 2009	Unpublished	N/A	N/A
VIDEO NON-BUFFERED LINE MEMORY	USA	USSN 12/502,927	14 Jul 2009	Unpublished	N/A	N/A
METHOD AND SYSTEM FOR STAGGERED PARALLELIZED VIDEO DECODING	USA	USSN 12/699,799	3 Feb 2010	Unpublished	N/A	N/A
INTERACTIVE DEBUG SYSTEM FOR MULTIPROCESSOR ARRAY	USA	USSN 11/673,986	12 Feb 2007	US2007180323	2 Aug 2007	7,577,874
DATA INTERFACE REGISTER STRUCTURE WITH REGISTERS FOR DATA, VALIDITY, GROUP MEMBERSHIP INDICATOR, AND READY TO ACCEPT NEXT MEMBER SIGNAL	USA	USSN 10/871,347	18 Jun 2004	US2005005250	6 Jan 2005	7,206,870
SYSTEM OF HARDWARE OBJECTS	USA	USSN 10/871,329	18 Jun 2004	US2005015733	20 Jan 2005	7,865,637

Title	Jurisdiction	Application No.	Date of Application	Publication No.	Publication Date	Patent No.
DEVELOPMENT SYSTEM FOR AN INTEGRATED CIRCUIT HAVING STANDARDIZED HARDWARE OBJECTS	USA	USSH 11/466,337	22 Aug 2006	US2006282813	14 Dec 2006	7,673,275
SYSTEM OF VIRTUAL DATA CHANNELS IN AN INTEGRATED CIRCUIT	USA	USSH 11/340,957	27 Jan 2006	US2007025382	1 Feb 2007	7,801,033
ASYNCHRONOUS COMMUNICATION AMONG HARDWARE OBJECT NODES IN IC WITH RECEIVE AND SEND PORTS USING PROTOCOL REGISTERS USING TEMPORARY REGISTER BYPASS SELECT FOR VALIDITY INFORMATION	USA	USSH 11/326,701	6 Jan 2006	US2006117275	1 Jun 2006	7,409,533
DEVELOPMENT SYSTEM FOR AN INTEGRATED CIRCUIT HAVING STANDARDIZED HARDWARE OBJECTS	USA	USSH 10/871,311	18 Jun 2004	US2005055557	10 Mar 2005	7,139,985
IC COMPRISING NETWORK OF MICROPROCESSORS COMMUNICATING DATA MESSAGES ALONG ASYNCHRONOUS CHANNEL SEGMENTS USING PORTS INCLUDING VALIDITY AND ACCEPT SIGNAL REGISTERS AND WITH SPLIT/JOIN CAPABILITY	USA	USSH 11/466,083	21 Aug 2006	US2006282812	14 Dec 2006	7,406,584
MULTI-FREQUENCY DEBUG NETWORK FOR A MULTIPROCESSOR ARRAY	USA	USSH 11/676,206	16 Feb 2007	US2007180334	2 Aug 2007	7,805,638
CLOCK GENERATION FOR MULTIPLE CLOCK DOMAINS	USA	USSH 11/460,231	26 Jul 2006	US2007064852	22 Mar 2007	7,945,803

Title	Jurisdiction	Application No.	Date of Application	Publication No.	Publication Date	Patent No.
SYSTEM FOR RECONFIGURING A PROCESSOR ARRAY	USA	USSH 12/018,062	22 Jan 2008	US2008229093	18 Sep 2008	8,103,866
INTEGRATED CIRCUIT DEVELOPMENT SYSTEM	Singapore	SG 200507929-9	18 Jun 2004			SG 117884
INTEGRATED CIRCUIT DEVELOPMENT SYSTEM	Taiwan	TW20040138894	15 Dec 2004		21 Aug 2007	TW1285825
MOTION COMPENSATION IN DIGITAL VIDEO	USA	USSH 11/733,135		US2007237233		N/A
PROCESSOR HAVING MULTIPLE INSTRUCTION SOURCES AND EXECUTION	USA	USSH 11/672,450	7 Feb 2007	US2007169022	19 Jul 2007	N/A
INTEGRATED CIRCUIT DEVELOPMENT SYSTEM	-	CA20042527970	18 Jun 2004	CA2527970	29 Dec 2004	
INTEGRATED CIRCUIT DEVELOPMENT SYSTEM	-	EP20040755597	18 Jun 2004	EP1636725	22 Mar 2006	N/A
PROCESSOR HAVING MULTIPLE SOURCES AND EXECUTION MODES	-	EP20070800122	15 Aug 2007	EP2057554	13 May 2009	N/A