

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
ADVANCED MICRO DEVICES, INC.	10/27/2008
RECEIVING PARTY DATA	
Name:	BROADCOM CORPORATION
Street Address:	5300 CALIFORNIA AVENUE
City:	IRVINE
State/Country:	CALIFORNIA
Postal Code:	92617-3038
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	14058706
CORRESPONDENCE DATA	
Fax Number:	(770)951-0933
Phone:	7709339500
Email:	tina.schafer@thomashorstemeyer.com
<i>Correspondence will be sent via US Mail when the email attempt is unsuccessful.</i>	
Correspondent Name:	CHARLES W. GRIGGERS
Address Line 1:	400 INTERSTATE NORTH PARKWAY
Address Line 2:	SUITE 1500
Address Line 4:	ATLANTA, GEORGIA 30339
ATTORNEY DOCKET NUMBER:	50229-1701 (ADV. - BROD.)
NAME OF SUBMITTER:	CHARLES W. GRIGGERS
Signature:	/Charles W. Griggers/
Date:	12/03/2013

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Total Attachments: 20

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ASSIGNMENT OF PATENTS

ADVANCED MICRO DEVICES, INC., a Delaware corporation, located at One AMD Place, Sunnyvale, California USA 94088, **ATI TECHNOLOGIES ULC** (formerly ATI Technologies, Inc.), a wholly owned subsidiary of Advanced Micro Devices, Inc., located at 1 Commerce Valley Drive East, Markham, Ontario, Canada, and **ATI INTERNATIONAL SRL**, a subsidiary of ATI Technologies ULC, located at Beaumont House, Hastings, Chris Church, Barbados, West Indies (collectively, "Assignors"), hereby irrevocably sell, transfer, convey and assign to **BROADCOM CORPORATION**, a California corporation, located at 5300 California Ave., Irvine, California USA 92617 ("Assignee"), the entire right, title and interest for the United States of America and its territorial possessions, and all foreign countries and patent regions, including all rights of priority, in inventions disclosed in the patents, patent applications, and disclosure documents identified on Schedule A, all such patents, patent applications, and disclosure documents, and in and to all Letters Patents of the United States and all foreign countries and patent regions, which may or shall be granted on said inventions, or any parts thereof, and any divisional, continuing, reissue, reexamination, extension or other applications based in whole or in part thereon or which claim priority or are related by terminal disclaimer thereto or therefrom, including the right to recover for past infringement ("Assignment"), to the extent that any such Assignment is not granted by any of the Assignors to Assignee pursuant to a separate assignment document.

The Assignors acknowledge having received consideration for this Assignment and agree for said consideration to execute all applications, amended specifications, deeds or other instruments, and to do all acts necessary or proper to secure the grant of Letters Patent in the United States and in all other countries and patent regions to Assignee, to vest and confirm in said corporation, its successors and assigns, the legal title to all such patents, including any separate written forms of assignment necessary to perfect the Assignment in specific countries and patent regions.

The Assignors do hereby authorize and request the Commissioner of Patents and Trademarks of the United States, and the equivalent authority in each other country and patent region in the world, to issue such Letters Patent as shall be granted upon said inventions or applications based thereon to said Assignee, its successors and assigns.

[Remainder of page left intentionally blank. Signature pages to follow.]

PATENT
REEL: 022083 FRAME: 0444

PATENT
REEL: 031706 FRAME: 0079

Witness my hand and seal this 27th day of October 2008.

ADVANCED MICRO DEVICES, INC.

Faina Medzonsky

Name: Faina Medzonsky

Title: Assistant General Counsel and
Assistant Secretary

STATE OF CALIFORNIA

City of Sunnyvale)
)
County of Santa Clara)

On October 27, 2008 before me, Rita S. Marquis, Notary Public in and for the State of California, personally appeared Faina Medzonsky, who proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that she executed the same in her authorized capacity and that by her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Rita S. Marquis
Notary Public

[Signature page 1 to U.S. Patent Assignment]

PATENT
REEL: 022083 FRAME: 0445

PATENT
REEL: 031706 FRAME: 0080

Witness my hand and seal this 27th day of October 2008.

ATI TECHNOLOGIES ULC

Faina Medzonsky
Name: Faina Medzonsky
Title: Secretary

STATEMENT OF WITNESS

I, Rita S. Marquis, whose full post office address is
(name of witness)
One AMD Place, Sunnyvale, California, U.S.A.
(address of witness)

state that I was personally present and did see Faina Medzonsky, who is personally known to me to be the person named in the above assignment, duly execute the same on the day and year set forth above.

Rita S. Marquis
(signature of witness)

DATED at Sunnyvale, California, this 27th day of October 2008.

[Signature page 2 to U.S. Patent Assignment]

PATENT
REEL: 022083 FRAME: 0446

PATENT
REEL: 031706 FRAME: 0081

Witness my hand and seal this 27th day of October 2008.

ATI INTERNATIONAL SRL

Faina Medzonsky

Position: Assistant General Counsel and
Assistant Secretary, Advanced Micro Devices, Inc.

STATEMENT OF WITNESS

I, Rita S. Marquis, whose full post office address is

(name of witness)

One AMD Place, Sunnyvale, California, U.S.A.

(address of witness)

state that I was personally present and did see Faina Medzonsky, who is personally known to me to be the person named in the above assignment, duly execute the same on the day and year set forth above.

Rita S. Marquis

(signature of witness)

DATED at Sunnyvale, California, this 27th day of October 2008.

PATENT
REEL: 022083 FRAME: 0447

PATENT
REEL: 031706 FRAME: 0082

SCHEDULE A

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Remote-control device with directional audio system	USA	11/839,798 08/16/07		
System and Method for Transferring a Multidimensional Array of Data to a Non-Contiguous Buffer	USA	11/451,322 06/13/06	US 2008/0003499	01/03/08
A system and method of feature emphasis and de-emphasis in image processing	USA	61/071,645	None	N/A
Method and Apparatus for Managing Tasks in a Multiprocessor System	USA	10/939,804 09/13/04	2006/0059484 A1	03/16/06
SIMD Processor with Register Addressing, Buffer Stall and Methods	USA	10/929,992 08/30/04	2006/0047937 A1	03/02/06
SIMD Processor with Register Addressing, Buffer Stall and Methods	USA	12/231,336 08/29/08		
Trellis Decoder for Decoding Data Stream including Symbols Coded with Multiple Convolutional Codes	USA	10/973,486 10/26/04	2006/0088119 A1	04/27/06
SIMD Processor having Enhanced Operand Storage Interconnects	USA	10/940,150 09/13/04	2006/0095712 A1	05/04/06
SIMD Processor Executing Min/Max Instructions	USA	10/940,123 09/13/04	2006/0095739	05/04/06
SIMD Processor Executing Min/Max Instructions	USA	12/231,891 09/05/08		
Digital Receiver Having Adaptive Carrier Recovery Circuit	USA	10/757,536 01/15/04	7,342,981	03/11/08
Audio/Video Separator	USA	10/812,279 03/29/04	2005/0212977 A1	09/29/05

PATENT
REEL: 022083 FRAME: 0448

PATENT
REEL: 031706 FRAME: 0083

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Audio/video separator	EP	05251940.2 03/29/05	EP 1585322 NA	10/12/05 NA
Worldwide Analog Television Signal Receiver	USA	10/940,153 09/13/04	7,394,500	07/01/08
World wide analog television signal receiver	EP	05255575.2 09/12/05	EP 1635560 NA	03/15/06 NA
Film-Mode (3:2/2:2 Pulldown) Detector, Method and Video Device	USA	10/837,835 05/03/04	2005/0243215	11/03/05
Film-mode (3:2/2:2 Pulldown) detector, method and video device	PCT	PCT (B2005001286 05/02/05	WO 2005/107253 NA	11/10/05 NA
Film-mode (3:2/2:2 Pulldown) detector, method and video device	EP	05739740.8 05/02/05	EP 1743482 NA	11/10/05
Low Latency Digital Filter and Method	USA	11/090,478 03/25/05	2006/0215746	09/28/06
Method and Apparatus for Using the Host-Pod Interface of a Digital Television or Other Digital Audio/Video Receiver for New Services	USA	11/306,317 12/22/05	2007/0146542 A1	06/28/07
Method and apparatus for using the host-pod interface of a digital television or other digital audio/video receiver for new services	PCT	PCT/IB06/003814 12/21/06	WO 07/072211 NA	06/28/07 NA
Method and apparatus for using the host-pod interface of a digital television or other digital audio/video receiver for new services	EP	06831812.0 12/21/06	EP 1972132 NA	06/28/07 NA
Method and apparatus for using the host-pod interface of a digital television or other digital audio/video receiver for new services	INDIA	3526/CHENP/2008 07/04/08		

PATENT
REEL: 022083 FRAME: 0449

PATENT
REEL: 031706 FRAME: 0084

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Methods and Apparatus for Processing Variable Length Coded Data	USA	11/046,048 01/28/05 Claims priority from Provisional 60/609,511	7,132,963	11/07/06
Methods and Apparatus for Processing Variable Length Coded Data	USA	11/538,336 10/03/06 Claims priority from Provisional 60/609,511	7,385,534	06/10/08
Methods and Apparatus for Processing Variable Length Coded Data	USA	12/133,489 06/05/2008 Claims priority from Provisional 60/609,511	n/a	n/a
Video Receiver Providing Video Attributes with Video Data	USA	11/402,902 04/13/06	2007/0262979 A1	11/15/07
Video Receiver Providing Video Attributes with Video Data	PCT	PCT/IB2007/001115 04/13/07	WO2007/119171	10/25/07
Image Analyser and Adaptive Image Scaling Circuit and Methods	USA	11/619,358 01/03/07 Claims priority from Provisional 60/755,084	n/a	n/a
Image analyser and adaptive image scaling circuit and methods	PCT	PCT/IB2007/000006 01/03/07	WO2007/077520 NA	07/12/07 NA
Video Receiver Providing Video Attributes with Video Data	EP		inventor: Ilya Klebanov AMD File No.: 050039EP	

PATENT
REEL: 022083 FRAME: 0450

PATENT
REEL: 031706 FRAME: 0085

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Video Receiver Providing Video Attributes with Video Data	CHINA		Inventor: Ilya Klebanov AMD File No.: 050039CN	
Video Receiver Providing Video Attributes with Video Data	JAPAN		Inventor: Ilya Klebanov AMD File No.: 050039JP	
Image analyser and adaptive image scaling circuit and methods	EP	07713010.2 01/03/07 Claims priority from Provisional 60/755,084	EP1977391 NA	07/12/07 NA
Enhanced Video Processing Using Motion Vector Data	USA	12/032,796 02/28/08	n/a	n/a
Low Latency Cadence Detection for Frame Rate Conversion	USA	11/616,192 12/26/06	2008/0151109 A1	06/26/08
Low Latency Cadence Detection for Frame Rate Conversion	PCT	PCT/US2007/08882 1 12/26/07	WO2008/083156	07/10/08
Video Processor Architecture and Method for Frame Rate Conversion	USA	11/616,188 12/26/06	2008/0151108 A1	06/26/08
Video Processor Architecture and Method for Frame Rate Conversion	PCT	PCT/US2007/088813 12/26/07	WO2008/083151	07/10/08
Harmonic Edge Synthesizer, Enhancer and Methods	USA	11/619,366 01/03/07 Claims priority from Provisional 60/755,083	2007/0152991 A1	07/05/07

PATENT
REEL: 022083 FRAME: 0451

PATENT
REEL: 031706 FRAME: 0086

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Harmonic edge synthesizer, enhancer and methods	PCT	PCT/IB07/000015 01/03/07	WO 2007/077524 NA	07/12/07 NA
Harmonic edge synthesizer, enhancer and methods	EP	07700444.8 01/03/07 Claims priority from Provisional 60/755,083	EP1974324 NA	07/12/07 NA
Device and Methods for Reducing Visual Artifacts in Color Images	USA	11/848,366 08/31/07	n/a	n/a
Adaptation of Frame Rate Converter Output Target Display Times Based on Video Content Detection	USA	61/071,470 04/30/08	n/a	n/a
Apparatus and Method for Synchronizing an SCDMA Upstream or any other...	USA	09/074,036 05/06/98	6,243,369	06/05/01
Apparatus and Method for Synchronizing an SCDMA Upstream or any other...	CANADA	04/29/99	2,270,721	07/29/03
Apparatus and Method for Synchronizing an SCDMA Upstream or any other...	GERMANY		69924537.5	04/06/05
Apparatus and method for synchronizing an SCDMA upstream or any other type upstream to an MCNS downstream or any other type downstream with a different clock rate than the upstream	EP	99660075.5 05/06/99	EP 0955742 EP 0955742	11/10/99 04/06/05
Apparatus and method for synchronizing an SCDMA upstream or any other type upstream to an MCNS downstream or any other type downstream with a different clock rate than the upstream	EP div	05075778.0 05/06/99	EP 1553716 NA	07/13/05 NA
Method for Synchronizing an SCDMA Upstream...	FRANCE		0955742	04/06/05
Method for Synchronizing an SCDMA Upstream...	JAPAN		3380768	12/13/02
Apparatus and Method for Synchronizing an SCDMA Upstream...	USA	09/798,845 03/01/01	6,353,604 B2	03/05/02

PATENT
REEL: 022083 FRAME: 0452

PATENT
REEL: 031706 FRAME: 0087

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Two Dimensional Interleave Process for CDMA...	USA	09/152,643 09/14/98	6,466,564	10/15/02
Two Dimensional Interleave Process for CDMA...	CANADA	09/09/99	2,281,480	05/11/04
Two dimensional interleave process for CDMA transmissions of one dimensional timeslot data	EP	99660147.2 09/14/99	EP 0987850 EP 0987850	03/22/00 12/20/06
Two dimensional interleave process for CDMA transmissions of one dimensional timeslot data	GB	99660147.2 09/14/99	EP 0987850 EP 0987850	03/22/00 12/20/06
Two dimensional interleave process for CDMA transmissions of one dimensional timeslot data	FRANCE	99660147.2 09/14/99	EP 0987850 EP 0987850	03/22/00 12/20/06
Two dimensional interleave process for CDMA transmissions of one dimensional timeslot data	GERMANY	99660147.2 09/14/99	69934465.4 EP 0987850	03/22/00 12/20/06
Two dimensional interleave process for CDMA transmissions of one dimensional timeslot data	EPdiv	06026316.7 09/14/99	EP1760891 NA	03/07/07 NA
Mixed DOCSIS 1.0 TDMA Burst with SCDMA...	USA	09/337,167 06/21/99	6,459,703	10/01/02
Mixed DOCSIS 1.0 TDMA bursts with SCDMA transmissions on the same frequency channel	EP	00660117.3 06/21/00	EP 1063801 EP 1063801	12/27/00 04/25/07
Mixed DOCSIS 1.0 TDMA bursts with SCDMA transmissions on the same frequency channel	GB	00660117.3	EP 1063801 EP 1063801	12/27/00 04/25/07
Mixed DOCSIS 1.0 TDMA bursts with SCDMA transmissions on the same frequency channel	FRANCE	00660117.3	EP 1063801 EP 1063801	12/27/00 04/25/07
Mixed DOCSIS 1.0 TDMA bursts with SCDMA transmissions on the same frequency channel	GERMANY	00660117.3	60034511.4 EP 1063801	04/25/07
Mixed DOCSIS 1.0 TDMA Burst with SCDMA...	JAPAN	06/20/99	3401479	02/21/03

PATENT
REEL: 022083 FRAME: 0453

PATENT
REEL: 031706 FRAME: 0088

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Method and Apparatus for Faster Block Size...	USA	09/648,278 08/23/00	6,848,036	01/25/05
Method and Apparatus to Improve SCDMA...	USA	09/974,202 10/09/01	7,002,899 B2	02/21/06
Method and Apparatus to Improve SCDMA...	USA	11/326,225 01/04/06		
Down-Stream Transmitter and Cable...	USA	10/944,962 09/18/04	20060085727	04/20/06
Downstream transmitter and cable modem receiver for 1024 QAM	PCT	PCT/TB2005/003086 09/16/05	WO2006/030317 NA	03/23/06 NA
Downstream transmitter and cable modem receiver for 1024 QAM	EP	05797961.9 09/16/05	EP 1790140 NA	03/23/06 NA
Downstream transmitter and cable modem receiver for 1024 QAM	USA	12/286,337 09/29/08		
Blind Cost Criterion Timing Recovery	USA	09/761,303 01/17/01 Claims priority from Provisional 60/242,306	6,983,549	01/10/06
Carrier Phase Estimation Based On Single-Axis Constant Modulus Cost Criterion And Bussgang Criteria	USA	09/922,006 08/03/01 Claims priority from Provisional 60/248,941	7,031,405	04/18/06

PATENT
REEL: 022083 FRAME: 0454

PATENT
REEL: 031706 FRAME: 0089

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Linear Prediction Based Initialization of A Single-Axis Blind Equalizer for VSB Modulations	USA	10/016,665 12/11/01 Claims priority from Provisional 60/234,829	7,027,500	04/11/06
DC Offset Correction For Constant Modulus Equalization	USA	10/213,854 08/07/02 Claims priority from Provisional 60/313,161	7,349,469	03/25/08
Rate Control for Digital Video Compression Processing	USA	11/049,103 02/02/03	US-2006- 0171457-A1	08/03/06
Picture adjustment methods and apparatus for image display device	USA	11/504,496 08/15/06	20080043031A1	2/21/08
Picture adjustment methods and apparatus for image display device	PCT	PCT/US07/74774 07/30/07	2008021710	2/21/08
Method of estimating trellis encoded symbols utilizing simplified trellis decoding	USA	09/099,730 06/19/98	6,178,209	01/23/01
Reduced complexity equalizer for multi mode signaling	USA	09/100,705 06/19/98	6,426,972	07/30/02
Data slicer for combined trellis decoding and equalization	USA	09/716,651 11/20/00	6,850,563	02/01/05
Dual loop automatic gain control	USA	10/253,245 09/24/02	7,116,955	10/3/06
Vertex data processing with multiple threads of execution	USA	09/632,759 8/4/00	6,963,347	11/8/05

PATENT
REEL: 022083 FRAME: 0455

PATENT
REEL: 031706 FRAME: 0090

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Vertex data processing with multiple threads of execution	USA	11/250,754 10/14/05	US 2006-0033757 A1	02/16/06
Memory request interlock	USA	09/638,550 8/14/00	6,810,470	10/26/04
Method and apparatus for detecting and correcting modulated signal impairments (phase noise)	USA	11/947,090 11/29/07 Claims priority from Provisional 60/867,687 and 60/884,874	US-2008- 0123788-A1	05/29/08
Method and apparatus for detecting and correcting modulated signal impairments (burst noise)	USA	11/947,124 11/29/07 Claims priority from Provisional 60/867,687 and 60/884,874	US-2008- 0123787-A1	05/29/08
Method and apparatus for detecting and correcting modulated signal impairments (gain noise)	USA	11/947,105 11/29/07 Claims priority from Provisional 60/867,687 and 60/884,874	US-2008- 0123786-A1	05/29/08
Method to detect and mitigate impulse noise on receivers	USA	11/775,836 7/10/07		
Method to detect and mitigate impulse noise on receivers	EP	08252329.1 07/08/08	NA NA	NA NA
Framebuffer sharing for video processing	USA	11/847,802 08/30/07 Claims priority from Provisional 60/841,404	US-2008- 0165287-A1	07/10/08

PATENT
REEL: 022083 FRAME: 0456

PATENT
REEL: 031706 FRAME: 0091

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Framebuffer sharing for video processing	PCT	PCT/US07/19136 08/30/07 Claims priority from Provisional 60/841,404	WO2008/027508	03/06/08
High performance embedded memory and I/O bridge design in SOC	USA	11/871,120 10/11/07 Claims priority from Provisional 60/824,112		
Method and system for motion compensated noise reduction	USA	11/847,820 08/30/07 Claims priority from Provisional 60/824,191	US-2008- 0055477-A1	03/06/08
Unified memory architecture for T-CON end back-end video processor ICS (Integrated Circuits)	USA	11/874,659 10/18/07 Claims priority from Provisional 60/829,893	US-2008- 0198937-A1	08/21/08
Video processing data provisioning	PCT	PCT/US07/22305 10/18/07 Claims priority from Provisional 60/829,893	WO2008/048682	04/24/08
Halo artifact removal method	USA	11/960,991 12/20/07		
Method and apparatus for impulse noise detection and suppression for DVB-T	USA	11/964,373 12/26/07		

PATENT
REEL: 022083 FRAME: 0457

PATENT
REEL: 031706 FRAME: 0092

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Method and apparatus for reduced complexity video processing via special chroma handling	USA	12/126,321 05/23/08		
H.264 data processing	USA	11/897,714 08/30/07 Claims priority from Provisional 60/842,001	US-2008-0165860-A1	07/10/08
Video stream timing detection	USA	11/839,361 08/15/07 Claims priority from Provisional 60/824,204	US-2008-0143876-A1	06/19/08
Video decoder block	USA	11/847,879 08/30/07 Claims priority from Provisional 60/824,239	US-2008-0056357-A1	03/6/08
Neighboring context management	USA	11/842,901 08/21/07 Claims priority from Provisional 60/841,806	US-2008-0056377-A1	3/6/2008
Method and system for providing error resiliency	USA	11/735,085 4/13/2007		
A system and method of motion vector estimation using content associativity	USA	11/966,095 12/28/07		
Hybrid memory compression scheme for decoder bandwidth reduction	USA	11/971,045 01/08/08		

PATENT
REEL: 022083 FRAME: 0458

PATENT
REEL: 031706 FRAME: 0093

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Method and System for Receiving and Framing Packetized Data	USA	09/490,350 01/24/00	6,763,390	07/13/04
Method and System for Receiving and Framing Packetized Data	USA	10/818,812 04/6/04	7,376,692	05/20/08
Method and System for Improved Display Filtering	USA	09/790,955 02/22/01	6,710,819	03/23/04
Method and Apparatus for Processing Video and Graphics Data	USA	09/923,516 080/7/01	6,828,987	12/07/04
Tiled Memory Configuration for Mapping Video Data and Method Thereof	USA	09/923,520 08/07/01	7,016,418	02/21/06
System For Testing Multiple Devices on a Single System and Method Thereof	USA	09/923,524 08/07/01	7,253,818	080/7/07
Memory Controller for Handling Multiple Clients and Method Thereof	USA	11/627,585 01/26/07	2007/0124793 A1	05/31/07
Method and System for Accessing Packetized Elementary Stream Data	USA	09/489,682 01/24/00	6,778,533	08/17/04
Method and System for Retrieving Adaptation Field Data Associated with a Transport Packet	USA	09/490,207 01/24/00	6,785,336	08/31/04
Method and Apparatus for Handling Private Data from Transport Stream Packets	USA	09/491,122 01/24/00	6,804,266	10/12/04
Method for Displaying Data	USA	09/489,681 01/24/00	6,999,424	2/14/06
Method and System for Generating Transport Stream Packets	USA	11/333,811 01/17/06	2006/0209709 A1	09/21/06
A Method and System for Handling Errors and a System for Receiving Packet Stream Data	USA	09/489,669 01/24/00	6,988,238	01/17/06
Method and System for Handling Errors	USA	10/919,943 08/17/04	7,366,961	04/29/08

PATENT
REEL: 022083 FRAME: 0459

PATENT
REEL: 031706 FRAME: 0094

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Method and System for Handling Data Packets	USA	09/791,045 2/22/2001	6,807,585	10/19/04
Method and System for Handling Errors	USA	12/031,584 02/14/08	N/A	N/A
System for Controlling a Clock Signal for Synchronizing a Counter to a Received Value and Method Thereof	USA	09/563,209 5/2/2000	6,674,805	01/06/04
System for Handling compressed Video Data and Method Thereof	USA	09/563,211 5/2/2000	7,113,546	09/26/06
System for Bit-Rate Controlled Digital Stream Playback and Method Thereof	USA	09/800,226 3/6/2001	6,907,481	06/14/05
System for Digitized Audio Stream Synchronization and Method Thereof	USA	09/800,224 3/6/2001	7,030,930	04/18/06
System for Digital Stream Transmission and Method Thereof	USA	09/800,223 03/06/01	7,047,330	05/16/06
System for Frame Based Audio Synchronization and Method Thereof	USA	09/833,493 04/11/01	7,130,316	10/31/06
Method for synchronizing to a data stream	USA	09/491,119 01/24/00	6,885,680	04/26/05
Systems, methods, and apparatus for synchronization of audio and video signals	USA	11/754,096 05/25/2007 Claims priority from Provisional 60/808,554	20070276670A1	11/29/07
Method and apparatus for deinterlacing video	USA	09/539,572 03/31/2000	6,680,752	01/20/04
Method for deinterlacing interlaced video by a graphics processor	USA	09/553,143 04/20/2000	6,970,206	11/29/05

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REEL: 031706 FRAME: 0095

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Diversity receiver with joint signal processing	USA	09/363,813 07/30/1999	6,560,299	05/06/03
Diversity receiver with joint signal processing	USA	10/384,048 03/07/03	6,970,523	11/29/05
Diversity receiver with joint signal processing	USA	10/409,371 04/09/03	6,870,892	03/22/05
Diversity receiver with joint signal processing	USA	10/408,881 04/09/03	6,937,677	08/30/05
Method and apparatus for generating a video overlay	USA	09/678,362 10/03/00	6,788,309	09/07/04
Method and apparatus for reducing instruction dependencies in extended SSA form instructions	USA	10/954,586 09/30/04	2006-0070050	03/30/06
Automatic audio and video synchronization	USA	10/907,073 03/18/05	2006-0209210	09/21/2006
Automatic Audio and Video Synchronization	PCT	PCT/ IB 2006/ 000774 03/17/06	WO 2006/097845 NA	09/21/06 NA
Automatic Audio and Video Synchronization	EP	06727417.5 03/17/06	EP 1864483 NA	09/21/06 NA
Automatic Audio and Video Synchronization	CHINA	200680008821.3 03/17/06	101204081 NA	06/18/08 NA
Receiver for robust data extension for 8VSB signaling	USA	10/251,650 09/20/02	7,194,047	03/20/07
Receiver for robust data extension for 8VSB signaling	USA	11/725,571 03/19/07	2007-237263	10/11/07

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REEL: 031706 FRAME: 0096

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Hybrid Soft and Hard Decision Feedback Equalizer	USA	09/549,368 04/14/00 Claims Priority from Provisional 60/129,478	7,006,565	02/28/06
Adaptive Equalizer with Enhanced Error Quantization	USA	09/231,888 01/14/99	6,418,164	07/09/02
Digital Pixel Clock Generation Circuit and Method Employing Independent Clock	USA	10/908,429 05/11/05	2006-0256102	11/16/06
Equalizer method and apparatus using constant modulus algorithm blind equalization and partial decoding	USA	09/457,496 12/09/99	6,668,014	12/23/03
Adaptive equalizer with decision directed constant modulus algorithm	USA	09/261,843 03/03/99	6,337,878	01/08/02
Method and apparatus for encoding video color information	USA	08/761,642 12/06/96	6,124,899	09/26/00
Intra motion prediction for response time compensation	USA	11/864,362 09/28/07		
Response time compression using a complexity value of image information	USA	11/864,391 09/28/07		
Compression Method and Apparatus for Response Time Compensation	USA	11/864,412 09/28/07		
Response time compression using a complexity value of image information	PCT	PCT/CA2008/001715 09/26/08		
Spread spectrum fifo and method for storing data for multiple display types	USA	09/484,367 01/14/00	6,580,432	06/17/03

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REEL: 022083 FRAME: 0462

PATENT
REEL: 031706 FRAME: 0097

Title	Country	Application Number/ Filing Date	Pub. No./ Patent No.	Pub. Date/ Issue Date
Method and apparatus for processing image data	USA	12/144,018 06/23/08		
Image processing method and system with repetitive pattern detection	USA	61/129,916 07/29/08		
Method and apparatus for transmitting and using picture descriptive information in a frame rate conversion processor	USA	12/174,738 07/17/08		
Null detection and erasure decoding for frequency selective channel in a broadcasting system	USA	12/143,434 06/20/08		
Frame Rate Converter For Input Frames With Video and Film Content (Formerly: "60 Hz Text on Film")	USA	8/6/08		

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PATENT
REEL: 022083 FRAME: 0463

RECORDED: 12/03/2013

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REEL: 031706 FRAME: 0098