PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
PS3 LUXCO SARL	08/21/2013

RECEIVING PARTY DATA

Name:	PS2 LUXCO SARL
Street Address:	208, VAL DES BONS MALADES
City:	GRAND DUCHY OF LUXEMBOURG
State/Country:	LUXEMBOURG
Postal Code:	L-2121

PROPERTY NUMBERS Total: 24

Property Type	Number
Patent Number:	6510488
Patent Number:	8244958
Patent Number:	6763424
Patent Number:	6968421
Patent Number:	7818490
Patent Number:	7657702
Patent Number:	7970987
Patent Number:	8316177
Patent Number:	7120729
Patent Number:	7552272
Patent Number:	7594135
Patent Number:	7962777
Patent Number:	7012835
Patent Number:	7224607
Patent Number:	7518919

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Patent Number:	8004895
Patent Number:	8050095
Patent Number:	6985992
Patent Number:	6831865
Patent Number:	7181611
Patent Number:	7809962
Patent Number:	6973531
Patent Number:	7096313
Patent Number:	7173852

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ATTORNEY DOCKET NUMBER:	PS3-PS2 ASSIGN AGREEMENT
NAME OF SUBMITTER:	STEPHEN A. TERRILE
Signature:	/Stephen A. Terrile/
Date:	12/06/2013

Total Attachments: 32
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ASSIGNMENT AND AGREEMENT

THIS ASSIGNMENT AND AGREEMENT (this "**Agreement**"), is entered into by and between PS3 Luxco Sarl., having the principal place of business at 208, Val des Bons Malades, L-2121, Luxembourg ("**Assignor**"), and PS2 Luxco Sarl, having the principal place of business at 208, Val des Bons Malades, L-2121, Luxembourg ("**Assignee**").

For good and valuable consideration, the receipt of which is hereby acknowledged, **Assignor** does hereby sell, assign, transfer and convey unto **Assignee,** its successors, assigns or its designees and legal representatives, **Assignor's** entire right, title and interest that exists today and may exist in the future in and to any and all of the patents and applications listed in the Appendix hereof ("**Patents**"), the inventions described therein, and any other governmental grants or issuances throughout the world which directly or indirectly claim priority from **Patents** or for which any of the **Patents** directly or indirectly forms a basis for priority, including but not limited to: (1) any patents, registrations, or certificates of invention that have issued or may issue in the future on any patent applications listed in the Appendix; (2) any and all counterpart United States, international, and foreign patents, applications, utility models, industrial design protection, design patent protection, and certificates of invention based upon, covering, or otherwise related to any portion of the foregoing; (3) any expired or abandoned applications or patents based upon, covering, or otherwise related to any portion of the foregoing; (4) patents or other governmental rights that may be granted in the future based upon, covering, or otherwise related to any portion of the foregoing; (5) any continuations, continuations-in-part, divisionals, provisionals, renewals, and extensions of any of the foregoing; (6) all reissues and reexamination certificates of any of the foregoing, and(7) all applications or patents subject to or resulting from any pending inter-partes review, supplemental examination, post-grant review, derivation proceedings, or other review proceeding of any of the foregoing(the foregoing are collectively referred to herein as the "**Patent Rights**").

Assignor further agrees to and hereby does sell, assign, transfer and convey unto **Assignee** all rights: (i) in and to causes of action and enforcement rights for the

Patent Rights (whether known or unknown or whether currently pending, filed, or otherwise), including, without limitation, all rights to pursue damages, injunctive relief and other remedies for past, current and future infringement of the **Patent Rights and to license the Patent Rights**; (ii) to apply in any or all countries of the world for patents, certificates of invention or other governmental grants for the **Patent Rights**, including, without limitation, to invoke and claim for any application for patent or other form of protection for said **Patents** filed by it or them, the benefit of the right of priority provided by under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement or understanding and to invoke and claim such right of priority without further written or oral authorization from **Assignor**; (iii) to assert the attorney/client and/or solicitor/client privilege associated with the prosecution of the **Patent Rights and/or work product immunity associated with enforcement of the Patent Rights**. Assignor further agrees to assert the attorney/client and/or solicitor/client privilege and/or work product immunity to the fullest extent of the law to promote enforcement of the patents; (iv) to collect royalties or other payments under or on account of the Patent Rights; and (v) of cooperation assigned or granted by any third party with respect to the Patent Rights. **Assignor** also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents or certificates of invention which may be granted upon any of the **Patent Rights** in the name of the **Assignee**, as the assignee to the entire interest therein.

Assignor agrees that, when requested, **Assignor** without any further consideration will (i) sign all papers, cause all rightful oaths to be taken, and do or cause to be done all acts which may be necessary, desirable or convenient for securing, maintaining, and enforcing said **Patents** in any and all countries and for vesting title thereto in said **Assignee**, its successors, assigns and legal representatives or nominees.

Assignor hereby consents that a copy of this assignment shall be deemed a full legal and formal equivalent of any assignment, consent to file or like document which may be required in any country for any purpose and more particularly in proof of the right of the said **Assignee** or nominee to claim the aforesaid benefit of the right

of priority provided by: (a) the International Convention for the protection of Industrial Property, as amended, or by any convention which may henceforth be substituted for it; and (b) the Patent Cooperation Treaty, as amended, or by any treaty which may henceforth be substituted for it.

Assignor covenants with said **Assignee**, its successors and assigns and legal representatives that the rights and property herein conveyed are free and clear of any encumbrance, and that **Assignor** owns and has full right to convey the same as herein expressed.

Should any part of this Agreement be rendered or declared invalid by a court of competent jurisdiction, such invalidation of such part or portion of this Agreement should not invalidate the remaining portions thereof, and the remaining portions shall remain in full force and effect.

IN WITNESS WHEREOF, the **Assignor** and the **Assignee** have caused this Assignment and Agreement to be duly executed below in duplicate originals by their duly authorized representatives on 21 day of August, 2013.

Assignor

PS3 Luxco Sarl,
Address: 208, Val des Bons Malades,
L-2121, Luxembourg
Grand Duchy of Luxembourg

By: _____

Name: Tom Wippman

Title: Manager A

Assignee

PS2 Luxco Sarl,
Address: 208, Val des Bons Malades
L-2121, Luxembourg
Grand Duchy of Luxembourg

By: _____

Name: Tom Wippman

Title: Manager A

APPENDIX

THE PORTFOLIO OF
PATENTS AND APPLICATIONS
SUBJECT TO THE FOREGOING
ASSIGNMENT AND AGREEMENT

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
1	CN	200310104647.0	Power Management Block for Use in a Non-Volatile Memory System	Abandoned	10/28/2003		
2	CN	3824784.1	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003		
3	CN	03824780.1	Tracking the Least Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003		

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
4	DE	03752244.8	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003	1559113	2/27/2008
5	EP	06728339.0	Method and system for fast wake-up of a flash memory system optimized for frequent power failures	Abandoned	5/9/2006		
6	EP	06016209.6	Automated Wear Leveling In Non-Volatile Storage Systems	Abandoned	10/9/2003		
7	EP	03752244.8	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003	1559113	2/27/2008

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
8	EP	03752227.3	Tracking the Least Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003		
9	ES	02703078.2	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Abandoned	1/7/2002	1352394	5/24/2006
10	FR	03752244.8	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003	1559113	2/27/2008
11	GB	03774734.2	Automated Wear Leveling In Non-Volatile Storage Systems	Abandoned	10/9/2003	1556868	9/5/2007
12	GB	04785192.8	Flash Memory Data Correction And Scrub Techniques	Abandoned	9/28/2004	1687720	1/2/2008

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
13	GB	03752225.7	Maintaining Erase Counts in Non-Volatile Storage Systems	Abandoned	9/10/2003	1559016	3/5/2008
14	GB	03752244.8	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003	1559113	2/27/2008
15	IT	03774734.2	Automated Wear Leveling In Non-Volatile Storage Systems	Abandoned	10/9/2003	1556868	9/5/2007
16	IT	04785192.8	Flash Memory Data Correction And Scrub Techniques	Abandoned	9/28/2004	1687720	1/2/2008
17	IT	03752244.8	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003	1559113	2/27/2008

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
18	JP	2004-548320	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003		
19	JP	2009-202432	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003		
20	JP	20040548318	Tracking the Least Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003		
21	JP	2008-536602	Corrected data storage and handling methods	Abandoned	10/4/2006		
22	KR	2002-7013199	Method for Fast Wake-Up of a Flash Memory System	Abandoned	2/4/2002	558631	2/1/2006
23	KR	2011-7009002	Wear-Leveling in Non-Volatile Storage Systems	Abandoned	9/10/2003		

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
24	KR	2003-0075147	Power Management Block for Use in a Non-Volatile Memory System	Abandoned	10/27/2003		
25	KR	2005-7007323	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003		
26	KR	2005-7007322	Tracking the Least Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003		
27	KR	2008-7011864	Corrected data storage and handling methods	Abandoned	10/4/2006		
28	NL	03752244.8	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/10/2003	1559113	2/27/2008

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
29	TW	092125804	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Abandoned	9/18/2003	I267866	12/11/2006
30	US	60/678,902	Method and system for fast wake-up of a flash memory system optimized for frequent power failures	Expired	5/9/2005		
31	US	60/422,173	Automated Wear Leveling in Non-Volatile Storage Systems	Expired	10/28/2002		
32	US	60/422,166	Power Management Block for Use in a Non-Volatile Memory System	Expired	10/28/2002		
33	WO	IL06/000550	Method and system for fast wake-up of a flash memory system optimized for frequent power failures	Expired	5/9/2006		

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
34	WO	US03/032050	Automated Wear Leveling In Non-Volatile Storage Systems	Expired	10/9/2003		
35	WO	US04/042965	Flash Memory System Startup Operation	Expired	12/16/2004		
36	WO	US04/031788	Flash Memory Data Correction And Scrub Techniques	Expired	9/28/2004		
37	WO	US03/028914	Wear-Leveling in Non-Volatile Storage Systems	Expired	9/10/2003		
38	WO	US03/028427	Maintaining Erase Counts in Non-Volatile Storage Systems	Expired	9/10/2003		
39	WO	US03/028502	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Expired	9/10/2003		

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
40	WO	US03/28429	Tracking the Least Frequently Erased Blocks in Non-Volatile Memory Systems	Expired	9/10/2003		
41	WO	US06/038808	Corrected data storage and handling methods	Expired	10/4/2006		
42	WO	US02/002808	Method for Fast Wake-Up of a Flash Memory System	Expired	2/4/2002		
43	WO	US02/000366	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Expired	1/7/2002		
44	CN	200680021190.9	Method and system for facilitating fast wake-up of a flash memory system	Issued	5/9/2006	ZL200680021190.9	11/23/2011
45	CN	02803882.7	Non-volatile Memory System and Method of Programming and Reading Updated	Issued	1/7/2002	ZL02803882.7	12/13/2006

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
			Data				
46	CN	0610142358.3	Non-volatile Memory System and Method of Programming and Reading Updated Data	Issued	1/7/2002	ZL0610142358.3	5/6/2009
47	CN	0610142359.8	Partial Block Data Programming and Reading Operations in a Non-Volatile Memory	Issued	1/7/2002	ZL0610142359.8	1/2/2009
48	CN	200380104715.1	Method for Performing Automated Wear Leveling In Non-Volatile Storage Systems	Issued	10/9/2003	ZL200380104715.1	4/29/2009
49	CN	200480039310.9	Flash Memory System Startup Operation	Issued	12/16/2004	ZL200480039310.9	5/12/2010
50	CN	200480033197.3	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	ZL200480033197.3	9/16/2009

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
51	CN	200910160836.7	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	ZL200910160836.7	8/15/2012
52	CN	03824786.0	Wear-Leveling in Non-Volatile Storage Systems	Issued	9/10/2003	ZL03824786.0	5/27/2009
53	CN	03824769.0	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	9/10/2003	ZL03824769.0	4/30/2008
54	DE	60211653.8-08	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	1352394	5/24/2006
55	DE	03774734.2	Automated Wear Leveling In Non-Volatile Storage Systems	Issued	10/9/2003	1556868	9/5/2007
56	DE	04785192.8	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	1687720	1/2/2008

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
57	DE	07015342.4	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	1847930	6/24/2009
58	DE	03752225.7	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	9/10/2003	1559016	3/5/2008
59	DE	032567935	Power Management Block for Use in a Non-Volatile Memory System	Issued	10/28/2003	1416379	11/21/2012
60	EP	02703078.2	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	1352394	5/24/2006
61	EP	03774734.2	Automated Wear Leveling In Non-Volatile Storage Systems	Issued	10/9/2003	1556868	9/5/2007
62	EP	04785192.8	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	1687720	1/2/2008

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
63	EP	07015342.4	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	1847930	6/24/2009
64	EP	03752225.7	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	9/10/2003	1559016	3/5/2008
65	EP	03256793.5	Power Management Block for Use in a Non-Volatile Memory System	Issued	10/28/2003	1416379	11/21/2012
66	EP	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013
67	FR	02703078.2	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	1352394	5/24/2006
68	FR	03774734.2	Automated Wear Leveling In Non-Volatile Storage Systems	Issued	10/9/2003	1556868	9/5/2007

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
69	FR	04785192.8	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	1687720	1/2/2008
70	FR	07015342.4	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	1847930	6/24/2009
71	FR	03752225.7	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	9/10/2003	1559016	3/5/2008
72	GB	02703078.2	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	1352394	5/24/2006
73	GB	032567935	Power Management Block for Use in a Non-Volatile Memory System	Issued	10/28/2003	1416379	11/21/2012
74	IL	151293	Method for Fast Wake-Up of a Flash Memory System	Issued	2/4/2002	151293	2/1/2007

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
75	IT	02703078.2	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	1352394	5/24/2006
76	IT	03752225.7	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	9/10/2003	1559016	3/5/2008
77	JP	2002-563351	Method for Fast Wake-Up of a Flash Memory System	Issued	2/4/2002	4279553	3/19/2009
78	JP	2008-510721	Method and system facilitating fast wake-up of a flash memory system	Issued	5/9/2006	5002586	5/25/2012
79	JP	2002-558275	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	4155824	7/18/2008

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
80	JP	2007-203823	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	4750766	5/27/2011
81	JP	2008-075388	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	4768771	6/24/2011
82	JP	2004-548368	Automated Wear Leveling In Non-Volatile Storage Systems	Issued	10/9/2003	4518951	5/28/2010
83	JP	2006-547272	Flash Memory System Startup Operation	Issued	12/16/2004	4933268	2/24/2012
84	JP	2006-534014	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	4723504	4/15/2011
85	JP	2004-548308	Wear-Leveling in Non-Volatile Storage Systems	Issued	9/10/2003	4456486	2/12/2010

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
86	JP	2004-548316	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	9/10/2003	4372013	9/11/2009
87	JP	2003-368140	Power Management Block for Use in a Non-Volatile Memory System	Issued	10/28/2003	4371771	9/11/2009
88	JP	2009-181121	Power Management Block for Use in a Non-Volatile Memory System	Issued	10/28/2003	5162535	12/21/2012
89	KR	2007-7028410	Method and system for fast wake-up of a flash memory system optimized for frequent power failures	Issued	5/9/2006	10-1286477	7/10/2013
90	KR	2003-7009551	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	10-0944996	2/23/2010

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
91	KR	2008-7028861	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	10-1076830	10/19/2011
92	KR	2005-7007417	Automated Wear Leveling In Non-Volatile Storage Systems	Issued	10/9/2003	10-1122511	2/24/2012
93	KR	2011-7014901	Automated Wear Leveling In Non-Volatile Storage Systems	Issued	10/9/2003	10-1174308	8/9/2012
94	KR	2006-7012949	Flash Memory System Startup Operation	Issued	12/16/2004	10-1029938	4/12/2011
95	KR	2006-7006479	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	10-1127882	3/12/2012
96	KR	2005-7007337	Wear-Leveling in Non-Volatile Storage Systems	Issued	9/10/2003	10-1110901	1/20/2012

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
97	KR	2005-7007326	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	9/10/2003	10-0914089	8/19/2009
98	KR	2009-0075023	Power Management Block for Use in a Non-Volatile Memory System	Issued	10/27/2003	10-1004876	12/22/2010
99	NL	02703078.2	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/7/2002	1352394	5/24/2006
100	NL	03774734.2	Automated Wear Leveling In Non-Volatile Storage Systems	Issued	10/9/2003	1556868	9/5/2007
101	NL	04785192.8	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	1687720	1/2/2008
102	NL	07015342.4	Flash Memory Data Correction And Scrub Techniques	Issued	9/28/2004	1847930	6/24/2009

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
103	NL	03752225.7	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	9/10/2003	1559016	3/5/2008
104	NL	032567935	Power Management Block for Use in a Non-Volatile Memory System	Issued	10/28/2003	1416379	11/21/2012
105	TW	91100155	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/8/2002	I221217	9/21/2004
106	TW	093141421	Flash Memory System Startup Operation	Issued	12/30/2004	I272536	2/1/2007
107	TW	093129922	Flash Memory Data Correction And Scrub Techniques	Issued	10/1/2004	I261840	9/11/2006
108	TW	092125794	Wear-Leveling in Non-Volatile Storage Systems	Issued	9/18/2003	I236019	7/11/2005

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
109	TW	092125808	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	9/18/2003	I261168	9/1/2006
110	TW	92125790	Tracking the Least Frequently Erased Blocks in Non-Volatile Memory Systems	Issued	9/18/2003	I249100	2/11/2006
111	US	09/775,499	Method for Fast Wake-Up of a Flash Memory System	Issued	2/5/2001	6,510,488	1/21/2003
112	US	11/382,056	Method and system for facilitating fast wake-up of a flash memory system	Issued	5/8/2006	8,244,958	8/14/2012
113	US	09/766,436	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	1/19/2001	6,763,424	7/13/2004

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
114	US	10/841,388	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	5/7/2004	6,968,421	11/22/2005
115	US	11/250,238	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	10/13/2005	7,818,490	10/19/2010
116	US	12/371,460	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	2/13/2009	7,657,702	2/2/2010
117	US	12/900,397	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	10/7/2010	7,970,987	6/28/2011

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
118	US	13/168,756	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Issued	6/24/2011	8,316,177	11/20/2012
119	US	10/686,399	Automated Wear Leveling In Non-Volatile Storage Systems	Issued	10/14/2003	7,120,729	10/10/2006
120	US	11/539,972	Automated wear leveling in non-volatile storage systems	Issued	10/10/2006	7,552,272	6/23/2009
121	US	10/751,033	Flash Memory System Startup Operation	Issued	12/31/2003	7,594,135	9/22/2009
122	US	12/484,350	Flash Memory System Startup Operation	Issued	6/15/2009	7,962,777	6/14/2011
123	US	10/678,345	Flash Memory Data Correction And Scrub Techniques	Issued	10/3/2003	7,012,835	3/14/2006
124	US	11/271,553	Flash Memory Data Correction And Scrub	Issued	11/10/2005	7,224,607	5/29/2007

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
			Techniques				
125	US	11/748,077	Flash Memory Data Correction And Scrub Techniques	Issued	5/14/2007	7,518,919	4/14/2009
126	US	12/415,158	Flash Memory Data Correction And Scrub Techniques	Issued	3/31/2009	8,004,895	8/23/2011
127	US	12/945,000	Flash Memory Data Correction And Scrub Techniques	Issued	11/12/2010	8,050,095	11/1/2011
128	US	10/281,739	Wear-Leveling in Non-Volatile Storage Systems	Issued	10/28/2002	6,985,992	1/10/2006
129	US	10/281,696	Maintaining Erase Counts in Non-Volatile Storage Systems	Issued	10/28/2002	6,831,865	12/14/2004

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
130	US	10/685,624	Power Management Block for Use in a Non-Volatile Memory System	Issued	10/14/2003	7,181,611	2/20/2007
131	US	11/616,687	Power Management Block for Use in a Non-Volatile Memory System	Issued	12/27/2006	7,809,962	10/5/2010
132	US	10/281,670	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	Issued	10/28/2002	6,973,531	12/6/2005
133	US	10/281,824	Tracking the Least Frequently Erased Blocks in Non-Volatile Memory Systems	Issued	10/28/2002	7,096,313	8/22/2006
134	US	11/253,531	Corrected Data Storage and Handling Methods	Issued	10/18/2005	7,173,852	2/6/2007

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
135	CN	201010173494.5	Power Management Block for Use in a Non-Volatile Memory System	Published	10/28/2003		
136	EP	05077929.7	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Published	1/7/2002		
137	EP	06075106.2	Partial Block Data Programming And Reading Operations In A Non-Volatile Memory	Published	1/7/2002		
138	EP	04815082.5	Flash Memory System Startup Operation	Published	12/16/2004		
139	EP	03809932.1	Wear-Leveling in Non-Volatile Storage Systems	Published	9/10/2003		
140	TW	095137575	Corrected data storage and handling methods	Published	10/12/2006		

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141	GB	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013
142	FR	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013
143	DE	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013
144	NL	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013
145	IT	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013
146	ES	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013
147	BE	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013
148	FI	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013
149	SE	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013

No.	Country Code	Application Number	Title	Status (expired, pending, or granted)	Filing Date	Patent Number	Grant Date
150	IE	06816218.9	Corrected data storage and handling methods	Issued	10/4/2006	1941368	8/28/2013