502621343 01/06/2014

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
HAO-HSIANG CHUANG	12/07/2013
JEN-SHIEN HSIEH	10/23/2013
CHUEI-TANG WANG	10/23/2013
CHEN-HUA YU	10/23/2013

RECEIVING PARTY DATA

Name:	TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD
Street Address:	NO.8, LI-HSIN RD.6, SCIENCE-BASED INDUSTRIAL PARK
City:	HSINCHU
State/Country:	TAIWAN
Postal Code:	300

PROPERTY NUMBERS Total: 1

Property Type	Number	
Application Number:	14070105	

CORRESPONDENCE DATA

Fax Number: (480)464-5692 Phone: 480-464-1111

Email: slaureanti@jacksonwhitelaw.com

Correspondence will be sent via US Mail when the email attempt is unsuccessful.

Correspondent Name: STEVEN J. LAUREANTI

Address Line 1: 40 NORTH CENTER STREET, SUITE 200

Address Line 4: MESA, ARIZONA 85201

ATTORNEY DOCKET NUMBER:	28503-019
NAME OF SUBMITTER:	STEVEN J. LAUREANTI
Signature:	/Steven J. Laureanti/
	PATENT

502621343 REEL: 031892 FRAME: 0827

Date:	01/06/2014
Total Attachments: 4 source=NP-13986-US-B-executed Assignment#page1.tif source=NP-13986-US-B-executed Assignment#page2.tif source=NP-13986-US-B-executed Assignment#page3.tif source=NP-13986-US-B-executed Assignment#page4.tif	

NP-13986-US-B TSMC-2-13-0838

Inventor(s)-to-Assignee

ASSIGNMENT

This assignment agreement is applicable to an invention entitled (Invention Title)		
INDUCTOR FOR SEMICONDUCTOR INTEGRATED CIRCUIT		
The PATENT RIGHTS referred to in this agreement are:		
(check one) a patent application for this invention, executed by the ASSIGNOR(S)		
concurrently with this assignment.		
XU.S. patent application Serial No.14/070,105, filed on November 1, 2013		
a U.S. patent application based on PCT International Application		
Nofiled on (date)(U.S. patent application		
Serial No, if known).		
U.S. patent No, issued		
The PATENT RIGHTS also include all divisions, reissues, continuations and extensions of		
the patents and patent applications identified above.		
The PATENT RIGHTS assigned under this agreement are:		
(check one) \(\overline{\text{U}}\). S. patent rights only.		
☐ Worldwide patent rights. In this case, the assignee shall have the right to		
claim the benefit of the filing date of any U.S. or foreign patent application		
for this invention.		
The ASSIGNOR(S) referred to in this agreement is (or are) the inventor(s) whose signatures		
appear on page 2 of this Assignment and any Supplemental Sheet(s).		
The ASSIGNEE referred to in this agreement is:		
(Name of Assignee) TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.		
(Address) NO.8, LI-HSIN RD.6, SCIENCE-BASED INDUSTRIAL PARK, HSINCHU,		
TAIWAN 300, R.O.C.		
The ASSIGNEE is:		
(check one) An individual.		
☐A Partnership.		
☑A Corporation of <u>TAIWAN, R.O.C.</u> (specify state or country)		
(other)		
The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and		
valuable consideration, receipt of which is acknowledged, hereby assign(s) the following		
rights to the ASSIGNEE, its successors and assigns:		

the full and exclusive right to the invention;

the entire right, title and interest in and to the PATENT RIGHTS;

provisions, based on any earlier patent applications for this invention.

the right to claim priority under 35 USC 119, 35 USC 120, or any other applicable

the right to sue and recover for any past infringement; and

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THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO ASSIGNEE: TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD. INVENTION TITLE: INDUCTOR FOR SEMICONDUCTOR INTEGRATED CIRCUIT

As to all U.S. patent applications assigned under this agreement, the ASSIGNOR(S) hereby authorize(s) and request(s) the Commissioner of Patents and Trademark to issue all Letters Patent to the ASSIGNEE as the assignee of the entire right, title and interest, for the sole use and enjoyment of said ASSIGNEE, its successors and assigns.

Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any fact known to the ASSIGNOR(S) with respect to said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause and all Letter Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

The ASSIGNOR(S) authorize(s) the attorneys and agents who have the power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

Hao-Hsiang CHUANG		
Name of sole or first inventor	Signature	Date
Jeng-Shien HSIEH	Jeng-Shien Hsieh	10/23/2013
Name of second inventor, if any	Signature	Date
Chuei-Tang WANG Name of third inventor, if any	Chuei-Tang Wang- Signature	Lo/23/20/3 Date
Chen-Hua YU Name of fourth inventor, if any	Signature	16/23/13 · Date
	V	

NP-13986-US-B TSMC-2-13-0838

Inventor(s)-to-Assignee

ASSIGNMENT

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Nofiled on (date)(U.S. patent application		
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appear on page 2 of this Assignment and any Supplemental Sheet(s).		
The ASSIGNEE referred to in this agreement is:		
(Name of Assignee) TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD.		
(Address) NO.8, LI-HSIN RD.6, SCIENCE-BASED INDUSTRIAL PARK, HSINCHU,		
TAIWAN 300, R.O.C.		
The ASSIGNEE is:		
(check one) An individual.		
A Partnership.		
A Corporation of <u>TAIWAN, R.O.C.</u> (specify state or country)		
(other)		
The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and		
valuable consideration, receipt of which is acknowledged, hereby assign(s) the following		
rights to the ASSIGNEE, its successors and assigns:		
the full and exclusive right to the invention;		
the entire right, title and interest in and to the PATENT RIGHTS;		
the right to sue and recover for any past infringement; and		

the right to claim priority under 35 USC 119, 35 USC 120, or any other applicable

provisions, based on any earlier patent applications for this invention.

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO
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INVENTION TITLE: INDUCTOR FOR SEMICONDUCTOR INTEGRATED
CIRCUIT

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Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any fact known to the ASSIGNOR(S) with respect to said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause and all Letter Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

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Hao-Hsiang CHUANG	Hao-Harang Cho	ngng >0/3.12.07 Date
Name of sole or first inventor	Signature	Date
Jeng-Shien HSIEH		
Name of second inventor, if any	Signature	Date
Chuei-Tang WANG		
Name of third inventor, if any	Signature	Date
Chen-Hua YU		_
Name of fourth inventor, if any	Signature	Date

PATENT REEL: 031892 FRAME: 0832

RECORDED: 01/06/2014