PATENT ASSIGNMENT COVER SHEET

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CONVEYING PARTY DATA

Name	Execution Date
DIGITAL IMAGING SYSTEMS GMBH	03/28/2011

RECEIVING PARTY DATA

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PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	13920231

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Signature:	/Bryan D. Kirkpatrick/
Date:	02/04/2014

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Exhibit B

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Digital Imaging Systems Gmbh, a German close corporation having offices at Neue Strasse 95, D-73230 Kirchheim/Teck, Germany (***"Assignor"***), does hereby sell, assign, transfer, and convey unto Youliza, Gehts B.V. Limited Liability Company, a Delaware limited liability company, having an address at 160 Greentree Drive, Suite 101; Dover, DE 19904, U.S.A. (***"Assignee"***), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the ***"Patent Rights"***):

(a) the provisional patent applications, patent applications and patents listed in the table below (the "***Patents***");

Patent or Application No.	**Country**	**Filing Date**	**Title of Patent and First Named Inventor**
7479994	US	11/30/2004	Image sensor having resolution adjustment employing an analog column averaging/row averaging for high intensity light or row binning for low intensity light Guang Yang
7663670	US	02/04/2002	Methods and systems for embedding camera information in images Vahid Orboubadian
12/657704	US	01/26/2010	Methods and systems for embedding camera information in images Vahid Orboubadian
12/387048	US	04/27/2009	Blended autofocus using mechanical and softlens technologies Scott P. Campbell
EP09013138.4	EP	10/17/2009	Blended autofocus using mechanical and "softlens" software based technologies Scott P. Campbell
7253461	US	05/27/2005	Snapshot CMOS image sensor with high shutter rejection ratio Guang Yang

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
7423302	US	11/21/2005	Pinned photodiode (PPD) pixel with high shutter rejection ratio for snapshot operating CMOS sensor Guang Yang
7833814	US	08/26/2008	Method of forming pinned photodiode (PPD) pixel with high shutter rejection ratio for snapshot operating CMOS sensor Guang Yang
7479995	US	05/19/2005	On chip real time FPN correction without imager size memory Taner Dosluoglu
11/998127	US	11/28/2007	Apparatus and method for shift invariant differential (SID) image data interpolation in non-fully populated shift invariant matrix Taner Dosluoglu
11/998126	US	11/28/2007	Simultaneous global shutter and correlated double sampling read out in multiple photosensor pixels Taner Dosluoglu
11/998099	US	11/28/2007	Apparatus and method for shift invariant differential (SID) image data interpolation in fully populated shift invariant matrix Taner Dosluoglu
11/998960	US	12/03/2007	High performance imager IC with minimum peripheral circuits Guang Yang
7146040	US	10/21/2002	Automatic white balance technique Anders Johannesson
6995346	US	12/23/2002	Fixed pattern noise compensation with low memory requirements Anders Johanneson
7414630	US	01/10/2005	Hexagonal color pixel structure with white pixels Detlef Schweng

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
7400332	US	01/10/2005	Hexagonal color pixel structure with white pixels Detlef Schweng
10/859797	US	06/03/2004	Extended dynamic range in color imagers Detlef Schweng
7443432	US	01/04/2005	Suppression of noise in pixel VDD supply Taner Dosluoglu
7247898	US	01/04/2005	Self adjusting transfer gate APS Taner Dosluoglu
7224833	US	10/15/2002	Method for fast color saturation control Anders Johannesson
7453502	US	03/09/2004	Lens shading algorithm Detlef Schweng
7567732	US	04/22/2004	Image resolution conversion Detlef Schweng
7529427	US	04/22/2004	Zoom algorithm Detlef Schweng
7812870	US	06/22/2004	Color space conversion in the analog domain Detlef Schweng
7567273	US	11/03/2004	Multiple frame grabber Dirk Huettmann
12/456925	US	06/24/2009	Multiple frame grabber Dirk Huettmann
7486298	US	11/16/2004	Size optimized pixel line to pixel block conversion algorithm Detlef Schweng
7515183	US	11/24/2004	Column averaging/row binning circuit for image sensor resolution adjustment in lower intensity light environment Guang Yang

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
7155119	US	08/30/2004	Multi-processing of a picture to speed up mathematics and calculation for one picture Dirk Huettmann
7692700	US	03/04/2003	Vignetting compensation Anders Johannesson
7176532	US	01/14/2005	CMOS active pixel sensor with improved dark current and sensitivity Taner Dosluoglu
7256828	US	01/21/2003	Weighted gradient based and color corrected interpolation Andreas Nilsson
6969652	US	07/08/2003	Natural analog or multilevel transistor DRAM-cell Horst Knoedgen
7307877	US	10/26/2005	Natural analog or multilevel transistor DRAM-cell Horst Knoedgen
6888568	US	02/02/2000	Method and apparatus for controlling pixel sensor elements Sarit Neter
7133073	US	02/02/2000	Method and apparatus for color interpolation Sarit Neter
11/113438	US	04/22/2005	Method and apparatus for controlling pixel sensor elements Sarit Neter
7623163	US	11/03/2006	Method and apparatus for color interpolation Sarit Neter
7013288	US	05/26/2000	Methods and systems for managing the distribution of image capture devices, images, and prints Gregory Urban

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
6965395	US	09/12/2000	Methods and systems for detecting defective imaging pixels and pixel values Sarit Neter
7427734	US	10/18/2005	Multiple photosensor pixel Guang Yang
7548261	US	11/30/2004	Column averaging/row averaging circuit for image sensor resolution adjustment in high intensity light environment Guang Yang
09/495971	US	02/02/2000	Method and apparatus for color compensation Sarit Neter

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority, and/or (iii) that were co-owned applications that incorporate by reference, or are incorporated by reference into, the Patents;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), and/or (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceeding brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as an allowable claim in any of the Patents and/or any item in the foregoing categories (b) through (e);

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

(1) damages,
(2) injunctive relief, and
(3) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Kirchheim on March 28, 2011.

ASSIGNOR:

Digital Imaging Systems Gmbh

By: _____
Name: Roland Pudelko
Title: Managing Director
(Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

The undersigned witnessed the signature of Roland Pudelko to the above Assignment of Patent Rights on behalf of Digital Imaging Systems Gmbh and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.

2. Roland Pudelko is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on March 28, 2011 to execute the above Assignment of Patent Rights on behalf of Digital Imaging Systems Gmbh.

3. Roland Pudelko subscribed to the above Assignment of Patent Rights on behalf of Digital Imaging Systems Gmbh.

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on March 28, 2011 (date)

_____
Print Name: Martin Sattelmaq