

## PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT										
NATURE OF CONVEYANCE:	ASSIGNMENT										
CONVEYING PARTY DATA											
<table border="1"><thead><tr><th>Name</th><th>Execution Date</th></tr></thead><tbody><tr><td>NEDELJKO VARNICA</td><td>02/03/2014</td></tr><tr><td>DUNG VIET NGUYEN</td><td>02/03/2014</td></tr><tr><td>SHASHI KIRAN CHILAPPAGARI</td><td>02/04/2014</td></tr></tbody></table>		Name	Execution Date	NEDELJKO VARNICA	02/03/2014	DUNG VIET NGUYEN	02/03/2014	SHASHI KIRAN CHILAPPAGARI	02/04/2014		
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RECEIVING PARTY DATA											
<table border="1"><tr><td>Name:</td><td>MARVELL SEMICONDUCTOR, INC.</td></tr><tr><td>Street Address:</td><td>5488 MARVELL LANE</td></tr><tr><td>City:</td><td>SANTA CLARA</td></tr><tr><td>State/Country:</td><td>CALIFORNIA</td></tr><tr><td>Postal Code:</td><td>95054</td></tr></table>		Name:	MARVELL SEMICONDUCTOR, INC.	Street Address:	5488 MARVELL LANE	City:	SANTA CLARA	State/Country:	CALIFORNIA	Postal Code:	95054
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PROPERTY NUMBERS Total: 1											
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CORRESPONDENCE DATA											
Fax Number: (408)222-2755 Phone: 408-222-2500 Email: agorthy@marvell.com <i>Correspondence will be sent via US Mail when the email attempt is unsuccessful.</i> Correspondent Name: HORACE NG Address Line 1: 5488 MARVELL LANE Address Line 4: SANTA CLARA, CALIFORNIA 95054											
ATTORNEY DOCKET NUMBER:	MP5216										
NAME OF SUBMITTER:	HORACE NG										
Signature:	/Horace Ng/										
Date:	02/12/2014										
Total Attachments: 3 source=MP5216-Assign_INV_to_MSI_(EXECUTED)#page1.tif source=MP5216-Assign_INV_to_MSI_(EXECUTED)#page2.tif source=MP5216-Assign_INV_to_MSI_(EXECUTED)#page3.tif											

PATENT

**ASSIGNMENT**

For good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned,

**Nedeljko Varnica, Dung Viet Nguyen and Shashi Kiran Chilappagari**

who have created a certain invention for which an application for United States Letters Patent was executed by me concurrently herewith and entitled:

**SYSTEMS AND METHODS FOR BIT FLIPPING DECODING WITH RELIABILITY  
INPUTS**

Do hereby sell, assign and transfer to Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, CA 95054, its successors, assigns, and legal representatives, the full and exclusive right to said invention and said application and to any and all inventions described in said application for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be granted therefor in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Do hereby further sell, assign and transfer to Marvell Semiconductor, Inc., its successors, assigns, and legal representatives, the full and exclusive right to United States Provisional Patent Application No. 61/764,901, filed February 14, 2013, entitled, GENERAL BIT FLIPPING DECODER ARCHITECTURE'S FOR SYSTEMS WITH RELIABILITY INPUTS, and to any and all inventions described in said provisional patent application(s) for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be entitled to the benefit thereof in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

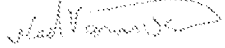
Agree that Marvell Semiconductor, Inc., hereinafter referred to as Assignee, may apply for and receive Letters Patent for said invention and said inventions, hereinafter referred to as said invention, in its own name, in the United States, its territorial possessions, and all foreign countries; and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of said Assignee, its successors, assigns and legal representatives, the undersigned will execute all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, execute all rightful oaths, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to said Assignee, its successors, assigns or legal representatives all facts known to the undersigned relating to said invention and the history thereof; and generally do everything possible which said Assignee, its successors, assigns, or legal representatives shall consider desirable for aiding in securing, maintaining and enforcing proper patent protection for said invention and for vesting title to said invention and all applications for patents on said invention in said Assignee, its successors, assigns, or legal representatives; and

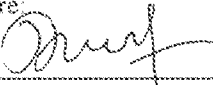
Covenant with said Assignee, its successors, assigns, or legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Docket#: MP5216

PATENT

IN TESTIMONY WHEREOF we have hereunto set our signatures on the date indicated below.

Full Name of Sole/First Inventor:	
Nedeljko Varnica	
Inventor's Signature: 	Date: Month/Day/Year 2 / 3 / 2014

Full Name of Second Inventor:	
Dung Viet Nguyen	
Inventor's Signature: 	Date: Month/Day/Year 02 / 03 / 2014

Full Name of Third Inventor:	
Shashi Kiran Chilappagari	
Inventor's Signature:	Date: Month/Day/Year

IN TESTIMONY WHEREOF we have hereunto set our signatures on the date indicated below.

Full Name of Sole/First Inventor:

Nedeljko Varnica

Inventor's Signature:

Date: Month/Day/Year

Full Name of Second Inventor:

Dung Viet Nguyen

Inventor's Signature:

Date: Month/Day/Year

Full Name of Third Inventor:

Shashi Kiran Chilappagari

Inventor's Signature:

*C. Shashi Kiran*

Date: Month/Day/Year

02-04-2104