

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT2760670

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
CHAO ZHANG	02/17/2014
RECEIVING PARTY DATA	
Name:	Semiconductor Manufacturing International (Beijing) Corporation
Street Address:	18 Wen Chang Road, Economic-Technological Development Area, Daxing District
City:	Beijing
State/Country:	CHINA
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Name:	Semiconductor Manufacturing International (Shanghai) Corporation
Street Address:	18 Zhang Jiang Road, Pudong New Area
City:	Shanghai
State/Country:	CHINA
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	14201525
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ATTORNEY DOCKET NUMBER:	87720-034100US-881377
NAME OF SUBMITTER:	DAH-BIN KAO
SIGNATURE:	/Dah-Bin Kao/
DATE SIGNED:	03/10/2014

Total Attachments: 2

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2013-0915-01-45

Attorney Docket No. 87720-034100US-881377

ASSIGNMENT
(Patent Application)

I, the undersigned, have invented certain inventions and improvements disclosed in a utility (provisional or non-provisional) or design patent application entitled

"NANOSCALE SILICON SCHOTTKY DIODE ARRAY FOR LOW POWER PHASE CHANGE MEMORY APPLICATION,"

the specification of which is provided with this Assignment and identified by the Attorney Docket No. above.

For one dollar (\$1.00) and other good and valuable consideration, the receipt and sufficiency of which I acknowledge, I:

1. Agree to assign, transfer, convey, and sell, hereby assign, transfer, convey, and sell and have assigned, transferred, conveyed, and sold to **Semiconductor Manufacturing International (Beijing) Corporation**, a corporation of the People's Republic of China, having a principal place of business at 18 Wen Chang Road, Economic-Technological Development Area, Daxing District, Beijing, 100176, People's Republic of China and **Semiconductor Manufacturing International (Shanghai) Corporation**, a corporation of the People's Republic of China, having a principal place of business at 18 Zhang Jiang Road, Pudong New Area, Shanghai, 201203, People's Republic of China ("Assignees"), the entire right, title, and interest in and to:
 - (a) all intellectual property (including, without limitation, any innovation, information, invention, discovery, product, process, work or design) disclosed, embodied, shown, or claimed in the above-referenced patent application, implicitly or explicitly;
 - (b) the above-referenced patent application, the right to claim priority to the above-referenced patent application, all applications based in whole or in part upon the above-referenced patent application, including, without limitation, all applications that are a provisional, non-provisional, design, divisional, continuation, continuation-in-part, registration, utility model, industrial design, reissue, renewal, substitute, extension, reexamination, post-grant review, inter partes review, supplemental examination or non-U.S. patent application or application for other rights based in whole or in part on the above-referenced patent application;
 - (c) all patents (including, without limitation, all U.S. and non-U.S. patents, registrations, utility models, industrial designs, design patents, counterparts, continuations, continuations-in-part, divisionals, reissues, renewals, substitutes, extensions, reexaminations, post-grant reviews, inter

ASSIGNMENT
NANOSCALE SILICON SCHOTTKY DIODE ARRAY FOR LOW POWER
PHASE CHANGE MEMORY APPLICATION
Attorney Docket No. 87720-034100US-881377
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- partes reviews and supplemental examinations) that are granted or issued upon, or that claim priority to, any and all applications described in (b) of this paragraph or that disclose or claim intellectual property described in (a) of this paragraph, in whole or in part; and
- (d) all claims for damages by reason of past infringement of any rights under the applications or patents described in (a), (b) or (c) of this paragraph (including provisional rights to reasonable royalties pursuant to 35 U.S.C. §154(d)) and the right to sue for and collect such damages and royalties for Assignee's own use.
2. Authorize and request the U.S. Patent and Trademark Office or any other U.S. or non-U.S. agency to issue to the Assignees any and all patent(s), or other rights or documents, resulting from the intellectual property, patent application(s) and patents described in paragraph 1 of this Assignment.
 3. Agree to sign all papers and documents, including without limitation, applications, declarations, oaths and petitions, and, at the Assignees' expense, perform any other acts that are necessary in connection with prosecution of patent application(s) or intellectual property described in paragraph 1 of this Assignment and the enforcement of patent(s) or other rights resulting from such patent application(s) or intellectual property.
 4. Agree that the terms, covenants, and conditions of this Assignment shall inure to the benefit of the Assignees, its successors, assigns and other legal representative, and shall be binding upon me, as well as my heirs, legal representatives, and assigns.
 5. Promise and affirm that I have not entered, and will not enter, into any assignment, contract, or understanding that conflicts with this Assignment.
 6. Authorize Kilpatrick Townsend & Stockton to insert above the application number and filing date of the above-described patent application when known.

Signed on the date indicated beside my signature.

Signature: Chao Zhang
CHAO ZHANG

Date: 2014-02-17

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