

<b>PATENT ASSIGNMENT COVER SHEET</b>
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 Stylesheet Version v1.2

EPAS ID: PAT2767300

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
TOUCHSTONE SEMICONDUCTOR, INC.	02/28/2014
<b>RECEIVING PARTY DATA</b>	
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<b>State/Country:</b>	TEXAS
<b>Postal Code:</b>	78701
<b>PROPERTY NUMBERS Total: 8</b>	
<b>Property Type</b>	<b>Number</b>
Patent Number:	8487697
Patent Number:	8581569
Patent Number:	8618848
Application Number:	13540380
Application Number:	13873873
Application Number:	61837492
Application Number:	13725890
Application Number:	61774468
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<b>ATTORNEY DOCKET NUMBER:</b>	TOUCHSTONE-SILABS ASSIGN
<b>NAME OF SUBMITTER:</b>	TAYSIE J. LOCKE
<b>SIGNATURE:</b>	/Taysie J. Locke/
<b>DATE SIGNED:</b>	03/13/2014

PATENT

**Total Attachments: 5**

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## PATENT ASSIGNMENT

THIS PATENT ASSIGNMENT (this "*Patent Assignment*") is entered into as of February 28, 2014, by and between Silicon Laboratories Inc., a Delaware corporation (the "*Purchaser*"), and Touchstone Semiconductor, Inc., a Delaware corporation (the "*Seller*"). Capitalized terms used herein but not otherwise defined shall have the meanings ascribed to such terms in the Purchase Agreement (as defined below).

### RECITALS

WHEREAS, the Purchaser and the Seller have entered into that certain Asset Purchase Agreement, dated as of the date hereof (the "*Purchase Agreement*"), pursuant to which the Seller has agreed to sell, assign, transfer, convey and deliver to the Purchaser all right, title and interest in and to the Assigned Patents (as defined below), and the Purchaser has agreed to purchase and acquire all right, title and interest in and to the Assigned Patents from the Seller.

NOW, THEREFORE, in consideration of the premises and for other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the parties hereto agree as follows:

1. Assigned Patents. "*Assigned Patents*" means the patents and patent applications listed on Schedule 1 attached hereto.

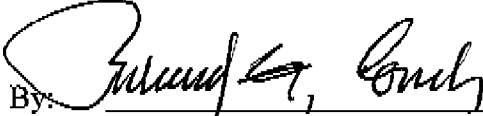
2. Assignment. The Seller does hereby sell, assign, transfer, convey and deliver to the Purchaser all of Seller's rights in the Assigned Patents, to have and to hold, to and for its and their proper use and benefit, forever, free and clear of all Encumbrances, together with any continuation, continuation-in-part, divisional and provisional applications of any of the Assigned Patents, any reissues, reexaminations, substitutes and extensions of any of the Assigned Patents and any foreign counterparts to any of the Assigned Patents, and all of Seller's rights, claims and privileges pertaining to any of the foregoing (including, without limitation, rights to the underlying inventions, the right to prosecute and maintain any of the of the foregoing and the right to sue and recover damages for past, present and future infringement of any of the foregoing).

*[Signatures on following page.]*

IN WITNESS WHEREOF, the undersigned have caused this Patent Assignment to be duly executed and delivered as of the date first set forth above.

**SELLER:**

**TOUCHSTONE SEMICONDUCTOR, INC.**

By:  \_\_\_\_\_

Name: Richard Couch

Title: Chief Restructuring Officer

**PURCHASER:**

**SILICON LABORATORIES INC.**

By: \_\_\_\_\_

Name: \_\_\_\_\_

Title: \_\_\_\_\_

IN WITNESS WHEREOF, the undersigned have caused this Patent Assignment to be duly executed and delivered as of the date first set forth above.

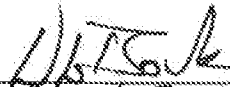
**SELLER:**

**TOUCHSTONE SEMICONDUCTOR, INC.**

By: \_\_\_\_\_  
Name: Richard Couch  
Title: Chief Restructuring Officer

**PURCHASER:**

**SILICON LABORATORIES INC.**

By:  \_\_\_\_\_  
Name: William G. Bock  
Title: President

## Schedule 1

### Assigned Patents

1. "Fully differential autozeroing amplifier".  
Gregory L. Schaffer.  
Granted: U.S. Patent 8,487,697
2. "Supply independent current reference generator in CMOS technology"  
Jeroen Fonderie  
Granted: U.S. Patent 8,581,569
3. "Clock generator with comparator error compensation"  
SanHwa Chee  
Granted: U.S. Patent 8,618,848
4. "INHERENTLY ACCURATE ADJUSTABLE SWITCHED CAPACITOR VOLTAGE REFERENCE WITH WIDE VOLTAGE RANGE"  
Gregory L. Schaffer  
U.S. Patent Publication #: 2014/0002052 A1; U.S. Serial No. 13/540,380
5. "High Voltage MOS Input Stage".  
Gregory L. Schaffer  
U.S. Patent Application Serial No. 13/873,873, filed April 30, 2013
6. "SCALABLE INPUT CURRENT SWITCHING REGULATOR SYSTEM WITH LOW QUIESCENT CURRENT".  
Martin Tomasz and Trang Nguyen  
Filed as U.S. Provisional Application 61/837,492 filed on 6/20/2013.
7. "SYSTEM AND METHOD FOR ADAPTIVE CURRENT LIMIT OF A SWITCHING REGULATOR".  
Martin Tomasz and Zabih Toosky.  
U.S. Patent Application Serial No. 13/725,890, filed on 12/21/2012
8. "A Highly Accurate Chopper Stabilized Bandgap Reference Circuit".  
Zabih Toosky  
Filed as U.S. Provisional Application 61/774,468 on 3/7/2013

**Exhibit B**

**Acquired Trademarks**