

## PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT2809864

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
TZU YU WANG	12/27/2013
WEI-CHENG WU	12/28/2013
KUO-CHING HSU	12/27/2013
SHANG-YUN HOU	01/12/2014
SHIN-PUU JENG	03/12/2014
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	Taiwan Semiconductor Manufacturing Company
<b>Street Address:</b>	No. 8, Li-Hsin Rd. 6
<b>Internal Address:</b>	Science-Based Industrial Park
<b>City:</b>	Hsin-Chu
<b>State/Country:</b>	TAIWAN
<b>Postal Code:</b>	300-77
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
Application Number:	14084813
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(972)732-9218
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	972-732-1001
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<b>Correspondent Name:</b>	SLATER & MATSIL, L.L.P.
<b>Address Line 1:</b>	17950 PRESTON ROAD
<b>Address Line 2:</b>	SUITE 1000
<b>Address Line 4:</b>	DALLAS, TEXAS 75252
<b>ATTORNEY DOCKET NUMBER:</b>	TSM13-0751
<b>NAME OF SUBMITTER:</b>	SHERRY L. COLGROVE
<b>SIGNATURE:</b>	/SHERRY L COLGROVE/
<b>DATE SIGNED:</b>	04/10/2014
<b>Total Attachments: 2</b>	

PATENT

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ASSIGNMENT

WHEREAS, I, the undersigned inventor (or one of the undersigned joint inventors), of residence as listed, having invented certain new and useful improvements as below entitled, for which application for United States Letters Patent is made; and

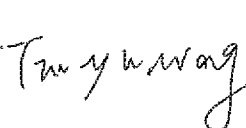
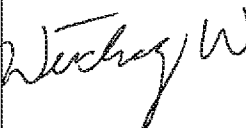
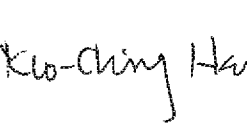

WHEREAS, Taiwan Semiconductor Manufacturing Company, Ltd. ("Assignee"), a corporation organized and existing under the laws of Taiwan, with its principal office at No. 8, Li-Hsin Rd. 6 Science-Based Industrial Park, is desirous of acquiring my entire right, title and interest in and to said invention, and to said application and any Letters Patent that may issue thereon in the United States and all other countries throughout the world;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, I hereby sell and assign to said Assignee, its successors and assigns, my entire right, title and interest in and to said invention and in and to said application and all patents which may be granted therefor, and all future non-provisional applications including divisions, reissues, substitutions, continuations, and extensions thereof; and I hereby authorize and request the Commissioner for Patents to issue all patents for said invention, or patent resulting therefrom, insofar as my interest is concerned, to said Assignee, as assignee of my entire right, title and interest. I declare that I have not executed and will not execute any agreement in conflict herewith.

I also hereby sell and assign to Assignee, its successors and assigns, my foreign rights to the invention disclosed in said application, in all countries of the world, including, but not limited to, the right to file applications and obtain patents under the terms of the International Convention for the Protection of Industrial Property, and of the European Patent Convention, and further agree to execute any and all patent applications, assignments, affidavits, and any other papers in connection therewith necessary to perfect such patent rights.

I hereby further agree that I will communicate to said Assignee, or to its successors, assigns, and legal representatives, any facts known to me respecting said invention or the file history thereof, and at the expense of said Assignee, its legal representatives, successors, or assigns, will testify in any legal proceedings, sign all lawful papers, execute all divisional, continuation, reissue and substitute applications, make all lawful oaths, and generally do everything possible to aid said Assignee, its legal representatives, successors, and assigns, to obtain and enforce proper patent protection for said invention in all countries.

IN WITNESS WHEREOF, I hereunto set my hand and seal this day and year;

TITLE OF INVENTION	Semiconductor Device Design Methods and Conductive Bump Pattern Enhancement Methods			
SIGNATURE OF INVENTOR AND NAME	 Tzu-Yu Wang	 Wei-Cheng Wu	 Kuo-Ching Hsu	 Shang-Yun Hou
DATE	12.27.13	12/28 13	12/27 13	1/12/2014
RESIDENCE	Taipei, Taiwan	Hsin-Chu, Taiwan	Taipei, Taiwan	Jubei City, Taiwan

ATTORNEY DOCKET NO.  
TSM13-0751

## ASSIGNMENT

WHEREAS, I, the undersigned inventor (or one of the undersigned joint inventors), of residence as listed, having invented certain new and useful improvements as below entitled, for which application for United States Letters Patent is made; and

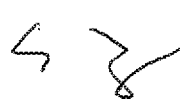
WHEREAS, Taiwan Semiconductor Manufacturing Company, Ltd. ("Assignee"), a corporation organized and existing under the laws of Taiwan, with its principal office at No. 8, Li-Hsin Rd. 6 Science-Based Industrial Park, is desirous of acquiring my entire right, title and interest in and to said invention, and to said application and any Letters Patent that may issue thereon in the United States and all other countries throughout the world;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, I hereby sell and assign to said Assignee, its successors and assigns, my entire right, title and interest in and to said invention and in and to said application and all patents which may be granted therefor, and all future non-provisional applications including divisions, reissues, substitutions, continuations, and extensions thereof; and I hereby authorize and request the Commissioner for Patents to issue all patents for said invention, or patent resulting therefrom, insofar as my interest is concerned, to said Assignee, as assignee of my entire right, title and interest. I declare that I have not executed and will not execute any agreement in conflict herewith.

I also hereby sell and assign to Assignee, its successors and assigns, my foreign rights to the invention disclosed in said application, in all countries of the world, including, but not limited to, the right to file applications and obtain patents under the terms of the International Convention for the Protection of Industrial Property, and of the European Patent Convention, and further agree to execute any and all patent applications, assignments, affidavits, and any other papers in connection therewith necessary to perfect such patent rights.

I hereby further agree that I will communicate to said Assignee, or to its successors, assigns, and legal representatives, any facts known to me respecting said invention or the file history thereof, and at the expense of said Assignee, its legal representatives, successors, or assigns, will testify in any legal proceedings, sign all lawful papers, execute all divisional, continuation, reissue and substitute applications, make all lawful oaths, and generally do everything possible to aid said Assignee, its legal representatives, successors, and assigns, to obtain and enforce proper patent protection for said invention in all countries.

IN WITNESS WHEREOF, I hereunto set my hand and seal this day and year;

TITLE OF INVENTION	Semiconductor Device Design Methods and Conductive Bump Pattern Enhancement Methods			
SIGNATURE OF INVENTOR AND NAME	 Shin-Puu Jeng			
DATE	3/12/2014			
RESIDENCE	Hsin-Chu, Taiwan			