

<b>PATENT ASSIGNMENT COVER SHEET</b>
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Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT2815133

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
SILICON GRAPHICS, INC.	05/08/2009
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	SILICON GRAPHICS INTERNATIONAL, INC.
<b>Street Address:</b>	46600 LANDING PARKWAY
<b>City:</b>	FREMONT
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	94538
<b>PROPERTY NUMBERS Total: 3</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	10434340
<b>Application Number:</b>	12907867
<b>Application Number:</b>	14042695
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(650)391-1395
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	650-391-1380
<b>Email:</b>	ipdockets@lrllaw.com
<b>Correspondent Name:</b>	ROCHELLE ROTEA
<b>Address Line 1:</b>	2440 W. EL CAMINO REAL
<b>Address Line 2:</b>	6TH FLOOR
<b>Address Line 4:</b>	MOUNTAIN VIEW, CALIFORNIA 94040
<b>ATTORNEY DOCKET NUMBER:</b>	SGL-054
<b>NAME OF SUBMITTER:</b>	NICOLE VILLANUEVA
<b>SIGNATURE:</b>	/Nicole Villanueva/
<b>DATE SIGNED:</b>	04/15/2014
<b>Total Attachments: 164</b>	
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**PATENT**







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## BILL OF SALE AND ASSIGNMENT AGREEMENT

FOR VALUE RECEIVED, and other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged on this 8th day of May, 2009 ("Effective Date"), Silicon Graphics, Inc., Silicon Graphics Real Estate, Inc., Silicon Graphics World Trade Corporation, ParaGraph International, Inc., WTI Development, Inc., Silicon Studio, Inc., Silicon Graphics of Manhattan, Inc., Cray Research, L.L.C., Cray Financial Corp., Cray Research America Latina Ltd., Cray Research (Eastern Europe) Ltd., Cray Research (India) Ltd., Cray Asia/Pacific, Inc. and Cray Research International Inc. on the one hand (collectively, the "Selling Entities") do hereby sell, assign, transfer, convey and deliver to Silicon Graphics International, Inc., a Delaware corporation (the "Buyer") on the other hand, all of Selling Entities' right, title and interest in and to all of the Purchased Assets as defined in that certain Asset Purchase Agreement by and among the Selling Entities and the Buyer dated March 31, 2009, as amended on April 30, 2009 (the "Purchase Agreement") except as set forth in Exhibit A hereto. Capitalized terms used without definitions herein shall have the meanings ascribed to such terms in the Purchase Agreement.

1. **Assignment of Purchased Assets.** In accordance with and subject to the terms and conditions set forth in the Purchase Agreement, the Selling Entities hereby sell, assign, convey, transfer and deliver unto Buyer all of their right, title and interest in and to the Purchased Assets, as described and other than as set forth in Exhibit A, free and clear of all Encumbrances (other than Permitted Encumbrances and subject to any order entered by the Bankruptcy Court as of the date hereof resolving any previously filed objection to the assumption and assignment of executory contracts and unexpired leases of nonresidential real property and/or proposed cure amount) as provided in the Purchase Agreement and the Sale Order; *provided* that the Selling Entities do not assume and assign unto Buyer any executory contract or unexpired lease of nonresidential property that is subject to any objections to the assumption and assignment thereof and/or proposed cure amount that have not been resolved as of the date hereof. Buyer hereby accepts such assignment. The parties acknowledge that title to and possession of such assets will pass in the jurisdiction where such assets are located, regardless of the jurisdiction in which this Bill of Sale and Assignment Agreement, or any other agreement, is executed.

2. **Assumed Agreements.** The Selling Entities, pursuant to the Asset Purchase Agreement and the Sale Order, hereby sell, assign, convey and transfer to Buyer all of their right, title and interest in and to the Assumed Agreements identified in Exhibit B to this Bill of Sale and Assignment Agreement free and clear of all Encumbrances (other than Permitted Encumbrances and subject to any order entered by the Bankruptcy Court as of the date hereof resolving any previously filed objection to the assumption and assignment of executory contracts and unexpired leases of nonresidential real property and/or proposed cure amount); *provided* that the Selling Entities do not assume and assign unto Buyer any executory contract or unexpired lease of nonresidential property that is subject to any objections to the assumption and assignment thereof and/or proposed cure amount that have not been resolved as of the date hereof. Buyer hereby accepts such assignment.

3. **No Liabilities Assumed.** Buyer does not assume and is not obligated to pay, perform or otherwise discharge any Liabilities of the Selling Entities or their Affiliates other than the Assumed Liabilities pursuant to the Purchase Agreement and the Assumption Agreement.

4. **Construction.** The terms "hereby," "hereto," "hereunder" and any similar terms as used in this Bill of Sale and Assignment Agreement, refer to this Bill of Sale and Assignment Agreement in its entirety and not only to the particular portion of this Bill of Sale and Assignment Agreement where the term is used. The term "including," when used herein without the qualifier, "without limitation," shall mean "including, without limitation." Wherever in this Bill of Sale and Assignment Agreement the singular number is used, the same shall include the plural, and the masculine gender shall include the feminine and neuter genders, and vice versa, as the context shall require. The word "or" shall not be construed to be exclusive. Provisions shall apply, when appropriate, to successive events and transactions. Unless otherwise indicated, references to Schedules and Exhibits refer to Schedules and Exhibits of and to this Bill of Sale and Assignment Agreement.

5. **Purchase Agreement Controls.** To the extent any terms and provisions of this Bill of Sale and Assignment Agreement are inconsistent with or in conflict with any term, condition or provision of the terms and provisions of the Purchase Agreement, the terms of the Purchase Agreement shall govern and control.

6. **Governing Law.** This Bill of Sale and Assignment Agreement shall be governed by the laws of the State of New York, without giving effect to the principles of conflicts of laws thereof.

7. **Counterparts.** This Bill of Sale and Assignment Agreement may be executed and delivered (including by facsimile transmission) in one or more counterparts, and by the different parties hereto in separate counterparts, each of which, when executed and delivered, shall be deemed to be an original but all of which taken together shall constitute one and the same agreement.

[SIGNATURE PAGE FOLLOWS]

IN WITNESS WHEREOF, the parties hereto have executed and entered into this Bill of Sale and Assignment Agreement effective as of the Effective Date.

<p>Silicon Graphics, Inc. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>	
<p>Silicon Graphics Real Estate, Inc. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>	<p>ParaGraph International, Inc. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>
<p>Silicon Graphics World Trade Corporation Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>	<p>WTI Development, Inc. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>
<p>Silicon Graphics of Manhattan, Inc. Debtor and Debtor In Possession By: _____ Name: _____ Its: _____</p>	<p>Silicon Studio, Inc. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>



<p>Cray Financial Corp. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>	<p>Cray Research, L.L.C. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>
<p>Cray Research (Eastern Europe) Ltd. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>	<p>Cray Research America Latina Ltd. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>
<p>Cray Asia/Pacific, Inc. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>	<p>Cray Research (India) Ltd. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>
	<p>Cray Research International Inc. Debtor and Debtor In Possession By: <u>[Signature]</u> Name: _____ Its: _____</p>

Signature Page to Bill of Sale and Assignment Agreement.

IN WITNESS WHEREOF, the parties hereto have executed and entered into this Bill of Sale and Assignment Agreement effective as of the Effective Date.

Agreed and Accepted

**SILICON GRAPHICS INTERNATIONAL, INC.**

By: James Wheat

Name: JAMES WHEAT

Its: Chief financial officer

## Exhibit A to Bill of Sale and Assignment Agreement

Capitalized terms used without definitions herein shall have the meanings ascribed to such terms in the Purchase Agreement (as defined in the Bill of Sale and Assignment Agreement to which this Exhibit is attached).

All of the Selling Entities' right, title and interest, free and clear of all Encumbrances (other than Permitted Encumbrances), in and to all of the properties, rights, interests and other tangible and intangible assets of the Selling Entities (wherever located and whether or not required to be reflected on a balance sheet prepared in accordance with generally accepted accounting principles); *provided, however*, that the Purchased Assets conveyed pursuant to the Bill of Sale and Assignment Agreement to which this Exhibit is attached shall not include the assets listed in Exhibit A to the Bill of Sale and Assignment Agreement entered into between the Selling Entities and Silicon Graphics International, Ltd., a Cayman Exempted Company ("Cayman") on the same date as the Bill of Sale and Assignment Agreement to which this Exhibit is attached and shall not include any Excluded Assets. Without limiting the generality of the foregoing, the Purchased Assets shall include the following (except to the extent listed or otherwise included as an Excluded Asset in the Purchase Agreement or the Schedules thereto or listed in Exhibit A to the Bill of Sale and Assignment Agreement entered into between the Selling Entities and Cayman on the same date as the Bill of Sale and Assignment Agreement to which this Exhibit is attached):

- a) all cash and cash equivalents of the Selling Entities as of the Closing in excess of the Seller's good-faith estimate of unpaid Administrative Expenses (other than Administrative Expenses that constitute trade payables) made prior to Closing;
- b) all Accounts Receivable of the Selling Entities as of the Closing, and other amounts receivable by the Selling Entities as of the Closing (under the Assumed Agreements or otherwise), together with all security or collateral therefor and any interest or unpaid financing charges accrued thereon;
- c) all Inventory, supplies, materials and spare parts of the Selling Entities as of the Closing;
- d) without duplication of the above, all royalties, advances, prepaid assets, security and other deposits, prepayments, Tax receivables, Tax refunds and other current assets relating to the Business, in each case of the Selling Entities as of the Closing;
- e) all Assumed Agreements;
- f) all Specified IP;
- g) all items of machinery, equipment, supplies, furniture, fixtures, leasehold improvements (to the extent of the Selling Entities' rights to any leasehold improvements under leases that are Assumed Agreements) and other tangible personal property owned by the Selling Entities as of the Closing;

h) all books, records, advertising and promotional materials and similar items of the Selling Entities as of the Closing (except as otherwise described in Section 2.2 of the Purchase Agreement);

i) all real property owned by the Selling Entities as of the Closing, including the owned real property listed on Schedule A-1;

j) all of the stock of the subsidiaries of the Selling Entities listed on Schedule A-2;

k) all other assets that are related to or used in connection with the Selling Entities' businesses and that are owned by any Selling Entity as of the Closing;

l) all claims (including claims for past infringement or misappropriation of Specified IP) and causes of action (other than, in each case, to the extent related to Excluded Assets or Excluded Liabilities) of the Selling Entities as of the Closing against Persons other than the Selling Entities (regardless of whether or not such claims and causes of action have been asserted by the Selling Entities), and all rights of indemnity, warranty rights, rights of contribution, rights to refunds, rights of reimbursement and other rights of recovery, including rights to insurance proceeds, possessed by the Selling Entities as of the Closing (regardless of whether such rights are currently exercisable) to the extent related to the Purchased Assets

m) the accounts listed on Schedule A-3.

**Schedule A-1 to Bill of Sale and Assignment Agreement**

<b>Street Address</b>	<b>Assessor's Parcel Number</b>	<b>Owner</b>
100 North Cashman Drive, Chippewa Falls, Wisconsin	3449.0500	Silicon Graphics, Inc.
1168 Technology Way (f/k/a Industrial Boulevard), Chippewa Falls, Wisconsin	3449	Silicon Graphics, Inc.
890 Technology Way (f/k/a Industrial Boulevard), Chippewa Falls, Wisconsin	3447.5000	Silicon Graphics, Inc.

**Schedule A-2 to Bill of Sale and Assignment Agreement**

Silicon Graphics Biomedical (1995) Ltd.

Silicon Graphics Computer Systems Limited

Silicon Graphics World Trade B.V.

Silicon Graphics Federal, Inc.

Cray Research (Canada) Inc.

Silicon Graphics Sdn. Bhd. [Malaysia]

Cray Research (Israel), Ltd.

**Schedule A-3 to Bill of Sale and Assignment Agreement**

[List of Accounts]

Exhibit A-3 to Bill of Sale and Assignment Agreement

Account Description	Acct/geo/proj	Bank Account #	Institution	Address
<b>US COMM CASH ACCOUNTS (Company 030)</b>				
	101000-0304-1007	417-3297425	Wells Fargo	(1)
	101140-0304-1007	417-3298498	Wells Fargo	(1)
	101025-0306-1007	904-2171331	Wells Fargo	(1)
	101000-0215-1020	310-1801748-59 (EUR)	ING	(2)
		375-1008343-10 (EUR)		
		310-1801748-59 (USD)		
	101010-0304-1007	412-1971139	Wells Fargo	(1)
	101020-0304-1007	412-1971139	Wells Fargo	(1)
	101012-0304-1007	412-5946418	Wells Fargo	(1)
	101025-0304-1007	412-5946418	Wells Fargo	(1)
	101200-0304-1007	960-0028822	Wells Fargo	(1)
	101020-0306-1007	494-5052348	Wells Fargo	(1)
	101012-0305-1007	417-3310939	Wells Fargo	(1)
	101220-0304-1007	417-3317439	Wells Fargo	(1)
	101100-0304-1007	417-3310061	Wells Fargo	(1)
	101130-0304-1007	417-3309063	Wells Fargo	(1)
	101310-2000-1023	4251-495533-12-2	Credit Suisse - Geneva	(3)
	101320-2000-1023	4251-495533-12-14	Credit Suisse - Geneva	(3)
<b>US COMM RESTRICTED CASH ACCOUNTS (Company 030)</b>				
	110100-0307-1007	23297200	Wells Fargo	(1)
	150000-0305-0000	475028228	JP Morgan	(6)
	150000-0305-0000	475028236	JP Morgan	(6)
	150000-0215-1020	375-1006348-15-011-9-300 USD	ING	(2)
	150000-0215-1020	310-1802747-88-003-0-300 EUR	ING	(2)
		375-1008343-10-003-0-300 EUR		

BANK ADDRESSES:

(1)	Wells Fargo	(5)
(2)	ING Belgium	(6)
(3)	Credit Suisse - Geneva	(6)
(4)	Perihsula Regional Commercial Banking Office	(2)
(5)	400 Hamilton Avenue	(2)
(6)	Palo Alto, CA. 94302	(2)
(7)	Business Branch Marnix	(2)
(8)	Rue du Trône 14-16 Troonstraat, 1st floor,	(2)
(9)	1000 Brussels, Belgium	(2)
(10)	JP Morgan	(2)
(11)	3 Chase Metrotech Center 7th Floor	(2)
(12)	Brooklyn, NY 11245	(2)



**Exhibit B to Bill of Sale and Assignment Agreement**

Assumed Agreements

**A list of additional Assumed Agreements has been provided to the bankruptcy court. Such agreements have been redacted from this Bill of Sale pursuant to a protective order, which provides that the name of the counterparty and other information not be made public. A list of such agreements is retained by the Buyer's General Counsel.**

## AMENDMENT TO ASSET PURCHASE AGREEMENT

This Amendment (this "Amendment") to the Asset Purchase Agreement (the "Asset Purchase Agreement") by and among SILICON GRAPHICS, INC., a Delaware corporation (the "Seller") and each of the subsidiaries of the Seller listed on Schedule I thereto (together with the Seller, the "Selling Entities"), and RACKABLE SYSTEMS, INC. a Delaware corporation (the "Buyer") dated as of March 31, 2009, is being entered into as of April 30, 2009, among the Seller, the Selling Entities and the Buyer in accordance with Section 10.1 of the Asset Purchase Agreement. All capitalized terms used but not otherwise defined in this Amendment have the meanings given to them in the Asset Purchase Agreement. The Asset Purchase Agreement is hereby amended as follows:

1. Section 3.3 (Subsidiary Tax Escrow), subsection 4.3(b), Section 4.4 (License), subsection 7.6(b) and subsection 8.1(b) are deleted from the Asset Purchase Agreement and replaced with "[Reserved]."
2. The following defined term and definition shall be added to Section 1.1(a):

“Agent” means Wilmington Trust Corporation as agent for the Selling Entities’ prepetition secured lenders.”
3. Schedule 2.1(f) to the Asset Purchase Agreement is amended and restated and is attached as Exhibit 1 to this Amendment.
4. Section 2.1(j) of the Asset Purchase Agreement shall be replaced with the following:

“(j) all of the stock of the subsidiaries of the Selling Entities listed on Schedule 2.1(j) (the "Acquired Subsidiaries"), except as otherwise set forth on Schedule 2.1(j);”
5. Schedule 2.1(j) to the Asset Purchase Agreement is amended and restated and is attached as Exhibit 2 to this Amendment.
6. Schedule 2.2(h) to the Asset Purchase Agreement is amended and restated and is attached as Exhibit 3 to this Amendment (it being understood that the definition of “Specified IP” shall take into account the amendment and restatement of Schedule 2.2(h)).
7. Section 3.1 (Purchase Price) of the Asset Purchase Agreement shall be replaced with the following:

“3.1 Purchase Price. In consideration for the Purchased Assets, and subject to the terms and conditions of this Agreement, and the entry and effectiveness of the Sale Order, at the Closing, the Buyer and/or (in the Buyer’s sole discretion) a Buyer Affiliate shall assume the Assumed Liabilities by executing the Assumption Agreement and the Buyer shall pay to the Agent (in the manner described in the next sentence) an amount in cash equal to: (i) US\$42,500,000;

minus (ii) the amount, if any, by which the aggregate amount of the unrestricted cash of the Acquired Subsidiaries and their subsidiaries as of the Closing (the "Subsidiary Closing Cash") is less than US\$5,000,000 (based on the applicable exchange rate quoted in the Western Edition of the Wall Street Journal on the Business Day immediately preceding the Closing, it being understood that the amount to be subtracted from clause "(i)" pursuant to clause "(ii)" can not exceed US\$5,000,000) (the amount determined by subtracting the amount described in clause "(ii)" from the amount described in clause "(i)" of this sentence, the "Purchase Price"). At the Closing, the Buyer shall: (a) pay and deliver to the Agent, by wire transfer of immediately available U.S. funds to an account designated by the Agent prior to the Closing, the Purchase Price less the Deposit (and less interest accrued on the Deposit); and (b) instruct the Escrow Holder (as defined below) to deliver the Deposit (and any interest accrued thereon) to the Agent, by wire transfer of immediately available U.S. funds to an account designated by the Agent prior to the Closing."

8. All references in the Asset Purchase Agreement to the "Base Purchase Price" shall be deemed to be references to the "Purchase Price."

9. Section 4.1 of the Asset Purchase Agreement shall be amended and restated as follows:

"Time and Place of the Closing. Upon the terms and subject to the satisfaction of the conditions contained in Article VIII of this Agreement, the closing of the sale of the Purchased Assets and the assumption of the Assumed Liabilities contemplated by this Agreement (the "Closing") shall take place at the offices of Cooley Godward Kronish LLP, 3175 Hanover Street, Palo Alto, California, at 10:00 A.M. (local time) no later than the later of: (a) May 8, 2009; and (b) the second Business Day following the date on which the conditions set forth in Article VIII have been satisfied (other than the conditions with respect to actions the respective parties hereto will take at the Closing itself) or, to the extent permitted, waived by the applicable party in writing, or at such other place and time as the Buyer and the Seller may mutually agree (it being understood that if, as of the date on which all of the conditions to the Buyer's obligation to effect the purchase of the Purchased Assets set forth in Section 8.1 and Section 8.2, other than the requirement set forth in Section 8.1(c) that the Sale Order shall be a Final Order, have been satisfied, no Person has filed an appeal with respect to the Sale Order and no stay relating to the Sale Order has been issued, the Buyer shall waive the requirement in Section 8.1(c) that the Sale Order shall be a "Final Order"). The date and time at which the Closing actually occurs is herein referred to as the "Closing Date."

10. Section 4.2.(j) of the Asset Purchase Agreement shall be amended and restated as follows:

"(j) Except as set forth on Schedule 4.2(j) and subject to Section 7.15, stock certificates representing all of the shares in the Acquired Subsidiaries, duly

endorsed (or accompanied by duly executed stock powers) by the Selling Entity owning such shares (or other appropriate instruments necessary to transfer the Selling Entities' equity interests therein to the Buyer or a Buyer Affiliate.”

11. A new Schedule 4.2(j) shall be added to the Schedules to the Asset Purchase Agreement and is attached as Exhibit 4 to this Amendment.
12. Subsection (a) of Schedule 5.3 to the Asset Purchase Agreement is amended and restated and is attached as Exhibit 5 to this Amendment.
13. The following contract shall be added to the end of the first section entitled “Patent License Agreements” of Schedule 2.1(e) and to the end of subsection (e) of Schedule 5.3 to the Asset Purchase Agreement:

<b>Title</b>	<b>Parties</b>	<b>Entered</b>
Subcontracting Agreement, as amended by Amendment #1 dated September 2, 2004	University of Utah and Silicon Graphics, Inc.	October 1, 2003

14. Schedule 5.8(d) to the Asset Purchase Agreement is amended and restated and is attached as Exhibit 6 to this Amendment.
15. The punctuation mark at the end of subsection 4.2(j) of the Asset Purchase Agreement shall be removed and replaced with “; and” and a new subsection 4.2(k) shall be added to the Asset Purchase Agreement as follows:

“a License Agreement, in the form attached as Exhibit 7 to the Amendment, dated as of April 30, 2009, to this Agreement duly executed by the Selling Entities.”

16. Section 7.7(c) of the Asset Purchase Agreement shall be amended and restated as follows:

“After receipt of the list of Specified Employees described in Section 7.7(a) above, but prior to the Closing Date, the Selling Entities shall terminate, effective as of a time prior to the Closing, all Persons who would otherwise be Employees as of the Closing other than: (i) the Specified Employees; and (ii) any Employees who are not Specified Employees and whom the Selling Entities decide to retain following the Closing (including Employees identified by the Seller and agreed to by the Buyer prior to the Closing) (“Retained Employees”), and such termination shall be deemed to have been made at the request, and with the consent, of the Buyer. An Employee who satisfies the conditions described in any of clauses “(A)” through “(C)” of this sentence is referred to as a “Terminated Employee”: (A) such Employee is terminated pursuant to the preceding sentence, (B) such Employee is terminated at any other time on or after the Petition Date and prior to the Closing at the direction or with the consent of the Buyer (such consent not to

be unreasonably withheld in the case of a proposed termination for “cause”); or (C) such Employee is a Specified Employee who does not become a Transferred Employee as of the Closing.”

17. A new subsection 7.7(j) shall be added to the Asset Purchase Agreement as follows:

“(j) The Buyer shall indemnify the directors and officers of the Seller against: (i) any Liability imposed on them under the WARN Act with respect to employees of the Selling Entities who were terminated prior to the Petition Date; and (ii) the reasonable costs and expenses of legal counsel incurred by them in defending a claim made against them seeking to impose the Liability referred to in clause “(i)” of this Section 7.7(j); *provided, however*, that the maximum aggregate amount of indemnification payments that the Buyer may be required to make pursuant to this Section 7.7(j) shall be US\$400,000.”

18. A new subsection 7.7(k) shall be added to the Asset Purchase Agreement as follows:

“(k) The Buyer shall provide, to the extent not paid or funded prior to the Closing Date, the funding for: (i) claims under the self-insured portion of the Seller’s Group Health, Life, and Dental Plan (Plan 501), or any successor, predecessor, or related plan (the “Self-Insured Plan”) that were incurred by eligible participants in such plan (or other eligible dependents) for medical, vision, and/or prescription drug expenses covered under the Self-Insured Plan and incurred on or prior to the Closing Date and not paid prior to the Closing Date (a “Self Insured Claim”); (ii) the continuation of the stop loss insurance policy related to the Self-Insured Plan (and terminal liability coverage under such policy) as in effect on the Closing Date (the “Stop Loss Policy”), and (iii) claim administration fees and expenses relating to the Self-Insured Plan pursuant to the Administrative Services Contract (and any amendments or supplements thereto) between the Seller and Aetna Life Insurance Company of Connecticut or any of its affiliates (collectively, “Aetna”) (clauses (i), (ii) and (iii) hereof, the “Self-Insured Plan Costs”); *provided, however*, that: (A) the Buyer shall be obligated to provide funding for a Self Insured Claim only to the extent such Self Insured Claim is not covered by the Stop Loss Policy; and (B) the maximum aggregate amount of funding that the Buyer may be required to provide pursuant to this Section 7.7(k) shall be US\$2,100,000. Unless the parties otherwise agree after consulting with Aetna, the funding of the amounts set forth in the preceding sentence shall be effected by means of a US\$2,100,000 reduction in the amount of cash otherwise included in the Purchased Assets at the Closing under Section 2.1(a) (with such amount to be held by the Seller in trust (it being understood that the Seller shall not be required to form an actual trust entity) for the benefit of the eligible participants in such plan and, to the extent not utilized pursuant to this Section 7.7(k), for the benefit of the Buyer, or, if Aetna so agrees, by Aetna, in either case, with any portion of such US\$2,100,000 not utilized pursuant to this Section 7.7(k) by the 180th day following the Closing Date becoming a Purchased Asset to be delivered to the Buyer on the 180th day following the Closing Date). The parties shall cause such

US\$2,100,000 of funds to be used for, and only for, the purposes contemplated by this Section 7.7(k) (including the payment of Self-Insured Plan Costs) and the Seller shall use its reasonable best efforts to keep the Stop Loss Policy in effect for all relevant periods.”

19. Clause “(iii)” of the second sentence of Section 7.9 of the Asset Purchase Agreement is amended and restated as follows: “(iii) the entry of an order in the form of Exhibit C approving this Agreement, in a form and substance reasonably acceptable to the Buyer and the Agent and not subject to Rules 6004(h) and 6006(d) of the Federal Rules of Bankruptcy Procedure (the “Sale Order”).”

20. A new subsection 7.15 shall be added to the Asset Purchase Agreement as follows:

“From and after the Closing, the Selling Entities will use commercially reasonable efforts (including, for avoidance of doubt, cooperate in the preparation and submission of the relevant audited financial statements to the registration authorities in Malaysia for the transfer of the shares in Silicon Graphics Sdn. Bhd.) to deliver to Buyer or Buyer Affiliate any stock certificates (or other appropriate instruments of transfer) that are not delivered to the Buyer or Buyer Affiliate at the Closing and secure the release of any Encumbrances on the common stock (or other equity interests) of any Acquired Subsidiaries that are not released as of the Closing.”

21. Section 8.2(a) of the Asset Purchase Agreement is amended and restated as follows:

“each of the Selling Entities shall have performed and complied in all material respects with the covenants contained in this Agreement which are required to be performed and complied with by it on or prior to the Closing Date (it being understood that the transfer of an aggregate amount of approximately US\$293,000 from Hong Kong and Malaysia to one or more of the Selling Entities prior to April 28, 2009 shall not be taken into account in determining whether this condition has been satisfied);”

22. Section 8.2(b) of the Asset Purchase Agreement is amended and restated as follows:

“each of the Selling Entities’ representations and warranties which are set forth in this Agreement (except for those contained in Sections 5.1 and 5.10) shall be true and correct in all respects as of the date of this Agreement and as of the date the Sale Order is entered by the Bankruptcy Court (the “Sale Order Date”) as though made at and as of the Sale Order Date (except to the extent that any such representation or warranty speaks as of a particular date in which case such representation or warranty shall be true and correct in all respects as of such date); *provided, however*, that in determining the accuracy of such representations and warranties for purposes of this Section 8.2(b): (i) all materiality qualifications that are contained in such representations and warranties and that limit the scope of such representations and warranties shall be disregarded; and (ii) any inaccuracies

in such representations and warranties shall be disregarded if the circumstances giving rise to all such inaccuracies (considered collectively) do not constitute, and would not reasonably be expected to have or result in, a Material Adverse Effect;”

23. Section 8.2(g) of the Asset Purchase Agreement is amended and restated as follows:

“between the date of this Agreement and the Sale Order Date, there shall have not have occurred any Material Adverse Effect, and no events, developments or effects shall have occurred (and no circumstances or conditions shall have come into existence) between the date of this Agreement and the Sale Order Date that would reasonably be expected to have or result in a Material Adverse Effect.”

24. Any action that may be taken by the Seller or any of the Selling Entities pursuant to Section 9.1(a), Section 9.3 or Section 10.1 of the Asset Purchase Agreement shall not be taken without the consent of the Agent, not to be unreasonably withheld, conditioned or delayed.

25. The language in the second bullet point of Annex A to the Asset Purchase Agreement that reads “by no later than the date of the entry of the Sale Order (as defined in the Agreement) (the “Sale Order Date”)” shall be replaced with the following: “by no later than the later of the entry of the Sale Order (as defined in the Agreement) or May 1, 2009 (such later date being referred to as the “Sale Order Date”)”.

26. All other provisions of the Asset Purchase Agreement shall continue in full force and effect. The provisions in Article X of the Asset Purchase Agreement and Section 1.2 shall apply to this Amendment as if included in this Amendment.

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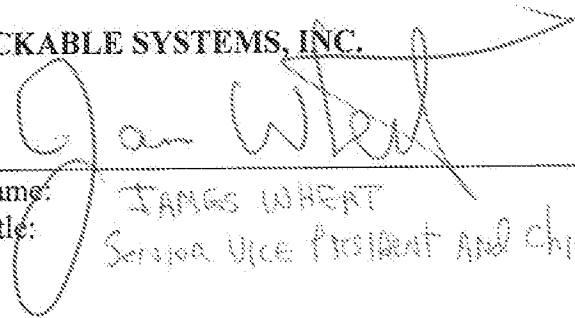
IN WITNESS WHEREOF, the parties hereto have caused this Amendment to be executed as of the date first above written by their respective officers thereunto duly authorized.

RACKABLE SYSTEMS, INC.

By:

Name:

Title:



JANGS WHERNT  
Senior Vice President and Chief Financial Officer

IN WITNESS WHEREOF, the parties hereto have caused this Amendment to be executed as of the date first above written by their respective officers thereunto duly authorized.

SILICON GRAPHICS, INC.

By: [Signature]  
Name: Greg Wood  
Title: CFO

SILICON GRAPHICS REAL ESTATE, INC.

By: [Signature]  
Name: Greg Wood  
Title: Chief Financial Officer

SILICON GRAPHICS WORLD TRADE CORPORATION

By: [Signature]  
Name: Greg Wood  
Title: Director Chief Financial Officer

SILICON GRAPHICS FEDERAL, INC.

By: \_\_\_\_\_  
Name:  
Title:

PARAGRAPH INTERNATIONAL, INC.

By: [Signature]  
Name: Greg Wood  
Title: Chief Financial Officer

WTI DEVELOPMENT, INC.

By: [Signature]  
Name: Greg Wood  
Title: Chief Financial Officer

IN WITNESS WHEREOF, the parties hereto have caused this Amendment to be executed as of the date first above written by their respective officers thereunto duly authorized.

SILICON GRAPHICS, INC.

By: \_\_\_\_\_  
Name:  
Title:

SILICON GRAPHICS REAL ESTATE, INC.

By: \_\_\_\_\_  
Name:  
Title:

SILICON GRAPHICS WORLD TRADE CORPORATION

By: \_\_\_\_\_  
Name:  
Title:

SILICON GRAPHICS FEDERAL, INC.

By: *Kent Rasmussen*  
Name: *Kent Rasmussen*  
Title: *Secretary*

PARAGRAPH INTERNATIONAL, INC.

By: \_\_\_\_\_  
Name:  
Title:

WTI DEVELOPMENT, INC.

By: \_\_\_\_\_  
Name:  
Title:

SILICON STUDIO, INC.

By: [Signature]  
Name: Greg Wood  
Title: Chief Financial officer

SILICON GRAPHICS OF MANHATTAN, INC.

By: [Signature]  
Name: Diane Gibson  
Title: President

CRAY RESEARCH, L.L.C.

By: [Signature]  
Name: Greg Wood  
Title: Chief Financial officer

CRAY FINANCIAL CORP.

By: [Signature]  
Name: Greg Wood  
Title: Chief Financial officer

CRAY RESEARCH AMERICA LATINA LTD.

By: [Signature]  
Name: Greg Wood  
Title: Chief Financial officer

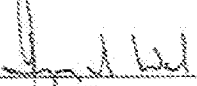
CRAY RESEARCH (EASTERN EUROPE)  
LTD.

By: [Signature]  
Name: Greg Wood  
Title: Chief Financial officer

CRAY RESEARCH (INDIA) LTD.

By: [Signature]  
Name: Greg Wood  
Title: Chief Financial officer

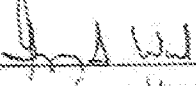
CRAY ASIA/PACIFIC, INC.

By: 

Name: Greg Wood

Title: Chief Financial Officer

CRAY RESEARCH INTERNATIONAL INC.

By: 

Name: Greg Wood

Title: Chief Financial Officer

**EXHIBIT 1**  
**AMENDED AND RESTATED SCHEDULE 2.1(f)**  
**SPECIFIED IP**

See attached.

Schedule 2.1(f)

US Patents and Patent Applications

Case Number	Country	InvTitle	Technology	PatNumber	IssDate	Application Status
0007.0151	US	BUS CONTROL SYSTEM FOR ARBITRATING REQUESTS WITH PREDETERMINED ON/OFF TIME LIMITATIONS	COMPUTER SYS ARCH	5193193	3/9/1993	Granted
0007.51	US	SYNCHRONIZED DRAM CONTROL APPARATUS USING TWO DIFFERENT CLOCK RATES	COMPUTER SYS ARCH	5179667	1/12/1993	Granted
0015.00	JP	COMPUTER THREE-WAY TRANSFER OPERATION	COMPUTER SYS ARCH	3120382	10/20/2000	Granted
0015.51	US	COMPUTER THREE-WAY TRANSFER OPERATION	COMPUTER SYS ARCH	5261074	11/9/1993	Granted
0019.00	US	RACK AND PINION RETAINING AND RELEASE DEVICE FOR REMOVABLE COMPUTER COMPONENTS	MECH/PHYS	5325263	6/28/1994	Granted
0032.00	US	AN IMPROVED METHOD AND APPARATUS FOR CLOCKING IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	5495596	2/27/1996	Granted
0038.00	GB	COMPUTER HOUSING	MECH/PHYS	2020473	10/14/1992	Granted
0042.00	US	RETAINING AND RELEASE MECHANISM FOR COMPUTER STORAGE DEVICES INCLUDING A PAWL LATCH ASSEMBLY	MECH/PHYS	5269698	12/14/1993	Granted

Schedule 2.1(f)

0051.00	US	APPARATUS AND METHOD FOR DETECTING THE ACTIVITIES OF A PLURALITY PROCESSORS ON A SHARED BUS	COMPUTER SYS ARCH	5423008	6/6/1995	Granted
0104.00	US	HIGH MEMORY CAPACITY DRAM SIMM	COMPUTER SYS ARCH	5272664	12/21/1993	Granted
0105.00	US	A SYSTEM AND METHOD FOR FAIR ARBITRATION ON A MULTI-DOMAIN MULTIPROCESSOR BUS	SYSTEMS SOFTWARE	5509125	4/16/1996	Granted
0106.00	US	A SYSTEM AND METHOD OF IMPLEMENTING READ RESOURCES TO MAINTAIN CACHE COHERENCY IN A MULTIPROCESSOR ENVIRONMENT PERMITTING SPLIT TRANSACTIONS	SYSTEMS SOFTWARE	5504874	4/2/1996	Granted
0106.51	US	SYSTEM AND METHOD OF IMPLEMENTING READ RESOURCES IN A MULTIPROCESSOR ENVIRONMENT	SYSTEMS SOFTWARE	5664151	9/2/1997	Granted
0112.00	US	SYSTEM AND METHOD FOR GENERATING A READ-MODIFY-WRITE OPERATION	MICROPROC ARCH	5544331	8/6/1996	Granted
0113.00	US	A SYSTEM AND METHOD FOR PIGGYBACKING OF READ RESPONSES ON A SHARED MEMORY MULTIPROCESSOR BUS	MICROPROC ARCH	5655102	8/5/1997	Granted
0121.00	US	TOP, SIDES AND FRONT PORTIONS OF A COMPUTER HOUSING	MECH/PHYS	D363703	10/31/1995	Granted



**Schedule 2.1(f)**

0127.00	US	DISK DRIVE BRACKET TOP FRONT AND SIDE PORTIONS OF A COMBINED COMPUTER HOUSING AND SUPPORT STANDS	MECH/PHYS	5564804	10/15/1996	Granted
0129.00	US	ARRANGEMENT FOR MODIFYING ELECTRICAL PRINTED CIRCUIT BOARDS	MECH/PHYS	D367269	2/20/1996	Granted
0136.00	US	REMOTE CONTROL DESIGN	MECH/PHYS	5834705	11/10/1998	Granted
0167.00	US	HIGH PERFORMANCE SINUSOIDAL HEAT SINK REMOVAL FROM ELECTRONIC EQUIPMENT	MECH/PHYS	D398009	9/8/1998	Granted
0176.00	US	HIERARCHICAL FAT HYPERCUBE ARCHITECTURE FOR PARALLEL PROCESSING SYSTEMS	GRAPHICS	5709263	1/20/1998	Granted
0190.00	US	DIRECTORY-BASED COHERENCE PROTOCOL ALLOWING EFFICIENT DROPPING OF CLEAN- EXCLUSIVE DATA	COMPUTER SYS ARCH	5669008	9/16/1997	Granted
0192.00	US	SYSTEM AND METHOD FOR MULTIPROCESSOR PARTITIONING TO SUPPORT HIGH AVAILABILITY	COMPUTER SYS ARCH	5680576	10/21/1997	Granted
0193.00	US	PROCESSOR-INCLUSIVE MEMORY MODULE	MICROPROC ARCH	5991895	11/23/1999	Granted
0197.00	US	PROCESSOR-INCLUSIVE MEMORY MODULE	COMPUTER SYS ARCH	5710733	1/20/1998	Granted
0197.01	US	PROCESSOR-INCLUSIVE MEMORY MODULE	COMPUTER SYS ARCH	5999437	12/7/1999	Granted

Schedule 2.1(f)

0197.0151	US	PROCESSOR-INCLUSIVE MEMORY MODULE	MECH/PHYS	5867419	2/2/1999	Granted
0200.00	US	DISTRIBUTED GLOBAL CLOCK SYSTEM	COMPUTER SYS ARCH	5822381	10/13/1998	Granted
0201.00	US	PROGRAMMABLE, DISTRIBUTED NETWORK ROUTING	COMPUTER SYS ARCH	5721819	2/24/1998	Granted
0202.00	JP	PAGE MIGRATION IN A NON- UNIFORM MEMORY ACCESS (NUMA) SYSTEM	COMPUTER SYS ARCH	3889044	12/8/2006	Granted
0202.51	US	APPARATUS AND METHOD FOR PAGE MIGRATION IN A NON- UNIFORM MEMORY ACCESS (NUMA) SYSTEM	COMPUTER SYS ARCH	5727150	3/10/1998	Granted
0203.00	US	SYSTEM & METHOD FOR MAINTAINING COHERENCY OF VIRTUAL-TO-PHYSICAL MEMORY TRANSLATIONS IN A MICROPROC MULTIPROCESSOR COMPUTER ARCH		5787476	7/28/1998	Granted
0203.01	US	SYSTEM AND METHOD FOR MAINTAINING COHERENCY OF VIRTUAL-TO-PHYSICAL MEMORY TRANSLATIONS IN A COMPUTER MULTIPROCESSOR COMPUTER	COMPUTER SYS ARCH	6182195	1/30/2001	Granted
0204.00	US	CACHE COHERENCY USING FLEXIBLE DIRECTORY BIT VECTORS	COMPUTER SYS ARCH	5634110	5/27/1997	Granted

Schedule 2.1(f)

0205.00	EP	SYSTEM AND METHOD FOR THE SYNCHRONOUS TRANSMISSION OF DATA IN A COMMUNICATION NETWORK UTILIZING A SOURCE CLOCK SIGNAL TO LATCH SERIAL DATA INTO FIRST REGISTERS AND A HANDSHAKE SIGNAL TO LATCH PARALLEL DATA INTO SECOND REGISTERS	COMPUTER SYS ARCH	0826179	11/3/1999	Granted
0205.00	FR	SYSTEM AND METHOD FOR THE SYNCHRONOUS TRANSMISSION OF DATA IN A COMMUNICATION NETWORK UTILIZING A SOURCE CLOCK SIGNAL TO LATCH SERIAL DATA INTO FIRST REGISTERS AND A HANDSHAKE SIGNAL TO LATCH PARALLEL DATA INTO SECOND REGISTERS	COMPUTER SYS ARCH	0826179	11/3/1999	Granted
0205.00	GB	SYSTEM AND METHOD FOR THE SYNCHRONOUS TRANSMISSION OF DATA IN A COMMUNICATION NETWORK UTILIZING A SOURCE CLOCK SIGNAL TO LATCH SERIAL DATA INTO FIRST REGISTERS AND A HANDSHAKE SIGNAL TO LATCH PARALLEL DATA INTO SECOND REGISTERS	COMPUTER SYS ARCH	0826179	11/3/1999	Granted

Schedule 2.1(f)

0205.00	US	SYSTEM AND METHOD FOR THE SYNCHRONOUS TRANSMISSION OF DATA IN A COMMUNICATION NETWORK UTILIZING A SOURCE CLOCK SIGNAL TO LATCH SERIAL DATA INTO FIRST REGISTERS AND A HANDSHAKE SIGNAL TO LATCH PARALLEL DATA INTO SECOND REGISTERS	COMPUTER SYS ARCH	5768529	6/16/1998	Granted
0205.00	DE	SYSTEM AND METHOD FOR THE SYNCHRONOUS TRANSMISSION OF DATA IN A COMMUNICATION NETWORK UTILIZING A SOURCE CLOCK SIGNAL TO LATCH SERIAL DATA INTO FIRST REGISTERS AND A HANDSHAKE SIGNAL TO LATCH PARALLEL DATA INTO SECOND REGISTERS	COMPUTER SYS ARCH	69605037.4	11/3/1999	Granted
0205.00	JP	SYSTEM AND METHOD FOR THE SYNCHRONOUS TRANSMISSION OF DATA IN A COMMUNICATION NETWORK UTILIZING A SOURCE CLOCK SIGNAL TO LATCH SERIAL DATA INTO FIRST REGISTERS AND A HANDSHAKE SIGNAL TO LATCH PARALLEL DATA INTO SECOND REGISTERS	COMPUTER SYS ARCH			Pending
0206.51	US	SYSTEM AND METHOD FOR INPUT/OUTPUT FLOW CONTROL IN A MULTIPROCESSOR COMPUTER SYSTEM	COMPUTER SYS ARCH	5974456	10/26/1999	Granted

**Schedule 2.1(f)**

0207.00	EP	DUAL IN-LINE MEMORY MODULE (DIMM)	COMPUTER SYS ARCH	1020864	4/10/2002	Granted
0207.00	FR	DUAL IN-LINE MEMORY MODULE (DIMM)	COMPUTER SYS ARCH	1020864	4/10/2002	Granted
0207.00	GB	DUAL IN-LINE MEMORY MODULE (DIMM)	COMPUTER SYS ARCH	1020864	4/10/2002	Granted
0207.00	DE	DUAL IN-LINE MEMORY MODULE (DIMM)	COMPUTER SYS ARCH	69610662.0	10/18/2000	Granted
0207.00	DE	DUAL IN-LINE MEMORY MODULE (DIMM)	COMPUTER SYS ARCH	69620650.1	4/10/2002	Granted
0207.00	JP	DUAL IN-LINE MEMORY MODULE (DIMM)	COMPUTER SYS ARCH			Pending
0207.51	US	HIGH MEMORY CAPACITY DIMM WITH DATA AND STATE MEMORY	COMPUTER SYS ARCH	5790447	8/4/1998	Granted
0207.52	US	HIGH MEMORY CAPACITY DIMM WITH DATA AND STATE MEMORY	COMPUTER SYS ARCH	6049476	4/11/2000	Granted
0209.00	US	SYSTEM & METHOD TO REDUCE PHASE OFFSET & PHASE JITTER IN PHASE-LOCKED & DELAY-LOCKED LOOPS USING SELF-BIASED CIRCUITS	MICROPROC ARCH	5727037	3/10/1998	Granted
0210.00	US	RAID SYSTEM WITH FIBER CHANNEL ARBITRATED LOOP	COMPUTER SYS ARCH	5812754	9/22/1998	Granted
0211.00	US	HEATSINK AND METHOD OF FORMING A HEATSINK	MECH/PHYS	5829512	11/3/1998	Granted

Schedule 2.1(f)

0222.00	US	MULTI-CONFIGURABLE PUSH-PULL/OPEN-DRAIN DRIVER CIRCUIT	MICROPROC ARCH	5811997	9/22/1998	Granted
0223.00	US	SYSTEM AND METHOD TO REDUCE PHASE JITTER IN DELAY-LOCKED LOOPS	CIRCUIT DESIGN	5790612	8/4/1998	Granted
0235.51	US	DIMM PAIR WITH DATA MEMORY AND STATE MEMORY	COMPUTER SYS ARCH	5686730	11/11/1997	Granted
0239.00	US	ACKNOWLEDGE TRIGGERED FORWARDING OF EXTERNAL BLOCK DATA RESPONSES IN A MULTIPROCESSOR	COMPUTER SYS ARCH	5832306	11/3/1998	Granted
0240.00	US	SYSTEM AND METHOD FOR UNCACHED STORE BUFFERING IN A MICROPROCESSOR	COMPUTER SYS ARCH	5963981	10/5/1999	Granted
0252.00	US	PROCEDURE AND SYSTEM FOR PLACEMENT OPTIMIZATION OF CELLS WITHIN CIRCUIT BLOCKS BY OPTIMIZING PLACEMENT OF INPUT/OUTPUT PORTS WITHIN AN INTEGRATED CIRCUIT DESIGN	CIRCUIT DESIGN	5757658	5/26/1998	Granted
0264.00	US	A METHOD TO PIPELINE WRITE MISSES IN SHARED CACHE MULTIPROCESSOR SYSTEMS	MICROPROC ARCH	5875468	2/23/1999	Granted
0265.00	US	READILY REMOVABLE HEAT SINK ASSEMBLY	MECH/PHYS	5662163	9/2/1997	Granted
0270.00	US	APPARATUS AND METHOD FOR DETERMINING THE SPEED OF A SEMICONDUCTOR CHIP	CIRCUIT DESIGN	5818250	10/6/1998	Granted

**Schedule 2.1(f)**

0280.00	US	SCAN CHAIN FOR SHIFTING THE STATE OF A PROCESSOR INTO MEMORY AT A SPECIFIED POINT DURING SYSTEM OPERATION FOR TESTING PURPOSES	SYSTEMS SOFTWARE	5671235	9/23/1997	Granted
0283.00	US	SCA DRIVE SLED ASSEMBLY	MECH/PHYS	5654873	8/5/1997	Granted
0285.00	US	HIGH PERFORMANCE SPIRAL HEAT SINK	MECH/PHYS	5661638	8/26/1997	Granted
0320.00	US	HEAT SINK WITH INTEGRAL ATTACHMENT MECHANISM	MECH/PHYS	5735340	4/7/1998	Granted
0327.00	US	APPARATUS AND METHOD FOR DYNAMIC CENTRAL PROCESSING UNIT CLOCK ADJUSTMENT	MICROPROC ARCH	5774704	6/30/1998	Granted
0328.00	US	SYSTEM AND METHOD FOR FINDING PREEMPTED THREADS IN A MULTI-THREADED APPLICATION	SYSTEMS SOFTWARE	6418460	7/9/2002	Granted
0329.00	US	BGA LAND PATTERN	MECH/PHYS	6091155	7/18/2000	Granted
0338.00	US	BULK HEAD GASKET ASSEMBLY	MECH/PHYS	5770822	6/23/1998	Granted
0366.00	US	DISK DRIVE LOADING MECHANISM	MECH/PHYS	5790373	8/4/1998	Granted
0367.00	FR	COMPACT COMPUTER HOUSING (AMENDED)	MECH/PHYS	974651	8/6/1997	Granted

Schedule 2.1(f)

0367.00	GB	COMPACT COMPUTER HOUSING (AMENDED)	MECH/PHYS	2068117	9/7/1998	Granted
0367.00	DE	COMPACT COMPUTER HOUSING (AMENDED)	MECH/PHYS	39753266 TM	11/7/1997	Granted
0367.00	AU	COMPACT COMPUTER HOUSING (AMENDED)	MECH/PHYS	746084(TM)	5/29/1998	Granted
0367.00	US	COMPACT COMPUTER HOUSING (AMENDED)	MECH/PHYS	D395286	6/16/1998	Granted
0368.00	US	PROGRAMMABLE PACKER AND UNPACKER WITH DITHERER	APPLICATION SOFTWARE	6065084	5/16/2000	Granted
0382.00	US	A TECHNIQUE FOR GUARANTEEING COMPLETION TIMES FOR BATCH JOBS WITHOUT STATIC PARTITIONING OF A MACHINE	SYSTEMS SOFTWARE	6353844	3/5/2002	Granted
0398.00	US	RESILIENT PANEL FOR HOUSING A MACHINE	MECH/PHYS	5897180	4/27/1999	Granted
0399.00	US	CABLE MANAGEMENT SYSTEM FOR A COMPUTER	MECH/PHYS	5957556	9/28/1999	Granted
0404.00	US	INSTRUCTION METHODS FOR PERFORMING DATA FORMATTING WHILE MOVING DATA BETWEEN MEMORY AND COMPUTER	A VECTOR REGISTER FILE SYS ARCH	5812147	9/22/1998	Granted



**Schedule 2.1(f)**

0408.00	US	DIRECT MEMORY ACCESS APPARATUS FOR TRANSFERRING A BLOCK OF DATA HAVING DISCONTINUOUS ADDRESSES USING AN ADDRESS CALCULATING CIRCUIT	CIRCUIT DESIGN	6108722	8/22/2000	Granted
0413.00	US	PACKET SWITCHED ROUTER ARCHITECTURE FOR PROVIDING MULTIPLE SIMULTANEOUS COMMUNICATIONS	COMPUTER SYS ARCH	6683876	1/27/2004	Granted
0413.00	DE	PACKET SWITCHED ROUTER ARCHITECTURE FOR PROVIDING MULTIPLE SIMULTANEOUS COMMUNICATIONS	COMPUTER SYS ARCH	DE69735546	3/22/2006	Granted
0413.00	FR	PACKET SWITCHED ROUTER ARCHITECTURE FOR PROVIDING MULTIPLE SIMULTANEOUS COMMUNICATIONS	COMPUTER SYS ARCH	EPO927477	3/22/2006	Granted
0413.00	GB	PACKET SWITCHED ROUTER ARCHITECTURE FOR PROVIDING MULTIPLE SIMULTANEOUS COMMUNICATIONS	COMPUTER SYS ARCH	EPO927477	3/22/2006	Granted
0413.00	EP	PACKET SWITCHED ROUTER ARCHITECTURE FOR PROVIDING MULTIPLE SIMULTANEOUS COMMUNICATIONS	COMPUTER SYS ARCH			Published

**Schedule 2.1(f)**

0414.00	US	GUARANTEED BANDWIDTH ALLOCATION METHOD IN A COMPUTER SYSTEM FOR INPUT/OUTPUT DATA TRANSFERS	COMPUTER SYS ARCH	5784569	7/21/1998	Granted
0415.00	US	PACKETIZED DATA TRANSMISSIONS IN A SWITCHED ROUTER ARCHITECTURE	COMPUTER SYS ARCH	6282195	8/28/2001	Granted
0415.51	US	PACKETIZED DATA TRANSMISSIONS IN A SWITCHED ROUTER ARCHITECTURE	COMPUTER SYS ARCH	6985484	1/10/2006	Granted
0416.00	US	HIGH BANDWIDTH PCI TO PACKET SWITCHED ROUTER BRIDGE HAVING MINIMIZED MEMORY LATENCY	COMPUTER SYS ARCH	5915104	6/22/1999	Granted
0419.00	US	COMPRESSION CONNECTOR LOADING AND PLACEMENT DEVICE FOR CONNECTING CIRCUIT BOARDS	MECH/PHYS	5730605	3/24/1998	Granted
0420.00	US		MECH/PHYS	5848906	12/15/1998	Granted
0421.00	FR	COMPACT COMPUTER HOUSING (DESIGN)	MECH/PHYS	974237	7/21/1997	Granted
0421.00	GB	COMPACT COMPUTER HOUSING (DESIGN)	MECH/PHYS	2067607	1/14/1998	Granted
0421.00	US	COMPACT COMPUTER HOUSING (DESIGN)	MECH/PHYS	D396698	8/4/1998	Granted
0421.00	DE	COMPACT COMPUTER HOUSING (DESIGN)	MECH/PHYS	M9706956.6	10/15/1997	Granted

**Schedule 2.1(f)**

0422.00	US	METHOD AND SYSTEM FOR SIMULTANEOUS HIGH BANDWIDTH INPUT OUTPUT	COMPUTER SYS ARCH	5991824	11/23/1999	Granted
0440.00	US	DISTRIBUTED SCHEDULING OF PARALLEL JOBS WITH NO KERNEL-TO-KERNEL COMMUNICATION	SYSTEMS SOFTWARE	6766515	7/20/2004	Granted
0446.00	US	ADAPTIVE FREQUENCY SYNTHESIZER WITH SYNCHRONIZATION	CIRCUIT DESIGN	6067411	5/23/2000	Granted
0448.00	US	SYSTEM FOR COMMUNICATIONS WHERE FIRST PRIORITY DATA TRANSFER IS NOT DISTURBED BY SECOND PRIORITY DATA TRANSFER AND WHERE ALLOCATED BANDWIDTH IS REMOVED WHEN PROCESS TERMINATES ABNORMALLY	COMPUTER SYS ARCH	RE38134	6/3/2003	Granted
0456.00	IT	HOUSING FOR DESKSIDE COMPUTER OR THE LIKE	MECH/PHYS	73445	2/13/2002	Granted
0456.00	FR	HOUSING FOR DESKSIDE COMPUTER OR THE LIKE	MECH/PHYS	971719	8/8/1997	Granted
0456.00	GB	HOUSING FOR DESKSIDE COMPUTER OR THE LIKE	MECH/PHYS	2064454	9/23/1996	Granted
0456.00	US	HOUSING FOR DESKSIDE COMPUTER OR THE LIKE	MECH/PHYS	D393249	4/7/1998	Granted
0456.00	DE	HOUSING FOR DESKSIDE COMPUTER OR THE LIKE	MECH/PHYS	M9702994.7	3/24/1997	Granted

Schedule 2.1(f)

0459.00	US	LOW-LATENCY REAL-TIME DISPATCHING IN GENERAL PURPOSE MULTIPROCESSOR SYSTEMS	SYSTEMS SOFTWARE	5928322	7/27/1999	Granted
0460.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR DYNAMICALLY ALLOCATING LARGE MEMORY PAGES OF DIFFERENT SIZES	COMPUTER SYS ARCH	6182089	1/30/2001	Granted
0461.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR VIRTUAL MEMORY SUPPORT FOR MANAGING TRANSLATION LOOK-ASIDE BUFFERS WITH MULTIPLE PAGE SIZE SUPPORT	COMPUTER SYS ARCH	6112285	8/29/2000	Granted
0462.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR MANAGING MEMORY IN A NON-UNIFORM MEMORY ACCESS SYSTEM	SYSTEMS SOFTWARE	6289424	9/11/2001	Granted
0462.01	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR MANAGING MEMORY IN A NON-UNIFORM MEMORY ACCESS SYSTEM	SYSTEMS SOFTWARE	6336177	1/1/2002	Granted
0464.00	US	SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR PAGE SHARING BETWEEN FAULT-ISOLATED CELLS IN A DISTRIBUTED SHARED MEMORY SYSTEM	SYSTEMS SOFTWARE	6148379	11/14/2000	Granted
0465.00	US	SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR ORGANIZING PAGE CACHES	SYSTEMS SOFTWARE	6115790	9/5/2000	Granted

**Schedule 2.1(f)**

0467.00	US	SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR DYNAMICALLY SIZING HASH TABLES	SYSTEMS SOFTWARE	5960434	9/28/1999	Granted
0468.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR ALLOCATING PHYSICAL MEMORY IN A DISTRIBUTED SHARED MEMORY NETWORK	SYSTEMS SOFTWARE	6249802	6/19/2001	Granted
0469.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR PROFILING THREAD VIRTUAL MEMORY ACCESS	COMPUTER SYS ARCH	5974536	10/26/1999	Granted
0472.00	US	REVERSE MAPPING PAGE FRAME DATA STRUCTURES TO PAGE TABLE ENTRIES	SYSTEMS SOFTWARE	6112286	8/29/2000	Granted
0477.00	US	POWER SWITCH PLUNGER MECHANISM	MECH/PHYS	5817997	10/6/1998	Granted
0478.00	US	ROTARY LATCH ASSEMBLY FOR A COMPUTER HOUSING	MECH/PHYS	5826922	10/27/1998	Granted
0485.00	IT	HOUSING FOR A COMPUTER	MECH/PHYS	73450	2/13/2002	Granted
0485.00	IN	HOUSING FOR A COMPUTER	MECH/PHYS	173463	9/23/1997	Granted
0485.00	FR	HOUSING FOR A COMPUTER	MECH/PHYS	971844	8/22/1997	Granted
0485.00	GB	HOUSING FOR A COMPUTER	MECH/PHYS	2064556	9/27/1996	Granted

Schedule 2.1(f)

0485.00	US	HOUSING FOR A COMPUTER	MECH/PHYS	D393449	4/14/1998	Granted
0485.00	DE	HOUSING FOR A COMPUTER SYSTEM AND METHOD FOR SCHEDULING AN EVENT SUBJECT TO THE AVAILABILITY OF REQUESTED PARTICIPANTS	MECH/PHYS	M9703109.7	3/27/1997	Granted
0492.00	US	NETWORK REQUEST DISTRIBUTION BASED ON STATIC RULES AND DYNAMIC PERFORMANCE DATA	SOFTWARE	5963913	10/5/1999	Granted
0493.00	US	ELECTROMAGNETIC INTERFERENCE SHIELDING ENCLOSURE AND HEAT SINK WITH COMPRESSION COUPLING MECHANISM	COMPUTER SYS ARCH	6173322	1/9/2001	Granted
0495.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR PAGE REPLICATION IN A NON-UNIFORM MEMORY ACCESS SYSTEM	MECH/PHYS	5880930	3/9/1999	Granted
0502.00	US	CIRCUIT FOR FILTERING A POWER SUPPLY FOR NOISE SENSITIVE DEVICES	SYSTEMS SOFTWARE	6167437	12/26/2000	Granted
0512.00	US	IMPROVED HOT-PLUG POWER SUPPLY FOR HIGH-AVAILABILITY COMPUTER SYSTEMS	CIRCUIT DESIGN	5825238	10/20/1998	Granted
0515.00	US	CROSSBAR SWITCH WITH COMMUNICATION RING BUS	COMPUTER SYS ARCH	6044424	3/28/2000	Granted
0529.00	US	COMMUNICATION RING BUS	COMPUTER SYS ARCH	6728206	4/27/2004	Granted

Schedule 2.1(f)

0533.00	US	LATCHING ASSEMBLY FOR A COMPUTER	GRAPHICS	5907962	6/1/1999	Granted
0535.00	US	SLIDING DOOR ASSEMBLY FOR A COMPUTER HOUSING	MECH/PHYS	5924780	7/20/1999	Granted
0541.00	US	MEDIA CO-PROCESSOR WITH GRAPHICS, VIDEO AND AUDIO TASKS PARTITIONED BY TIME DIVISION MULTIPLEXING	COMPUTER SYS ARCH	6275239	8/14/2001	Granted
0555.00	US	MODULAR CARD CAGE WITH CONNECTION MECHANISM	MECH/PHYS	6171120	1/9/2001	Granted
0558.00	US	DISTRIBUTED CONTROL AND SYNCHRONIZATION OF MULTIPLE DATA PROCESSORS USING FLEXIBLE COMMAND QUEUES	COMPUTER SYS ARCH	6055579	4/25/2000	Granted
0563.00	US	METHOD AND SYSTEM FOR EFFICIENT CONTEXT SWITCHING IN A COMPUTER GRAPHICS SYSTEM	COMPUTER SYS ARCH	6208361	3/27/2001	Granted
0602.00	US	INTEGRAL ACTUATOR FOR A PRINTED CIRCUIT BOARD	COMPUTER SYS ARCH	5967825	10/19/1999	Granted
0619.00	US	EXCHANGING MESSAGES BETWEEN COMPUTER SYSTEMS COMMUNICATIVELY COUPLED IN A COMPUTER SYSTEM NETWORK	COMPUTER SYS ARCH	6766358	7/20/2004	Granted
0624.00	US	METHOD AND SYSTEM FOR CLOCK CYCLE MEASUREMENT AND DELAY OFFSET	CIRCUIT DESIGN	6680636	1/20/2004	Granted

Schedule 2.1(f)

0626.00	US	AN INPUT/OUTPUT DEVICE MANAGED TIMER PROCESS	SYSTEMS SOFTWARE	6594787	7/15/2003	Granted
0655.00	US	ELECTRICALLY CONDUCTIVE PATH THROUGH A DIELECTRIC MATERIAL	MECH/PHYS	6541853	4/1/2003	Granted
0660.00	US	QUANTUM ACCELERATION OF CONVENTIONAL NON- QUANTUM COMPUTERS	COMPUTER SYS ARCH	6081882	6/27/2000	Granted
0688.00	US	METHOD AND APPARATUS FOR BROADCASTING INVALIDATION MESSAGES IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	6421712	7/16/2002	Granted
0711.00	US	MEMORY SYSTEM WITH MULTIPLE ADDRESSING AND CONTROL BUSES	COMPUTER SYS ARCH	5870325	2/9/1999	Granted
0711.51	US	MEMORY SYSTEM WITH ADDRESSING AND CONTROL BUSES	COMPUTER SYS ARCH	6078515	6/20/2000	Granted
0719.00	US	SCAN INTERFACE CHIP (SIC) SYSTEM AND METHOD FOR SCAN TESTING ELECTRONIC SYSTEMS	CIRCUIT DESIGN	6760876	7/6/2004	Granted
0733.00	US	METHOD AND DEVICE FOR CONTROLLING ACCESS TO MEMORY	SYSTEMS SOFTWARE	6393533	5/21/2002	Granted
0737.00	US	A METHOD TO ENABLE THE USE OF MORE DEVICES AND HIGHER BUS SPEEDS BY REDUCING DEVICE LOADING ON A PCI BUS	COMPUTER SYS ARCH	7197589	3/27/2007	Granted
0769.00	AU	COMPUTER HOUSING	MECH/PHYS	140491	4/20/2000	Granted



**Schedule 2.1(f)**

0769.00	GB	COMPUTER HOUSING	MECH/PHYS	2083735	8/18/1999	Granted
0769.00	DE	COMPUTER HOUSING	MECH/PHYS	49905366.4	6/2/1999	Granted
0769.00	FR	COMPUTER HOUSING	MECH/PHYS	591467/475	9/1/2000	Granted
0769.00	US	COMPUTER HOUSING	MECH/PHYS	D431242	9/26/2000	Granted
0770.00	FR	COMPUTER HOUSING OR SIMILAR ARTICLE (DESIGN)	MECH/PHYS	555348	9/17/1999	Granted
0770.00	JP	COMPUTER HOUSING OR SIMILAR ARTICLE (DESIGN)	MECH/PHYS	1084129	6/30/2000	Granted
0770.00	GB	COMPUTER HOUSING OR SIMILAR ARTICLE (DESIGN)	MECH/PHYS	2082932	8/10/1999	Granted
0770.00	DE	COMPUTER HOUSING OR SIMILAR ARTICLE (DESIGN)	MECH/PHYS	49904122.4	9/21/1999	Granted
0770.00	US	COMPUTER HOUSING OR SIMILAR ARTICLE (DESIGN)	MECH/PHYS	D417203	11/30/1999	Granted
0778.00	US	METHOD FOR EFFICIENT TRANSLATION OF MEMORY ADDRESSES IN COMPUTER SYSTEMS	SYSTEMS SOFTWARE	6223270	4/24/2001	Granted
0784.00	US	ADVANCED BOOT SEQUENCE FOR AN X86 COMPUTER SYSTEM THAT MAINTAINS EXPANSION CARD DEVICE COMPATIBILITY	COMPUTER SYS ARCH	6128731	10/3/2000	Granted

**Schedule 2.1(f)**

0790.00	US	DESIGN FOR CIRCUIT BOARD ORTHOGONAL INSTALLATION AND REMOVAL	MECH/PHYS	6256196	7/3/2001	Granted
0791.00	US	ADVANCED FIRMWARE BOOT SEQUENCE X86 COMPUTER SYSTEM THAT MAINTAINS LEGACY HARDWARE AND SOFTWARE COMPATIBILITY	COMPUTER SYS ARCH	6357003	3/12/2002	Granted
0794.00	US	MEMORY SYSTEM WITH SWITCHING FOR DATA ISOLATION	COMPUTER SYS ARCH	6115278	9/5/2000	Granted
0794.01	US	MEMORY SYSTEM WITH SWITCHING FOR DATA ISOLATION	COMPUTER SYS ARCH	6215686	4/10/2001	Granted
0796.00	US	DOOR ASSEMBLY FOR A COMPUTER	MECH/PHYS	6132019	10/17/2000	Granted
0802.00	US	METHOD FOR MANAGING CONCURRENT ACCESS TO VIRTUAL MEMORY DATA STRUCTURES	SYSTEMS SOFTWARE	6496909	12/17/2002	Granted
0812.51	US	CAMABLE, BLIND MATE/DEMATE OPTICAL EDGE CONNECTOR	MECH/PHYS			Pending
0813.00	US	SYSTEM AND METHOD FOR COOLING ELECTRONIC COMPONENTS	MECH/PHYS	6366461	4/2/2002	Granted
0814.00	US	NOISE ESTIMATION FOR COUPLED RC INTERCONNECTS IN DEEP-SUBMICRON INTEGRATED CIRCUITS	CIRCUIT DESIGN	6732065	5/4/2004	Granted
0815.00	US	MEMORY SYSTEM INCLUDING GUIDES THAT RECEIVE MEMORY MODULES	COMPUTER SYS ARCH	6629855	10/7/2003	Granted

**Schedule 2.1(f)**

0816.00	US	ABSTRACT VERIFICATION ENVIRONMENT	COMPUTER SYS ARCH	6856950	2/15/2005	Granted
0824.00	US	MULTIPROCESSOR SYSTEM AND METHOD OF ACCESSING DATA THEREIN	COMPUTER SYS ARCH	6651157	11/18/2003	Granted
0824.00	DE	MULTIPROCESSOR SYSTEM AND METHOD OF ACCESSING DATA THEREIN	COMPUTER SYS ARCH			Pending
0824.00	FR	MULTIPROCESSOR SYSTEM AND METHOD OF ACCESSING DATA THEREIN	COMPUTER SYS ARCH			Pending
0824.00	GB	MULTIPROCESSOR SYSTEM AND METHOD OF ACCESSING DATA THEREIN	COMPUTER SYS ARCH			Pending
0824.00	JP	MULTIPROCESSOR SYSTEM AND METHOD OF ACCESSING DATA THEREIN	COMPUTER SYS ARCH			Pending
0824.00	EP	MULTIPROCESSOR SYSTEM AND METHOD OF ACCESSING DATA THEREIN	COMPUTER SYS ARCH			Published
0824.00	WO	MULTIPROCESSOR SYSTEM AND METHOD OF ACCESSING DATA THEREIN	COMPUTER SYS ARCH			Published
0824.51	US	MULTIPROCESSOR SYSTEM AND METHOD OF ACCESSING DATA THEREIN	COMPUTER SYS ARCH			Pending
0826.00	US	METERED FORCE SINGLE POINT HEATSINK ATTACH MECHANISM	MECH/PHYS	6169659	1/2/2001	Granted
0833.00	US	SYNCHRONIZATION OF HARDWARE SIMULATION PROCESSES	CIRCUIT DESIGN	6879948B1	4/12/2005	Granted
0844.00	US	A CIRCUIT DESIGN FOR HIGH-SPEED DIGITAL COMMUNICATION	CIRCUIT DESIGN	6775339	8/10/2004	Granted

**Schedule 2.1(f)**

0848.00	US	CABLE DOCK FIXTURE WITH EMI SHIELDING SYSTEM AND METHOD TO PROVIDE POWER TO A SEA OF GATES STANDARD CELL BLOCK AN OVERHEAD BUMP GRID	MECH/PHYS	6280257	8/28/2001	Granted
0852.00	US	OPTIMIZING REPEATERS POSITIONING ALONG INTERCONNECTS	CIRCUIT DESIGN	6281108	8/28/2001	Granted
0857.00	US	(This is the second application filed from 610.00)	MICROPROC ARCH	6389581	5/14/2002	Granted
0858.00	US	A PROGRAMMABLE DIFFERENTIAL DELAY CIRCUIT WITH FINE DELAY ADJUSTMENT	CIRCUIT DESIGN	6417713	7/9/2002	Granted
0858.01	US	A PROGRAMMABLE DIFFERENTIAL DELAY CIRCUIT WITH FINE DELAY ADJUSTMENT	CIRCUIT DESIGN	6803872	10/12/2004	Granted
0858.02	US	A PROGRAMMABLE DIFFERENTIAL DELAY CIRCUIT WITH FINE DELAY ADJUSTMENT	CIRCUIT DESIGN	6486723	11/26/2002	Granted
0870.00	US	NODE TRANSLATION AND PROTECTION IN A CLUSTERED MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	7356026	4/8/2008	Granted
0871.00	US	REMOTE ADDRESS TRANSLATION IN A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	6925547	8/2/2005	Granted

Schedule 2.1(f)

0881.00	US	COMPENSATED CANCELLER AND EQUALIZER FOR SIMULTANEOUS BI-DIRECTIONAL COMMUNICATIONS IN A COMPUTING SYSTEM	CIRCUIT DESIGN		Pending
0882.00	US	SYSTEM AND METHOD FOR MAINTAINING AND RECOVERING DATA CONSISTENCY ACROSS MULTIPLE PAGES	SYSTEMS SOFTWARE	6721739	4/13/2004 Granted
0894.00	US	METHOD AND SYSTEM FOR RENAMING REGISTERS IN A MICROPROCESSOR	MICROPROC ARCH	7406587	7/29/2008 Granted
0909.00	US	BEZEL FOR A COMPUTER	MECH/PHYS	D429248	8/8/2000 Granted
0916.00	US	DETERMINING A WORST CASE SWITCHING FACTOR FOR INTEGRATED CIRCUIT DESIGN	CIRCUIT DESIGN	6353917	3/5/2002 Granted
0920.00	US	METHOD AND APPARATUS FOR ANALYZING BUFFER ALLOCATION TO A DEVICE ON A PERIPHERAL COMPONENT INTERCONNECT BUS	COMPUTER SYS ARCH	6397274	5/28/2002 Granted
0922.00	US	MODULAR INPUT/OUTPUT CONTROLLER CAPABLE OF ROUTING PACKETS OVER BUSES OPERATING AT DIFFERENT SPEEDS	COMPUTER SYS ARCH	6553446	4/22/2003 Granted
0923.00	US	TRANSLATION OF PCI LEVEL INTERRUPTS INTO PACKET BASED MESSAGES FOR EDGE EVENT DRIVE MICROPROCESSORS	COMPUTER SYS ARCH	6604161	8/5/2003 Granted

**Schedule 2.1(f)**

0924.01	US	BACKSHELL ASSEMBLY TRANSFER ATTRIBUTE ENCODING WITHIN AN ADDRESS ON A BUS	MECH/PHYS	7124505	10/24/2006	Granted
0925.00	US	UPSTREAM SITUATED APPARATUS AND METHOD FOR PROVIDING HIGH BANDWIDTH DATA FLOW CONTROL TO AN INPUT/OUTPUT UNIT	COMPUTER SYS ARCH	6711636	3/23/2004	Granted
0926.00	US	SYSTEM AND METHOD FOR A HIERARCHICAL SYSTEM MANAGEMENT	COMPUTER SYS ARCH	6622182	9/16/2003	Granted
0930.51	US	ARCHITECTURE OF A HIGHLY SCALABLE COMPUTING SYSTEM	COMPUTER SYS ARCH	6845410	1/18/2005	Granted
0930.5101	US	SYSTEM AND METHOD FOR A HIERARCHICAL SYSTEM MANAGEMENT	COMPUTER SYS ARCH	7219156	5/15/2007	Granted
0932.00	US	ARCHITECTURE OF A HIGHLY SCALABLE COMPUTING SYSTEM	CIRCUIT DESIGN	7031420	4/18/2006	Granted
0932.51	US	SYSTEM AND METHOD FOR ADAPTIVELY DESKEWING PARALLEL DATA SIGNALS RELATIVE TO A CLOCK	CIRCUIT DESIGN	7433441	10/7/2008	Granted
0932.5101	US	SYSTEM AND METHOD FOR ADAPTIVELY DESKEWING PARALLEL DATA SIGNALS RELATIVE TO A CLOCK	CIRCUIT DESIGN			Published

**Schedule 2.1(f)**

0933.00	US	DIAGNOSTIC SYSTEM AND METHOD FOR A HIGHLY SCALABLE COMPUTING SYSTEM	SYSTEMS SOFTWARE	6601183	7/29/2003	Granted
0936.00	US	COMPUTER MODULE MOUNTING SYSTEM AND METHOD	MECH/PHYS	6452805	9/17/2002	Granted
0937.00	JP	METHOD AND APPARATUS FOR HANDLING INVALIDATION REQUESTS TO PROCESSORS NOT PRESENT IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	3825692	7/7/2006	Granted
0937.00	US	METHOD AND APPARATUS FOR HANDLING INVALIDATION REQUESTS TO PROCESSORS NOT PRESENT IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	6339812	1/15/2002	Granted
0937.00	WO	METHOD AND APPARATUS FOR HANDLING INVALIDATION REQUESTS TO PROCESSORS NOT PRESENT IN A COMPUTER SYSTEM	COMPUTER SYS ARCH			Pending
0937.51	US	METHOD AND APPARATUS FOR HANDLING INVALIDATION REQUESTS TO PROCESSORS NOT PRESENT IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	6578115	6/10/2003	Granted
0949.00	US	METHOD AND APPARATUS FOR PREFETCHING INFORMATION AND STORING THE INFORMATION IN A STREAM BUFFER	COMPUTER SYS ARCH	6901500B1	5/31/2005	Granted
0952.00	US	METHOD AND ASSEMBLY FOR INSTALLATION OR REMOVAL OF PRINTED CIRCUIT CARD	MECH/PHYS	6496385	12/17/2002	Granted

**Schedule 2.1(f)**

0953.00	US	FASTENERLESS CIRCUIT BOARD SUPPORT	MECH/PHYS	6428352	8/6/2002	Granted
0956.51	US	PANEL	MECH/PHYS	D464054	10/8/2002	Granted
0957.51	US	PANEL	MECH/PHYS	D464973	10/29/2002	Granted
0958.51	US	INDUSTRIAL RACK	MECH/PHYS	D463796	10/1/2002	Granted
0960.00	US	PANEL GASKET PRINTED CIRCUIT BOARD CARRIER	MECH/PHYS	6483024	11/19/2002	Granted
0961.00	US	INSERTION/EXTRACTION ASSEMBLY MODULAR COMPUTING ARCHITECTURE HAVING COMMON COMMUNICATION	MECH/PHYS	6193532	2/27/2001	Granted
0962.00	US	INTERFACE MODULAR COMPUTING ARCHITECTURE HAVING COMMON COMMUNICATION	MECH/PHYS	6829666	12/7/2004	Granted
0962.00	WO	INTERFACE PARTITIONING A DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER TO FACILITATE SELECTIVE HARDWARE MAINTENANCE	MECH/PHYS			Published
0963.00	US	MULTIPROCESSOR NODE CONTROLLER CIRCUIT AND METHOD	COMPUTER SYS ARCH	6516372	2/4/2003	Granted
0965.00	DE		COMPUTER SYS ARCH	1222559	11/26/2003	Granted



**Schedule 2.1(f)**

0965.00	FR	MULTIPROCESSOR NODE CONTROLLER CIRCUIT AND METHOD	COMPUTER SYS ARCH	1222559	11/26/2003	Granted
0965.00	GB	MULTIPROCESSOR NODE CONTROLLER CIRCUIT AND METHOD	COMPUTER SYS ARCH	1222559	11/26/2003	Granted
0965.00	US	MULTIPROCESSOR NODE CONTROLLER CIRCUIT AND METHOD	COMPUTER SYS ARCH	6751698	6/15/2004	Granted
0965.00	EP	MULTIPROCESSOR NODE CONTROLLER CIRCUIT AND METHOD	COMPUTER SYS ARCH	EP1222559	11/26/2003	Granted
0965.00	JP	MULTIPROCESSOR NODE CONTROLLER CIRCUIT AND METHOD	COMPUTER SYS ARCH			Pending
0965.00	WO	MULTIPROCESSOR NODE CONTROLLER CIRCUIT AND METHOD	COMPUTER SYS ARCH			Published
0965.51	US	MULTIPROCESSOR NODE CONTROLLER CIRCUIT AND METHOD	COMPUTER SYS ARCH	7406086	7/29/2008	Granted
0965.5151	US	MULTIPROCESSOR NODE CONTROLLER CIRCUIT AND METHOD	COMPUTER SYS ARCH			Published
0967.00	EP	NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM	MICROPROC ARCH	1222557	4/18/2007	Granted
0967.00	US	NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM	MICROPROC ARCH	6973559 B1	12/6/2005	Granted
0967.00	FR	NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM	MICROPROC ARCH			Pending
0967.00	GB	NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM	MICROPROC ARCH			Pending

Schedule 2.1(f)

0967.00	JP	NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM	MICROPROC ARCH	6406257	6/18/2002	Granted	Pending
0967.00	WO	NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM	MICROPROC ARCH	6381681	4/30/2002	Granted	Pending
0967.00	DE	NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM	MICROPROC ARCH	6453408	9/17/2002	Granted	Published
0967.5151	US	NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM	MICROPROC ARCH	6532501	3/11/2003	Granted	Pending
0972.00	US	MODULAR AIR MOVING SYSTEM AND METHOD	MECH/PHYS	6546451	4/8/2003	Granted	6/18/2002
0974.00	US	SYSTEM AND METHOD FOR SHARED MEMORY PROTECTION IN A MULTIPROCESSOR COMPUTER	COMPUTER SYS ARCH	3856696	9/22/2006	Granted	4/30/2002
0975.00	US	SYSTEM AND METHOD FOR MEMORY PAGE MIGRATION IN A MULTI-PROCESSOR COMPUTER	COMPUTER SYS ARCH				9/17/2002
0977.00	US	SYSTEM AND METHOD FOR DISTRIBUTING OUTPUT QUEUE SPACE	COMPUTER SYS ARCH				3/11/2003
0978.00	US	METHOD AND APPARATUS FOR DECOUPLING PROCESSOR SPEED FROM MEMORY SUBSYSTEM SPEED IN A NODE CONTROLLER	COMPUTER SYS ARCH				4/8/2003
0979.00	JP	CONFIGURABLE SYNCHRONIZER FOR DOUBLE DATA RATE SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY	COMPUTER SYS ARCH				9/22/2006

Schedule 2.1(f)

0979.00	US	CONFIGURABLE SYNCHRONIZER FOR DOUBLE DATA RATE SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY	COMPUTER SYS ARCH	6279073	8/21/2001	Granted
0979.00	DE	CONFIGURABLE SYNCHRONIZER FOR DOUBLE DATA RATE SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY	COMPUTER SYS ARCH			Pending
0979.00	FR	CONFIGURABLE SYNCHRONIZER FOR DOUBLE DATA RATE SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY	COMPUTER SYS ARCH			Pending
0979.00	GB	CONFIGURABLE SYNCHRONIZER FOR DOUBLE DATA RATE SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY	COMPUTER SYS ARCH			Pending
0979.00	WO	CONFIGURABLE SYNCHRONIZER FOR DOUBLE DATA RATE SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY	COMPUTER SYS ARCH			Pending
0979.00	EP	CONFIGURABLE SYNCHRONIZER FOR DOUBLE DATA RATE SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY	COMPUTER SYS ARCH			Published
0981.00	US	METHOD AND APPARATUS FOR PROCESSING ERRORS IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	6457146	9/24/2002	Granted
0981.00	JP	METHOD AND APPARATUS FOR PROCESSING ERRORS IN A COMPUTER SYSTEM	COMPUTER SYS ARCH			Pending

**Schedule 2.1(f)**

0981.00	WO	METHOD AND APPARATUS FOR PROCESSING ERRORS IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	6487685	11/26/2002	Granted	Published
0982.00	US	SYSTEM AND METHOD FOR MINIMIZING ERROR CORRECTION CODE BITS IN VARIABLE SIZED DATA FORMATS	COMPUTER SYS ARCH	6487685	11/26/2002	Granted	
0983.00	US	DATA SYNCHRONIZER FOR A MULTIPLE RATE CLOCK SOURCE AND METHOD THEREOF	COMPUTER SYS ARCH	6529570	3/4/2003	Granted	
0986.00	US	ELECTRICAL CONNECTOR WITH LATCHING BACKPLATE ASSEMBLY	MECH/PHYS	6293813	9/25/2001	Granted	
0989.00	US	METHOD AND SYSTEM FOR HANDLING INTERRUPTS IN A NODE CONTROLLER WITHOUT ATTACHED PROCESSORS	COMPUTER SYS ARCH	6564277	5/13/2003	Granted	
1006.00	US	ELECTRONIC DEVICE SUPPORT AND METHOD	MECH/PHYS	6513770	2/4/2003	Granted	
1007.00	US	HOUSING GROUND BRACKET AND METHOD	MECH/PHYS	6574121	6/3/2003	Granted	
1008.00	US	CABLE ORGANIZER AND METHOD	MECH/PHYS	6438309	8/20/2002	Granted	
1009.00	US	SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR IMPLEMENTING SCALABLE MULTI-READER/SINGLE-WRITER LOCKS	SYSTEMS SOFTWARE	6601120	7/29/2003	Granted	

**Schedule 2.1(f)**

1018.00	US	CARRIER FOR COMPUTER PERIPHERAL DEVICE	MECH/PHYS	6381139	4/30/2002	Granted
1022.00	US	CONDITIONING AND FILLING SYSTEM FOR A SPRAY EVAPORATIVE COOLING WORKING FLUID	MECH/PHYS	6345515	2/12/2002	Granted
1030.00	US	OPTIMIZE GLOBAL NET TIMING WITH REPEATER BUFFERS	CIRCUIT DESIGN	6684373	1/27/2004	Granted
1032.00	US	RADIAL COMPUTER SYSTEM	MECH/PHYS	D436950	1/30/2001	Granted
1033.00	US	I/O IMPEDANCE CONTROLLER SYSTEM, METHOD, AND COMPUTER PROGRAM	CIRCUIT DESIGN	6703908	3/9/2004	Granted
1034.00	US	PRODUCT FOR REMOTE GRAPHICS PROCESSING	COMPUTER SYS ARCH	7274368	9/25/2007	Granted
1034.51	US	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR REMOTE GRAPHICS PROCESSING	COMPUTER SYS ARCH			Published
1036.00	US	SERVER DOOR	MECH/PHYS	D445112	7/17/2001	Granted
1037.00	US	DRIVE SLED DOOR METHOD AND SYSTEM FOR CALCULATING INTERCONNECT MOMENTS AND DELAY	MECH/PHYS	D445425	7/24/2001	Granted
1039.00	US		CIRCUIT DESIGN	6532575	3/11/2003	Granted

**Schedule 2.1(f)**

Patent No.	Country	Title	Class	No.	Date	Status
1045.00	US	MAINTAINING MEMBERSHIP IN HIGH AVAILABILITY COMPUTING SYSTEMS	APPLICATION SOFTWARE			Pending
1051.00	US	CACHE LINE CONVERTER SYSTEM AND METHOD FOR RETROFITTING A PROCESSOR INTO A SYSTEM DESIGNED FOR ANOTHER PROCESSOR	COMPUTER SYS ARCH	6751705	6/15/2004	Granted
1054.00	US	ADDRESS-SELECTED ATTRIBUTE ENCODING AND BYTE-SWAPPING	COMPUTER SYS ARCH			Pending
1057.00	US	METHOD AND SYSTEM FOR ESTIMATING INTERCONNECT DELAY	COMPUTER SYS ARCH			Pending
1071.00	US	METHOD AND SYSTEM FOR DETERMINING REPEATER ALLOCATION REGIONS	CIRCUIT DESIGN	6853969	2/8/2005	Granted
1072.00	US	VARIABLE MODE BI-DIRECTIONAL AND UNI-DIRECTIONAL COMPUTER COMMUNICATION SYSTEM	CIRCUIT DESIGN	6550048	4/15/2003	Granted
1084.00	US	AN INTERFACE FOR SYNCHRONOUS DATA TRANSFER BETWEEN DOMAINS CLOCKED AT DIFFERENT FREQUENCIES	COMPUTER SYS ARCH	6831924	12/14/2004	Granted
1085.00	US	DUAL-BANK FIFO FOR SYNCHRONIZATION OF READ DATA IN DDR SDRAM	CIRCUIT DESIGN	7333516	2/19/2008	Granted
1086.00	US	DISCRETE DELAY LINE SYSTEM AND METHOD	CIRCUIT DESIGN	6920526	7/19/2005	Granted
1087.00	US		CIRCUIT DESIGN	6518812	2/11/2003	Granted

Schedule 2.1(f)

1088.00	US	SYNTHESIS WITH AUTOMATED PLACEMENT INFORMATION FEEDBACK	CIRCUIT DESIGN	6701496	3/2/2004	Granted
1089.00	US	MEMORY DEVICE STORING DATA AND DIRECTORY INFORMATION THEREON, AND METHOD FOR PROVIDING THE DIRECTORY INFORMATION AND THE DATA IN THE MEMORY DEVICE	CIRCUIT DESIGN	6775742	8/10/2004	Granted
1091.00	US	METHOD AND APPARATUS FOR COMMUNICATING COMPUTER DATA FROM ONE POINT TO ANOTHER OVER A COMMUNICATIONS MEDIUM	COMPUTER SYS ARCH	7248635	7/24/2007	Granted
1092.00	US	QUEUE CIRCUIT AND METHOD FOR MEMORY ARBITRATION EMPLOYING SAME	COMPUTER SYS ARCH	7406554	7/29/2008	Granted
1093.00	US	SYSTEM AND METHOD FOR MEMORY ARBITRATION	COMPUTER SYS ARCH	6816947	11/9/2004	Granted
1094.00	US	METHOD AND APPARATUS FOR MANAGING NODE CONTROLLERS USING PARTITIONS IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	6877029B1	4/5/2005	Granted
1095.00	US	SYSTEM AND METHOD FOR REMOVING DATA FROM PROCESSOR CACHES IN A DISTRIBUTED MULTIPROCESSOR COMPUTER SYSTEM	COMPUTER SYS ARCH			Pending
1097.00	US	SYSTEM AND METHOD FOR REDUCING MEMORY LATENCY DURING READ REQUESTS	COMPUTER SYS ARCH	6678798	1/13/2004	Granted

**Schedule 2.1(f)**

1097.51	US	SYSTEM AND METHOD FOR REDUCING MEMORY LATENCY DURING READ REQUESTS	COMPUTER SYS ARCH	6938128	8/30/2005	Granted
1098.00	US	METHOD AND SYSTEM FOR USING HIGH COUNT INVALIDATE ACKNOWLEDGEMENTS IN DISTRIBUTED SHARED MEMORY SYSTEMS	COMPUTER SYS ARCH	6718442	4/6/2004	Granted
1099.00	US	SYSTEM AND METHOD FOR HANDLING UPDATES TO MEMORY IN A DISTRIBUTED SHARED MEMORY SYSTEM	COMPUTER SYS ARCH	6915387B1	7/5/2005	Granted
1100.00	US	METHOD AND SYSTEM FOR MANAGING DATA AT AN INPUT/OUTPUT INTERFACE FOR A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	6859863B1	2/22/2005	Granted
1101.00	US	METHOD AND SYSTEM FOR STORING DATA AT INPUT/OUTPUT (I/O) INTERFACES FOR A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	6795900	9/21/2004	Granted
1104.00	US	METHOD AND SYSTEM FOR EFFICIENT USE OF A MULTI-DIMENSIONAL SHARING VECTOR IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	6915388B1	7/5/2005	Granted
1105.00	US	SYSTEM AND METHOD FOR TRANSFERRING OWNERSHIP OF DATA IN A DISTRIBUTED SHARED MEMORY SYSTEM	COMPUTER SYS ARCH	6829683B1	12/7/2004	Granted
1106.51	US	MEMORY DAUGHTER CARD APPARATUS, CONFIGURATIONS, AND METHODS	MECH/PHYS	6726505	4/27/2004	Granted



**Schedule 2.1(f)**

1108.00	US	SYSTEM AND METHOD FOR ACCURATE ADJUSTMENT OF DISCRETE INTEGRATED CIRCUIT DELAY LINES	CIRCUIT DESIGN	6496048	12/17/2002	Granted
1109.00	US	PRINTED CIRCUIT BOARD STIFFENER	MECH/PHYS	6512676	1/28/2003	Granted
1109.01	US	PRINTED CIRCUIT BOARD STIFFENER	MECH/PHYS	6771517	8/3/2004	Granted
1110.00	US	METHOD AND APPARATUS FOR ACCESSING MMR REGISTERS DISTRIBUTED ACROSS A LARGE ASIC	COMPUTER SYS ARCH	6779072	8/17/2004	Granted
1112.00	US	METHOD AND SYSTEM FOR COVERING MULTIPLE RESOURCES WITH A SINGLE CREDIT IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	7007097B1	2/28/2006	Granted
1115.00	US	BAFFLE SYSTEM FOR AIR COOLED COMPUTER ASSEMBLY	MECH/PHYS	6483699	11/19/2002	Granted
1124.00	US	HEAT SINK ATTACHMENT CLIP ASSEMBLY PROCESS AND HEAT SINK DESIGN FOR HIGH POWERED PROCESSOR	MECH/PHYS	6434007	8/13/2002	Granted
1125.00	US	METHOD AND CIRCUIT FOR RELIABLE DATA CAPTURE IN THE PRESENCE OF BUS-MASTER CHANGEOVERS	MECH/PHYS	6831834	12/14/2004	Granted
1127.00	US	TRANSLATING WRITE COMMANDS TO INITIATE PURGE REQUESTS TO MULTIPLE PROCESSORS	CIRCUIT DESIGN	6839856	1/4/2005	Granted
1128.00	US		COMPUTER SYS ARCH	6604185	8/5/2003	Granted

**Schedule 2.1(f)**

1129.00	US	METHOD AND SYSTEM FOR MANAGING MEMORY IN A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	7069306	6/27/2006	Granted
1129.51	US	METHOD AND SYSTEM FOR MANAGING MEMORY IN A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	7500068	3/3/2009	Granted
1131.00	US	DRIVE SLED HANDLE	MECH/PHYS	D444471	7/3/2001	Granted
1132.00	US	DRIVE SLED COVER	MECH/PHYS	D444474	7/3/2001	Granted
1133.00	US	SERVER COVER	MECH/PHYS	D444461	7/3/2001	Granted
1134.00	US	METHOD AND SYSTEM FOR MAINTAINING DATA AT INPUT/OUTPUT (I/O) INTERFACES FOR A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	6981101	12/27/2005	Granted
1135.00	US	METHOD AND SYSTEM FOR CONTROLLING DATA ACCESS BETWEEN AT LEAST TWO MEMORY ARRANGEMENTS	CIRCUIT DESIGN	6839820B1	1/4/2005	Granted
1136.00	US	SYSTEM AND METHOD FOR GENERATING CLOCK SIGNALS	CIRCUIT DESIGN	6441666	8/27/2002	Granted
1142.00	US	MULTIPROCESSOR SYSTEM UTILIZING MULTIPLE LINKS TO IMPROVE POINT TO POINT BANDWIDTH	COMPUTER SYS ARCH	6643764	11/4/2003	Granted
1148.51	US	FLEXIBLE FAILOVER POLICIES IN HIGH AVAILABILITY COMPUTING SYSTEMS	COMPUTER SYS ARCH			Pending

Schedule 2.1(f)

1151.00	US	APPARATUS AND METHOD FOR CONTROLLING THE FLOW AND ORDERING OF DATA TRANSFERRED OVER MULTIPLE CHANNELS	COMPUTER SYS ARCH	7453878	11/18/2008	Granted
1151.51	US	APPARATUS AND METHOD FOR CONTROLLING THE FLOW AND ORDERING OF DATA TRANSFERRED OVER MULTIPLE CHANNELS	COMPUTER SYS ARCH			Pending
1164.00	US	PRINTED CIRCUIT BOARD COMPONENT PACKAGING	MECH/PHYS	6487082	11/26/2002	Granted
1165.00	US	SYSTEM AND METHOD FOR REPAIRING A MEMORY COLUMN	CIRCUIT DESIGN	6724669	4/20/2004	Granted
1168.00	US	SYSTEM AND METHOD FOR IMPROVING SPEED OF OPERATION OF INTEGRATED CIRCUITS	CIRCUIT DESIGN	6621300	9/16/2003	Granted
1172.00	US	TRANSPARENT DISTRIBUTION AND EXECUTION OF DATA IN A MULTIPROCESSOR ENVIRONMENT	APPLICATION SOFTWARE	7249357	7/24/2007	Granted
1177.00	US	SYNCHRONIZATION OF VERTICAL RETRACE FOR MULTIPLE PARTICIPATING GRAPHICS COMPUTERS	GRAPHICS	6791551	9/14/2004	Granted
1188.00	US	SERVER PRODUCT FACEPLATE	MECH/PHYS	D464056	10/8/2002	Granted
1189.00	JP	MULTI-DEVICE FACEPLATES	MECH/PHYS	1205713	4/2/2004	Granted

**Schedule 2.1(f)**

1189.00	GB	MULTI-DEVICE FACEPLATES	MECH/PHYS	3002487	7/18/2002	Granted
1189.00	DE	MULTI-DEVICE FACEPLATES	MECH/PHYS	402028961	3/27/2002	Granted
1189.00	FR	MULTI-DEVICE FACEPLATES	MECH/PHYS	671245-46	8/2/2002	Granted
1189.00	US	MULTI-DEVICE FACEPLATES	MECH/PHYS	D473561	4/22/2003	Granted
1219.00	US	CLUSTER FILE SYSTEM	SYSTEMS SOFTWARE	6950833B2	9/27/2005	Granted
1219.01	US	MEMBERSHIP VERSION SUPPORT	SYSTEMS SOFTWARE			Published
1219.02	US	INTERRUPTIBLE TOKEN ACQUISITION	SYSTEMS SOFTWARE			Pending
1219.53	US	RELOCATION OF METADATA SERVER WITH OUTSTANDING DMAPI REQUESTS	SYSTEMS SOFTWARE			Pending
1219.56	US	RECOVERY AND RELOCATION OF A DISTRIBUTED NAME SERVICE IN A CLUSTERED FILE SYSTEM	SYSTEMS SOFTWARE			Pending
1219.57	US	FAILURE HIERARCHY IN A CLUSTER FILESYSTEM	SYSTEMS SOFTWARE			Pending
1219.58	US	MESSAGING BETWEEN HETEROGENEOUS CLIENTS OF A STORAGE AREA NETWORK	SYSTEMS SOFTWARE			Pending

**Schedule 2.1(f)**

1219.60	US	CLUSTERED FILESYSTEM FOR MIX OF TRUSTED AND UNTRUSTED NODES	SYSTEMS SOFTWARE	6799238	9/28/2004	Granted	Pending
1219.61	US	MULTI-CLASS HETEROGENEOUS CLIENTS IN A CLUSTERED FILESYSTEM	SYSTEMS SOFTWARE				Pending
1253.00	US	BUS SPEED CONTROLLER USING SWITCHES	COMPUTER SYS ARCH	6799238	9/28/2004	Granted	
1257.00	US	METHOD AND SYSTEM FOR CACHE COHERENCE IN DSM MULTIPROCESSOR SYSTEM WITHOUT GROWTH OF THE SHARING VECTOR	COMPUTER SYS ARCH	6877030	4/5/2005	Granted	
1257.00	EP	METHOD AND SYSTEM FOR CACHE COHERENCE IN DSM MULTIPROCESSOR SYSTEM WITHOUT GROWTH OF THE SHARING VECTOR	COMPUTER SYS ARCH				Pending
1257.00	JP	METHOD AND SYSTEM FOR CACHE COHERENCE IN DSM MULTIPROCESSOR SYSTEM WITHOUT GROWTH OF THE SHARING VECTOR	COMPUTER SYS ARCH				Pending
1257.00	WO	METHOD AND SYSTEM FOR CACHE COHERENCE IN DSM MULTIPROCESSOR SYSTEM WITHOUT GROWTH OF THE SHARING VECTOR	COMPUTER SYS ARCH				Pending
1258.00	US	ACTUATABLE CONNECTOR SYSTEM	MECH/PHYS	6672878	1/6/2004	Granted	
1265.00	US	SYNCHRONIZATION CIRCUITS FOR THE DISTRIBUTION OF UNIQUE DATA TO SCATTERED LOCATIONS	MICROPROC ARCH	7302027	11/27/2007	Granted	

**Schedule 2.1(f)**

1269.00	US	SYSTEM AND METHOD FOR SELF-CALIBRATING SENSE AMPLIFIER STROBE	CIRCUIT DESIGN	6714464	3/30/2004	Granted
1269.00	EP	SYSTEM AND METHOD FOR SELF-CALIBRATING SENSE AMPLIFIER STROBE	CIRCUIT DESIGN		11/2/2006	Granted
1269.00	FR	SYSTEM AND METHOD FOR SELF-CALIBRATING SENSE AMPLIFIER STROBE	CIRCUIT DESIGN			Pending
1269.00	GB	SYSTEM AND METHOD FOR SELF-CALIBRATING SENSE AMPLIFIER STROBE	CIRCUIT DESIGN			Pending
1269.00	JP	SYSTEM AND METHOD FOR SELF-CALIBRATING SENSE AMPLIFIER STROBE	CIRCUIT DESIGN			Pending
1269.00	KR	SYSTEM AND METHOD FOR SELF-CALIBRATING SENSE AMPLIFIER STROBE	CIRCUIT DESIGN			Pending
1269.00	TW	SYSTEM AND METHOD FOR SELF-CALIBRATING SENSE AMPLIFIER STROBE	CIRCUIT DESIGN			Pending
1269.00	WO	SYSTEM AND METHOD FOR SELF-CALIBRATING SENSE AMPLIFIER STROBE	CIRCUIT DESIGN			Pending
1269.00	DE	SYSTEM AND METHOD FOR SELF-CALIBRATING SENSE AMPLIFIER STROBE	CIRCUIT DESIGN	1516341		Published
1270.00	US	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR APPLYING DIFFERENT TRANSPORT MECHANISMS FOR USER INTERFACE AND IMAGE PORTIONS OF A REMOTELY RENDERED IMAGE	COMPUTER SYS ARCH			Pending

Schedule 2.1(f)

1274.00	US	SYSTEM AND METHOD FOR CONVEYING INFORMATION	COMPUTER SYS ARCH		Pending
1277.00	US	EXTERNAL FAN AND METHOD FOR EXCHANGING AIR WITH MODULAR BRICKS IN A COMPUTER SYSTEM	MECH/PHYS	7088581B2	8/8/2006 Granted
1278.00	US	METHOD AND RACK FOR EXCHANGING AIR WITH MODULAR BRICKS IN A COMPUTER SYSTEM	MECH/PHYS	6882531	4/19/2005 Granted
1279.00	US	MODULAR FAN BRICK AND METHOD FOR EXCHANGING AIR IN A BRICK-BASED COMPUTER SYSTEM	MECH/PHYS	6765795	7/20/2004 Granted
1282.00	US	OPTIMIZING COMPUTATIONAL DENSITY IN HPC SYSTEMS	COMPUTER SYS ARCH		Published
1285.00	US	SYSTEM AND METHOD FOR HIERARCHICAL APPROXIMATION OF LEAST RECENTLY USED REPLACEMENT ALGORITHMS	MICROPROC ARCH	6986001B2	1/10/2006 Granted
1289.00	US	REAL-TIME STORAGE AREA NETWORK	SYSTEMS SOFTWARE		Pending
1289.00	WO	REAL-TIME STORAGE AREA NETWORK	SYSTEMS SOFTWARE		Pending
1289.00	DE	REAL-TIME STORAGE AREA NETWORK	SYSTEMS SOFTWARE	60316141308	Published
1289.00	EP	REAL-TIME STORAGE AREA NETWORK	SYSTEMS SOFTWARE	EPI 504 343	Published

**Schedule 2.1(f)**

1289.00	FR	REAL-TIME STORAGE AREA NETWORK	SYSTEMS SOFTWARE			Published
1289.00	GB	REAL-TIME STORAGE AREA NETWORK	SYSTEMS SOFTWARE			Published
1293.00	US	EMI CABLE BACKSHELL ASSEMBLY FOR HIGH-DENSITY INTERCONNECT	MECH/PHYS	7485003	2/3/2009	Granted
1294.00	US	SYSTEM AND METHOD FOR CONVEYING INFORMATION	COMPUTER SYS ARCH	7447794	11/4/2008	Granted
1294.01	US	METHOD AND SYSTEM FOR CONVEYING INFORMATION	COMPUTER SYS ARCH			Pending
1296.00	US	COMPUTER ENCLOSURE AND METHOD FOR MANUFACTURE	MECH/PHYS	6813151	11/2/2004	Granted
1301.00	US	GLOBAL POINTERS FOR SCALABLE PARALLEL APPLICATIONS	SYSTEMS SOFTWARE	7451278	11/11/2008	Granted
1305.00	US	DETECTION AND CONTROL OF RESOURCE CONGESTION BY A NUMBER OF PROCESSORS	MICROPROC ARCH			Pending
1306.00	US	SYSTEM AND METHOD FOR ENCODING PAGE SIZE INFORMATION	MICROPROC ARCH	7100018	8/29/2006	Granted
1309.00	US	SYSTEM AND METHOD FOR RESOLVING VIRTUAL ADDRESSES USING A PAGE SIZE TAG	MICROPROC ARCH	7089398	8/8/2006	Granted
1310.00	US	METHOD FOR EQUITABLE RESOURCE SHARING BETWEEN LOCAL AND NETWORK FILESYSTEMS	SYSTEMS SOFTWARE			Pending



**Schedule 2.1(f)**

1311.00	US	NETWORK FILESYSTEM ASYNCHRONOUS I/O SCHEDULING	SYSTEMS SOFTWARE		Pending
1322.00	US	A METHOD AND APPARATUS FOR MAINTAINING COHERENCE INFORMATION IN MULTI-CACHE SYSTEMS	COMPUTER SYS ARCH	7370154	5/6/2008 Granted
1323.00	US	MEMORY ACCESS MANAGEMENT IN A SHARED MEMORY MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	7174437	2/6/2007 Granted
1335.00	US	METHOD FOR PERFORMING CACHE COHERENCY IN A COMPUTER SYSTEM	COMPUTER SYS ARCH		Pending
1341.00	US	SYSTEM AND METHOD FOR PERFORMING ADDRESS TRANSLATION IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	7181589	2/20/2007 Granted
1341.00	JP	SYSTEM AND METHOD FOR PERFORMING ADDRESS TRANSLATION IN A COMPUTER SYSTEM	COMPUTER SYS ARCH		Pending
1341.00	WO	SYSTEM AND METHOD FOR PERFORMING ADDRESS TRANSLATION IN A COMPUTER SYSTEM	COMPUTER SYS ARCH		Pending
1341.00	EP	SYSTEM AND METHOD FOR PERFORMING ADDRESS TRANSLATION IN A COMPUTER SYSTEM	COMPUTER SYS ARCH		Published
1342.00	US	SYSTEM AND METHOD FOR PERFORMING MEMORY OPERATIONS IN A COMPUTING SYSTEM	COMPUTER SYS ARCH	7398359	7/8/2008 Granted

Schedule 2.1(f)

1342.51	US	SYSTEM AND METHOD FOR PERFORMING MEMORY OPERATIONS IN A COMPUTING SYSTEM	COMPUTER SYS ARCH	Pending
1354.00	US	BUS WIDE PCI HOT-PLUG DESIGN	COMPUTER SYS ARCH	Published
1365.00	US	FAULT TOLERANT MEMORY SYSTEM	COMPUTER SYS ARCH	Published
1371.00	US	METHOD AND SYSTEM FOR CONTROLLING UTILISATION OF A FILE SYSTEM	SYSTEMS SOFTWARE	Pending
1383.00	US	CLUSTER FILESYSTEM	SYSTEMS SOFTWARE	Published
1393.00	US	SYSTEM AND METHOD FOR SYNCHRONIZING DISTRIBUTION OF TRANSACTION OPERATIONS IN A COMPUTER SYSTEM	COMPUTER SYS ARCH	Pending
1401.00	US	NODE SYNCHRONIZATION FOR A MULTI-PROCESSOR COMPUTER SYSTEMS	COMPUTER SYS ARCH	12/9/2008 Granted
1401.51	US	NODE SYNCHRONIZATION FOR A MULTI-PROCESSOR COMPUTER SYSTEMS	COMPUTER SYS ARCH	Pending
1413.00	US	SYSTEM AND METHOD FOR REDUCING BACK FLOW IMPROVED GTL-DRIVER	MECH/PHYS	9/16/2008 Granted
1414.00	US	New title: Anticipatory Programmable Interface Pre-Driver	CIRCUIT DESIGN	7327167 2/5/2008 Granted

**Schedule 2.1(f)**

1419.00	US	METHOD AND ASSEMBLY FOR INSERTION AND EXTRACTION OF AN ELECTRONIC MODULE	MECH/PHYS	7466561	12/16/2008	Granted
1420.00	US	NEW SCALABLE DIRECTORY SCHEME FOR VERY LARGE SHARED MEMORY COMPUTER SYSTEMS	COMPUTER SYS ARCH			Published
1422.00	US	FAIL-SOFT CAPABILITY FOR MULTIPLE CPU SYSTEM	SYSTEMS SOFTWARE			Published
1436.00	US	GENERATION AND USE OF SYSTEM LEVEL DEFECT TABLES FOR MAIN MEMORY	COMPUTER SYS ARCH			Pending
1436.00	WO	GENERATION AND USE OF SYSTEM LEVEL DEFECT TABLES FOR MAIN MEMORY	COMPUTER SYS ARCH			Pending
1438.00	US	A MECHANISM FOR MAINTAINING ACCURATE GLOBAL REAL TIME CLOCK IN ANY ARBITRARY DISTRIBUTED SYSTEM WITH DYNAMIC PARTITIONS AND UNRELIABLE NETWORKS	COMPUTER SYS ARCH			Published
1438.00	WO	A MECHANISM FOR MAINTAINING ACCURATE GLOBAL REAL TIME CLOCK IN ANY ARBITRARY DISTRIBUTED SYSTEM WITH DYNAMIC PARTITIONS AND UNRELIABLE NETWORKS	COMPUTER SYS ARCH			Published
1442.00	US	LOAD BALANCING FOR PARALLEL TASKS	APPLICATION SOFTWARE			Pending

**Schedule 2.1(f)**

1447.00	US	DIRECTORY DELEGATION AND UPDATE PUSHING IN SHARED MEMORY MICROPROCESSOR SYSTEMS	COMPUTER SYS ARCH	7386680	6/10/2008	Granted
1454.00	US	DIRECT MEMORY ACCESS (DMA) ENGINE FOR DATA TRANSFERS OF UNKNOWN LENGTH	COMPUTER SYS ARCH			Published
1457.00	US	A Non-Saturating Fairness Protocol and Method for Nacking Systems	COMPUTER SYS ARCH			Pending
1468.00	US	VENTILATED HOUSING FOR ELECTRONIC COMPONENTS	MECH/PHYS	6975510	12/13/2005	Granted
1468.51	US	VENTILATED HOUSING FOR ELECTRONIC COMPONENTS	MECH/PHYS	7227751	6/5/2007	Granted
1469.00	US	SUB RACK BASED VERTICAL HOUSING FOR COMPUTER SYSTEMS	MECH/PHYS	6487080	11/26/2002	Granted
1469.51	US	SUB RACK BASED VERTICAL HOUSING FOR COMPUTER SYSTEMS	MECH/PHYS	6661667	12/9/2003	Granted
1469.5151	US	SUB RACK BASED VERTICAL HOUSING FOR COMPUTER SYSTEMS	MECH/PHYS	6847526	1/25/2005	Granted
1469.5152	US	SUB RACK BASED VERTICAL HOUSING FOR COMPUTER SYSTEMS	MECH/PHYS	7193857	3/20/2007	Granted
1469.5153	US	SUB RACK BASED VERTICAL HOUSING FOR COMPUTER SYSTEMS	MECH/PHYS			Pending
1470.00	US	APPARATUS AND METHOD FOR MAXIMIZING EQUIPMENT STORAGE DENSITY	MECH/PHYS	6862187	3/1/2005	Granted
1470.51	US	APPARATUS AND METHOD FOR MAXIMIZING EQUIPMENT STORAGE DENSITY	MECH/PHYS	6906925	6/14/2005	Granted

Schedule 2.1(f)

C029.00	JP	FLEXIBLE CHAINING IN VECTOR PROCESSOR WITH SELECTIVE USE OF VECTOR REGISTERS AS OPERAND AND RESULT REGISTERS	COMPUTER SYS ARCH	2511397	4/16/1996	Granted
C125.00	US	SINGLE DISK EMULATION INTERFACE FOR AN ARRAY OF SYNCHRONOUS SPINDLE DISK DRIVES	COMPUTER SYS ARCH	5128810	7/7/1992	Granted
C128.00	US	COMPUTER SIGNAL INTERCONNECT APPARATUS	COMPUTER SYS ARCH	5144691	9/1/1992	Granted
C132.00	US	OPTICAL CLOCK DISTRIBUTION METHOD AND APPARATUS	CIRCUIT DESIGN	5442475	8/15/1995	Granted
C147.00	EP	APPARATUS FOR CALCULATING DELAY WHEN EXECUTING VECTOR TAILGATING INSTRUCTIONS AND USING DELAY TO FACILITATE SIMULTANEOUS READING OF OPERANDS FROM AND WRITING OF RESULTS TO SAME VECTOR REGISTER	COMPUTER SYS ARCH	0419499	10/21/1992	Granted
C147.00	FR	APPARATUS FOR CALCULATING DELAY WHEN EXECUTING VECTOR TAILGATING INSTRUCTIONS AND USING DELAY TO FACILITATE SIMULTANEOUS READING OF OPERANDS FROM AND WRITING OF RESULTS TO SAME VECTOR REGISTER	COMPUTER SYS ARCH	0419499	10/21/1992	Granted

Schedule 2.1(f)

C147.00	GB	APPARATUS FOR CALCULATING DELAY WHEN EXECUTING VECTOR TAILGATING INSTRUCTIONS AND USING DELAY TO FACILITATE SIMULTANEOUS READING OF OPERANDS FROM AND WRITING OF RESULTS TO SAME VECTOR REGISTER	0419499	10/21/1992	Granted
C147.00	JP	APPARATUS FOR CALCULATING DELAY WHEN EXECUTING VECTOR TAILGATING INSTRUCTIONS AND USING DELAY TO FACILITATE SIMULTANEOUS READING OF OPERANDS FROM AND WRITING OF RESULTS TO SAME VECTOR REGISTER	4144153	6/27/2008	Granted
C147.00	US	APPARATUS FOR CALCULATING DELAY WHEN EXECUTING VECTOR TAILGATING INSTRUCTIONS AND USING DELAY TO FACILITATE SIMULTANEOUS READING OF OPERANDS FROM AND WRITING OF RESULTS TO SAME VECTOR REGISTER	5349677	9/20/1994	Granted
C147.00	DE	APPARATUS FOR CALCULATING DELAY WHEN EXECUTING VECTOR TAILGATING INSTRUCTIONS AND USING DELAY TO FACILITATE SIMULTANEOUS READING OF OPERANDS FROM AND WRITING OF RESULTS TO SAME VECTOR REGISTER	68903280.3	10/21/1992	Granted

**Schedule 2.1(f)**

C170.51	US	INTEGRATED CIRCUIT CHIP CARRIER LID	MECH/PHYS	5258576	11/2/1993	Granted
C186.00	US	SKEW-COMPENSATED CLOCK DISTRIBUTION SYSTEM	CIRCUIT DESIGN	5258660	11/2/1993	Granted
C186.01	US	METHOD OF ADJUSTING FOR CLOCK SKEW	CIRCUIT DESIGN	5414381	5/9/1995	Granted
C186.51	US	METHOD FOR ADJUSTING CLOCK SKEW	CIRCUIT DESIGN	5467040	11/14/1995	Granted
C190.00	US	APPARATUS FOR SHARING MEMORY IN A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	5142638	8/25/1992	Granted
C190.01	US	METHOD FOR SHARING MEMORY IN A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	5202970	4/13/1993	Granted
C192.00	US	FLEXIBLE AUTOMATED BONDING METHOD AND APPARATUS	MECH/PHYS	5127570	7/7/1992	Granted
C193.00	EP	METHOD AND APPARATUS FOR SHARING MEMORY IN A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	0532542	6/8/1994	Granted
C193.00	US	METHOD AND APPARATUS FOR SHARING MEMORY IN A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	5247637	9/21/1993	Granted
C200.00	US	MODULAR INPUT/OUTPUT SYSTEM FOR SUPERCOMPUTERS	COMPUTER SYS ARCH	5347637	9/13/1994	Granted
C203.00	US	DISTRIBUTING SYSTEM FOR MULTI-PROCESSOR INPUT AND OUTPUT USING CHANNEL ADAPTERS	COMPUTER SYS ARCH	5388217	2/7/1995	Granted

**Schedule 2.1(f)**

C213.00	US	SINGLE DISK EMULATION INTERFACE FOR AN ARRAY OF ASYNCHRONOUSLY OPERATING DISK DRIVES	COMPUTER SYS ARCH	5218689	6/8/1993	Granted
C223.00	US	REDUCED CAPACITANCE CHIP CARRIER	MECH/PHYS	5134247	7/28/1992	Granted
C227.00	US	SYSTEM FOR MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	5526487	6/11/1996	Granted
C231.00	US	HIGH POWER, HIGH DENSITY INTERCONNECT METHOD AND APPARATUS FOR INTEGRATED CIRCUITS	MECH/PHYS	5127986	7/7/1992	Granted
C231.01	US	HIGH POWER, HIGH DENSITY INTERCONNECT METHOD AND APPARATUS FOR INTEGRATED CIRCUITS	MECH/PHYS	5185502	2/9/1993	Granted
C261.00	US	DUAL CONTACT BEAM ASSEMBLY FOR AN IC TEST FIXTURE	MECH/PHYS	5175496	12/29/1992	Granted
C262.00	EP	A SCALABLE PARALLEL VECTOR COMPUTER SYSTEM	COMPUTER SYS ARCH	0553158	12/28/1994	Granted
C262.00	FR	A SCALABLE PARALLEL VECTOR COMPUTER SYSTEM	COMPUTER SYS ARCH	0553158	12/28/1994	Granted
C262.00	GB	A SCALABLE PARALLEL VECTOR COMPUTER SYSTEM	COMPUTER SYS ARCH	0553158	12/28/1994	Granted
C262.00	DE	A SCALABLE PARALLEL VECTOR COMPUTER SYSTEM	COMPUTER SYS ARCH	69106384.2	12/28/1994	Granted
C262.00	US	A SCALABLE PARALLEL VECTOR COMPUTER SYSTEM	COMPUTER SYS ARCH			Pending



**Schedule 2.1(f)**

C276.00	US	VECTOR BIT-MATRIX MULTIPLY FUNCTIONAL UNIT	MICROPROC ARCH	5170370	12/8/1992	Granted
C287.00	US	AIR JET IMPINGEMENT ON MINIATURE PIN-FIN HEAT SINKS FOR COOLING	MECH/PHYS	5083194	1/21/1992	Granted
C287.51	US	AIR MANIFOLD FOR COOLING ELECTRONIC DEVICES	MECH/PHYS	5166775	11/24/1992	Granted
C293.00	US	FIBER OPTIC CHANNEL EXTENDER INTERFACE METHOD AND APPARATUS COMMUNICATION PROTOCOL FOR TRANSFERRING INFORMATION ACROSS A SERIAL COMMUNICATION LINK	COMPUTER SYS ARCH	5390041	2/14/1995	Granted
C293.02	US	FAULT TOLERANT NETWORKING ARCHITECTURE	COMPUTER SYS ARCH	5592487	1/7/1997	Granted
C295.00	US	METHOD OF FABRICATING METALLIZED CHIP CARRIERS FROM WAFER-SHAPED SUBSTRATES	MECH/PHYS	5182420	1/26/1993	Granted
C297.00	US	METHOD OF FABRICATING METALLIZED CHIP CARRIERS FROM WAFER-SHAPED SUBSTRATES	MECH/PHYS	5358826	10/25/1994	Granted
C297.51	US	ERROR RECOVERY METHOD AND APPARATUS FOR HIGH PERFORMANCE DISK DRIVES	SYSTEMS SOFTWARE	5283791	2/1/1994	Granted

**Schedule 2.1(f)**

C326.00	US	VECTOR SHIFT FUNCTIONAL UNIT FOR SUCCESSIVELY SHIFTING OPERANDS STORED IN A VECTOR REGISTER BY CORRESPONDING SHIFT COUNTS STORED IN ANOTHER VECTOR REGISTER	MICROPROC ARCH	5481746	1/2/1996	Granted
C326.01	US	VECTOR WORD SHIFT BY VO SHIFT COUNT IN VECTOR SUPERCOMPUTER PROCESSOR	MICROPROC ARCH	5848286	12/8/1998	Granted
C326.0151	US	VECTOR SHIFT FUNCTIONAL UNIT FOR SUCCESSIVELY SHIFTING OPERANDS STORED IN A VECTOR REGISTER BY CORRESPONDING SHIFT COUNTS STORED IN ANOTHER VECTOR REGISTER	MICROPROC ARCH	6098162	8/1/2000	Granted
C338.00	US	ASSOCIATIVE SCALAR DATA CACHE WITH WRITE-THROUGH CAPABILITIES FOR A VECTOR PROCESSOR	COMPUTER SYS ARCH	5717895	2/10/1998	Granted
C343.00	US	METHOD OF MANUFACTURING INTERCONNECT BUMPS	MECH/PHYS	5283948	2/8/1994	Granted
C344.00	US	METHOD AND APPARATUS FOR LASER MASKING OF LEAD BONDING	MECH/PHYS	5194710	3/16/1993	Granted
C356.00	EP	SYSTEM FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	0571395	7/30/1997	Granted
C356.00	FR	SYSTEM FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	0571395	7/30/1997	Granted

**Schedule 2.1(f)**

C356.00	GB	SYSTEM FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	0571395	7/30/1997	Granted
C356.00	EP	SYSTEM FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	0712076	5/10/2000	Granted
C356.00	FR	SYSTEM FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	0712076	5/10/2000	Granted
C356.00	GB	SYSTEM FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	0712076	5/10/2000	Granted
C356.00	US	SYSTEM FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	5434970	7/18/1995	Granted
C356.00	DE	SYSTEM FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	69127101.1	7/30/1997	Granted
C356.00	DE	SYSTEM FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATION	COMPUTER SYS ARCH	69132195.7	5/10/2000	Granted
C356.01	US	MULTIPROCESSING SYSTEM USING INDIRECT ADDRESSING TO ACCESS RESPECTIVE LOCAL SEMAPHORE REGISTERS BITS FOR SETTING THE BIT OR BRANCHING IF THE COMPUTER BIT IS SET	COMPUTER SYS ARCH	5367690	11/22/1994	Granted
C356.51	US	SYSTEM AND METHOD FOR DISTRIBUTED MULTIPROCESSOR COMMUNICATIONS	COMPUTER SYS ARCH	5920714	7/6/1999	Granted

**Schedule 2.1(f)**

C360.00	EP	REAL TIME I/O OPERATION IN A VECTOR PROCESSING COMPUTER SYSTEM BY RUNNING DESIGNATED PROCESSORS IN PRIVILEGED MODE AND BYPASS THE OPERATING SYSTEM	COMPUTER SYS ARCH	0577614	10/4/1995	Granted
C360.00	FR	REAL TIME I/O OPERATION IN A VECTOR PROCESSING COMPUTER SYSTEM BY RUNNING DESIGNATED PROCESSORS IN PRIVILEGED MODE AND BYPASS THE OPERATING SYSTEM	COMPUTER SYS ARCH	0577614	10/4/1995	Granted
C360.00	GB	REAL TIME I/O OPERATION IN A VECTOR PROCESSING COMPUTER SYSTEM BY RUNNING DESIGNATED PROCESSORS IN PRIVILEGED MODE AND BYPASS THE OPERATING SYSTEM	COMPUTER SYS ARCH	0577614	10/4/1995	Granted
C360.00	US	REAL TIME I/O OPERATION IN A VECTOR PROCESSING COMPUTER SYSTEM BY RUNNING DESIGNATED PROCESSORS IN PRIVILEGED MODE AND BYPASS THE OPERATING SYSTEM	COMPUTER SYS ARCH	5390300	2/14/1995	Granted
C360.00	DE	REAL TIME I/O OPERATION IN A VECTOR PROCESSING COMPUTER SYSTEM BY RUNNING DESIGNATED PROCESSORS IN PRIVILEGED MODE AND BYPASS THE OPERATING SYSTEM	COMPUTER SYS ARCH	69113639.4	10/4/1995	Granted

**Schedule 2.1(f)**

C361.00	US	MEMORY RANGE MONITORING APPARATUS FOR A MULTIPROCESSOR COMPUTER SYSTEM	COMPUTER SYS ARCH	5295260	3/15/1994	Granted
C366.00	US	METHOD OF FABRICATING SILICON-BASED CARRIERS PRINTED CIRCUIT BOARD WITH COOLING MONITORING SYSTEM	MECH/PHYS	5196377	3/23/1993	Granted
C377.01	US		MECH/PHYS	5281026	1/25/1994	Granted
C387.00	EP	HIGH PERFORMANCE MANTISSA DIVIDER	CIRCUIT DESIGN	0658256	9/23/1998	Granted
C387.00	US	HIGH PERFORMANCE MANTISSA DIVIDER	CIRCUIT DESIGN	5258944	11/2/1993	Granted
C401.00	US	METALLIZED CONNECTOR BLOCK	MECH/PHYS	5211567	5/18/1993	Granted
C404.00	EP	SOLID STATE STORAGE DEVICE	COMPUTER SYS ARCH	0642685	8/23/2000	Granted
C404.00	FR	SOLID STATE STORAGE DEVICE	COMPUTER SYS ARCH	0642685	8/23/2000	Granted
C404.00	GB	SOLID STATE STORAGE DEVICE	COMPUTER SYS ARCH	0642685	8/23/2000	Granted
C404.00	US	SOLID STATE STORAGE DEVICE	COMPUTER SYS ARCH	5321697	6/14/1994	Granted
C404.00	DE	SOLID STATE STORAGE DEVICE	COMPUTER SYS ARCH	69329282.2	8/23/2000	Granted

Schedule 2.1(f)

Patent No.	Country	Title	IPC Class.	Grant Date	Status
C404.00	JP	SOLID STATE STORAGE DEVICE	COMPUTER SYS ARCH		Pending
C425.00	US	OPTICAL CLOCK DISTRIBUTION SYSTEM PARALLEL VECTOR PROCESSING SYSTEM FOR INDIVIDUAL AND BROADCAST DISTRIBUTION OF OPERANDS AND CONTROL INFORMATION	COMPUTER SYS ARCH	5537498	7/16/1996 Granted
C434.01	US	SYSTEM AND METHOD OF ADDRESSING DISTRIBUTED MEMORY WITHIN A MASSIVELY PARALLEL PROCESSING SYSTEM	COMPUTER SYS ARCH	5226171	7/6/1993 Granted
C452.00	EP	SYSTEM AND METHOD OF ADDRESSING DISTRIBUTED MEMORY WITHIN A MASSIVELY PARALLEL PROCESSING SYSTEM	COMPUTER SYS ARCH	0733236	8/6/2003 Granted
C452.00	US	DIRECTION ORDER PRIORITY ROUTING OF PACKETS BETWEEN NODES IN A NETWORKED SYSTEM	COMPUTER SYS ARCH	5765181	6/9/1998 Granted
C465.00	US	CONFIGURING OF NETWORKED SYSTEM TO PERMIT REPLACEMENT OF FAILED MODES AND SELECTION OF ALTERNATE PATHS	COMPUTER SYS ARCH	5533198	7/2/1996 Granted
C465.01	US	BARRIER SYNCHRONIZATION FOR DISTRIBUTED MEMORY MASSIVELY PARALLEL PROCESSING SYSTEMS	COMPUTER SYS ARCH	5689646	11/18/1997 Granted
C466.00	EP	MASSIVELY PARALLEL PROCESSING SYSTEMS	COMPUTER SYS ARCH	0733234	7/14/1999 Granted

**Schedule 2.1(f)**

C466.00	US	BARRIER SYNCHRONIZATION FOR DISTRIBUTED MEMORY MASSIVELY PARALLEL PROCESSING SYSTEMS	COMPUTER SYS ARCH	5434995	7/18/1995	Granted
C479.00	US	APPARATUS FOR COOLING DAUGHTER BOARDS	MECH/PHYS	5343359	8/30/1994	Granted
C481.00	US	BOUNDARY SCAN TESTING USING CLOCKED SIGNAL	COMPUTER SYS ARCH	5487074	1/23/1996	Granted
C483.00	US	MATRIX PROCESSOR SYSTEM MESSAGING FACILITY WITH HARDWARE TAIL POINTER AND SOFTWARE IMPLEMENTED HEAD POINTER MESSAGE QUEUE FOR DISTRIBUTED MEMORY MASSIVELY PARALLEL PROCESSING SYSTEM	COMPUTER SYS ARCH	6282583	8/28/2001	Granted
C486.00	EP	MESSAGING FACILITY WITH HARDWARE TAIL POINTER AND SOFTWARE IMPLEMENTED HEAD POINTER MESSAGE QUEUE FOR DISTRIBUTED MEMORY MASSIVELY PARALLEL PROCESSING SYSTEM	COMPUTER SYS ARCH	0734554	7/14/1999	Granted
C486.00	US	CLUSTER ARCHITECTURE FOR A HIGHLY PARALLEL SCALAR/VECTOR MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	5581705	12/3/1996	Granted
C505.00	US	MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	5197130	3/23/1993	Granted

Schedule 2.1(f)

C505.51	US	INTERLEAVED MEMORY ACCESS SYSTEM HAVING VARIABLE-SIZED SEGMENTS LOGICAL ADDRESS SPACES AND MEANS FOR DIVIDING/MAPPING PHYSICAL ADDRESS INTO HIGHER AND LOWER ORDER ADDRESSES	COMPUTER SYS ARCH	5561784	10/1/1996	Granted
C506.00	US	PACKAGING ARCHITECTURE FOR A HIGHLY PARALLEL MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	5251097	10/5/1993	Granted
C507.00	US	METHOD AND APPARATUS FOR MON-SEQUENTIAL RESOURCE ACCESS	COMPUTER SYS ARCH	5208914	5/4/1993	Granted
C508.00	US	METHOD & APPARATUS FOR A SPECIAL PURPOSE BOOLEAN UNIT	COMPUTER SYS ARCH	5175862	12/29/1992	Granted
C508.00	JP	METHOD & APPARATUS FOR A SPECIAL PURPOSE BOOLEAN UNIT	COMPUTER SYS ARCH			Pending
C509.00	US	METHOD AND APPARATUS FOR USER SIDE SCHEDULING IN A MULTIPROCESSOR OPERATING SYSTEM PROGRAM THAT IMPLEMENTS DISTRIBUTIVE SCHEDULING OF PROCESSES	COMPUTER SYS ARCH	5179702	1/12/1993	Granted
C509.01	US	METHOD AND APPARATUS FOR USER SIDE SCHEDULING IN A MULTIPROCESSOR OPERATING SYSTEM PROGRAM THAT IMPLEMENTS DISTRIBUTIVE SCHEDULING OF PROCESSES	COMPUTER SYS ARCH	6195676	2/27/2001	Granted
C512.00	US	DISTRIBUTED ARCHITECTURE FOR INPUT/OUTPUT FOR A MULTI-PROCESSOR SYSTEM	COMPUTER SYS ARCH	5168547	12/1/1992	Granted



Schedule 2.1(f)

C513.00	US	CONTROL AND MAINTENANCE SUBSYSTEM FOR USE WITH A MULTIPROCESSOR COMPUTER SYSTEM	COMPUTER SYS ARCH	5253359	10/12/1993	Granted
C514.00	US	GLOBAL REGISTERS FOR A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	5165038	11/17/1992	Granted
C514.01	US	METHOD AND APPARATUS FOR ACCESSING GLOBAL REGISTERS IN A MULTIPROCESSOR SYSTEM	COMPUTER SYS ARCH	5524255	6/4/1996	Granted
C515.00	US	METHOD AND APPARATUS FOR A MULTIPLE REQUEST TOGGING PRIORITY SYSTEM	COMPUTER SYS ARCH	5168570	12/1/1992	Granted
C516.00	US	DEDICATED CENTRALIZED SIGNALING MECHANISM FOR SELECTIVELY SIGNALING DEVICES IN A MULTI- PROCESSOR SYSTEM	COMPUTER SYS ARCH	5239629	8/24/1993	Granted
C517.00	US	FAST INTERRUPT MECHANISM FOR INTERRUPTING PROCESSORS IN PARALLEL IN A MULTIPROCESSOR SYSTEM WHEREIN PROCESSORS ARE ASSIGNED PROCESS ID NUMBERS	COMPUTER SYS ARCH	5193187	3/9/1993	Granted
C518.00	US	METHOD AND APPARATUS FOR SINGLE STEP CLOCKING ON SIGNAL PATHS LONGER THAN A CLOCK CYCLE	COMPUTER SYS ARCH	5055707	10/8/1991	Granted

**Schedule 2.1(f)**

C519.00	EP	COMPUTER WITH INTEGRATED HIERARCHICAL REPRESENTATION OF PROGRAM WHEREIN IHR FILE IS AVAILABLE FOR DEBUGGING AND OPTIMIZING DURING TARGET EXECUTION	COMPUTER SYS ARCH	0537257	12/8/1999	Granted
C519.00	FR	COMPUTER WITH INTEGRATED HIERARCHICAL REPRESENTATION OF PROGRAM WHEREIN IHR FILE IS AVAILABLE FOR DEBUGGING AND OPTIMIZING DURING TARGET EXECUTION	COMPUTER SYS ARCH	0537257	12/8/1999	Granted
C519.00	GB	COMPUTER WITH INTEGRATED HIERARCHICAL REPRESENTATION OF PROGRAM WHEREIN IHR FILE IS AVAILABLE FOR DEBUGGING AND OPTIMIZING DURING TARGET EXECUTION	COMPUTER SYS ARCH	0537257	12/8/1999	Granted
C519.00	US	COMPUTER WITH INTEGRATED HIERARCHICAL REPRESENTATION OF PROGRAM WHEREIN IHR FILE IS AVAILABLE FOR DEBUGGING AND OPTIMIZING DURING TARGET EXECUTION	COMPUTER SYS ARCH	5175856	12/29/1992	Granted
C519.00	DE	COMPUTER WITH INTEGRATED HIERARCHICAL REPRESENTATION OF PROGRAM WHEREIN IHR FILE IS AVAILABLE FOR DEBUGGING AND OPTIMIZING DURING TARGET EXECUTION	COMPUTER SYS ARCH	69131832.8	12/8/1999	Granted

**Schedule 2.1(f)**

C522.00	US	MINIATURE CONTROLLED-IMPEDANCE TRANSMISSION LINE CABLE AND METHOD OF MANUFACTURE	MECH/PHYS	5015800	5/14/1991	Granted
C523.00	EP	METHOD FOR EFFICIENT NON-VIRTUAL MAIN MEMORY MANAGEMENT	COMPUTER SYS ARCH	0533805	4/26/2000	Granted
C523.00	FR	METHOD FOR EFFICIENT NON-VIRTUAL MAIN MEMORY MANAGEMENT	COMPUTER SYS ARCH	0533805	4/26/2000	Granted
C523.00	GB	METHOD FOR EFFICIENT NON-VIRTUAL MAIN MEMORY MANAGEMENT	COMPUTER SYS ARCH	0533805	4/26/2002	Granted
C523.00	US	METHOD FOR EFFICIENT NON-VIRTUAL MAIN MEMORY MANAGEMENT	COMPUTER SYS ARCH	5159678	10/27/1992	Granted
C523.00	DE	METHOD FOR EFFICIENT NON-VIRTUAL MAIN MEMORY MANAGEMENT	COMPUTER SYS ARCH	0533805		Pending
C524.00	US	MULTILAYER INTERCONNECT SYSTEM FOR AN AREA ARRAY INTERCONNECTION USING SOLID STATE DIFFUSION	MECH/PHYS	5276955	1/11/1994	Granted
C526.00	US	TRANSMISSION LINE WITH FLUID PERMEABLE JACKET	MECH/PHYS	5142100	8/25/1992	Granted
C527.00	US	METHOD AND APPARATUS FOR A UNIFIED PARALLEL PROCESSING ARCHITECTURE	COMPUTER SYS ARCH	5428803	6/27/1995	Granted
C527.01	US	EXTENDIBLE CLOCK MECHANISM	COMPUTER SYS ARCH	5625831	4/29/1997	Granted
C528.00	US	CLOCK DISTRIBUTION APPARATUS AND PROCESS PARTICULARLY USEFUL IN MULTIPROCESSOR SYSTEMS	COMPUTER SYS ARCH	5293626	3/8/1994	Granted

Schedule 2.1(f)

C529.00	EP	DUAL LEVEL SCHEDULING OF PROCESS TO MULTIPLE PARALLEL REGIONS OF A MULTITHREADED PROGRAM ON A TIGHTLY COUPLED MULTIPROCESSOR COMPUTER SYSTEM	544822	5/12/1999	Granted
C529.00	FR	DUAL LEVEL SCHEDULING OF PROCESS TO MULTIPLE PARALLEL REGIONS OF A MULTITHREADED PROGRAM ON A TIGHTLY COUPLED MULTIPROCESSOR COMPUTER SYSTEM	544822	5/12/1999	Granted
C529.00	GB	DUAL LEVEL SCHEDULING OF PROCESS TO MULTIPLE PARALLEL REGIONS OF A MULTITHREADED PROGRAM ON A TIGHTLY COUPLED MULTIPROCESSOR COMPUTER SYSTEM	544822	5/12/1999	Granted
C529.00	US	DUAL LEVEL SCHEDULING OF PROCESS TO MULTIPLE PARALLEL REGIONS OF A MULTITHREADED PROGRAM ON A TIGHTLY COUPLED MULTIPROCESSOR COMPUTER SYSTEM	5339415	8/16/1994	Granted
C529.00	DE	DUAL LEVEL SCHEDULING OF PROCESS TO MULTIPLE PARALLEL REGIONS OF A MULTITHREADED PROGRAM ON A TIGHTLY COUPLED MULTIPROCESSOR COMPUTER SYSTEM	69131228.1	5/12/1999	Granted

**Schedule 2.1(f)**

C533.00	US	SYSTEM FOR COMMUNICATING AMONG PROCESSORS HAVING DIFFERENT SPEEDS	SYSTEMS SOFTWARE	5202988	4/13/1993	Granted
C535.00	US	CLOCK PULSE MEASURING AND DESKEWING SYSTEM AND CIRCUIT PROCESS	DESIGN	5329188	7/12/1994	Granted
C536.00	US	CLOCK STARTUP STABILIZATION FOR COMPUTER SYSTEMS	CIRCUIT DESIGN	5355397	10/11/1994	Granted
C537.00	US	VERTICAL SEMICONDUCTOR INTERCONNECTION METHOD AND STRUCTURE	MECH/PHYS	5041903	8/20/1991	Granted
C540.00	US	REPETITIVE SIGNAL DETECTOR FOR PREVENTING THERMAL RUNAWAY	MECH/PHYS	5418481	5/23/1995	Granted
C544.00	EP	VIRTUAL TO LOGICAL TO PHYSICAL ADDRESS TRANSLATION FOR DISTRIBUTED MEMORY MASSIVELY PARALLEL PROCESSING SYSTEMS	COMPUTER SYS ARCH	0737338	2/14/2001	Granted
C544.00	US	VIRTUAL TO LOGICAL TO PHYSICAL ADDRESS TRANSLATION FOR DISTRIBUTED MEMORY MASSIVELY PARALLEL PROCESSING SYSTEMS	COMPUTER SYS ARCH	5784706	7/21/1998	Granted
C546.00	US	SYSTEM FOR ALLOCATING MESSAGES BETWEEN VIRTUAL CHANNELS TO AVOID DEADLOCK AND TO OPTIMIZE THE AMOUNT OF MESSAGE TRAFFIC ON EACH TYPE OF VIRTUAL CHANNEL	COMPUTER SYS ARCH	5583990	12/10/1996	Granted

**Schedule 2.1(f)**

C546.00	EP	SYSTEM FOR ALLOCATING MESSAGES BETWEEN VIRTUAL CHANNELS TO AVOID DEADLOCK AND TO OPTIMIZE THE AMOUNT OF MESSAGE TRAFFIC ON EACH TYPE OF VIRTUAL CHANNEL	COMPUTER SYS ARCH	5797035	8/18/1998	Granted	Pending
C546.01	US	NETWORKED MULTIPROCESSOR SYSTEM WITH GLOBAL DISTRIBUTED MEMORY AND BLOCK TRANSFER ENGINE	COMPUTER SYS ARCH	5797035	8/18/1998	Granted	
C546.02	US	MULTIPROCESSOR COMPUTER SYSTEM WITH INTERLEAVED PROCESSING ELEMENT NODES	COMPUTER SYS ARCH	5737628	4/7/1998	Granted	
C549.00	EP	APPARATUS AND METHOD FOR TESTING USING A CLOCKED TEST ACCESS PORT CONTROLLER FOR LEVEL SENSITIVE SCAN DESIGNS	COMPUTER SYS ARCH	0792486	4/12/2000	Granted	
C549.00	FR	APPARATUS AND METHOD FOR TESTING USING A CLOCKED TEST ACCESS PORT CONTROLLER FOR LEVEL SENSITIVE SCAN DESIGNS	COMPUTER SYS ARCH	0792486	4/12/2000	Granted	
C549.00	GB	APPARATUS AND METHOD FOR TESTING USING A CLOCKED TEST ACCESS PORT CONTROLLER FOR LEVEL SENSITIVE SCAN DESIGNS	COMPUTER SYS ARCH	0792486	4/12/2000	Granted	
C549.00	US	APPARATUS AND METHOD FOR TESTING USING A CLOCKED TEST ACCESS PORT CONTROLLER FOR LEVEL SENSITIVE SCAN DESIGNS	COMPUTER SYS ARCH	6173428	1/9/2001	Granted	

**Schedule 2.1(f)**

C549.00	DE	APPARATUS AND METHOD FOR TESTING USING A CLOCKED TEST ACCESS PORT CONTROLLER FOR LEVEL SENSITIVE SCAN DESIGNS	COMPUTER SYS ARCH	69516303.5	4/12/2000	Granted
C550.00	US	CONFIGURABLE SPARE MEMORY CHIPS	COMPUTER SYS ARCH	5392292	2/21/1995	Granted
C553.00	US	RECURSIVE ADDRESS CENTRIFUGE FOR DISTRIBUTED MEMORY MASSIVELY PARALLEL PROCESSING SYSTEMS	COMPUTER SYS ARCH	5696922	12/9/1997	Granted
C553.01	US	RECURSIVE ADDRESS CENTRIFUGE FOR DISTRIBUTED MEMORY MASSIVELY PARALLEL PROCESSING SYSTEMS METHOD OF MANAGING DISTRIBUTED MEMORY WITHIN A MASSIVELY PARALLEL PROCESSING SYSTEM	COMPUTER SYS ARCH	6119198	9/12/2000	Granted
C565.00	US	METHOD AND APPARATUS FOR LOCKING SHARED MEMORY LOCATIONS IN MULTIPROCESSING SYSTEMS	COMPUTER SYS ARCH	5566321	10/15/1996	Granted
C567.00	US	METHOD FOR THE DYNAMIC ALLOCATION OF PAGE SIZES IN VIRTUAL MEMORY	COMPUTER SYS ARCH	5535365	7/9/1996	Granted
C568.00	US	METHOD FOR THE DYNAMIC ALLOCATION OF ARRAY SIZES IN A MULTIPROCESSOR SYSTEM	SYSTEMS SOFTWARE	5802341	9/1/1998	Granted
C569.00	US		SYSTEMS SOFTWARE	5586325	12/17/1996	Granted

**Schedule 2.1(f)**

C576.00	US	ARRAY ADDRESS AND LOOP ALIGNMENT CALCULATIONS	MICROPROC ARCH	6128639	10/3/2000	Granted
C577.00	US	METHOD AND APPARATUS FOR REMOVING POWER-OF-TWO RESTRICTIONS ON DISTRIBUTED ADDRESSING	COMPUTER SYS ARCH	5900023	5/4/1999	Granted
C580.00	EP	BARRIER AND EUREKA SYNCHRONIZATION ARCHITECTURE FOR MULTIPROCESSORS	COMPUTER SYS ARCH	0829047	2/20/2002	Granted
C580.00	FR	BARRIER AND EUREKA SYNCHRONIZATION ARCHITECTURE FOR MULTIPROCESSORS	COMPUTER SYS ARCH	0829047	2/20/2002	Granted
C580.00	GB	BARRIER AND EUREKA SYNCHRONIZATION ARCHITECTURE FOR MULTIPROCESSORS	COMPUTER SYS ARCH	0829047	2/20/2002	Granted
C580.00	US	BARRIER AND EUREKA SYNCHRONIZATION ARCHITECTURE FOR MULTIPROCESSORS	COMPUTER SYS ARCH	5721921	2/24/1998	Granted
C580.00	DE	BARRIER AND EUREKA SYNCHRONIZATION ARCHITECTURE FOR MULTIPROCESSORS	COMPUTER SYS ARCH	69619366.3	2/20/2002	Granted
C581.00	US	USING EXTERNAL REGISTERS TO EXTEND MEMORY REFERENCE CAPABILITIES OF A MICROPROCESSOR	MICROPROC ARCH	5835925	11/10/1998	Granted



**Schedule 2.1(f)**

C581.51	US	METHOD OF HANDLING ARBITRARY SIZE MESSAGE QUEUES IN WHICH A MESSAGE IS WRITTEN INTO AN ALIGNED BLOCK OF EXTERNAL REGISTERS WITHIN A PLURALITY OF EXTERNAL REGISTERS	COMPUTER SYS ARCH	6029212	2/22/2000	Granted
C582.00	US	STREAM BUFFERS FOR HIGH- PERFORMANCE COMPUTER MEMORY SYSTEMS	COMPUTER SYS ARCH	5761706	6/2/1998	Granted
C583.00	EP	ADAPTIVE ROUTING MECHANISM FOR TORUS INTERCONNECTION NETWORK	COMPUTER SYS ARCH	0821816	1/12/2000	Granted
C583.00	FR	ADAPTIVE ROUTING MECHANISM FOR TORUS INTERCONNECTION NETWORK	COMPUTER SYS ARCH	0821816	1/12/2000	Granted
C583.00	GB	ADAPTIVE ROUTING MECHANISM FOR TORUS INTERCONNECTION NETWORK	COMPUTER SYS ARCH	0821816	1/12/2000	Granted
C583.00	JP	ADAPTIVE ROUTING MECHANISM FOR TORUS INTERCONNECTION NETWORK	COMPUTER SYS ARCH	3532574	3/12/2004	Granted
C583.00	US	ADAPTIVE ROUTING MECHANISM FOR TORUS INTERCONNECTION NETWORK	COMPUTER SYS ARCH	5701416	12/23/1997	Granted
C583.00	DE	ADAPTIVE ROUTING MECHANISM FOR TORUS INTERCONNECTION NETWORK	COMPUTER SYS ARCH	69514550.9	1/12/2000	Granted
C586.00	US	SYSTEM FOR RANDOMLY MODIFYING VIRTUAL CHANNEL ALLOCATION AND ACCEPTING THE RANDOM MODIFICATION BASED ON THE COST FUNCTION	COMPUTER SYS ARCH	5659796	8/19/1997	Granted

**Schedule 2.1(f)**

C588.00	US	TRANSPARENT RELOCATION OF REAL MEMORY ADDRESSES IN THE MAIN MEMORY OF A DATA PROCESSOR	COMPUTER SYS ARCH	5765198	6/9/1998	Granted
C593.00	US	SYSTEM AND METHOD FOR DENSITY TIME VECTOR CONDITIONAL OPERATIONS	MICROPROC ARCH	5940625	8/17/1999	Granted
C595.00	US	SPATIAL DERIVATIVE BUS ENCODER AND DECODER	COMPUTER SYS ARCH	6480548	11/12/2002	Granted
C595.51	US	SPATIAL DERIVATIVE BUS ENCODER AND DECODER	COMPUTER SYS ARCH	7167523	1/23/2007	Granted
C597.00	US	DAUGHTER CARD ASSEMBLY	MECH/PHYS	5761043	6/2/1998	Granted
C598.00	US	MESSAGING IN DISTRIBUTED MEMORY MULTIPROCESSING SYSTEM HAVING SHELL CIRCUITRY FOR ATOMIC CONTROL OF MESSAGE STORAGE QUEUE'S TAIL POINTER STRUCTURE IN LOCAL MEMORY MASSIVELY PARALLEL PROCESSING SYSTEM USING TWO DATA PATHS: ONE CONNECTING ROUTER CIRCUIT TO THE INTERCONNECT NETWORK AND THE OTHER CONNECTING ROUTER CIRCUIT TO I/O CONTROLLER	COMPUTER SYS ARCH	5841973	11/24/1998	Granted
C600.00	US		COMPUTER SYS ARCH	5864738	1/26/1999	Granted

Schedule 2.1(f)

C601.00	EP	VIRTUAL MAINTENANCE NETWORK IN MULTIPROCESSING SYSTEM HAVING A NON-FLOW CONTROLLED VIRTUAL MAINTENANCE CHANNEL	COMPUTER SYS ARCH	0858633	11/24/1999	Granted
C601.00	FR	VIRTUAL MAINTENANCE NETWORK IN MULTIPROCESSING SYSTEM HAVING A NON-FLOW CONTROLLED VIRTUAL MAINTENANCE CHANNEL	COMPUTER SYS ARCH	0858633	11/24/1999	Granted
C601.00	GB	VIRTUAL MAINTENANCE NETWORK IN MULTIPROCESSING SYSTEM HAVING A NON-FLOW CONTROLLED VIRTUAL MAINTENANCE CHANNEL	COMPUTER SYS ARCH	0858633	11/24/1999	Granted
C601.00	JP	VIRTUAL MAINTENANCE NETWORK IN MULTIPROCESSING SYSTEM HAVING A NON-FLOW CONTROLLED VIRTUAL MAINTENANCE CHANNEL	COMPUTER SYS ARCH	3933620	3/30/2007	Granted
C601.00	US	VIRTUAL MAINTENANCE NETWORK IN MULTIPROCESSING SYSTEM HAVING A NON-FLOW CONTROLLED VIRTUAL MAINTENANCE CHANNEL	COMPUTER SYS ARCH	6055618	4/25/2000	Granted
C601.00	DE	VIRTUAL MAINTENANCE NETWORK IN MULTIPROCESSING SYSTEM HAVING A NON-FLOW CONTROLLED VIRTUAL MAINTENANCE CHANNEL	COMPUTER SYS ARCH	69605319.5	11/24/1999	Granted

**Schedule 2.1(f)**

C605.00	US	SYSTEM FOR ARBITRATING PACKETIZED DATA FROM THE NETWORK TO THE PERIPHERAL RESOURCES AND PRIORITIZING THE DISPATCHING OF PACKETS ONTO THE NETWORK	COMPUTER SYS ARCH	5761534	6/2/1998	Granted
C608.00	US	COOLING APPROACH FOR HIGH POWER INTEGRATED CIRCUITS MOUNTED ON PRINTED CIRCUIT BOARDS	MECH/PHYS	5768104	6/16/1998	Granted
C609.00	US	AIR OR LIQUID COOLED COMPUTER MODULE COLD PLATE	MECH/PHYS	6305463	10/23/2001	Granted
C610.00	US	COOLING CAP METHOD AND APPARATUS FOR TAB PACKAGED INTEGRATED CIRCUITS	MECH/PHYS	5805418	9/8/1998	Granted
C610.01	US	COOLING CAP METHOD AND APPARATUS FOR TAB PACKAGED INTEGRATED CIRCUITS	MECH/PHYS	5963428	10/5/1999	Granted
C611.00	US	METHOD AND APPARATUS FOR ADJUSTING THE POWER SUPPLY VOLTAGE PROVIDED TO MICROPROCESSORS	CIRCUIT DESIGN	5694028	12/2/1997	Granted
C612.00	US	RECONFIGURABLE RING- BASED NETWORK SYSTEM	COMPUTER SYS ARCH	6233704	5/15/2001	Granted
C614.00	US	ADAPTIVE CONGESTION CONTROL MECHANISM FOR MODULAR COMPUTER NETWORKS	COMPUTER SYS ARCH	5748900	5/5/1998	Granted

Schedule 2.1(f)

C614.51	US	ADAPTIVE CONGESTION CONTROL MECHANISM FOR MODULAR COMPUTER NETWORKS	COMPUTER SYS ARCH	5958017	9/28/1999	Granted
C617.00	US	METHOD AND APPARATUS FOR COOLING DAUGHTER CARD MODULES	MECH/PHYS	5801924	9/1/1998	Granted
C622.00	US	RAID SYSTEM USING I/O BUFFER SEGMENT TO TEMPORARY STORE STRIPED AND PARITY DATA AND CONNECTING ALL DISK DRIVES VIA A SINGLE TIME MULTIPLEXED NETWORK	SYSTEMS SOFTWARE	5862313	1/19/1999	Granted
C623.00	US	RAID-5 PARITY GENERATION AND DATA RECONSTRUCTION SOFTWARE	SYSTEMS SOFTWARE	5805788	9/8/1998	Granted
C626.00	US	DEMOUNTABLE, COMPLIANT AREA ARRAY INTERCONNECT DIAMOND-BASED TRANSFORMERS AND POWER CONVERTERS	COMPUTER SYS ARCH	6142789	11/7/2000	Granted
C627.00	US	LARGE AREA, MULTI-DEVICE HEAT PIPE FOR STACKED MCM-BASED SYSTEMS	MECH/PHYS	6181231	1/30/2001	Granted
C629.00	US	ADAPTIVE BANDWIDTH SHARING	MECH/PHYS	6055157	4/25/2000	Granted
C632.00	US	SYSTEM AND METHOD FOR STACKING OF INTEGRATED CIRCUIT PACKAGES	COMPUTER SYS ARCH	6205119	3/20/2001	Granted
C635.00	US	THE FABRICATION OF TEST LOGIC FOR LEVEL SENSITIVE SCAN ON A CIRCUIT	MECH/PHYS	6172874	1/9/2001	Granted
C636.00	US		CIRCUIT DESIGN	6092226	7/18/2000	Granted

Schedule 2.1(f)

C638.00	US	POROUS METAL HEAT SINK	MECH/PHYS	6018459	1/25/2000	Granted
C642.00	US	SERIALIZED RACE-FREE VIRTUAL BARRIER NETWORK	COMPUTER SYS ARCH	6085303	7/4/2000	Granted
C643.00	US	HYBRID HYPERCUBE/TORUS ARCHITECTURE	COMPUTER SYS ARCH	6230252	5/8/2001	Granted
C644.00	EP	ROUTER TABLE LOOKUP MECHANISM	COMPUTER SYS ARCH	1032887	3/6/2002	Granted
C644.00	FR	ROUTER TABLE LOOKUP MECHANISM	COMPUTER SYS ARCH	1032887	3/6/2002	Granted
C644.00	GB	ROUTER TABLE LOOKUP MECHANISM	COMPUTER SYS ARCH	1032887	3/6/2002	Granted
C644.00	US	ROUTER TABLE LOOKUP MECHANISM	COMPUTER SYS ARCH	5970232	10/19/1999	Granted
C644.00	DE	ROUTER TABLE LOOKUP MECHANISM	COMPUTER SYS ARCH	69804131.8	3/6/2002	Granted
C645.00	US	MULTIPROCESSOR COMPUTER SYSTEM AND METHOD FOR MAINTAINING CACHE COHERENCE UTILIZING A MULTI-DIMENSIONAL CACHE COHERENCE DIRECTORY STRUCTURE	COMPUTER SYS ARCH	6633958	10/14/2003	Granted

Schedule 2.1(f)

C645.00	DE	MULTIPROCESSOR COMPUTER SYSTEM AND METHOD FOR MAINTAINING CACHE COHERENCE UTILIZING A MULTI-DIMENSIONAL CACHE COHERENCE DIRECTORY STRUCTURE	COMPUTER SYS ARCH	69828813.0	1/26/2005	Granted
C645.00	EP	MULTIPROCESSOR COMPUTER SYSTEM AND METHOD FOR MAINTAINING CACHE COHERENCE UTILIZING A MULTI-DIMENSIONAL CACHE COHERENCE DIRECTORY STRUCTURE	COMPUTER SYS ARCH			Pending
C645.00	FR	MULTIPROCESSOR COMPUTER SYSTEM AND METHOD FOR MAINTAINING CACHE COHERENCE UTILIZING A MULTI-DIMENSIONAL CACHE COHERENCE DIRECTORY STRUCTURE	COMPUTER SYS ARCH			Pending
C645.00	GB	MULTIPROCESSOR COMPUTER SYSTEM AND METHOD FOR MAINTAINING CACHE COHERENCE UTILIZING A MULTI-DIMENSIONAL CACHE COHERENCE DIRECTORY STRUCTURE	COMPUTER SYS ARCH			Pending
C646.00	US	SYSTEM AND METHOD FOR FAST BARRIER SYNCHRONIZATION	COMPUTER SYS ARCH	6216174	4/10/2001	Granted

**Schedule 2.1(f)**


C647.00	US	METHOD AND APPARATUS FOR PROCESSING A SET OF DATA VALUES WITH PLURAL PROCESSING UNITS MASK BITS GENERATED BY OTHER PROCESSING	COMPUTER SYS ARCH	6308250	10/23/2001	Granted
C648.00	US	DISTRIBUTED VECTOR ARCHITECTURE	COMPUTER SYS ARCH	5946496	8/31/1999	Granted
C649.00	US	STOP ALIGN LATERAL MODULE TO MODULE INTERCONNECT	MECH/PHYS	6116915	9/12/2000	Granted
C659.00	US	INTERLEAVING MEMORY IN DISTRIBUTED VECTOR ARCHITECTURE	COMPUTER SYS ARCH	5913069	6/15/1999	Granted
C663.00	EP	MESSAGE BUFFERING FOR A COMPUTER-BASED NETWORK	COMPUTER SYS ARCH	1038375	11/24/2004	Granted
C663.00	FR	MESSAGE BUFFERING FOR A COMPUTER-BASED NETWORK	COMPUTER SYS ARCH	1038375	11/24/2004	Granted
C663.00	GB	MESSAGE BUFFERING FOR A COMPUTER-BASED NETWORK	COMPUTER SYS ARCH	1038375	11/24/2004	Granted
C663.00	US	MESSAGE BUFFERING FOR A COMPUTER-BASED NETWORK	COMPUTER SYS ARCH	6256677	7/3/2001	Granted
C663.00	DE	MESSAGE BUFFERING FOR A COMPUTER-BASED NETWORK	COMPUTER SYS ARCH	69827843.7	11/24/2004	Granted
C680.00	US	PRECISE DETECTION OF ERRORS USING HARDWARE WATCHPOINT MECHANISM	COMPUTER SYS ARCH	5987626	11/16/1999	Granted





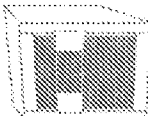
**Schedule 2.1(f)**



C689.00	US	EIGEN-MODE ENCODING OF SIGNALS IN A DATA GROUP	CIRCUIT DESIGN	6226330	5/1/2001	Granted
C700.00	EP	VIRTUAL CHANNEL ASSIGNMENT IN LARGE TORUS COMPUTER SYSTEMS	SYS ARCH	1031096	7/24/2002	Granted
C700.00	FR	VIRTUAL CHANNEL ASSIGNMENT IN LARGE TORUS COMPUTER SYSTEMS	SYS ARCH	1031096	7/24/2002	Granted
C700.00	GB	VIRTUAL CHANNEL ASSIGNMENT IN LARGE TORUS COMPUTER SYSTEMS	SYS ARCH	1031096	7/24/2002	Granted
C700.00	WO	VIRTUAL CHANNEL ASSIGNMENT IN LARGE TORUS COMPUTER SYSTEMS	SYS ARCH	1031096	7/24/2002	Granted
C700.00	US	VIRTUAL CHANNEL ASSIGNMENT IN LARGE TORUS COMPUTER SYSTEMS	SYS ARCH	6101181	8/8/2000	Granted
C700.00	DE	VIRTUAL CHANNEL ASSIGNMENT IN LARGE TORUS COMPUTER SYSTEMS	SYS ARCH	69806798.3	7/24/2002	Granted
C703.00	US	AGE-BASED NETWORK ARBITRATION SYSTEM AND METHOD	COMPUTER SYS ARCH	6674720	1/6/2004	Granted
C704.00	US	HIGH PERFORMANCE GAS COOLING SYSTEM AND METHOD	MECH/PHYS	6167947	1/2/2001	Granted



**U.S. Trademarks and Trademark Applications**

Trademark	Status	Appl. No.	Reg. No.	Reg. Date	Unreleased Liens
ALTIX	REGISTERED	78182640	2803777	6-Jan-04	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
CLUSTERWORX	REGISTERED	75895390	2568116	7-May-02	Assignee: MORGAN STANLEY & CO., INC. Brief: SECURITY INTEREST Recorded: 16-DEC-2008 Reel/Frame: 3904/0912
	RENEWED (REGISTERED)	73587923	1439837	19-May-87	Assignee: MORGAN STANLEY & CO., INCORPORATED Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
Design Only EVOLOCITY	REGISTERED	76114918	2714075	6-May-03	Assignee: MORGAN STANLEY & CO., INC. Brief: SECURITY INTEREST Recorded: 16-DEC-2008 Reel/Frame: 3904/0912
FAILSAFE	REGISTERED	76279586	2743739	29-Jul-03	Assignee: MORGAN STANLEY & CO., INCORPORATED Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
ICEBOX	REGISTERED	76308934	3011701	1-Nov-05	Assignee: MORGAN STANLEY & CO., INC. Brief: SECURITY INTEREST Recorded: 16-DEC-2008 Reel/Frame: 3904/0912

Trademark	Status	Appl. No.	Reg. No.	Reg. Date	Unreleased Liens
IMAGEVISION LIBRARY	RENEWED (REGISTERED)	74168496	1798517	12-Oct-93	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
INDIGO	RENEWED (REGISTERED)	74136537	1783257	20-Jul-93	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
INFINITE REALITY	RENEWED (REGISTERED)	75049218	2095910	9-Sep-97	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
IRIS	RENEWED (REGISTERED) Section 44(D)	72352782	929992	29-Feb-72	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
IRIS FAILSAFE	REGISTERED	76279589	2787974	2-Dec-03	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
IRIX	RENEWED (REGISTERED)	75212191	2127113	6-Jan-98	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
 Linux NetworX	REGISTERED	76124835	2896522	26-Oct-04	Assignee: MORGAN STANLEY & CO., INC. Brief: SECURITY INTEREST Recorded: 16-DEC-2008 Reel/Frame: 3904/0912
LINUX NETWORKX	REGISTERED	75702640	2579277	11-Jun-02	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
 SGI	REGISTERED				

Trademark	Status	Appl. No.	Reg. No.	Reg. Date	Unreleased Liens
	REGISTERED Supplemental Register	78781227	3451652	17-Jun-08	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
Design Only NUMAFLEX	REGISTERED	76110858	3485375	12-Aug-08	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
NUMALINK	REGISTERED	76110851	3093486	16-May-06	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
OCTANE	REGISTERED	75214146	2168514	23-Jun-98	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
ONYX	RENEWED (REGISTERED)	74352513	1935775	14-Nov-95	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
OPENGL	RENEWED (REGISTERED)	74457527	1861701	8-Nov-94	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
<small>OPEN INVENTOR</small>	REGISTERED	78489817	3353285	11-Dec-07	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
OPEN INVENTOR					

Trademark	Status	Appl. No.	Reg. No.	Reg. Date	Unreleased Liens
	REGISTERED	75916455	2756592	26-Aug-03	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
OPENGL	REGISTERED	76037014	2576637	4-Jun-02	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
ORIGIN	REGISTERED	75158865	2498533	16-Oct-01	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
REALITY CENTER	REGISTERED	75706196	2815943	24-Feb-04	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
REALITY CENTER	REGISTERED	75983314	2923548	1-Feb-05	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
SGI	REGISTERED	75574897	2517897	11-Dec-01	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
	REGISTERED	75609926	2517956	11-Dec-01	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
SGI					

Trademark	Status	Appl. No.	Reg. No.	Reg. Date	Unreleased Liens
SGIUG	REGISTERED	78411146	3159951	17-Oct-06	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
SGIUG					
SILICON GRAPHICS	RENEWED (REGISTERED)	73587922	1519576	10-Jan-89	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
 SiliconGraphics	REGISTERED	73694562	1505659	27-Sep-88	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
SILICONGRAPHICS					
SiliconLIVE	PUBLISHED (PENDING) Intent to Use	77270954			
SILICONLIVE					
SILICON GRAPHICS FUEL	REGISTERED	76350213	2759937	2-Sep-03	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
 SiliconGraphics	REGISTERED	78494679	3146570	19-Sep-06	
SILICON GRAPHICS PRISM					

Trademark	Status	Appl. No.	Reg. No.	Reg. Date	Unreleased Liens
TEZRO	REGISTERED	78240435	2821359	9-Mar-04	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
UPSAFE	REGISTERED	75912147	2663825	17-Dec-02	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
VIRTU	PUBLISHED (PENDING) Intent to Use	77428359			
VIRTU					
VIZSERVER	REGISTERED	78705573	3424248	6-May-08	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227
VIZSERVER					
XFS	REGISTERED	78143151	2709544	22-Apr-03	Assignee: MORGAN STANLEY & CO., INC. Brief: ASSIGNMENT OF TRADEMARK SECURITY INTEREST Recorded: 12-OCT-2007 Reel/Frame: 3639/0227

## U.S. Copyrights and Copyright Applications

TITLE OF WORK	OWNER	COPY RIGHT NO.	REGISTRATION DATE	Unreleased Liens
Advanced RISC computing standards specification	Compaq Computer Corporation, Microsoft Corporation, Digital Equipment Corporation, MIPS Computer Systems, Inc., Santa Cruz Operation & Silicon Graphics, Inc.	TX3321564	15-Nov-91	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
Cosmo 3D	Silicon Graphics, Inc.	TX4508220	4-Sep-97	
FX 5.2	Silicon Graphics, Inc.	TX4011743	14-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
FX 5.3	Silicon Graphics, Inc.	TX4080492	14-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
INST for IRIX operating system 5.3	Silicon Graphics, Inc.	TX4030517	17-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
INST. For IRIX operating system 5.2	Silicon Graphics, Inc.	TX4039752	20-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
IRIS graphics library; IRS 1000 graphics library	Silicon Graphics, Inc.	TX2254968	30-Oct-87	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
IRIS Graphics library	Silicon Graphics, Inc.	TX2189440	30-Oct-87	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
IRIS graphics library; prev. or alternative ti., IRIS 4D/60 graphics library	Silicon Graphics, Inc.	TX2258964	2-Nov-87	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
IRIX operating system 5.0	Silicon Graphics, Inc.	TX4049554	14-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
IRIX operating system 4.0	Silicon Graphics, Inc.	TX4043632	20-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882



TITLE OF WORK	OWNER	COPYRIGHT NO.	REGISTRATION DATE	Unreleased Liens
IRIX operating system 5.1	Silicon Graphics, Inc.	TX4030275	17-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
IRIX operating system 5.2	Silicon Graphics, Inc.	TX4035236	20-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
IRIX operating system 5.3	Silicon Graphics, Inc.	TX4049552	14-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
IRIX operating system 6.5	Silicon Graphics, Inc.	TX5620232	13-Mar-02	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
LIA 4862 (XMAP2)	Silicon Graphics Computer Systems	MW6779	5-Feb-91	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
LIA 5328 (REV 2.1B)	Silicon Graphics Computer Systems	MW6778	5-Feb-91	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
Mask Work as contained in the R4400	Silicon Graphics, Inc.	MW9080	8-Jun-93	
Mask Work as contained in the R8000	Silicon Graphics, Inc.	MW9985	18-Apr-94	
Mask Work as contained in the R8010	Silicon Graphics, Inc.	MW9990	18-Apr-94	
Mask Work as contained in the R4200	Silicon Graphics, Inc.	MW10307	13-Sep-94	
Open GL programming guide: the official guide to learning OpenGL, version 1.1/OpenGL Architecture Review Board, Mason Woo, Jackie Neider, Tom Davis	Silicon Graphics, Inc.	TX4511339	8-Apr-97	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
Open GL programming guide/Mason Woo, Tom Davis, Jackie Neider, David Schreiner	Silicon Graphics, Inc.	TX5084206	22-Oct-99	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
Open GL programming guide	Silicon Graphics, Inc.	TX6244408	22-Sep-05	

TITLE OF WORK	OWNER	COPYRIGHT NO.	REGISTRATION DATE	Unreleased Liens
Open inventor C++ reference manual; the official reference document for open inventor, release 2.	Silicon Graphics, Inc.	TX3903037	31-Oct-94	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
OPENGL optimizer 1.0	Silicon Graphics, Inc.	TX4508192	3-Sep-07	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
OPENGL programming guide - the official guide to learning OPENGL, release 1	Silicon Graphics, Inc.	TX3616133	29-Sep-93	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
OpenGL programming guide: the official guide to learning OpenGL, version 1.4 / David Shreiner, Jackie Neider, Mason Woo, Tom Davis	Silicon Graphics, Inc.	TX5994534	9-Jun-04	
OPENGL reference manual: the official reference document to OPENGL, version 1.2	Silicon Graphics, Inc.	TX5144068	22-Feb-00	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
OPENGL reference manual: the official reference document to OPENGL, version 1.1	Silicon Graphics, Inc.	TX4511415	8-Apr-97	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
OpenGL reference manual: the official reference document to OpenGL, version 1.4 / editor, Dave Shreiner	Silicon Graphics, Inc.	TX5986324	23-Jun-04	
SASH	Silicon Graphics, Inc.	TX4043937	14-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
SASH for IRIX operating system 5.3	Silicon Graphics, Inc.	TX4049553	14-Apr-95	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
Silicon Graphics, Inc. (high tech series, vol. 2)	Silicon Graphics, Inc.	TX4213024		
The inventor mentor: programming object-oriented 3d graphics with open inventor, release 2.	Silicon Graphics, Inc.	TX3807545	29-Apr-94	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882

TITLE OF WORK	OWNER	COPYRIGHT NO.	REGISTRATION DATE	Unreleased Liens
The inventory toolmaker: extending open inventor, release 2.	Silicon Graphics, Inc.	TX3888466	29-Aug-94	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
The VRML 2.0 handbook: building moving worlds on the web	Silicon Graphics, Inc.	TX4238282	16-Dec-96	Holder: General Electric Corp. Recorded: October 18, 2006 Microfilm: V003543 P882
Directory of applications software for Cray supercomputers	Cray Research, Inc.	TX2183151	15-Oct-87	
Interface: a monthly newsletter for employees of Cray Research, Inc. /editor, Candace Louviere	Cray Research, Inc.	TX769005	21-Sep-81	

**Special Note:**

Whether or not Seller may own rights in the patent family listed below has not been conclusively determined. Such determination would likely require court proceedings. No representations or warranties are made with respect to this patent family.

Title	Status	Application No.
Patents and applications derived from U.S. Patent Application No. 07/510898, INTEGRATED CIRCUIT I/O USING A HIGH PERFORMANCE BUS INTERFACE, now abandoned.	Pending/ Issued	7510898

**EXHIBIT 2**

**AMENDED AND RESTATED SCHEDULE 2.1(j)**

**ACQUIRED SUBSIDIARIES**

Silicon Graphics Biomedical (1995) Ltd.  
Silicon Graphics Computer Systems Limited  
Silicon Graphics World Trade B.V.  
Cray Research (Canada) Inc.  
Silicon Graphics Sdn. Bhd. [Malaysia]  
Cray Research (Israel), Ltd.

**EXHIBIT 3**

**AMENDED AND RESTATED SCHEDULE 2.2(h)**

**EXCLUDED PATENTS<sup>1</sup>**

See attached.

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<sup>1</sup> Including any renewals, revivals, reissues, reexaminations, extensions, continuations, continuations-in-part, divisions and foreign counterparts of the patents and patent applications listed on this Schedule and any substitute applications therefor.

Schedule 2.2(h)

Schedule 2.2(h): Excluded IP<sup>1</sup>

U.S. Patents and Patent Applications

Case Number	Country	InvTitle	PatNumber	IssDate	Application Status	OwnerName
0016.00	JP	FILE CHARACTERIZATION FOR COMPUTER OPERATING AND FILE MANAGEMENT SYSTEMS	2986209	10/11/1999	Granted	SILICON GRAPHICS, INC.
0016.00	US	FILE CHARACTERIZATION FOR COMPUTER OPERATING AND FILE MANAGEMENT SYSTEMS	5226163	7/6/1993	Granted	SILICON GRAPHICS, INC.
0025.00	US	COMPUTER-IMPLEMENTED METHOD AND AN APPARATUS FOR CONVERTING DATA ACCORDING TO A SELECTED DATA TRANSFORMATION	5694578	12/2/1997	Granted	SILICON GRAPHICS, INC.
0025.01	US	COMPUTER-IMPLEMENTED METHOD AND AN APPARATUS FOR INPUTTING DATA STRUCTURE CHARACTERISTICS	5652874	7/29/1997	Granted	SILICON GRAPHICS, INC.
0029.00	US	COMPUTER MOUSE	D373760	9/17/1996	Granted	SILICON GRAPHICS, INC.
0030.00	US	FILE ALTERATION MONITOR FOR COMPUTER OPERATING AND FILE MANAGEMENT SYSTEMS	5287504	2/15/1994	Granted	SILICON GRAPHICS, INC.
0041.00	US	THREE DIMENSIONAL NAVIGATION SYSTEM AND INTERFACE	5555354	9/10/1996	Granted	SILICON GRAPHICS, INC.

<sup>1</sup> Including any renewals, revivals, reissues, reexaminations, extensions, continuations, continuations-in-part, divisions and foreign counterparts of the patents and patent application listed on this schedule and any substitute applications therefor.

779632 v3/HN

**Schedule 2.2(h)**

0053.51	US	A CENTRAL PROCESSING UNIT FOR PROCESSING A PLURALITY OF THREADS USING DEDICATED GENERAL PURPOSE REGISTERS AND MASQUE REGISTER FOR PROVIDING ACCESS TO THE REGISTERS	5524250	6/4/1996	Granted	SILICON GRAPHICS, INC.
0082.00	US	CIRCULAR SCHEDULING METHOD AND APPARATUS FOR EXECUTING COMPUTER PROGRAMS BY MOVING INDEPENDENT INSTRUCTIONS OUT OF A LOOP	5386562	1/31/1995	Granted	SILICON GRAPHICS, INC.
0103.00	US	A METHOD AND APPARATUS FOR DISPLAYING DATA WITHIN A THREE-DIMENSIONAL INFORMATIONAL LANDSCAPE	5528735	6/18/1996	Granted	SILICON GRAPHICS, INC.
0103.01	US	A METHOD AND APPARATUS FOR INCLUDING SELECTED OBJECTS BY SPOTLIGHT	5671381	9/23/1997	Granted	SILICON GRAPHICS, INC.
0103.0151	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR VISUALIZING DATA USING PARTIAL HIERARCHIES	6137499	10/24/2000	Granted	SILICON GRAPHICS, INC.
0103.0152	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR NAVIGATING THROUGH PARTIAL HIERARCHIES	6111578	8/29/2000	Granted	SILICON GRAPHICS, INC.
0103.0153	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR MAPPING BETWEEN AN OVERVIEW AND A PARTIAL HIERARCHY	6259451	7/10/2001	Granted	SILICON GRAPHICS, INC.
0103.0154	US	METHOD AND APPARATUS FOR INDICATING SELECTED OBJECTS BY SPOTLIGHT	5861885	1/19/1999	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN



**Schedule 2.2(h)**

0119.00	US	THREE DIMENSIONAL MODEL WITH THREE DIMENSIONAL POINTERS AND MULTIMEDIA FUNCTIONS LINKED TO THE POINTERS	5526478	6/11/1996	Granted	SILICON GRAPHICS, INC.
0119.51	US	THREE DIMENSIONAL MODEL WITH THREE DIMENSIONAL POINTERS AND MULTIMEDIA FUNCTIONS LINKED TO THE POINTERS	6313836	11/6/2001	Granted	SILICON GRAPHICS, INC.
0148.00	US	A SYSTEM & METHOD OF PERFORMING TOMOGRAPHIC RECONSTRUCTION & VOLUME RENDERING USING TEXTURE MAPPING	6002738	12/14/1999	Granted	SILICON GRAPHICS, INC.
0150.00	US	SYSTEM AND METHOD OF GENERATING OBJECT CODE USING AGGREGATE INSTRUCTION MOVEMENT	5557761	9/17/1996	Granted	SILICON GRAPHICS, INC.
0151.00	US	LOOP SCHEDULER	5491823	2/13/1996	Granted	SILICON GRAPHICS, INC.
0153.00	US	SYSTEM AND METHOD FOR ENABLING, WITHOUT RECOMPILATION, MODIFICATION OF CLASS DEFINITIONS & IMPLEMENTATIONS IN AN OBJECT-ORIENTED COMPUTER PROGRAM	5613120	3/18/1997	Granted	SILICON GRAPHICS, INC.
0154.00	JP	SYSTEM AND METHOD FOR CONDITIONALLY COMPILING A SOFTWARE COMPILATION UNIT	3802058	5/12/2006	Granted	SILICON GRAPHICS, INC.
0154.00	US	SYSTEM AND METHOD FOR CONDITIONALLY COMPILING A SOFTWARE COMPILATION UNIT	5692196	11/25/1997	Granted	SILICON GRAPHICS, INC.
0169.00	US	SYSTEM AND METHOD FOR OPTIMIZING A SOURCE CODE REPRESENTATION AS A FUNCTION OF RESOURCE UTILIZATION	5734908	3/31/1998	Granted	SILICON GRAPHICS, INC.

779632 v3/HN

**Schedule 2.2(h)**

0180.00	US	CROSS-MODULE OPTIMIZATION FOR DYNAMICALLY-SHARED PROGRAMS AND LIBRARIES	5923882	7/13/1999	Granted	SILICON GRAPHICS, INC.
0184.00	US	APPARATUS FOR GENERATING DIFFERENTIAL NOISE BETWEEN POWER AND GROUND PLANES	5793259	8/11/1998	Granted	SILICON GRAPHICS, INC.
0185.00	US	SYSTEM AND METHOD FOR PROCESSING GRAPHIC LANGUAGE CHARACTERS	5802482	9/1/1998	Granted	SILICON GRAPHICS, INC.
0187.00	IN	EFFICIENT ULTRA LOW DROPOUT POWER REGULATOR	199707	3/9/2007	Granted	SILICON GRAPHICS, INC.
0187.00	IN	EFFICIENT ULTRA LOW DROPOUT POWER REGULATOR			Pending	SILICON GRAPHICS, INC.
0187.00	US	EFFICIENT ULTRA LOW DROPOUT POWER REGULATOR	5736843	4/7/1998	Granted	SILICON GRAPHICS, INC.
0189.00	US	FOUR-DIMENSIONAL GRAPHICAL USER INTERFACE	5678015	10/14/1997	Granted	SILICON GRAPHICS, INC.
0191.00	US	SYSTEM AND METHOD FOR NETWORK EXPLORATION AND ACCESS	5682479	10/28/1997	Granted	SILICON GRAPHICS, INC.
0194.00	US	FLAT PANEL MONITOR COMBINING DIRECT VIEW WITH OVERHEAD PROJECTION CAPABILITY	5696529	12/9/1997	Granted	SILICON GRAPHICS, INC.
0194.01	US	MULTI-LAYER FLAT PANEL DISPLAY SCREEN APPARATUS	5986737	11/16/1999	Granted	SILICON GRAPHICS, INC.
0194.02	US	FLAT PANEL DISPLAY SCREEN APPARATUS WITH OPTICAL JUNCTION AND REMOVABLE BACKLIGHTING ASSEMBLY	5831697	11/3/1998	Granted	SILICON GRAPHICS, INC.
0195.00	US	REMOVABLE BACKLIGHTING ASSEMBLY FOR FLAT PANEL DISPLAY SUBSYSTEM	5896119	4/20/1999	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

0195.01	US	REMOVABLE BACKLIGHTING ASSEMBLY FOR FLAT PANEL DISPLAY	6144360	11/7/2000	Granted	SILICON GRAPHICS, INC.
0196.00	US	OVERHEAD PROJECTOR FOR FLAT PANEL DISPLAY CAPABILITY	5593221	1/14/1997	Granted	SILICON GRAPHICS, INC.
0212.00	US	SYSTEM AND METHOD FOR SAVING STATE INFORMATION IN AN INTERACTIVE TELEVISION SYSTEM	5835717	11/10/1998	Granted	SGI/TIME WARNER
0213.00	US	SYSTEM AND METHOD FOR MEDIA STREAM SYNCHRONIZATION WITH A BASE ATOM INDEX FILE AND AN AUXILIARY ATOM INDEX FILE	5751280	5/12/1998	Granted	SGI/TIME WARNER
0214.00	US	SYSTEM & METHOD USING COVER BUNDLES TO PROVIDE IMMEDIATE FEEDBACK TO A USER IN AN INTERACTIVE TELEVISION ENVIRONMENT	5802284	9/1/1998	Granted	SGI/TIME WARNER
0217.52	US	SYSTEM AND METHOD FOR RESOURCE RECOVERY IN A DISTRIBUTED SYSTEM	6728895	4/27/2004	Granted	SGI/TIME WARNER
0218.00	US	SYSTEM AND METHOD FOR COORDINATING COMMUNICATION BETWEEN LAYERS OF A LAYERED COMMUNICATION SYSTEM	5784567	7/21/1998	Granted	SGI/TIME WARNER
0224.00	EP	ANTI_ALIASING OF SILHOUETTE EDGES	0767444	1/2/2003	Granted	SGI/NINTEENDO
0224.00	US	ANTI_ALIASING OF SILHOUETTE EDGES	5742277	4/21/1998	Granted	SGI/NINTEENDO
0224.00	CN	ANTI_ALIASING OF SILHOUETTE EDGES	96112418.0	11/26/2003	Granted	SGI/NINTEENDO
0224.00	TW	ANTI_ALIASING OF SILHOUETTE EDGES	NI-111077	5/24/2000	Granted	SGI/NINTEENDO
0224.00	JP	ANTI_ALIASING OF SILHOUETTE EDGES			Pending	SGI/NINTEENDO

779632 \*3/HN

**Schedule 2.2(h)**

0228.00	US	OPERATING SYSTEM HAVING A MECHANISM FOR HANDLING A GROUP OF RELATED PROCESSES RESIDING ON SEPARATE MACHINES	6279028	8/21/2001	Granted	SILICON GRAPHICS, INC.
0248.00	DE	SYSTEM AND METHOD FOR GENERATING AND DISPLAYING COMPLEX GRAPHIC IMAGES AT A CONSTANT FRAME RATE	69729916.3	7/21/2004	Granted	SILICON GRAPHICS, INC.
0248.00	EP	SYSTEM AND METHOD FOR GENERATING AND DISPLAYING COMPLEX GRAPHIC IMAGES AT A CONSTANT FRAME RATE	0827615	7/21/2004	Granted	SILICON GRAPHICS, INC.
0248.00	GB	SYSTEM AND METHOD FOR GENERATING AND DISPLAYING COMPLEX GRAPHIC IMAGES AT A CONSTANT FRAME RATE	0827615	7/21/2004	Granted	SILICON GRAPHICS, INC.
0248.00	JP	SYSTEM AND METHOD FOR GENERATING AND DISPLAYING COMPLEX GRAPHIC IMAGES AT A CONSTANT FRAME RATE	4043518	2/6/2008	Granted	SILICON GRAPHICS, INC.
0248.00	US	SYSTEM AND METHOD FOR GENERATING AND DISPLAYING COMPLEX GRAPHIC IMAGES AT A CONSTANT FRAME RATE	5889529	3/30/1999	Granted	SILICON GRAPHICS, INC.
0251.00	US	SYSTEM AND METHOD FOR ALLOWING A PERFORMER TO CONTROL AND INTERACT WITH AN ON-STAGE DISPLAY DEVICE	5790124	8/4/1998	Granted	SILICON GRAPHICS, INC.
0262.00	US	OUTPUT PIN FOR SELECTIVELY OUTPUTTING ONE OF A PLURALITY OF SIGNALS INTERNAL TO A SEMICONDUCTOR CHIP ACCORDING TO A PROGRAMMABLE REGISTER FOR DIAGNOSTICS	5717695	2/10/1998	Granted	SILICON GRAPHICS, INC.

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**Schedule 2.2(h)**

0282.00	US	SYSTEM AND METHOD FOR AN ICONIC DRAG AND DROP INTERFACE FOR ELECTRONIC FILE TRANSFER	5801700	9/1/1998	Granted	SILICON GRAPHICS, INC.
0284.00	US	METHOD FOR SEEDING A PSEUDO-RANDOM NUMBER GENERATOR WITH A CRYPTOGRAPHIC HASH OF A DIGITIZATION OF A CHAOTIC SYSTEM	5732138	3/24/1998	Granted	SILICON GRAPHICS, INC.
0298.00	US	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING	6331856	12/18/2001	Granted	SGI/NINTENDO
0298.00	CN	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING			Pending	SGI/NINTENDO
0298.00	EP	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING			Pending	SGI/NINTENDO
0298.00	JP	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING			Pending	SGI/NINTENDO

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Schedule 2.2(h)

0298.00	TW	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING		Pending	SGI/NINTENDO	
0298.01	US	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING	6342892	1/29/2002	Granted	SGI/NINTENDO
0298.03	US	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING	6593929	7/15/2003	Granted	SGI/NINTENDO
0299.00	EP	RESTORATION FILTER FOR TRUNCATED PIXELS	0775983	5/15/2002	Granted	SGI/NINTENDO
0299.00	JP	RESTORATION FILTER FOR TRUNCATED PIXELS			Pending	SGI/NINTENDO
0299.00	US	RESTORATION FILTER FOR TRUNCATED PIXELS	5699079	12/16/1997	Granted	SGI/NINTENDO
0300.00	EP	SYSTEM AND METHOD FOR MERGING PIXEL FRAGMENTS BASED ON DEPTH RANGE VALUES	EP 775982	3/31/2004	Granted	SGI/NINTENDO
0300.00	JP	SYSTEM AND METHOD FOR MERGING PIXEL FRAGMENTS BASED ON DEPTH RANGE VALUES			Pending	SGI/NINTENDO
0300.00	US	SYSTEM AND METHOD FOR MERGING PIXEL FRAGMENTS BASED ON DEPTH RANGE VALUES	5854631	12/29/1998	Granted	SGI/NINTENDO

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**Schedule 2.2(h)**

0301.00	US	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING	6166748	12/26/2000	Granted	SGI/NINTENDO
0301.51	US	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING	6239810	5/29/2001	Granted	SGI/NINTENDO
0301.52	US	HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING	6556197	4/29/2003	Granted	SGI/NINTENDO
0303.00	US	DATABASE-INDEPENDENT, SCALABLE, OBJECT-ORIENTED ARCHITECTURE AND API FOR MANAGING DIGITAL MULTIMEDIA ASSETS	6181336	1/30/2001	Granted	SILICON GRAPHICS, INC.
0303.01	US	DATABASE-INDEPENDENT, SCALABLE, OBJECT-ORIENTED ARCHITECTURE AND API FOR MANAGING DIGITAL MULTIMEDIA ASSETS	6654029	11/25/2003	Granted	SILICON GRAPHICS, INC.
0307.00	US	COLLABORATIVE WORK ENVIRONMENT SUPPORTING THREE-DIMENSIONAL OBJECTS AND MULTIPLE, REMOTE PARTICIPANTS	5821925	10/13/1998	Granted	SILICON GRAPHICS, INC.

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**Schedule 2.2(h)**

0307.51	US	6219057	COLLABORATIVE WORK ENVIRONMENT SUPPORTING THREE-DIMENSIONAL OBJECTS AND MULTIPLE, REMOTE PARTICIPANTS	4/17/2001	Granted	SILICON GRAPHICS, INC.
0324.00	US	5768596	SYSTEM AND METHOD TO EFFICIENTLY REPRESENT ALIASES AND INDIRECT MEMORY OPERATIONS IN STATIC SINGLE ASSIGNMENT FORM DURING COMPILATION	6/16/1998	Granted	SILICON GRAPHICS, INC.
0324.51	US	6131189	SYSTEM AND METHOD TO EFFICIENTLY REPRESENT ALIASES AND INDIRECT MEMORY OPERATIONS IN STATIC SINGLE ASSIGNMENT FORM DURING COMPILATION	10/10/2000	Granted	SILICON GRAPHICS, INC.
0326.00	US	5694532	A METHOD FOR SELECTING A THREE-DIMENSIONAL OBJECT FROM A GRAPHICAL USER INTERFACE	12/2/1997	Granted	SILICON GRAPHICS, INC.
0335.00	US	5933155	SYSTEM AND METHOD FOR BUFFERING MULTIPLE FRAMES WHILE CONTROLLING LATENCY	8/3/1999	Granted	SILICON GRAPHICS, INC.
0335.51	US	6075543	SYSTEM AND METHOD FOR BUFFERING MULTIPLE FRAMES WHILE CONTROLLING LATENCY	6/13/2000	Granted	SILICON GRAPHICS, INC.
0339.00	US	6480194	COMPUTER-RELATED METHOD, SYSTEM AND PROGRAM PRODUCT FOR CONTROLLING DATA VISUALIZATION IN EXTERNAL DIMENSIONS(S)	11/12/2002	Granted	SILICON GRAPHICS, INC.
0339.51	US	6842176	COMPUTER-RELATED METHOD AND SYSTEM FOR CONTROLLING DATA VISUALIZATION IN EXTERNAL DIMENSION(S)	1/11/2005	Granted	SILICON GRAPHICS, INC.

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**Schedule 2.2(h)**

0341.00	US	MECHANISM FOR NON-LINEAR BROWSING OF DIVERSE INFORMATION SOURCES	5835092	11/10/1998	Granted	SILICON GRAPHICS, INC.
0346.00	US	AN APPARATUS AND METHOD FOR COMPILER IDENTIFICATION OF ADDRESS DATA	6059839	5/9/2000	Granted	SILICON GRAPHICS, INC.
0348.00	US	SYSTEM AND METHOD FOR CONSTANT PROPAGATION CLONING FOR UNKNOWN EDGES IN IPA	5812855	9/22/1998	Granted	SILICON GRAPHICS, INC.
0350.00	US	INTER-PROCEDURAL ANALYSIS USER INTERFACE	5778212	7/7/1998	Granted	SILICON GRAPHICS, INC.
0354.00	US	COMPILER HAVING AUTOMATIC COMMON BLOCKS OF MEMORY SPLITTING	5848275	12/8/1998	Granted	SILICON GRAPHICS, INC.
0355.00	US	METHOD FOR SELECTING OPTIMAL PARAMETERS FOR COMPILING SOURCE CODE	6226790	5/1/2001	Granted	SILICON GRAPHICS, INC.
0357.00	US	METHOD FOR UNROLLING TWO-DEEP LOOPS WITH CONVEX BOUNDS AND IMPERFECTLY NESTED CODE, AND FOR UNROLLING ARBITRARILY DEEP NESTS WITH CONSTANT BOUNDS AND IMPERFECTLY NESTED CODE	6567976	5/20/2003	Granted	SILICON GRAPHICS, INC.
0365.00	US	METHOD AND APPARATUS FOR EXTRACTION OF PROGRAM REGION	6077311	6/20/2000	Granted	SILICON GRAPHICS, INC.
0373.00	US	BROWSER DRIVEN USER INTERFACE TO A MEDIA ASSET DATABASE	5893110	4/6/1999	Granted	SILICON GRAPHICS, INC.
0380.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM FOR VISUALLY APPROXIMATING SCATTERED DATA	5861891	1/19/1999	Granted	SILICON GRAPHICS, INC.

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**Schedule 2.2(h)**

0380.51	US	INTERPOLATION BETWEEN RELATIONAL TABLES FOR PURPOSES OF ANIMATING A DATA VISUALIZATION	6034697	3/7/2000	Granted	SILICON GRAPHICS, INC.
0380.52	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR VISUALLY APPROXIMATING SCATTERED DATA USING COLOR TO REPRESENT VALUES OF A CATEGORICAL VARIABLE	6373483	4/16/2002	Granted	SILICON GRAPHICS, INC.
0405.00	CA	COMPRESSION AND DECOMPRESSION SCHEME PERFORMED ON SHARED WORKSTATION MEMORY BY MEDIA COPROCESSOR	2259513	11/21/2006	Granted	SILICON GRAPHICS, INC.
0405.00	DE	COMPRESSION AND DECOMPRESSION SCHEME PERFORMED ON SHARED WORKSTATION MEMORY BY MEDIA COPROCESSOR	69720477.4	4/2/2003	Granted	SILICON GRAPHICS, INC.
0405.00	EP	COMPRESSION AND DECOMPRESSION SCHEME PERFORMED ON SHARED WORKSTATION MEMORY BY MEDIA COPROCESSOR	0925687	4/2/2003	Granted	SILICON GRAPHICS, INC.
0405.00	FR	COMPRESSION AND DECOMPRESSION SCHEME PERFORMED ON SHARED WORKSTATION MEMORY BY MEDIA COPROCESSOR	0925687	4/2/2003	Granted	SILICON GRAPHICS, INC.
0405.00	GB	COMPRESSION AND DECOMPRESSION SCHEME PERFORMED ON SHARED WORKSTATION MEMORY BY MEDIA COPROCESSOR	0925687	4/2/2003	Granted	SILICON GRAPHICS, INC.

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**Schedule 2.2(h)**

0405.00	US	COMPRESSION AND DECOMPRESSION SCHEME PERFORMED ON SHARED WORKSTATION MEMORY BY MEDIA COPROCESSOR	5768445	6/16/1998	Granted	SILICON GRAPHICS, INC.
0407.00	US	CIRCUIT TO SEPARATE AND COMBINE COLOR SPACE COMPONENT DATA OF A VIDEO IMAGE	5835729	11/10/1998	Granted	SILICON GRAPHICS, INC.
0411.00	US	A METHOD AND SYSTEM FOR AN EFFICIENT USER MODE CACHE MANIPULATION USING A SIMULATED INSTRUCTION	6049866	4/11/2000	Granted	SILICON GRAPHICS, INC.
0412.00	US	METHOD AND APPARATUS FOR ADDRESS SPACE TRANSLATION USING A TLB	5815686	9/29/1998	Granted	SILICON GRAPHICS, INC.
0418.00	JP	SYNCHRONIZATION INFRASTRUCTURE FOR USE IN A COMPUTER SYSTEM			Pending	SILICON GRAPHICS, INC.
0418.00	US	SYNCHRONIZATION INFRASTRUCTURE FOR USE IN A COMPUTER SYSTEM	5764965	6/9/1998	Granted	SILICON GRAPHICS, INC.
0418.00	WO	SYNCHRONIZATION INFRASTRUCTURE FOR USE IN A COMPUTER SYSTEM			Pending	SILICON GRAPHICS, INC.
0424.00	US	A METHOD AND APPARATUS FOR INTERACTIVELY SELECTING AMONG THREE-DIMENSIONAL ON-SCREEN OBJECTS	5982382	11/9/1999	Granted	SILICON GRAPHICS, INC.
0429.00	US	METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR VISUALIZING AN EVIDENCE CLASSIFIER	5930803	7/27/1999	Granted	SILICON GRAPHICS, INC.
0429.51	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR VISUALIZING AN EVIDENCE CLASSIFIER	6460049	10/1/2002	Granted	SILICON GRAPHICS, INC.

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**Schedule 2.2(h)**

0441.00	US	EARNINGS-BASED TIME-SHARE SCHEDULING	6714960	3/30/2004	Granted	SILICON GRAPHICS, INC.
0447.00	US	SYSTEM AND METHOD FOR INDEPENDENTLY CONFIGURING INTERNATIONAL LOCATION AND LANGUAGE	6073090	6/6/2000	Granted	SILICON GRAPHICS, INC.
0471.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR VISUALIZING A DECISION-TREE CLASSIFIER	6278464	8/21/2001	Granted	SILICON GRAPHICS, INC.
0479.00	US	SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR PARTIAL REDUNDANCY ELIMINATION BASED ON STATIC SINGLE ASSIGNMENT FORM DURING COMPILATION	6026241	2/15/2000	Granted	SILICON GRAPHICS, INC.
0480.00	US	BAYES RULE BASED AND DECISION TREE HYBRID CLASSIFIER	6182058	1/30/2001	Granted	SILICON GRAPHICS, INC.
0481.00	US	SYSTEM AND METHOD FOR SELECTION OF IMPORTANT ATTRIBUTES	6026399	2/15/2000	Granted	SILICON GRAPHICS, INC.
0488.00	US	LIGHT BAR AND REFLECTOR ASSEMBLY	5861815	1/19/1999	Granted	SILICON GRAPHICS, INC.
0497.00	US	SYSTEM AND METHOD FOR AUTOMATICALLY CREATING A DESKTOP ICON FOR ACCESS TO A REMOTE RESOURCE	5880730	3/9/1999	Granted	SILICON GRAPHICS, INC.
0514.00	US	METHOD AND SYSTEM FOR DECODING DATA ENCODED IN A VARIABLE LENGTH CODE WORD	6219457	4/17/2001	Granted	SILICON GRAPHICS, INC.
0518.00	US	RESUMPTION OF A PREEMPTED NON-PRIVILEGED THREAD WITH NO KERNEL INTERVENTION	5872963	2/16/1999	Granted	SILICON GRAPHICS, INC.

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**Schedule 2.2(h)**

0524.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR COMPUTING HISTOGRAM AGGREGATIONS	5960435	9/28/1999	Granted	SILICON GRAPHICS, INC.
0532.00	US	SYSTEM AND METHOD FOR MAINTAINING TRANSLATION LOOK-ASIDE BUFFER (TLB) CONSISTENCY	6105113	8/15/2000	Granted	SILICON GRAPHICS, INC.
0539.00	US	METHOD AND SYSTEM FOR PERFORMING FLOATING POINT OPERATIONS IN UNNORMALIZED FORMAT USING A FLOATING POINT ACCUMULATOR	6256655	7/3/2001	Granted	SILICON GRAPHICS, INC.
0544.00	US	METHOD AND SYSTEM FOR DESKEWING PARALLEL BUS CHANNELS	6031847	2/29/2000	Granted	SILICON GRAPHICS, INC.
0547.00	US	ACOUSTIC PERSPECTIVE IN A VIRTUAL THREE-DIMENSIONAL ENVIRONMENT	6330486	12/11/2001	Granted	SILICON GRAPHICS, INC.
0548.00	US	SYSTEM PROVIDING EXTENSIBLE CONTENT PROCESSING AND INSTALLATION IN A DISTRIBUTED MULTIMEDIA ASSET MANAGEMENT SYSTEM			Pending	SILICON GRAPHICS, INC.
0557.00	US	SYSTEM AND METHOD FOR CODING COLORS AND STORING COMPENSATION FACTORS USED IN COLOR SPACE CONVERSION	5995655	11/30/1999	Granted	SILICON GRAPHICS, INC.
0586.00	US	A VIDEO FRAME DETECTOR READILY ADAPTABLE TO VIDEO SIGNAL FORMATS WITHOUT MANUAL PROGRAMMING AND METHOD FOR SAME	6160589	12/12/2000	Granted	SILICON GRAPHICS, INC.
0590.00	EP	SYSTEM AND METHOD FOR HIGH-SPEED EXECUTION OF GRAPHICS APPLICATION PROGRAMS INCLUDING SHADING LANGUAGE INSTRUCTIONS	1070301	12/18/2002	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

0590.00	GB	SYSTEM AND METHOD FOR HIGH-SPEED EXECUTION OF GRAPHICS APPLICATION PROGRAMS INCLUDING SHADING LANGUAGE INSTRUCTIONS	1070301	12/18/2002	Granted	SILICON GRAPHICS, INC.
0590.00	US	SYSTEM AND METHOD FOR HIGH-SPEED EXECUTION OF GRAPHICS APPLICATION PROGRAMS INCLUDING SHADING LANGUAGE INSTRUCTIONS	6578197	6/10/2003	Granted	SILICON GRAPHICS, INC.
0590.00	FR	SYSTEM AND METHOD FOR HIGH-SPEED EXECUTION OF GRAPHICS APPLICATION PROGRAMS INCLUDING SHADING LANGUAGE INSTRUCTIONS	1070301 B1	12/18/2002	Granted	SILICON GRAPHICS, INC.
0590.00	DE	SYSTEM AND METHOD FOR HIGH-SPEED EXECUTION OF GRAPHICS APPLICATION PROGRAMS INCLUDING SHADING LANGUAGE INSTRUCTIONS	DE69904579	12/18/2002	Granted	SILICON GRAPHICS, INC.
0590.00	JP	SYSTEM AND METHOD FOR HIGH-SPEED EXECUTION OF GRAPHICS APPLICATION PROGRAMS INCLUDING SHADING LANGUAGE INSTRUCTIONS			Pending	SILICON GRAPHICS, INC.
0591.00	US	HASHED DIRECT-MAPPED TEXTURE CACHE	6233647	5/15/2001	Granted	SILICON GRAPHICS, INC.
0595.00	US	MULTIPLE LIGHT SOURCE COLOR BALANCING SYSTEM WITHIN A LIQUID CRYSTAL FLAT PANEL DISPLAY	6366270	4/2/2002	Granted	SILICON GRAPHICS, INC.
0595.51	US	MULTIPLE LIGHT SOURCE COLOR BALANCING SYSTEM WITHIN A LIQUID CRYSTAL FLAT PANEL DISPLAY	6535190	3/18/2003	Granted	SILICON GRAPHICS, INC.

779632 v3/HN

**Schedule 2.2(h)**

0603.00	US	LIQUID CRYSTAL FLAT PANEL DISPLAY WITH ENHANCED BACKLIGHT BRIGHTNESS AND SPECIALLY SELECTED LIGHT SOURCES	6243068	6/5/2001	Granted	SILICON GRAPHICS, INC.
0603.01	US	LIQUID CRYSTAL FLAT PANEL DISPLAY WITH ENHANCED BACKLIGHT BRIGHTNESS AND SPECIALLY SELECTED LIGHT SOURCES	6448955	9/10/2002	Granted	SILICON GRAPHICS, INC.
0603.51	US	LIQUID CRYSTAL FLAT PANEL DISPLAY WITH ENHANCED BACKLIGHT BRIGHTNESS AND SPECIALLY SELECTED LIGHT SOURCES	6657607	12/2/2003	Granted	SILICON GRAPHICS, INC.
0609.00	US	INTERCONNECT CAPACITIVE EFFECTS ESTIMATION	6314546	11/6/2001	Granted	SILICON GRAPHICS, INC.
0632.00	US	DISPLAY SYSTEM HAVING FLOATING POINT RASTERIZATION AND FLOATING POINT FRAME BUFFERING	6650327	11/18/2003	Granted	SILICON GRAPHICS, INC.
0632.51	US	DISPLAY SYSTEM HAVING FLOATING POINT RASTERIZATION AND FLOATING POINT FRAME BUFFERING			Pending	SILICON GRAPHICS, INC.
0632.5151	US	DISPLAY SYSTEM HAVING FLOATING POINT RASTERIZATION AND FLOATING POINT FRAME BUFFERING			Published	SILICON GRAPHICS, INC.
0637.00	US	APPARATUS AND METHOD FOR UTILIZING MULTIPLE RENDERING PIPES FOR A SINGLE 3-D DISPLAY	6292200	9/18/2001	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

0658.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR PERFORMING REGISTER PROMOTION VIA LOAD AND STORE PLACEMENT OPTIMIZATION WITHIN AN OPTIMIZING COMPILER	6128775	10/3/2000	Granted	SILICON GRAPHICS, INC.
0670.00	US	METHOD SYSTEM AND COMPUTER PROGRAM PRODUCT FOR VISUALIZING A DATA STRUCTURE	6301579	10/9/2001	Granted	SILICON GRAPHICS, INC.
0677.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR USING STATIC SINGLE ASSIGNMENT FORM AS A PROGRAM REPRESENTATION AND A MEDIUM FOR PERFORMING GLOBAL SCALAR OPTIMIZATION	6301704	10/9/2001	Granted	SILICON GRAPHICS, INC.
0678.00	US	METHOD AND APPARATUS FOR COMPRESSING AND UNCOMPRESSING IMAGE DATA	7058218	6/6/2006	Granted	SILICON GRAPHICS, INC.
0678.01	US	METHOD AND APPARATUS FOR COMPRESSING AND UNCOMPRESSING IMAGE DATA	7330587	2/12/2008	Granted	SILICON GRAPHICS, INC.
0678.0151	US	METHOD OF AND APPARATUS FOR COMPRESSING AND UNCOMPRESSING IMAGE DATA			Published	
0721.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR EXTENDING SPARSE PARTIAL REDUNDANCY ELIMINATION TO SUPPORT SPECULATIVE CODE MOTION WITHIN AN OPTIMIZING COMPILER	6151706	11/21/2000	Granted	SILICON GRAPHICS, INC.
0725.00	US	ENHANCED GRAPHICS CACHE MEMORY	6513099	1/28/2003	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN



**Schedule 2.2(h)**

0747.00	US	LARGE AREA WIDE ASPECT RATIO FLAT PANEL MONITOR HAVING HIGH RESOLUTION FOR HIGH INFORMATION CONTENT DISPLAY	6816145	11/9/2004	Granted	SILICON GRAPHICS, INC.
0748.00	US	METHOD AND SYSTEM FOR EFFICIENT EDGE BLENDING IN HIGH FIDELITY MULTICHANNEL COMPUTER GRAPHICS DISPLAYS	6545685	4/8/2003	Granted	SILICON GRAPHICS, INC.
0749.00	JP	DISPLAY ASSEMBLY OR SIMILAR ARTICLE (DESIGN)	1095736	10/27/2000	Granted	SILICON GRAPHICS, INC.
0749.00	US	DISPLAY ASSEMBLY OR SIMILAR ARTICLE (DESIGN)	D419141	1/18/2000	Granted	SILICON GRAPHICS, INC.
0750.00	US	FLAT PANEL DISPLAY AND STAND WITH VERTICAL ADJUSTMENT AND TILT ADJUSTMENT	6061104	5/9/2000	Granted	SILICON GRAPHICS, INC.
0753.00	US	SYSTEM AND METHOD FOR PROVIDING A WIDE ASPECT RATIO FLAT PANEL DISPLAY MONITOR INDEPENDENT WHITE-BALANCE ADJUSTMENT AND GAMMA CORRECTION CAPABILITIES	6611249	8/26/2003	Granted	SILICON GRAPHICS, INC.
0753.51	US	SYSTEM AND METHOD FOR PROVIDING A WIDE ASPECT RATIO FLAT PANEL DISPLAY MONITOR INDEPENDENT WHITE-BALANCE ADJUSTMENT AND GAMMA CORRECTION CAPABILITIES	7136076	11/14/2006	Granted	SILICON GRAPHICS, INC.
0753.5151	US	SYSTEM AND METHOD FOR PROVIDING A WIDE ASPECT RATIO FLAT PANEL DISPLAY MONITOR INDEPENDENT WHITE-BALANCE ADJUSTMENT AND GAMMA CORRECTION CAPABILITIES			Published	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

0764.00	US	METHOD AND APPARATUS FOR REPRESENTING, MANIPULATING AND RENDERING SOLID SHAPES USING VOLUMETRIC PRIMITIVES	6600487	7/29/2003	Granted	SILICON GRAPHICS, INC.
0768.00	US	METHOD AND SYSTEM FOR HYBRID MAPPING OF OBJECTS INTO A RELATIONAL DATA BASE TO PROVIDE HIGH-SPEED PERFORMANCE AND UPDATE FLEXIBILITY	6615204	9/2/2003	Granted	SILICON GRAPHICS, INC.
0774.51	US	METHOD FOR MODELING AND UPDATING A COLORIMETRIC REFERENCE PROFILE FOR A FLAT PANEL DISPLAY	6559826	5/6/2003	Granted	SILICON GRAPHICS, INC.
0801.00	DE	COLOR CALIBRATION DEVICE FOR A DISPLAY (DESIGN)	49906462.3	10/21/1999	Granted	SILICON GRAPHICS, INC.
0801.00	FR	COLOR CALIBRATION DEVICE FOR A DISPLAY (DESIGN)	995251	8/25/1999	Granted	SILICON GRAPHICS, INC.
0801.00	GB	COLOR CALIBRATION DEVICE FOR A DISPLAY (DESIGN)	2084674	9/8/1999	Granted	SILICON GRAPHICS, INC.
0801.00	JP	COLOR CALIBRATION DEVICE FOR A DISPLAY (DESIGN)	1094490	10/20/2000	Granted	SILICON GRAPHICS, INC.
0801.00	US	COLOR CALIBRATION DEVICE FOR A DISPLAY (DESIGN)	D419465	1/25/2000	Granted	SILICON GRAPHICS, INC.
0827.00	US	MULTITHREADED LAYERED-CODE PROCESSOR	6769122	7/27/2004	Granted	SILICON GRAPHICS, INC.
0837.00	US	EFFICIENT MEMORY STRUCTURE SIMULATION FOR SEQUENTIAL CIRCUIT DESIGN VERIFICATION	6813599	11/2/2004	Granted	SILICON GRAPHICS, INC.

779632 v3/HN

**Schedule 2.2(h)**

0854.00	US	SCAN INTERFACE CHIP (SIC) SYSTEM AND METHOD FOR SCAN TESTING ELECTRONIC SYSTEMS	6754863	6/22/2004	Granted	SILICON GRAPHICS, INC.
0855.00	US	SCAN INTERFACE CHIP (SIC) SYSTEM AND METHOD FOR SCAN TESTING ELECTRONIC SYSTEMS	6813739	11/2/2004	Granted	SILICON GRAPHICS, INC.
0859.00	US	METHOD AND COMPUTER PROGRAM PRODUCT FOR GLOBAL MINIMIZATION OF SIGN-EXTENSION AND ZERO-EXTENSION OPERATIONS	6571387B1	5/27/2003	Granted	SILICON GRAPHICS, INC.
0864.00	US	MODELING AND FABRICATION OF OBJECTS REPRESENTED AS DEVELOPABLE SURFACES	6493603	12/10/2002	Granted	SILICON GRAPHICS, INC.
0876.00	US	FLAT PANEL DISPLAY SCREEN WITH PROGRAMMABLE GAMMA FUNCTIONALITY	6359389	3/19/2002	Granted	SILICON GRAPHICS, INC.
0877.51	US	DISPLAY CAPABLE OF DISPLAYING IMAGES IN RESPONSE TO SIGNALS OF A PLURALITY OF SIGNAL FORMATS	7138989	11/21/2006	Granted	SILICON GRAPHICS, INC.
0877.5151	US	DISPLAY CAPABLE OF DISPLAYING IMAGES IN RESPONSE TO SIGNALS OF A PLURALITY OF SIGNAL FORMATS			Published	SILICON GRAPHICS, INC.
0877.5152	US	DISPLAY CAPABLE OF DISPLAYING IMAGES IN RESPONSE TO SIGNALS OF A PLURALITY OF SIGNAL FORMATS			Published	
0878.00	US	SYSTEM AND METHOD FOR MAINTAINING AND RECOVERING DATA CONSISTENCY ACROSS MULTIPLE INSTANCES OF A DATABASE	6751636	6/15/2004	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

0879.00	US	A SYSTEM AND METHOD FOR MAINTAINING AND RECOVERING DATA CONSISTENCY IN A DATA BASE PAGE	6993523 B1	1/31/2006	Granted	SILICON GRAPHICS, INC.
0901.00	US	METHOD AND SYSTEM FOR DISTRIBUTED RENDERING			Pending	Silicon Graphics/Wam!Net
0910.00	US	METHOD AND COMPUTER PROGRAM PRODUCT FOR PRECISE FEEDBACK DATA GENERATION AND UPDATING FOR COMPILE-TIME OPTIMIZATIONS	7120906B1	10/10/2006	Granted	SILICON GRAPHICS, INC.
0919.00	US	METHOD AND APPARATUS FOR PREPARING A PERSPECTIVE VIEW OF AN APPROXIMATELY SPHERICAL SURFACE PORTION	6476813	11/5/2002	Granted	SILICON GRAPHICS, INC.
0919.51	US	METHOD AND APPARATUS FOR PREPARING A PERSPECTIVE VIEW OF AN APPROXIMATELY SPHERICAL SURFACE PORTION	6618049	9/9/2003	Granted	SILICON GRAPHICS, INC.
0940.51	US	MULTI-MODE DISPLAY	7009616	3/7/2006	Granted	SILICON GRAPHICS, INC.
0993.00	US	SYSTEM AND METHOD FOR DISPLAYING AN IMAGE USING DISPLAY DISTORTION CORRECTION	6819333B1	11/16/2004	Granted	SILICON GRAPHICS, INC.
1012.00	US	IMAGE DATA COMPRESSION AND DECOMPRESSION	6683979	1/27/2004	Granted	SILICON GRAPHICS, INC.
1013.00	US	ARCHITECTURE FOR DISTRIBUTING RENDERING	7062527	6/13/2006	Granted	SILICON GRAPHICS, INC.
1014.00	US	METHOD AND SYSTEM FOR SECURE REMOTE DISTRIBUTED RENDERING	7092983	8/15/2006	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

1017.00	US	SYSTEM AND METHOD FOR REORDERING DATA			Pending	SILICON GRAPHICS, INC.	
1019.00	US	TRACE RECORDER ON-CHIP TRACE RECORDING	7007205B1		2/28/2006	Granted	SILICON GRAPHICS, INC.
1020.00	US	DEVICE AND METHOD FOR STORING INFORMATION IN MEMORY	6738885		5/18/2004	Granted	SILICON GRAPHICS, INC.
1026.00	US	METHOD AND SYSTEM FOR MINIMIZING AN AMOUNT OF DATA NEEDED TO TEST DATA AGAINST SUBAREA BOUNDARIES IN SPATIALLY COMPOSITED DIGITAL VIDEO	7358974		4/15/2008	Granted	SILICON GRAPHICS, INC.
1026.51	US	METHOD AND SYSTEM FOR MINIMIZING AN AMOUNT OF DATA NEEDED TO TEST DATA AGAINST SUBAREA BOUNDARIES IN SPATIALLY COMPOSITED DIGITAL VIDEO				Published	
1035.00	US	METHOD AND APPARATUS FOR RECORDING PROGRAM EXECUTION IN A MICROPROCESSOR BASED INTEGRATED CIRCUIT	6634011		10/14/2003	Granted	SILICON GRAPHICS, INC.
1047.00	US	COMMON USER INTERFACE DEVELOPMENT TOOLKIT	6971086		11/29/2005	Granted	SILICON GRAPHICS, INC.
1047.51	US	COMMON USER INTERFACE DEVELOPMENT TOOLKIT FOR A SYSTEM ADMINISTRATION PROGRAM				Published	SILICON GRAPHICS, INC.
1074.00	US	METHOD AND CACHE-COHERENCE SYSTEM ALLOWING PURGING OF MID-LEVEL CACHE ENTRIES WITHOUT PURGING LOWER-LEVEL CACHE ENTRIES	6681293		1/20/2004	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

1075.00	US	GTL+ DRIVER	6864706	3/8/2005	Granted	SILICON GRAPHICS, INC.
1075.01	US	GTL+ DRIVER	6686765	2/3/2004	Granted	SILICON GRAPHICS, INC.
1076.00	US	GTL + ONE-ONE/ZERO-ZERO DETECTOR	6433627	8/13/2002	Granted	SILICON GRAPHICS, INC.
1118.00	JP	COMPACT FLAT PANEL COLOR CALIBRATION SYSTEM			Pending	SILICON GRAPHICS, INC.
1118.00	KR	COMPACT FLAT PANEL COLOR CALIBRATION SYSTEM	10-0849964	7/28/2008	Granted	SILICON GRAPHICS, INC.
1118.00	TW	COMPACT FLAT PANEL COLOR CALIBRATION SYSTEM	188942	2/16/2004	Granted	SILICON GRAPHICS, INC.
1118.00	US	COMPACT FLAT PANEL COLOR CALIBRATION SYSTEM	6853387	2/8/2005	Granted	SILICON GRAPHICS, INC.
1118.00	WO	COMPACT FLAT PANEL COLOR CALIBRATION SYSTEM			Pending	SILICON GRAPHICS, INC.
1118.51	US	COMPACT FLAT PANEL COLOR CALIBRATION SYSTEM	7068263	6/27/2006	Granted	SILICON GRAPHICS, INC.
1118.52	US	COMPACT FLAT PANEL COLOR CALIBRATION SYSTEM	7405742	7/29/2008	Granted	
1118.53	US	COMPACT FLAT PANEL COLOR CALIBRATION SYSTEM			Published	
1130.00	US	METHOD AND SYSTEM FOR MINIMIZING AN AMOUNT OF DATA NEEDED TO COMMUNICATE TILE INFORMATION IN SPATIALLY COMPOSITED DIGITAL VIDEO			Pending	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

1139.00	US	METHOD AND SYSTEM FOR SPATIALLY COMPOSITING DIGITAL VIDEO IMAGES WITH A TILE PATTERN LIBRARY	7027072	4/11/2006	Granted	SILICON GRAPHICS, INC.
1147.00	US	METHOD AND SYSTEM FOR SPATIALLY COMPOSITING DIGITAL VIDEO IMAGES WITH COARSE TILES			Pending	SILICON GRAPHICS, INC.
1152.00	US	METHOD AND SYSTEM FOR PRESENTING THREE-DIMENSIONAL COMPUTER GRAPHICS USING MULTIPLE GRAPHICS PROCESSING UNITS	7405734	7/29/2008	Granted	SILICON GRAPHICS, INC.
1161.00	US	METHOD, SYSTEM AND COMPUTER PROGRAM PRODUCT FOR DETERMINING A STRUCTURE OF A GRAPHICS COMPOSITOR TREE	7034837	4/25/2006	Granted	SILICON GRAPHICS, INC.
1163.00	US	SYSTEM FOR SYNCHRONIZING DISPLAY OF IMAGES IN A MULTI-DISPLAY COMPUTER SYSTEM	7499044	3/3/2009	Granted	SILICON GRAPHICS, INC.
1163.51	US	SYSTEM FOR SYNCHRONIZING DISPLAY OF IMAGES IN A MULTI-DISPLAY COMPUTER SYSTEM			Pending	SILICON GRAPHICS, INC.
1170.00	US	METHOD AND SYSTEM FOR PREFETCHING DATA	6918010B1	7/12/2005	Granted	SILICON GRAPHICS, INC.
1177.00	US	SYNCHRONIZATION OF VERTICAL RETRACE FOR MULTIPLE PARTICIPATING GRAPHICS COMPUTERS	6791551	9/14/2004	Granted	SILICON GRAPHICS, INC.
1181.00	US	SWAP BUFFER SYNCHRONIZATION IN A DISTRIBUTED RENDERING SYSTEM	6809733	10/26/2004	Granted	SILICON GRAPHICS, INC.
1191.00	US	SYNCHRONIZED IMAGE DISPLAY AND BUFFER SWAPPING IN A MULTIPLE DISPLAY ENVIRONMENT	6831648	12/14/2004	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

1193.00	US	SYSTEM AND METHOD FOR DECOUPLING THE USER INTERFACE AND APPLICATION WINDOW IN A GRAPHICS APPLICATION	6985149	1/10/2006	Granted	SILICON GRAPHICS, INC.
1201.00	US	AUTOMATIC FRAME ACCUMULATOR	5977965	11/2/1999	Granted	SILICON GRAPHICS, INC.
1202.00	US	INCREASING COLOR ACCURACY	6650337	11/18/2003	Granted	SILICON GRAPHICS, INC.
1220.00	US	SYSTEM AND METHOD FOR GENERATING SEQUENCES AND GLOBAL INTERRUPTS IN A CLUSTER OF NODES	7016998	3/21/2006	Granted	SILICON GRAPHICS, INC.
1220.51	US	SYSTEM FOR GENERATING SYNCHRONIZED EVENTS AND IMAGES			Pending	SILICON GRAPHICS, INC.
1223.00	US	METHOD OF PROCESSING FOR SYNCHRONIZED STREAMS OF IMAGES, DIGITAL MOTION PICTURES, AUDIO AND OTHER MEDIA FOR ARBITRARY RESOLUTION & DIMENSIONAL REPRESENTATIONS			Published	SILICON GRAPHICS, INC.
1224.00	US	SYSTEM AND METHOD FOR COMMUNICATING IMAGE DATA USING ERROR CORRECTION CODING	7243291	7/10/2007	Granted	SILICON GRAPHICS, INC.
1224.51	US	SYSTEM AND METHOD FOR COMMUNICATING IMAGE DATA USING ERROR CORRECTION CODING			Pending	
1228.00	US	CACHE MEMORY FOR IDENTIFYING LOCKED AND LEAST RECENTLY USED STORAGE LOCATIONS	6904501	6/7/2005	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN



**Schedule 2.2(h)**

1233.00	US	SYSTEM METHOD AND COMPUTER PROGRAM PRODUCT FOR INTUITIVE INTERACTIVE NAVIGATION CONTROL IN VIRTUAL ENVIRONMENTS	7058896	6/6/2006	Granted	SILICON GRAPHICS, INC.
1240.00	US	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR REAL-TIME SHADING OF COMPUTER GENERATED IMAGES	6657624	12/2/2003	Granted	SILICON GRAPHICS, INC.
1245.00	US	SYSTEM AND METHOD FOR PROVIDING INTERACTIVE IMAGES	7401116	7/15/2008	Granted	SILICON GRAPHICS, INC.
1245.51	US	SYSTEM AND METHOD FOR PROVIDING INTERACTIVE IMAGES			Pending	
1246.00	US	SYSTEM AND METHOD FOR PROVIDING COLLABORATION OF A GRAPHICS SESSION			Pending	SILICON GRAPHICS, INC.
1247.00	US	SYSTEM AND METHOD FOR PROVIDING INTERFRAME COMPRESSION IN A GRAPHICS SESSION			Pending	SILICON GRAPHICS, INC.
1248.00	US	SYSTEM AND METHOD FOR DISCARDING FRAMES OF AN IMAGE DURING TRANSPORT ACROSS A NETWORK LINK			Published	SILICON GRAPHICS, INC.
1249.00	US	SYSTEM AND METHOD FOR PROVIDING DYNAMIC CONTROL OF A GRAPHICS SESSION			Pending	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

1250.00	US	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR BLENDING TEXTURES DURING RENDERING OF A COMPUTER GENERATED IMAGE USING A SINGLE TEXTURE AS A MASK	6809739	10/26/2004	Granted	SILICON GRAPHICS, INC.
		*** provisional combined and filed with 1251.00***				
		non provisionals to be filed as two separate cases				
1251.00	US	METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR BLENDING TEXTURES IN A TEXTURE PAGING SCHEME	7295212	11/13/2007	Granted	SILICON GRAPHICS, INC.
1252.00	US	SYSTEM AND METHOD FOR MANAGING GRAPHICS APPLICATIONS	7140024	11/21/2006	Granted	SILICON GRAPHICS, INC.
1254.00	US	SYSTEM AND METHOD FOR IMPLEMENTING SHADOWS USING PRE-COMPUTED TEXTURES	7064755	6/20/2006	Granted	SILICON GRAPHICS, INC.
1255.00	US	SYSTEM AND METHOD FOR MANAGING GRAPHICS APPLICATIONS	6982682	1/3/2006	Granted	SILICON GRAPHICS, INC.
1255.51	US	System and Method for Managing Graphics Applications			Pending	
1256.00	US	SYSTEM AND METHOD FOR MANAGING GRAPHICS APPLICATIONS			Pending	SILICON GRAPHICS, INC.
1283.00	US	SYSTEM AND METHOD FOR ALLOCATING COMPUTING RESOURCES	7168049	1/23/2007	Granted	SILICON GRAPHICS, INC.
1288.00	EP	SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR NEAR-REAL TIME LOAD BALANCING ACROSS MULTIPLE RENDERING PIPELINES			Pending	SILICON GRAPHICS, INC.

779632 v3/HN

**Schedule 2.2(h)**

1288.00	JP	SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR NEAR-REAL TIME LOAD BALANCING ACROSS MULTIPLE RENDERING PIPELINES	6885376	Pending	SILICON GRAPHICS, INC.
1288.00	US	SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR NEAR-REAL TIME LOAD BALANCING ACROSS MULTIPLE RENDERING PIPELINES	6885376	4/26/2005 Granted	SILICON GRAPHICS, INC.
1288.00	WO	SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR NEAR-REAL TIME LOAD BALANCING ACROSS MULTIPLE RENDERING PIPELINES		Published	SILICON GRAPHICS, INC.
1291.00	US	METHOD AND SYSTEM FOR FORMING AN OBJECT PROXY	6831642	12/14/2004 Granted	SILICON GRAPHICS, INC.
1292.00	US	SYSTEM AND METHOD FOR IMAGE-BASED RENDERING WITH PROXY SURFACE ANIMATION	6924805	8/2/2005 Granted	SILICON GRAPHICS, INC.
1299.00	US	SYSTEM AND METHOD FOR IMAGE-BASED RENDERING WITH OBJECT PROXIES	7362335	4/22/2008 Granted	SILICON GRAPHICS, INC.
1299.01	US	SYSTEM AND METHOD FOR IMAGE-BASED RENDERING WITH OBJECT PROXIES		Pending	SILICON GRAPHICS, INC.
1352.00	US	TEXTURE ROAMING VIA DIMENSION ELEVATION	7012614	3/14/2006 Granted	SILICON GRAPHICS, INC.
1367.00	US	SYSTEM AND METHOD FOR GRAPHICS CULLING	7212204	5/11/2007 Granted	SILICON GRAPHICS, INC.
1367.51	US	System and Method for Graphics Culling	7388582	6/17/2008 Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

1379.00	US	SYSTEM AND METHOD FOR PROVIDING DYNAMIC COMPRESSION CONTROL OF A GRAPHICS SESSION		Pending	SILICON GRAPHICS, INC.
1380.00	US	METHOD AND APPARATUS FOR PROCESSING PRIMITIVE DATA FOR POTENTIAL DISPLAY ON A DISPLAY DEVICE	7158132	1/2/2007 Granted	SILICON GRAPHICS, INC.
1381.00	US	PRIMITIVE CULLING APPARATUS AND METHOD	6900818B1	5/31/2005 Granted	SILICON GRAPHICS, INC.
1381.00	WO	PRIMITIVE CULLING APPARATUS AND METHOD		Pending	SILICON GRAPHICS, INC.
1382.00	US	A SYSTEM AND METHOD FOR COLOR COMPUTATION OF A PIXEL USING MULTIPLE SHADER PROGRAMS		Published	SILICON GRAPHICS, INC.
1389.00	US	DISTRIBUTED GRAPHICS PROCESSING APPARATUS AND METHOD		Published	SILICON GRAPHICS, INC.
1389.00	WO	DISTRIBUTED GRAPHICS PROCESSING APPARATUS AND METHOD		Published	SILICON GRAPHICS, INC.
1396.00	US	COMPOSITING IMAGES USING LOGICALLY DIVIDED OBJECT SPACE	7385614	6/10/2008 Granted	SILICON GRAPHICS, INC.
1407.00	US	A MOVABLE AUDIO VIDEO COMMUNICATION INTERFACE SYSTEM		Published	SILICON GRAPHICS, INC.
1410.00	US	FLEXISCAPE - A FLEXIBLE LANDSCAPE BACKGROUND FOR INFORMATION DISPLAY AND CONTROL IN ADVANCED IMMERSIVE VIRTUAL ENVIRONMENTS		Published	SILICON GRAPHICS, INC.

779632 \*3/HN

Schedule 2.2(h)

Patent No.	Country	Title	Pub No.	Date	Status	Applicant
1411.00	US	REMOTE ACCESS VISUALIZATION CONTROL AND INTERACTION OF HETEROGENEOUS DEVICES AND STREAMS IN AN EXTENDABLE MEDIA FUSION ENVIRONMENT			Published	SILICON GRAPHICS, INC.
1415.00	US	METHOD AND SYSTEM FOR USING SCALABLE GRAPHICS TO STREAM HIGH-RESOLUTION IMAGERY	7460126	12/2/2008	Granted	SILICON GRAPHICS, INC.
1415.51	US	METHOD AND SYSTEM FOR STREAMING HIGH-RESOLUTION MEDIA			Pending	SILICON GRAPHICS, INC.
1421.00	US	APPLICATION-FUSION: INTEGRATION OF GRAPHICAL APPLICATION CONTEXT INTO THE GRAPHICAL SCENE OF ANOTHER APPLICATION			Published	SILICON GRAPHICS, INC.
1424.00	US	TRANSLATION LOOKASIDE BUFFER CHECKPOINT SYSTEM			Published	SILICON GRAPHICS, INC.
1427.00	US	SELF-TUNING TIME INTERPOLATOR			Pending	SILICON GRAPHICS, INC.
1428.00	US	PREDICTIVE PREFALUTING IN THE PAGE FAULT HANDLER			Pending	SILICON GRAPHICS, INC.
1439.00	US	USE OF RAY-TRACING FOR GENERATING IMAGES FOR AUTO-STEREO DISPLAYS			Pending	SILICON GRAPHICS, INC.
1453.00	US	Universal User Input/Output Application Layer			Published	SILICON GRAPHICS, INC.
C357.00	DE	APPARATUS AND METHOD FOR TESTING OF NEW OPERATING SYSTEMS THROUGH PRIVILEGED INSTRUCTION TRAPPING	69110908.7	6/28/1995	Granted	SILICON GRAPHICS, INC.

779632 v3/HN

**Schedule 2.2(h)**

C:357.00	EP	APPARATUS AND METHOD FOR TESTING OF NEW OPERATING SYSTEMS THROUGH PRIVILEGED INSTRUCTION TRAPPING	0578646	6/28/1995	Granted	SILICON GRAPHICS, INC.
C:357.00	FR	APPARATUS AND METHOD FOR TESTING OF NEW OPERATING SYSTEMS THROUGH PRIVILEGED INSTRUCTION TRAPPING	0578646	6/28/1995	Granted	SILICON GRAPHICS, INC.
C:357.00	GB	APPARATUS AND METHOD FOR TESTING OF NEW OPERATING SYSTEMS THROUGH PRIVILEGED INSTRUCTION TRAPPING	0578646	6/28/1995	Granted	SILICON GRAPHICS, INC.
C:357.00	US	APPARATUS AND METHOD FOR TESTING OF NEW OPERATING SYSTEMS THROUGH PRIVILEGED INSTRUCTION TRAPPING	5371879	12/6/1994	Granted	SILICON GRAPHICS, INC.
C:510.00	US	SCALAR/VECTOR PROCESSOR	5430884	7/4/1995	Granted	SILICON GRAPHICS, INC.
C:510.01	US	METHOD AND APPARATUS FOR CHAINING VECTOR INSTRUCTIONS	5640524	6/17/1997	Granted	SILICON GRAPHICS, INC.
C:510.51	US	PARTITIONED ADDRESSING APPARATUS FOR VECTOR/SCALAR REGISTERS	5745721	4/28/1998	Granted	SILICON GRAPHICS, INC.
C:510.52	US	VECTOR PROCESSOR HAVING REGISTERS FOR CONTROL BY VECTOR REGISTERS	5544337		Granted	SILICON GRAPHICS, INC.
C:510.53	US	VECTOR PROCESSOR HAVING FUNCTIONAL UNIT PATHS OF DIFFERING PIPELINE LENGTHS	5598547	8/6/1996	Granted	SILICON GRAPHICS, INC.
C:510.54	US	METHOD OF PROCESSING A SEQUENCE OF CONDITIONAL VECTOR IF STATEMENTS	5623650	1/28/1997	Granted	SILICON GRAPHICS, INC.
				4/22/1997	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

C:510.55	US	METHOD OF PROCESSING CONDITIONAL BRANCH INSTRUCTIONS IN SCALAR/VECTOR PROCESSOR	5706490	1/6/1998	Granted	SILICON GRAPHICS, INC.
C:510.56	US	DATA PROCESSING SYSTEM FOR PROCESSING ONE AND TWO PARCEL INSTRUCTIONS	5717881	2/10/1998	Granted	SILICON GRAPHICS, INC.
C:510.57	US	VECTOR/SCALAR PROCESSOR WITH SIMULTANEOUS PROCESSING AND INSTRUCTION CACHE FILLING	5659706	8/19/1997	Granted	SILICON GRAPHICS, INC.
C:511.00	US	METHOD AND APPARATUS FOR A MULTIPROCESSOR RESOURCE LOCKOUT INSTRUCTION	5499356	3/12/1996	Granted	SILICON GRAPHICS, INC.
C:520.00	FR	METHOD FOR OPTIMIZING INSTRUCTION SCHEDULING FOR A PROCESSOR HAVING MULTIPLE FUNCTIONAL RESOURCES	0535107	12/8/1999	Granted	SILICON GRAPHICS, INC.
C:520.00	GB	METHOD FOR OPTIMIZING INSTRUCTION SCHEDULING FOR A PROCESSOR HAVING MULTIPLE FUNCTIONAL RESOURCES	0535107	12/8/1999	Granted	SILICON GRAPHICS, INC.
C:520.00	JP	METHOD FOR OPTIMIZING INSTRUCTION SCHEDULING FOR A PROCESSOR HAVING MULTIPLE FUNCTIONAL RESOURCES	3288372	6/4/2002	Granted	SILICON GRAPHICS, INC.
C:520.00	US	METHOD FOR OPTIMIZING INSTRUCTION SCHEDULING FOR A PROCESSOR HAVING MULTIPLE FUNCTIONAL RESOURCES	5202975	4/13/1993	Granted	SILICON GRAPHICS, INC.
C:520.00	DE	METHOD FOR OPTIMIZING INSTRUCTION SCHEDULING FOR A PROCESSOR HAVING MULTIPLE FUNCTIONAL RESOURCES	69131830.1	6/21/2000	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN

**Schedule 2.2(h)**

C520.01	US	METHOD FOR INSERTING A PATH INSTRUCTION DURING COMPILATION OF COMPUTER PROGRAMS FOR PROCESSORS HAVING MULTIPLE FUNCTIONAL UNITS	5307478	4/26/1994	Granted	SILICON GRAPHICS, INC.
C521.00	US	METHOD FOR REPRESENTING SCALAR DATA DEPENDENCIES FOR AN OPTIMIZING COMPILER	5107418	4/21/1992	Granted	SILICON GRAPHICS, INC.
C525.00	US	METHOD & APPARATUS FOR SEPARATE MARK AND WAIT INSTRUCTIONS FOR PROCESSORS HAVING MULTIPLE PORTS	5381536	1/10/1995	Granted	SILICON GRAPHICS, INC.
C530.00	US	SYSTEM HAVING INTEGRATED DISPATCHER FOR SELF-SCHEDULING PROCESSORS TO EXECUTE MULTIPLE TYPES OF PROCESSORS	5452452	9/19/1995	Granted	SILICON GRAPHICS, INC.
C531.00	US	VECTORIZED LR PARSING OF COMPUTER PROGRAMS	5193192	3/9/1993	Granted	SILICON GRAPHICS, INC.
C532.00	US	RESPONDING TO SERVICE REQUESTS USING MINIMAL SYSTEM-SIDE CONTEXT IN A MULTIPROCESSOR ENVIRONMENT	5390329	2/14/1995	Granted	SILICON GRAPHICS, INC.
C534.00	US	GRAPHICAL DISPLAY OF COMPILER-GENERATED INTERMEDIATE DATABASE REPRESENTATION	5187789	2/16/1993	Granted	SILICON GRAPHICS, INC.
C554.00	US	SYNCHRONIZED STEREOSCOPIC DISPLAY SYSTEM	5245319	9/14/1993	Granted	SILICON GRAPHICS, INC.
C556.00	US	METHOD FOR COMPILING LOOPS HAVING RECURSIVE EQUATIONS BY DETECTING AND CORRECTING RECURRING DATA POINTS BEFORE STORING THE RESULT TO MEMORY	5247696	9/21/1993	Granted	SILICON GRAPHICS, INC.

779632 \*3/HN



**Schedule 2.2(h)**

C557.00	EP	METHODS FOR EFFICIENT DISTRIBUTION OF PARALLEL TASKS TO SLAVE PROCESSES IN A MULTIPROCESSING SYSTEM	0491342	6/3/1998	Granted	SILICON GRAPHICS, INC.
C557.00	US	METHODS FOR EFFICIENT DISTRIBUTION OF PARALLEL TASKS TO SLAVE PROCESSES IN A MULTIPROCESSING SYSTEM	5257372	10/26/1993	Granted	SILICON GRAPHICS, INC.
C559.00	US	OPTIMIZATION OF ALTERNATE LOOP EXITS	5361354	11/1/1994	Granted	SILICON GRAPHICS, INC.
C563.00	US	METHOD FOR USE IN DESIGNING AN ARBITRARILY SHAPED OBJECT	5453934	9/26/1995	Granted	SILICON GRAPHICS, INC.
C571.00	US	OUTER LOOP VECTORIZATION	5802375	9/1/1998	Granted	SILICON GRAPHICS, INC.
C637.00	US	EMBEDDING A DIGITAL SIGNATURE IN A VIDEO SEQUENCE	5960081	9/28/1999	Granted	SILICON GRAPHICS, INC.
C656.00	US	FLOATING-POINT ADDER PERFORMING FLOATING-POINT AND INTEGER OPERATIONS	6529928	3/4/2003	Granted	SILICON GRAPHICS, INC.
C676.00	US	METHOD AND APPARATUS FOR PARTIAL-SCAN BUILT-IN SELF-TEST LOGIC	6349398	2/19/2002	Granted	SILICON GRAPHICS, INC.
C688.00	US	METHOD FOR DETERMINING THE OPTIMUM LOCATIONS FOR SCAN LATCHES IN A PARTIAL-SCAN IC BUILT IN SELF TEST SYSTEM	6651197	11/18/2003	Granted	SILICON GRAPHICS, INC.

779632 v3/HN

## EXHIBIT 4

### SCHEDULE 4.2(j)

#### EXCEPTIONS TO DELIVERY OF STOCK CERTIFICATES

1. Silicon Graphics Sdn. Bhd. [Malaysia].
  - Beneficial interest in the shares can be transferred at Closing, but the formal share transfer (and registration thereof) requires the 2008 audited accounts of the company, which will not be available at Closing.
  - A Charge on the company's shares remains registered in the public records, although the company believes that this Charge has been released
2. Cray Research (Israel) Ltd.
  - One share of the company remains issued to a certain John Carlson, who the company and external counsel have not been able to locate to date. The company has not been able to contact this shareholder for a number of years. This share of the company will not be transferable at Closing.
3. Silicon Graphics Biomedical (1995) Ltd.
  - A lien on one share of the company exists in favor of a Bank Hapoalim. To date, neither the company nor external counsel has been able to obtain any further information on this matter.

**EXHIBIT 5**

**AMENDED AND RESTATED SCHEDULE 5.3(a)**

5.3(a)

1. Item 1. of Section (d) of this Schedule 5.3 and Section (e) of this of this Schedule 5.3 are incorporated herein by reference.
2. Pursuant to the SGI Japan Ltd. Stockholders' Agreement, SGI Japan Ltd. and NEC have a right to purchase 100% of the shares of SGI Japan Ltd. then currently held by Seller, Silicon Graphics World Trade B.V., Silicon Graphics World Trade Corporation (together, the "SGI Entities") or any other person that, directly or indirectly through one or more intermediaries controls, or is controlled by or is under common control with any such SGI Entity in the event of a direct or indirect sale of all or substantially all of the assets of Seller, upon notification to SGI Japan Ltd. and NEC. SGI Japan Ltd. also has a right of first offer to purchase such shares in the event any stockholder wishes to transfer its shares to anyone other than a permitted transferee.
3. Ordinary course encumbrances held by leasing partners of one or more Selling Entities on equipment leased to customers of such Selling Entity(ies) through such leasing partners.
4. The items set forth in the table below are in possession of a third person or entity:

<b>Type of Property</b>	<b>Person or Entity in Possession of the Property</b>
Cash Security Deposit - \$920,400	Christensen Holdings, L.P.
Cash Security Deposit - \$1,238	Plaza Partners, L.L.C.
Cash Security Deposit - \$15,390	Ibahn General Holdings Corporation
Utility Deposit - \$139,000 (Mountain View)	PG&E
Utility Deposit - \$77,000 (Sunnyvale)	PG&E
Escrow Account - \$750,000	Wells Fargo Bank
Spare Parts Inventory - Estimated value as of February 20, 2009 is \$17,000,000	UPS Freight Services, Inc.
Spare Parts Inventory - Estimated value as of February 20 2009 is \$8,000,000	DHL Worldwide Express Logistics
Spare Parts Inventory - Estimated value as of February 20, 2009 is \$6,000,000	Pinnacle Data Systems, Inc.
Spare Parts Inventory - Estimated value as of February 20, 2009 is \$200,000	Barco Visual Solutions, LLC
Spare Parts Inventory - Estimated value as of February 20, 2009 is \$100,000	Data Direct networks, Inc..
Spare Parts Inventory - Estimated value as of February 20, 2009 is \$200,000	S & C - Mexico City
Spare Parts Inventory - Estimated value as of February 20, 2009 is \$1,200,000	Instrimpex

Type of Property	Person or Entity in Possession of the Property
Manufacturing Inventory - Estimated value as of February 20, 2009 is \$2,000,000	Benchmark Electronics, Inc.

5. The intellectual property set forth below is jointly owned by a third person or entity:

Intellectual Property	Person(s) or Entity(ies) Sharing Joint Ownership
U.S. Copyright – “Advanced RISC computing standards specification”	Jointly owned by Compaq Computer Corporation, Microsoft Corporation, Digital Equipment Corporation, MIPS Computer Systems, Inc., Santa Cruz Operation and Silicon Graphics, Inc.
U.S. Patent – SYSTEM AND METHOD FOR COORDINATING COMMUNICATION BETWEEN LAYERS OF A LAYERED COMMUNICATION SYSTEM, application number 8571366, patent number 5784567, issued 7/21/98	Jointly owned by Silicon Graphics, Inc. and Time-Warner Entertainment Co., L.P.
U.S. Patent – SYSTEM AND METHOD FOR RESOURCE RECOVERY IN A DISTRIBUTED SYSTEM, application number 9547702, patent number 6728895, issued 4/27/04	Jointly owned by Silicon Graphics, Inc. and Time-Warner Entertainment Co., L.P.
U.S. Patent – HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING, application number 08561718, patent number 6331856, issued 12/18/01	Jointly owned by Silicon Graphics, Inc. and Nintendo Co.
U.S. Patent – HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING, application number 9186137, patent number 6342892, issued 1/29/02	Jointly owned by Silicon Graphics, Inc. and Nintendo Co.
U.S. Patent – HIGH PERFORMANCE LOW COST VIDEO GAME SYSTEM WITH COPROCESSOR PROVIDING HIGH SPEED EFFICIENT 3D GRAPHICS AND DIGITAL AUDIO SIGNAL PROCESSING, application number 10106445, patent number 6593929, issued 7/15/03	Jointly owned by Silicon Graphics, Inc. and Nintendo Co.
AN IMPROVED METHOD AND APPARATUS FOR CLOCKING IN A COMPUTER SYSTEM,	Jointly owned by Silicon Graphics, Inc. and Protocol Engines, Inc.

application number 923086, patent number 5495596, issued 2/27/1996	
U.S. Patent – RESTORATION FILTER FOR TRUNCATED PIXELS, application number 8561584, patent number 5699079, issued 12/16/1997	Jointly owned by Silicon Graphics, Inc. and Nintendo Co.
ANTIALIASING OF SILHOUETTE EDGES, application number 8539956, patent number 5742277, issued 4/21/1998	Jointly owned by Silicon Graphics, Inc. and Nintendo Co.
SYSTEM AND METHOD FOR MERGING PIXEL FRAGMENTS BASED ON DEPTH RANGE VALUES, application number 8562283, patent number 5854631, issued 12/29/1998	Jointly owned by Silicon Graphics, Inc. and Nintendo Co.
SYSTEM AND METHOD FOR PERFORMING ADDRESS TRANSLATION IN A COMPUTER SYSTEM, application number 10835855, patent number 7181589, issued 2/20/2007	Jointly owned by Silicon Graphics, Inc. and University of Utah.
REMOTE CONTROL DESIGN, application number 36927, patent number D398009, issued 9/8/1998	Jointly owned with Scientific-Atlanta, Inc. and MIPs Technologies, Inc.
U.S. Patent – SYSTEM AND METHOD FOR PERFORMING MEMORY OPERATIONS IN A COMPUTING SYSTEM, application number 10836932, patent number 7398359, issued 7/8/08	Jointly owned by Silicon Graphics, Inc. and certain inventors: Miller, Rudolph, Leiserson, Kuszmaul, Asanovic. Massachusetts Institute of Technology may have an interest in this patent.
U.S. Patent – REMOTE CONTROL DESIGN, application number 36927, patent number D398009, issued 9/8/98	Jointly owned with Scientific-Atlanta, Inc. and MIPs Technologies, Inc. No record of assignment from certain inventors on file with U.S. PTO.

6. Silicon Graphics Sdn. Bhd. [Malaysia].

- Beneficial interest in the shares can be transferred at Closing, but the formal share transfer (and registration thereof) requires the 2008 audited accounts of the company, which will not be available at Closing.
- A Charge on the company's shares remains registered in the public record, although the company believes this Charge has been released.

7. Cray Research (Israel) Ltd.

- One share of the company remains issued to a certain John Carlson, who the company and external counsel have not been able to locate for date. The company has not been able to contact this shareholder for a number of years. This share of the company will not be transferable at Closing.

8. Silicon Graphics Biomedical (1995) Ltd.

- A lien on one share of the company exists in favor of a Bank Hapoalim. To date, neither the company nor external counsel has been able to obtain any further information on this matter.
- A filing is being made with the Companies Registrar to correct an error whereby the Companies Registrar previously recorded the transfer of 1,112 to Silicon Graphics S.A. [Switzerland] instead of to Silicon Graphics World Trade Corporation.

**EXHIBIT 6**

**AMENDED AND RESTATED SCHEDULE 5.8(d)**

**SUBSIDIARIES**

<b>Entity Name</b>	<b>Stockholders</b>
Silicon Graphics Real Estate, Inc.	100% of shares held by Silicon Graphics, Inc.
Silicon Graphics World Trade Corporation	100% of shares held by Silicon Graphics, Inc.
Silicon Graphics Federal, Inc.	100% of Common Stock held by Silicon Graphics, Inc.
ParaGraph International, Inc.	100% of Common Stock and Series A Preferred Stock held by Silicon Graphics, Inc.
WTI Development, Inc.	100% of Common Stock held by Silicon Graphics, Inc.
Silicon Studio, Inc.	100% of shares held by Silicon Graphics, Inc.
Cray Research, L.L.C.	Sole member is Silicon Graphics, Inc.
Silicon Graphics of Manhattan, Inc.	100% of shares held by Silicon Graphics, Inc.
Silicon Graphics Biomedical (1995) Ltd.	99% of shares held by Silicon Graphics World Trade Corporation;  1 share held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics Computer Systems Limited	99.99% held by Silicon Graphics World Trade Corporation;  1 share held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics World Trade B.V.	100% of shares held by Silicon Graphics World Trade Corporation
Alias/Wavefront NV	99.9% of Common Shares held by Silicon Graphics World Trade B.V.  1 share held by Silicon Graphics, S.A. [Switzerland]

Silicon Graphics S.A./N.V. [Belgium]	99.99% of shares held by Silicon Graphics World Trade B.V.;  1 share held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics s.r.o. [Czech Republic]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics A/S [Denmark]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics GmbH [Germany]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics S.P.A. [Italy]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics S.A. [France]	99.99% of shares held by Silicon Graphics World Trade B.V.;  1 share held by Silicon Graphics World Trade Corporation;  1 share held by Silicon Graphics, S.A. [Switzerland];  1 share held by Silicon Graphics Finance S.A.;  1 share held by Pascal Barbolosi;  2 shares held by Sabine Legeay
Silicon Graphics B.V. [Netherlands]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics AS [Norway]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics Aktiebolag [Sweden]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics, S.A. [Spain]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics Limited [United Kingdom]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics (Pty.) Ltd. [South Africa]	100% of shares held by Silicon Graphics World

	Trade B.V.
Silicon Graphics, S.A. [Switzerland]	99.99% of shares held by Silicon Graphics World Trade B.V.  1 share held by Andre Kaplun;  1 share held by Patrick Bittel;  1 share held by Thomas Oswold
Silicon Graphics SA [Argentina]	99% of shares held by Silicon Graphics World Trade B.V.  1 share held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics Comércio e Serviços Ltda. [Brazil]	99.99% of shares held by Silicon Graphics World Trade B.V.  1 share held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics SA [Chile]	99.99% of shares held by Silicon Graphics World Trade B.V.  1 share held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics S.A. de C.V. [Mexico]	99.99% of shares held by Silicon Graphics World Trade B.V.  1 share held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics SA [Venezuela]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics Pty Ltd. [Australia]	79.2% of shares held by Silicon Graphics World Trade B.V.  20.8% of shares held by Silicon Graphics World Trade Corporation;  1 share held by Silicon Graphics, S.A [Switzerland].
Silicon Graphics Limited [Taiwan]	100% of shares held by Silicon Graphics World Trade B.V.



	1 share held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics Computer Engineering & Technology (China) Co., Ltd. [China]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics Limited [Hong Kong]	99.99% of shares held by Silicon Graphics World Trade B.V.  1 share held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics Systems (India) Limited [India]	99.99% of shares held by Silicon Graphics World Trade B.V.  20 shares held by Silicon Graphics, S.A. [Switzerland]
Silicon Graphics Limited [New Zealand]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics Pte Ltd [Singapore]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics Europe Trade BV [Netherlands]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics Finance SA [Switzerland]	99.57% of shares held by Silicon Graphics World Trade B.V.;  1 share held by Andre Kaplun;  1 share held by Patrick Bittel;  1 share held by Thomas Oswald
Korea Silicon Graphics, Ltd. [South Korea]	100% of shares held by Silicon Graphics World Trade B.V.
Silicon Graphics Trading Sarl [Switzerland]	95% of shares held by Silicon Graphics Europe Trade BV;
Silicon Graphics AE [Greece]	99% of shares held by Silicon Graphics World Trade B.V.;  1% of shares held by Silicon Graphics Europe Trade BV [Netherlands]
Silicon Graphics Limited [Canada]	100% of Class A, Class E and Class F shares held by Silicon Graphics Finance SA;

	<p>100% of Class C shares held by Silicon Graphics, Inc.;</p> <p>100% of Class D shares held by Morgan Stanley</p>
Cray Financial Corp.	100% of shares held by Cray Research, L.L.C.
Cray Research America Latina Ltd.	100% of shares held by Cray Research, L.L.C.
Cray Research (Eastern Europe) Ltd.	100% of shares held by Cray Research, L.L.C.
Cray Research (India) Ltd.	100% of shares held by Cray Research, L.L.C.
Cray Asia/Pacific, Inc.	100% of shares held by Cray Research, L.L.C.
Cray Research International Inc.	100% of shares held by Cray Research, L.L.C.
Cray Research (Canada) Inc.	100% of shares held by Cray Research, L.L.C.
Silicon Graphics Sdn. Bhd. [Malaysia]	100% of shares held by Cray Research, L.L.C.
Cray Research (Israel), Ltd.	<p>99.9% of shares held by Cray Research, L.L.C.;</p> <p>1 share held by John Carlson</p>
SGI Japan Ltd.	<p>10.48% of shares held by Silicon Graphics World Trade B.V.</p> <p>50% of shares held by NEC and NEC Soft</p> <p>9% of shares held by Morgan Stanley Japan Securities Co., Ltd.</p> <p>1% of shares held by JAFCO Co., Ltd.</p> <p>4% of shares held by Nomura Securities Co., Ltd.</p> <p>10% of shares held by Sony</p> <p>1% of shares held by SOFTBANK Media &amp; Marketing Group</p> <p>4% of shares held by NIWS Co., Ltd.</p> <p>10% of shares held by Canon Sales Co. Inc.</p>

**EXHIBIT 7**  
**LICENSE AGREEMENT**

## LICENSE AGREEMENT

This **LICENSE AGREEMENT** (this "Agreement") is entered into effective as of April 30, 2009 (the "Effective Date") between Rackable Systems, Inc, a Delaware corporation ("Rackable") and Silicon Graphics, Inc., a Delaware corporation ("SGI").

### BACKGROUND

**A.** Rackable and SGI (and certain of its subsidiaries) entered into an Asset Purchase Agreement dated March 31, 2009 (the "Asset Purchase Agreement").

**B.** The Asset Purchase Agreement contemplated a license to Rackable of certain patents. Now the parties desire to supersede the language in Section 4.4 of the Asset Purchase Agreement with the license grant and other terms of this Agreement relating to certain of SGI's patents.

In consideration of the mutual promises and consideration set forth in this Agreement, the parties agree as follows:

### AGREEMENT

**1. DEFINITIONS.** As used in this Agreement:

**1.1 "Affiliate"** means, with respect to Rackable, any other Person that directly or indirectly, through one or more intermediaries, controls, is controlled by or is under common control with, Rackable. For purposes of this definition, "control" (and any similar term) means the power of one or more Persons to direct the affairs of another Person by reason of ownership of voting stock, contract or otherwise.

**1.2 "Affiliate"** means, with respect to SGI, any other Person that directly or indirectly, through one or more intermediaries, controls, is controlled by or is under common control with, SGI. For purposes of this definition, "control" (and any similar term) means the power of one or more Persons to direct the affairs of another Person by reason of ownership of voting stock, contract or otherwise.

**1.3 "Covenant"** means the covenant granted to Rackable in Section 2.3 (Covenant Not to Sue).

**1.4 "License"** means the license granted to Rackable in Section 2.1 (License Grant).

**1.5 "Licensed Patent(s)"** means (a) all patents and patent applications listed in Exhibit A, and (b) all patents issuing or claiming priority from any of the patents and patent applications described in Exhibit A including continuations, continuations-in-part, divisional, reexaminations, reissues, and foreign counterparts.

**1.6 "Person"** means any individual, corporation, partnership, limited partnership, limited liability company, syndicate, group, trust, association or other organization or entity or government, political subdivision, agency or instrumentality of a government.

**1.7** “**Product(s)**” means any product that is made by or on behalf of Rackable and is sold by Rackable, directly or through a distributor.

**1.8** “**Rackable**” means Rackable or any wholly owned subsidiary of Rackable.

**1.9** “**Service(s)**” means any service provided, sold, or otherwise offered by Rackable.

**1.10** “**SGI**” means SGI, or any assignee or successor in interest of SGI.

**1.11** “**Subsidiary**” means any subsidiary of Rackable where all of the capital stock or other equity interests in such subsidiary are, directly or indirectly, owned by Rackable (other than any capital stock or other equity interest that are director qualifying shares or similar shares not held by Rackable in order to facilitate compliance with applicable Legal Requirements (as defined in the Asset Purchase Agreement)).

## **2. LICENSES**

**2.1 License Grant.** SGI hereby grants to Rackable a non-exclusive, irrevocable, perpetual, transferable (but only as described in Section 3.1), fully paid, royalty-free, worldwide, sublicensable (but only as described in Section 2.2) license under the Licensed Patents to (a) use, develop, make, have made, sell, offer to sell, import, lease, and otherwise transfer or dispose of any Product or perform any Service (whether currently existing or developed in the future); (b) use any method or process in manufacturing any Product (whether currently existing or developed in the future); (c) use and perform any method or process in connection with providing a Service.

**2.2 Sublicensing.** Under the License, Rackable will have the right to sublicense any or all of the license rights granted under Section 2.1 (a) to one or more tiers of Rackable’s sublicensees solely in connection with any Product or Service or (b) in connection with the sale of all or a material portion of any Subsidiary, division, or business unit of Rackable, whether by merger, sale of assets, sale of stock, or otherwise; provided that in the event of any sublicense permitted under 2.2(b), the definitions of Products and Services will include only (i) the Products and Services attributable to such division or business unit or the material portion thereof that is being sold existing on the date of said sale, and (ii) any future products or services of said sublicensee that are derived primarily therefrom; but will exclude (without limitation) existing products and services of the sublicensee on the date of said sublicense and any future products or services of said assignee that are derived therefrom. For the avoidance of doubt, the sublicense permitted pursuant to 2.2(b) shall relate only to the Licensed Patents reasonably necessary for the manufacture, sale, provision or offering of the relevant Products or Services as of the date of sale.

**2.3 Covenant Not to Sue.** SGI, its Affiliates, successors, and assigns agrees and covenants not to assert any claim or commence any suit against Rackable or any of its Affiliates, licensees, distributors, or customers for infringement of any Licensed Patent solely with respect to any Product or Service (whether currently existing or developed in the future).

**2.4 Duration of License.** This Agreement and the License will take effect on the Effective Date and will remain in effect until each Licensed Patent is no longer in effect.

**2.5 Representations and Warranties.** SGI represents and warrants that it has full right, power, and authority to grant the License and the Covenant and has all of the intellectual property rights necessary to grant the License and the Covenant, and that the grant of the License and Covenant does not conflict with or result in a breach of any other agreement to which SGI is a party or any judgment, order, or decree by which SGI is bound.

### **3. GENERAL**

**3.1 Assignment.** This Agreement and all of the provisions hereof shall be binding upon and inure to the benefit of the parties hereto and their respective permitted successors and assigns, as set forth below in this Section 3.1. Rackable will have the right to assign this Agreement, in whole or in relevant part, with advance written notice to SGI and without SGI's consent or the consent of any other person: (a) in connection with the sale of all or a material portion of any Subsidiary, division or business unit of Rackable, whether by merger, sale of assets, sale of stock, or otherwise; (b) in connection with the sale of all or substantially all of the assets of Rackable, whether by merger, sale of assets, sale of stock, or otherwise; or (c) to a trustee in connection with any type of reorganization or other proceeding for Rackable under the U.S. Bankruptcy Code. In the event of any such assignment, the definitions of Products and Services will include only (i) the Relevant Products and Services of Rackable existing on the date of said sale, reorganization or proceeding and (ii) any future products or services of said assignee that are derived primarily therefrom; but will exclude (without limitation) existing products and services of the assignee on the date of said assignment and any future products or services of said assignee that are derived therefrom. **"Relevant Products and Services"** for these purposes means (i) in the case of with the sale of all or a material portion of any division or business unit of Rackable, the Products and Services attributable to such division or business unit or the material portion thereof that is being sold; (ii) in the case of the sale of all or substantially all of the assets of Rackable, the Products and Services attributable to such assets and (iii) and in the case of any type of reorganization or other proceeding for Rackable under the U.S. Bankruptcy Code, all Products and Services of Rackable attributable to Rackable Systems, Inc. and/or any of its subsidiaries subject to such reorganization or proceeding, as the case may be. For the avoidance of doubt, the assignment permitted pursuant to this Section shall relate only to the Licensed Patents reasonably necessary for the manufacture, sale, provision or offering of the Relevant Products or Services as of the date of sale, reorganization or other proceeding.

Rackable may otherwise assign rights under this Agreement only with SGI's prior written consent, to be granted in SGI's sole discretion. SGI shall have the right to freely assign this Agreement without Rackable's consent.

**3.2 Governing Law.** This Agreement shall be governed by the laws of the State of New York, without giving effect to the principles of conflicts of laws thereof.

**3.3 Waiver.** All waivers must be in writing. Any waiver or failure to enforce any provision of this Agreement on one occasion will not be deemed a waiver of any other provision or of such provision on any other occasion.

**3.4 Severability.** If any term or other provision of this Agreement is invalid, illegal or incapable of being enforced by any rule of law or public policy, all other terms, conditions and provisions of this Agreement shall nevertheless remain in full force and effect so long as the economic or legal substance of the transactions contemplated hereby is not affected in any

manner materially adverse to any party. Upon such determination that any term or other provision is invalid, illegal or incapable of being enforced, the parties hereto shall negotiate in good faith to modify this Agreement so as to effect the original intent of the parties as closely as possible in a mutually acceptable manner in order that the transactions contemplated hereby be consummated as originally contemplated to the fullest extent possible.

**3.5 Bankruptcy Code.** The parties acknowledge and agree that this Agreement is a contract under which SGI is a licensor of intellectual property as provided in section 365(n) of Title 11, United States Code (the "Bankruptcy Code"). SGI acknowledges that if SGI, as a debtor in possession or a trustee in bankruptcy in a case under the Bankruptcy Code (the "Bankruptcy Trustee"), rejects this Agreement, Rackable may elect to retain its rights under this Agreement as provided in section 365(n) of the Bankruptcy Code. Upon written request of Rackable to SGI or the Bankruptcy Trustee, SGI or such Bankruptcy Trustee will not interfere with the rights of Rackable as provided in this Agreement.

**3.6 Construction.** The descriptive headings contained in this Agreement are included for convenience of reference only and shall not affect in any way the meaning or interpretation of this Agreement. As used in this Agreement, the words "include" and "including," and variations thereof, will not be deemed to be terms of limitation, but rather will be deemed to be followed by the words "without limitation." Except as otherwise expressly indicated, all references in this Agreement to "Sections" are intended to refer to Sections of this Agreement.

**3.7 Counterparts.** This Agreement may be executed and delivered (including by facsimile transmission) in one or more counterparts, and by the different parties hereto in separate counterparts, each of which, when executed and delivered, shall be deemed to be an original but all of which taken together shall constitute one and the same agreement.

**3.8 Provision of Reasonable Assistance.** Upon request by SGI or an Affiliate of SGI, Rackable will provide access to Rackable employees with appropriate knowledge to assist SGI in prosecution of any of the Licensed Patents, and/or to assist SGI in any legal action regarding or relating to the Licensed Patents. SGI shall provide reimbursement for any of Rackable's reasonable out-of-pocket expenses incurred in connection with assistance requested by SGI under this Section 3.8.

**3.9 Entire Agreement.** This Agreement may not be amended, modified, altered, or supplemented other than by means of a written instrument duly executed and delivered on behalf of all parties. This Agreement (including all Exhibits) and the Asset Purchase Agreement constitute the entire agreement among the parties with respect to the subject matter hereof and supersede all prior agreements and understandings among the parties with respect thereto. In the event of any conflict between the Asset Purchase Agreement and this Agreement, this Agreement will control. For avoidance of doubt, this Agreement supersedes and replaces Section 4.4 of the Asset Purchase Agreement.

**IN WITNESS WHEREOF**, the parties have caused this Agreement to be executed and delivered as of the Effective Date.

**RACKABLE SYSTEMS, INC.**

**SILICON GRAPHICS, INC.**

By: \_\_\_\_\_

By: \_\_\_\_\_

Name: \_\_\_\_\_

Name: \_\_\_\_\_

Title: \_\_\_\_\_

Title: \_\_\_\_\_



**Exhibit A**  
**Licensed Patents**