

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT2898696

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
TARGET COMPILER TECHNOLOGIES NV	04/24/2014
RECEIVING PARTY DATA	
Name:	SYNOPSYS, INC.
Street Address:	700 EAST MIDDLEFIELD ROAD
City:	MOUNTAIN VIEW
State/Country:	CALIFORNIA
Postal Code:	94043
PROPERTY NUMBERS Total: 3	
Property Type	Number
Patent Number:	5854929
Patent Number:	5918035
Application Number:	09019076
CORRESPONDENCE DATA	
Fax Number:	(650)938-5200
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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Correspondent Name:	RAJIV P. PATEL
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Address Line 2:	801 CALIFORNIA STREET
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ATTORNEY DOCKET NUMBER:	22524-01000
NAME OF SUBMITTER:	RAJIV P. PATEL, REG. NO. 39,327
SIGNATURE:	/Rajiv P. Patel/
DATE SIGNED:	06/15/2014
Total Attachments: 2	
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source=Synopsys-Compiler_Assignment#page2.tif	

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, **TARGET COMPILER TECHNOLOGIES NV**, a company organized under the laws of Belgium, having a place of business at Technologielaan 11-0002, Leuven, Belgium 3001 (“**ASSIGNOR**”), has sold, assigned, and transferred and does hereby sell, assign, and transfer to **SYNOPSYS, INC.**, a Delaware corporation, having a place of business at 700 East Middlefield Road, Mountain View, California, 94043 (“**ASSIGNEE**”), for itself and its successors, transferees, and assignees, the following:

1 The entire worldwide right, title, and interest in all inventions and improvements (“**SUBJECT MATTER**”) that are disclosed in the following applications and Letters Patents (“**PATENT RIGHTS**”) that are listed on **Schedule A** and;

2. The entire worldwide right, title, and interest in and to:
(a) the **PATENT RIGHTS**, including any right of priority; (b) any provisional, divisional, continuation, substitute, renewal, reissue, and other related applications thereto which have been or may be filed in the United States or elsewhere in the world; (c) any patents which may be granted on the applications set forth in (a) and (b) above, and (d) the right to sue in its own name and to recover for past infringement of any or all of any applications or patents issuing therefrom together with all rights to recover damages for infringement of provisional rights.

ASSIGNOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all oaths, assignments, powers of attorney, applications, and other papers necessary or desirable to fully secure to **ASSIGNEE** the rights, titles and interests herein conveyed, (2) communicate to **ASSIGNEE** all known facts relating to the **SUBJECT MATTER** of the above-identified patent applications and Letters Patents; and (3) generally do all lawful acts that **ASSIGNEE** shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the **SUBJECT MATTER** of the above-identified patent applications and Letters Patents and for vesting in **ASSIGNEE** the rights, titles, and interests herein conveyed. **ASSIGNOR** further agrees to provide any successor, transferee, assignee, or legal representative of **ASSIGNEE** with the benefits and assistance provided to **ASSIGNEE** hereunder

ASSIGNOR represents that **ASSIGNOR** has the rights, titles, and interests to convey as set forth herein, and covenants with **ASSIGNEE** that the **ASSIGNOR** has made or will make hereafter no assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

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SCHEDULE A

TITLE	PATENT NO.	ISSUED	COUNTRY
Method of Generating Code for Programmable Processors, Code Generator and Application Thereof	5,854,929	December 29, 1998	US
Method for Processor Modeling in Code Generation and Instruction Set Simulation	5,918,035	June 29, 1999	US

TITLE	SERIAL NO.	FILED	COUNTRY
Method for Processor Modeling in Code Generation and Instruction Set Simulation	09/019,076	February 5, 1998	US

Duly Authorized Representative of ASSIGNOR

Date of Signature



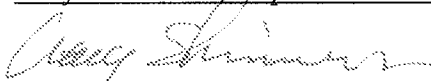
April 24, 2014

Erika Varga McEnroe
Director

TARGET COMPILER TECHNOLOGIES NV

Duly Authorized Representative of ASSIGNEE

Date of Signature



April 24, 2014

Craig Shinnars
Director, Legal Counsel
SYNOPTIS, INC.