

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT2908141

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
ST-ERICSSON SA	08/02/2013
ST-ERICSSON AT SA	02/12/2009
RECEIVING PARTY DATA	
Name:	ERICSSON MODEMS SA
Street Address:	CHEMIN DU CHAMP-DES-FILLES 39
City:	PLAN-LES-OUATES
State/Country:	SWITZERLAND
Postal Code:	1228
PROPERTY NUMBERS Total: 2	
Property Type	Number
Application Number:	13383941
Application Number:	12471568
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	972-583-5799
Email:	diane.taylor@ericsson.com
Correspondent Name:	ROGER S. BURLEIGH
Address Line 1:	6300 LEGACY DRIVE
Address Line 4:	PLANO, TEXAS 75024
ATTORNEY DOCKET NUMBER:	C01228-US1; C02008-US1
NAME OF SUBMITTER:	ROGER S. BURLEIGH
SIGNATURE:	/Roger S. Burleigh/
DATE SIGNED:	06/20/2014
Total Attachments: 22	
source=Assignment-STE SA - STE AT SA - Modems#page1.tif	
source=Assignment-STE SA - STE AT SA - Modems#page2.tif	
source=Assignment-STE SA - STE AT SA - Modems#page3.tif	
source=Assignment-STE SA - STE AT SA - Modems#page4.tif	
source=Assignment-STE SA - STE AT SA - Modems#page5.tif	
PATENT	

source=Assignment-STE SA - STE AT SA - Modems#page6.tif
source=Assignment-STE SA - STE AT SA - Modems#page7.tif
source=Assignment-STE SA - STE AT SA - Modems#page8.tif
source=Assignment-STE SA - STE AT SA - Modems#page9.tif
source=Assignment-STE SA - STE AT SA - Modems#page10.tif
source=Assignment-STE SA - STE AT SA - Modems#page11.tif
source=Assignment-STE SA - STE AT SA - Modems#page12.tif
source=Assignment-STE SA - STE AT SA - Modems#page13.tif
source=Assignment-STE SA - STE AT SA - Modems#page14.tif
source=Assignment-STE SA - STE AT SA - Modems#page15.tif
source=Assignment-STE SA - STE AT SA - Modems#page16.tif
source=Assignment-STE SA - STE AT SA - Modems#page17.tif
source=Assignment-STE SA - STE AT SA - Modems#page18.tif
source=Assignment-STE SA - STE AT SA - Modems#page19.tif
source=Assignment-STE SA - STE AT SA - Modems#page20.tif
source=Assignment-STE SA - STE AT SA - Modems#page21.tif
source=Assignment-STE SA - STE AT SA - Modems#page22.tif

PATENT ASSIGNMENT CONFIRMATION

WHEREAS, ST-Ericsson S.A., a Swiss corporation having its principal office and place of business at 39 Chemin du Champ-des-Filles, 1228 Plan les Ouates, Geneva, Switzerland, (hereinafter called "ASSIGNOR") was, as of August 2, 2013, the owner of all right, title, and interest in and to Patents and Pending Patent Applications listed in Appendix A, and the inventions for which the same were made and which the same describe (hereinafter collectively referred to as "PATENT RIGHTS");

WHEREAS, ST-Ericsson AT SA (subsequently changed to "Ericsson Modems SA"), a company incorporated in Switzerland whose registered office is at Chemin du Champ-des-Filles 39, 1228, Plan-les-Ouates (GE), Switzerland, (hereinafter called "ASSIGNEE"), was, as of August 2, 2013, desirous of acquiring the entire right, title, and interest in and to said PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration, the receipt of which is acknowledged, the said ASSIGNOR, effective as of August 2, 2013, has sold, conveyed, transferred, and assigned to ASSIGNEE, subject to prior encumbrances, all its right, title and interest in and to said PATENT RIGHTS, the same to be held and enjoyed by the ASSIGNEE for its own use and benefit and for the use and benefit of its successors, assigns or other legal representatives; together with any and all patents or patent applications anywhere worldwide to which any of the PATENT RIGHTS directly or indirectly claim priority, including, but not limited to, provisional applications thereof, or for which any of the PATENT RIGHTS directly or indirectly form a basis for priority, together with all existing and/or future continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing together with all claims for damages, injunctive relief, and any other remedies of any kind by reason of past, current and future infringement of said PATENT RIGHTS, with the right to sue for and collect the same for its own use and benefit and for the use and benefit of its successors, assigns, or other legal representatives to the full end of the term for which the aforementioned rights may be granted anywhere in the world.

The assignment of the above mentioned rights includes a transfer of the whole right to use a priority (including priority according to any convention, multilateral agreement, bilateral agreement and national law).

ASSIGNOR covenants that it had, as of August 2, 2013, the full right to sell, convey, transfer, and assign its interest in said PATENT RIGHTS, and that it has not executed, and will not execute, any agreement in conflict herewith or in conflict with the sale, conveyance, transfer and assignment that is the subject hereof.

ASSIGNOR further hereby agrees to execute and deliver all further instruments of sale, conveyance, transfer, assignment, and further assurances and perform all such other acts as may be desirous or required to sell, convey, transfer and assign all of the ASSIGNOR'S right, title, and interest in and to and under said PATENT RIGHTS to the ASSIGNEE, and to otherwise assist in perfecting, obtaining, and securing the aforementioned rights to

Handwritten signature

ASSIGNEE, its legal representatives, successors, and assigns, for any jurisdiction in the world.

ASSIGNOR further hereby provides ASSIGNEE the full and perpetual Power of Attorney for said PATENT RIGHTS, including but not limited the Power of Attorney to execute and deliver all such documents related to sale, conveyance, transfer, assignment and recording of the same for any jurisdiction in the world.

At the expense of ASSIGNEE, or its legal representatives, successors, or assigns, ASSIGNOR agrees to assist in any legal proceedings, sign all lawful papers, make all lawful oaths, and generally do everything reasonably possible to aid ASSIGNEE, its legal representatives, successors, and assigns, to enforce the aforementioned rights in any and all countries and regions worldwide.

Whereby, by its duly authorized officers, ASSIGNOR hereby confirms its sale, conveyance, transfer and assignment, effective as of August 2, 2013, of the PATENT RIGHTS to ASSIGNEE, and ASSIGNEE, by its duly authorized officers, hereby confirms its acceptance of such sale, conveyance, transfer and assignment.

By:  By: _____
Name: Carla Ferro Name: _____
Title: CEO, President Title: _____
COMPANY: ST-Ericsson S.A.

By:  By: _____
Name: Mats Norén Name: _____
Title: VP&GM Title: _____
COMPANY: ST-Ericsson AT SA

US Patents and Patent Applications

Reference	Country	Patent No	PublicationNo	Application No	Title
C00064-US1	US	5,617,055		08/509,304	ELECTRONIC SWITCH HAVING REDUCED BODY-EFFECT
C00069-US1	US	5,642,388		08/383,334	FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL BASED MI
C00070-US1	US	6,006,081		08/593,954	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00074-US1	US	5,764,171		08/626,530	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00081-US1	US	6,138,000		08/517,155	A LOW VOLTAGE TEMPERATURE VCC COMPENSATED RF MIXER
C00085-US1	US	5,928,314		08/709,454	IMPROVED ROUNDING IN DIGITAL FILTERS
C00115-US1	US	5,815,675		08/670,273	METHOD AND APPARATUS FOR DIRECT ACCESS TO MAIN MEMORY BY AN
C00120-US1	US	6,138,136		09/011,673	SIGNAL PROCESSOR
C00123-US1	US	6,151,667		08/931,396	TELECOMMUNICATION DEVICE WITH REDUCED POWER CONSUMPTION
C00132-US1	US	5,955,922		08/956,274	A TWO STAGE FULLY DIFFERENTIAL OPERATIONAL AMPLIFIER WITH EFFICIENT COMMON-MODE FEEDBACK CIRCUIT
C00157-US1	US	6,163,685		09/080,837	AGC FOR DIGITAL RECEIVERS
C00177-US1	US	6,442,407		09/123,031	POWER MANAGEMENT UNIT
C00180-US1	US	6,301,317		08/910,817	SYNCHRONIZATION SYSTEM AND METHOD FOR DIGITAL COMMUNICATION SYSTEMS
C00180-US2	US	6,400,784		09/651,086	SYNCHRONIZATION SYSTEM AND METHOD FOR DIGITAL COMMUNICATION SYSTEMS
C00204-US1	US	6,298,222		09/207,528	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-US2	US	7,321,750	20010014613	09/840,819	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00215-US1	US	6,418,127		09/252,702	POWER CONSUMPTION REDUCTION IN A MS II
C00223-US1	US	6,272,330		09/260,342	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00277-US1	US	6,285,315		09/314,260	POSITIONING SYSTEMS
C00287-US1	US	6,369,730		09/553,602	SIGMA DELTA CONVERTER WITH RTZ IN SIGNAL ERROR BRANCH
C00329-US1	US	7,130,595		09/469,887	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00329-US2	US	7,899,424	20070190949	11/536,312	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00344-US1	US	7,190,953	20010024961	09/795,000	DOWNLOAD OF CODEC ALGORITHM
C00377-US1	US	7,313,641	20020055979	09/947,104	SYMTAN - SYMMETRIC TANDEM
C00383-US1	US	6,754,252		09/678,471	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00397-US1	US	6,975,692	20020067780	09/729,748	SCALING OF DEMODULATED DATA IN AN INTERLEAVER
C00445-US1	US	6,621,293	20020181641	09/871,231	AN INTEGRATED CIRCUIT ARRANGEMENT WITH FEATURE
C00457-US1	US	6,993,098	20030053566	09/904,355	METHOD AND APPARATUS FOR EFFICIENT CALCULATING
C00481-US1	US	7,185,267	20030088820	10/277,567	ITERATIVE DECODER WITH LLR CORRECTION
C00495-US1	US	7,792,509	20050169416	10/500,620	TRANSCEIVER WITH MULTI-STATE DDS DRIVEN PLL
C00510-US1	US	7,183,747	20050104559	10/504,749	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT
C00525-US1	US	7,684,832	20050164723	10/515,081	POWER EFFICIENT OFF-LINE PROCESSING FOR UMTS IDLE MODE
C00526-US1	US			10/515,451	COMBINED RECONFIGURABLE VECTOR AND SCALAR PIPELINE
C00546-US1	US	7,642,891	20050237144	10/521,854	PLANAR INDUCTANCE WITH REDUCED MAGNETIC FIELD
C00554-US1	US	7,430,631	20050240729	10/515,452	COMBINED VECTOR AND SCALAR MEMORY
C00555-US1	US	7,383,419	20060010255	10/515,462	VECTORIZATION OF ACU CONFIGURATION
C00577-US1	US	8,036,317	20060039506	10/536,641	METHOD FOR PHASE-GAIN IMBALANCE ESTIMATION AND COMPENSATION
C00581-US1	US	7,920,654	20060193403	10/539,355	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00587-US1	US	7,715,510	20060227853	10/540,694	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING IN TD
C00596-US1	US	8,005,159	20060023653	10/532,912	CHANNEL ESTIMATION FOR UMTS RAKE RECEIVER
C00611-US1	US	7,702,706	20060269037	10/552,048	CONFIGURABLE MULTI-STEP LFSR
C00616-US1	US			60/465,127	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00616-US2	US	7,548,591	20060208820	10/554,020	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00616-US3	US	7,893,758	20090267701	12/484,544	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00628-US1	US	7,877,105	20060234737	10/562,875	ADAPTIVE AFC-ALGORITHM...
C00634-US1	US			10/565,926	DEVICE AND METHOD FOR COMPOSING CODES
C00655-US1	US	7,831,252	20070115884	10/580,729	HANDOVER SCHEME FOR PEER_TO-PEER ENABLE WIRELESS SYSTEM
C00657-US1	US	7,483,474	20070127434	10/581,809	STATION COMPRISING A RAKE RECEIVER
C00657-US2	US	7,720,450	20090141777	12/359,176	STATION COMPRISING A RAKE RECEIVER
C00687-US1	US		20050228913	10/814,426	COMMUNICATION APPARATUS IMPLEMENTING TIME DOMAIN ISOLATION WITH RESTRICTED BUS ACCESS
C00687-US2	US	7,380,033	20060253634	11/479,733	COMMUNICATION APPARATUS IMPLEMENTING TIME DOMAIN ISOLATION WITH RESTRICTED BUS ACCESS

Appendix A - Patents from STE to Ericsson

Reference	Country	Patent No	PublicationNo	Application No	Title
C00688-US1	US	7,808,225	20080265862	11/547,416	PARALLEL ARRANGED POWER SUPPLIES
C00704-US1	US	8,116,363		11/597,604	METHOD OF DETERMINING TRUE ERROR VECTOR MAGNITUDE IN A WIRELESS LAN
C00724-US1	US	7,761,056	20060019686	10/897,953	METHOD OF CONTROLLING A PROCESSOR FOR RADIO ISOLATION USING A TIMER
C00743-US1	US	7,804,732	20080259699	11/575,865	MEMORY CONTROL WITH SELECTIVE DATA RETENTION
C00743-US2	US	8,305,828	20110051501	12/871,834	MEMORY CONTROL WITH SELECTIVE RETENTION/US
C00763-US1	US	7,957,492	20080219384	11/720,316	TFCI DECODING APPARATUS AND METHOD
C00767-US1	US	7,984,365	20100241935	11/720,830	TURBO DECODER.
C00768-US1	US	8,238,409	20100103996	11/720,835	UMTS SYNCHRO.
C00775-US1	US	7,262,481		11/014,142	FILL STRUCTURES FOR USE WITH A SEMICONDUCTOR INTEGRATED CIRCUIT INDUCTOR
C00780-US1	US	8,019,382	20060141946	11/025,672	COMMUNICATION APARATUS HAVING A STANDARD SERIAL COMMUNICATION INTERFACE COMPATIBLE WITH RADIO ISOLATION
C00784-US1	US			11/323,981	A SYSTEM FOR DYNAMIC POWER MANAGEMENT
C00784-US2	US	7,689,839	20070094525	11/498,332	A SYSTEM FOR DYNAMIC POWER MANAGEMENT
C00785-US1	US	7,457,607	20060148437	11/028,831	CDMA RECEIVER POWER CONSUMPTION REDUCTION BY DYNAMIC CONTROL OF ITS LINEARITY AND PHASE NOISE PARAMETERS
C00805-US1	US	7,805,170	20060229024	11/094,583	SYSTEM AND METHOD FOR EFFICIENT POWER SUPPLY REGULATION COMPATIBLE WITH RADIO FREQUENCY OPERATION
C00815-US1	US	8,045,986	20090082018	11/915,473	HIGH SPEED CELL SEARCH FUNCTION
C00823-US1	US	8,270,534	20090016472	11/816,100	ESTIMATION OF ERROR RATES ON UNKNOWN DATA BITS
C00856-US1	US	7,683,692	20090085631	12/067,485	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00856-US2	US	7,872,517	20100189287	12/661,675	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00864-US1	US		20080212540	12/068,000	TIME REDUCTION OF INITIAL CELL SEARCH IN WB-CDMA (3G)
C00877-US1	US	7,890,736	20080294876	12/092,615	FLAG REGISTERS AS AN OPTIMISED SOLUTION FOR DUAL CORE SYNCHRONIZATION
C00879-US1	US	8,031,746	20080279225	12/094,305	RX CLOCKING BY NOP SIGNALLING VIA A SELF-CLOCKING LINK WITHOUT SENDING DATA OR EXPLICIT CLOCK SIGNAL
C00883-US1	US	8,199,793	20090274198	12/096,217	DETERMINATION OF ACTIVE SPREADING CODES AND THEIR POWER THROUGH FAST WALSH HADAMARD TRANSFORMATION
C00904-US1	US		20090028124	12/162,818	METHOD AND APPARATUS FOR IMPROVING THE SPEED OF CONVERGENCE AND TRACKING PERFORMANCE OF SYMBOL LEVEL
C00927-US1	US			60/795,909	PROGRAMMABLE POWER MODE TRANSITION CONTROLLER
C00927-US2	US		20090132835	12/258,221	PROGRAMMABLE POWER MODE TRANSITION CONTROLLER
C00928-US1	US			12/259,140	HIGHER UART BAUD RATES GENERATION AT LOWER XTAL OSCILLATOR FREQUENCIES USING OVER SAMPLING
C00928-US2	US			12/467,850	HIGHER UART BAUD RATES GENERATION AT LOWER XTAL OSCILLATOR FREQUENCIES USING OVER SAMPLING
C00928-US3	US			12/634,541	HIGHER UART BAUD RATES GENERATION AT LOWER XTAL OSCILLATOR FREQUENCIES USING OVER SAMPLING
C00928-US4	US			12/768,947	HIGHER UART BAUD RATES GENERATION AT LOWER XTAL OSCILLATOR FREQUENCIES USING OVER SAMPLING
C00928-US5	US	8,331,427	20110116557	12/876,782	HIGHER UART BAUD RATES GENERATION AT LOWER XTAL OSCILLATOR FREQUENCIES USING OVER SAMPLING
C00929-US1	US		20110039503	12/226,647	NOVEL METHOD AND STRUCTURE OF SOFTWARE DEFINED RADIO DEVICE CONFIGURATION
C00933-US1	US	8,135,897	20090300310	12/227,344	LOW-COST SIMD MEMORY WITH LIMITED SCATTER GATHER
C00973-US1	US		20100067602	12/439,513	SPACE DIVERSITY RF RECEIVER FOR UTRA-FDD/WCDMA STANDARD
C00974-US1	US	8,218,687	20100008449	12/395,549	CORRECTION OF LINEAR FREQUENCY DEPENDENT SIGNAL PATH ERRORS
C00978-US1	US		20100232304	12/311,088	IMPROVED TIMING CONCEPT FOR HS/LP OPERATION MODE SWITCHING IN MIPI PHYS
C00986-US1	US	8,312,355	20100115378	12/514,424	MULTI-STANDARD CHANNEL ENCODER
C01013-US1	US		20100105407	12/593,569	OUTER LOOP POWER CONTROL IN UNLOADED CELLS
C01019-US1	US		20100284493	12/596,364	DOWNSAMPLED CHANNEL ESTIMATION FOR LTE
C01025-US1	US		20100250974	12/451,891	STRATEGY FOR LOW POWER MANAGEMENT
C01025-US1	US			12/451,891	STRATEGY FOR LOW POWER MANAGEMENT
C01030-US1	US		20100248773	12/665,300	IMPROVED POWER CONSUMPTION IN MOBILE PHONE USING TDMA
C01084-US1	US		20110055657	12/865,702	HIGHLY EFFICIENT AND FLEXIBLE EARLY STOPPING RULES FOR TURBO CODES/US
C01127-US1	US		20120020439	13/126,696	RECEIVER WITH CHANNEL ESTIMATION CIRCUITRY

Appendix A - Patents from STE to Ericsson

Reference	Country	Patent No	PublicationNo	Application No	Title
C01130-US1	US		20110274224	13/127,941	METHOD OF STORING DATA IN A MEMORY DEVICE AND A PROCESSING DEVICE FOR PROCESSING SUCH DATA AND CORRE
C01146-US1	US		20110211652	13/001,934	METHOD FOR SHORT-TIME OFDM TRANSMISSION AND APPARATUS FOR PERFORMING FLEXIBLE OFDM MODULATION
C01147-US1	US		20120033720	13/142,594	DIGITAL INTERFACE BETWEEN A RF AND BASEBAND CIRCUIT AND PROCESS FOR CONTROLLING SUCH INTERFACE
C01148-US1	US			13/142,782	PROCESS FOR BLIND DETECTION OF A SYNCHRONIZATION SIGNAL FOR LTE
C01167-US1	US			13/254,655	WIRELESS COMMUNICATING APPARATUS PROCESSING UNIT CONTROL AND CORRESPONDING WIRELESS COMMUNICATING AP
C01178-US1	US		20120171974	13/264,102	NOISE SUPPRESSION
C01206-US1	US			13/375,366	MANAGEMENT OF RESSOURCES OF COMMUNICATION TERMINALS
C01217-US1	US		20120169426	13/379,277	FREQUENCY MULTIBAND OSCILLATOR SYSTEM
C01228-US1	US			13/383,941	LOW DROP OUT REGULATOR
C01262-US1	US			13/500,575	PULSE WIDTH MODULATION FOR SWITCHING AMPLIFIERS
C01269-US1	US		20120269302	13/504,103	PROCESS FOR DECODING ALAMOUTI BLOCK CODE IN AN OFDM SYSTEM, AND RECEIVER FOR THE SAME
C01289-US1	US		20120236919	13/514,338	METHOD AND DEVICE FOR IDENTIFYING AT LEAST ONE COMMUNICATION CHANNEL WITH AN INCIDENT SIGNAL
C02008-US1	US	8,055,212	20100304695	12/471,568	Phase Compensation without Trigonometric Involvement
C02013-US1	US			61/263,425	VCO with Wide Tuning Range
C02013-US2	US		20120223751	13/508,414	VCO with Wide Tuning Range
C02017-US1	US			61/247,736	Flexible Power Supply Startup Sequence
C02017-US2	US		20120185720	13/395,287	Flexible Power Supply Startup Sequence
C02020-US1	US			13/503,168	Bootstrapped Passive Mixer
C02038-US1	US			61/294,524	Area Reduction using a Branch Processor Approach...
C02038-US2	US		20120269303	13/518,437	Area Reduction using a Branch Processor Approach...
C02052-US1	US			61/394,406	Opportunistic comma code matching in an encoded Phy
C02052-US2	US			13/878,327	Opportunistic comma code matching in an encoded Phy
C02152-US1	US			61/394,070	PROTOCOL TO DETECT AND COMMUNICATE SYNCHRONIZATION LOSS
C02152-US2	US			13/879,137	PROTOCOL TO DETECT AND COMMUNICATE SYNCHRONIZATION LOSS
C02181-US1	US			61/432,824	Vector-based matcher of address and data streams
C02181-US2	US			13/992,837	Vector-based matcher of address and data streams
C02219-US1	US			61/356,864	UniPro error handling for errors detected in-between
C02219-US2	US			13/696,822	UniPro error handling for errors detected in-between
C02255-US1	US		20110257983	12/858,670	Method for Minimizing Speech Delay (2)
C02283-US1	US			61/388,209	DutyCycle adjustment to improve efficiency of a digital RF-PA
C02283-US2	US			12/941,216	DutyCycle adjustment to improve efficiency of a digital RF-PA
C02302-US1	US			13/253,754	An SIMD memory to support up and downsampling and transposition
C02305-US1	US			61/386,055	PA load-line tuning with capacitors around a balun/US
C02305-US2	US	8,339,203		12/982,276	PA load-line tuning with capacitors around a balun
C02308-US1	US			61/386,790	RF Divider using Direct Digital Synthesis
C02308-US2	US		20120076231	13/033,060	RF Divider using Direct Digital Synthesis
C02310-US1	US			61/388,318	Current saving by reduced modulation code and disabled clock buffers
C02310-US2	US			13/248,371	Current saving by reduced modulation code and disabled clock buffers
C02319-US1	US			61/386,133	Calibrate output matching for correct output power
C02319-US2	US	8,294,514		12/982,420	Calibrate output matching for correct output power/US
C02381-US1	US			13/076,717	Double Balanced Digital Transmitter
C02382-US1	US			13/156,751	High Output Power Digital TX
C02388-US1	US			13/252,661	DVFS-enabled multiprocessor
C02389-US1	US			61/566,926	Improved scalar distribution in an SIMD system
C02394-US1	US			61/523,536	Simplified Preprocessing for the Sphere Decoder..
C02397-US1	US			61/515,361	Power efficient branch prediction
C02398-US1	US			13/239,586	Dynamic Power Scaling of Covariance Computations
C02417-US1	US			13/118,621	Hardware Controlled Switching Between DVFS Operating Points
C02482-US1	US			61/515,173	Method to ensure reliable data exchange closing between two chips
C02494-US1	US			61/493,794	A Generic Multi-RAT L1 SW Architecture

Appendix A - Patents from STE to Ericsson

Reference	Country	Patent No	PublicationNo	Application No	Title
C02494-US2	US			13/307,172	A Generic Multi-RAT L1 SW Architecture
C02495-US1	US			61/493,795	Flexible Distributed Sequencer
C02495-US2	US			13/307,178	Flexible Distributed Sequencer
C02496-US1	US			61/493,801	Radio Planner
C02496-US2	US			13/307,182	Radio Planner
C02794-US1	US			61/595,234	LTE transmitter with reduced spurious emissions
C02807-US1	US			61/598,013	Signed polar modulator
C02819-US1	US			13/197,022	High Efficiency Injection-Locked Power Amplifier
C02884-US1	US			13/664,014	SWR meter for integrated antenna tuner
C02998-US1	US			61/637,323	Dual VCO
C03021-US1	US			61/667,004	Dynamic List based Sphere Decoder for Turbo Equalization
C03028-US1	US			61/661,903	System for vector element selection
C03032-US1	US			61/602,140	SIP B2BUA and Proxy on Modem
C03075-US1	US			13/440,771	Uplink transmit diversity UE architecture optimization
C03194-US1	US			61/659,540	Protection of SIP B2BUA on Modem
C03194-US2	US			13/866,259	Protection of SIP B2BUA on Modem
C03237-US1	US			61/807,899	Connecting auxiliary battery charger IC to mobile chipset

Non-US Patents and Patent Applications

Reference	Country	Registration No	PublicationNo	Application No	Title
C00064-DE1	DE	69413814		94830387.0	ELECTRONIC SWITCH HAVING REDUCED BODY-EFFECT
C00064-EP1	EP	0698966	0698966	94830387.0	ELECTRONIC SWITCH HAVING REDUCED BODY-EFFECT
C00064-FR1	FR	0698966		94830387.0	ELECTRONIC SWITCH HAVING REDUCED BODY-EFFECT
C00064-GB1	GB	0698966		94830387.0	ELECTRONIC SWITCH HAVING REDUCED BODY-EFFECT
C00064-IT1	IT	0698966		94830387.0	ELECTRONIC SWITCH HAVING REDUCED BODY-EFFECT
C00064-JP1	JP	3449830	8065124	19950194623	ELECTRONIC SWITCH HAVING REDUCED BODY-EFFECT
C00069-JP1	JP	3906395	9265332	19960070526	FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL BASED MI
C00069-KR1	KR			199610220	FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL BASED MI
C00070-CN1	CN	1067193	1148913	19961090242.6	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00070-DE1	DE	69616201.6		96900405.0	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00070-EP1	EP	0756780	0756780	96900405.0	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00070-FR1	FR	0756780		96900405.0	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00070-GB1	GB			9503064.9	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00070-GB2	GB	0756780		96900405.0	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00070-IN1	IN	186280		1996CA00268	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00070-JP1	JP	3887018	10500550T	19960524786T	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00070-KR1	KR	0392150		19960705899	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00070-PCT1	WO		9625790	IB9600065	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS
C00074-DE1	DE	69616222.9		96904247.2	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00074-EP1	EP			95200840.7	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00074-EP2	EP	0763278	0763278	96904247.2	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00074-FR1	FR	0763278		96904247.2	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00074-GB1	GB	0763278		96904247.2	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00074-IT1	IT	0763278		96904247.2	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00074-JP1	JP	4148992	10501673T	19960530140T	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00074-KR1	KR	379048	19970703653	19960706833	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00074-PCT1	WO		9631943	IB9600242	SIGMA DELTA MODULATOR WITH POLYPHASE FILTER
C00081-EP1	EP		0801849	96925945.6	A LOW VOLTAGE TEMPERATURE VCC COMPENSATED RF MIXER
C00081-JP1	JP	4065024	1998507894	19970509097T	A LOW VOLTAGE TEMPERATURE VCC COMPENSATED RF MIXER
C00081-KR1	KR			19960702608	A LOW VOLTAGE TEMPERATURE VCC COMPENSATED RF MIXER
C00081-PCT1	WO		9707596	IB9600815	A LOW VOLTAGE TEMPERATURE VCC COMPENSATED RF MIXER
C00085-DE1	DE	69611155.1		96927163.4	IMPROVED ROUNDING IN DIGITAL FILTERS
C00085-EP1	EP			95202417.2	IMPROVED ROUNDING IN DIGITAL FILTERS
C00085-EP2	EP	0791242	0791242	96927163.4	IMPROVED ROUNDING IN DIGITAL FILTERS
C00085-FR1	FR	0791242		96927163.4	IMPROVED ROUNDING IN DIGITAL FILTERS
C00085-GB1	GB	0791242		96927163.4	IMPROVED ROUNDING IN DIGITAL FILTERS
C00085-JP1	JP		10509011	1996-511025	IMPROVED ROUNDING IN DIGITAL FILTERS
C00085-PCT1	WO		9709780	IB1996000881	IMPROVED ROUNDING IN DIGITAL FILTERS
C00120-DE1	DE		19625569	19961025569.4	SIGNAL PROCESSOR
C00120-EP1	EP		0846287	97925232.7	SIGNAL PROCESSOR
C00120-JP1	JP		11511885T	19970502611T	SIGNAL PROCESSOR
C00120-PCT1	WO		9750030	IB9700760	SIGNAL PROCESSOR
C00123-DE1	DE		19638772	19961038772.8	TELECOMMUNICATION DEVICE WITH REDUCED POWER CONSUMPTION
C00123-DE2	DE	29724639.9		29724639.9	TELECOMMUNICATION DEVICE WITH REDUCED POWER CONSUMPTION
C00123-EP1	EP		0831666	97202819.5	TELECOMMUNICATION DEVICE WITH REDUCED POWER CONSUMPTION
C00123-JP1	JP	4005675	10136050	1997-254896	TELECOMMUNICATION DEVICE WITH REDUCED POWER CONSUMPTION
C00132-EP1	EP	0840442	0840442	96830555.7	A TWO STAGE FULLY DIFFERENTIAL OPERATIONAL AMPLIFIER WITH EFFICIENT COMMON-MODE FEEDBACK CIRCUIT
C00132-GB1	GB	0840442		96830555.7	A TWO STAGE FULLY DIFFERENTIAL OPERATIONAL AMPLIFIER WITH EFFICIENT COMMON-MODE FEEDBACK CIRCUIT
C00132-IT1	IT	0840442		96830555.7	A TWO STAGE FULLY DIFFERENTIAL OPERATIONAL AMPLIFIER WITH EFFICIENT COMMON-MODE FEEDBACK CIRCUIT
C00157-CN1	CN	1115032	1234937	19988000990.0	AGC FOR DIGITAL RECEIVERS
C00157-DE1	DE	69829105.0		98913993.6	AGC FOR DIGITAL RECEIVERS
C00157-EP1	EP			97201555.6	AGC FOR DIGITAL RECEIVERS
C00157-EP2	EP	0920766	0920766	98913993.6	AGC FOR DIGITAL RECEIVERS
C00157-FR1	FR	0920766		98913993.6	AGC FOR DIGITAL RECEIVERS

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C00157-GB1	GB	0920766		98913993.6	AGC FOR DIGITAL RECEIVERS
C00157-JP1	JP	4027431	2000515708T	19980529437T	AGC FOR DIGITAL RECEIVERS
C00157-KR1	KR	572133	1,02E+12	19997000456	AGC FOR DIGITAL RECEIVERS
C00157-PCT1	WO		9853579	1B9800637	AGC FOR DIGITAL RECEIVERS
C00177-DE1	DE	19733530	19733530	19971033530.6	POWER MANAGEMENT UNIT
C00177-EP1	EP		0895394	98202505.8	POWER MANAGEMENT UNIT
C00177-JP1	JP		11145897	19980217219	POWER MANAGEMENT UNIT
C00177-KR1	KR	479948		19980031252	POWER MANAGEMENT UNIT
C00177-TW1	TW	123118	411684B	87114818	POWER MANAGEMENT UNIT
C00204-CN1	CN	1118927	1252184	19988003907.9	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-DE1	DE	69829650.8		98955852.3	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-EP1	EP			97203902.8	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-EP2	EP	0960474	0960474	98955852.3	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-ES1	ES	0960474		98955852.3	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-FR1	FR	0960474		98955852.3	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-GB1	GB	0960474		98955852.3	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-IN1	IN	199411		IN9900007	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-IT1	IT	0960474		98955852.3	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-JP1	JP	4159617	2001513972	1999-0532246	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-KR1	KR	573988		19997007169	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-SG1	SG	67131		9903826.7	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00204-WO1	WO		9931798	1B1998001960	COMMUNICATION SYSTEM, DEVICE AND METHOD
C00215-CN1	CN	1130037	1256818	19998000157.0	POWER CONSUMPTION REDUCTION IN A MS II
C00215-DE1	DE	69938485.0		99901807.0	POWER CONSUMPTION REDUCTION IN A MS II
C00215-EP1	EP			98400422.6	POWER CONSUMPTION REDUCTION IN A MS II
C00215-EP2	EP	0985281	0985281	99901807.0	POWER CONSUMPTION REDUCTION IN A MS II
C00215-FR1	FR	0985281		99901807.0	POWER CONSUMPTION REDUCTION IN A MS II
C00215-GB1	GB	0985281		99901807.0	POWER CONSUMPTION REDUCTION IN A MS II
C00215-IN1	IN	202544		IN19990049	POWER CONSUMPTION REDUCTION METHOD IN A DIGITAL MOBILE RADIO SYSTEM AND A MOBILE RADIO STATION
C00215-JP1	JP	4029940	2001520852	1999-0542290	POWER CONSUMPTION REDUCTION IN A MS II
C00215-KR1	KR	613758	20010006548	1999-7009638	POWER CONSUMPTION REDUCTION IN A MS II
C00215-SG1	SG	68496		9905149.2	POWER CONSUMPTION REDUCTION IN A MS II
C00215-TW1	TW	122551	411690B	088101301	POWER CONSUMPTION REDUCTION IN A MS II
C00215-WO1	WO		9943098	1B1999000196	POWER CONSUMPTION REDUCTION IN A MS II
C00223-CN1	CN	1227810	1266551	19998000655.6	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00223-DE1	DE	69919281.1		99902752.7	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00223-EP1	EP	0981855	0981855	99902752.7	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00223-FR1	FR	0981855		99902752.7	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00223-GB1	GB			9805148.5	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00223-GB2	GB	0981855		99902752.7	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00223-JP1	JP	04251507	2001526874T	19990545533T	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00223-KR1	KR	605057		19997010342	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00223-PCT1	WO		9946855	1B9900291	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00223-TW1	TW	130743	432811B	88102731	GAIN CONTROLLED LOW NOISE RF AMPLIFIER
C00277-GB1	GB	2347035 B	2347035A	9903521.4	POSITIONING SYSTEMS
C00287-DE1	DE	60015894.2		00926885.5	SIGMA DELTA CONVERTER WITH RTZ IN SIGNAL ERROR BRANCH
C00287-EP1	EP			99201263.3	SIGMA DELTA CONVERTER WITH RTZ IN SIGNAL ERROR BRANCH
C00287-EP2	EP	1088397	1088397	00926885.5	SIGMA DELTA CONVERTER WITH RTZ IN SIGNAL ERROR BRANCH
C00287-FR1	FR	1088397		00926885.5	SIGMA DELTA CONVERTER WITH RTZ IN SIGNAL ERROR BRANCH
C00287-GB1	GB	1088397		00926885.5	SIGMA DELTA CONVERTER WITH RTZ IN SIGNAL ERROR BRANCH
C00287-JP1	JP		2002543656T	20000614562T	SIGMA DELTA CONVERTER WITH RTZ IN SIGNAL ERROR BRANCH
C00287-PCT1	WO		0065723	EP0003274	SIGMA DELTA CONVERTER WITH RTZ IN SIGNAL ERROR BRANCH
C00329-CN1	CN	1201484	1384999	20008006557.8	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00329-DE1	DE	60031870.2		00989962.6	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00329-EP1	EP	1219026	1219026	00989962.6	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00329-FR1	FR	1219026		00989962.6	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00329-GB1	GB	1219026		00989962.6	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00329-JP1	JP	4815555	2003518794	2001-547730	TRANSMITTER POWER AMPLIFIER RAMPING METHOD

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C00329-KR1	KR	0780116	20010102275	20017010579	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00329-PCT1	WO		0147106	EP0012387	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00329-TW1	TW	160853	498609B	090109896	TRANSMITTER POWER AMPLIFIER RAMPING METHOD
C00344-CN1	CN	1264369	1312662	20011017247.9	DOWNLOAD OF CODEC ALGORITHM
C00344-DE1	DE		10009444	20001009444.9	DOWNLOAD OF CODEC ALGORITHM
C00344-EP1	EP		1130935	01200655.7	DOWNLOAD OF CODEC ALGORITHM
C00344-IN1	IN	216484		0179MAS2001	DOWNLOAD OF CODEC ALGORITHM
C00344-JP1	JP		2001298501	20010052774	DOWNLOAD OF CODEC ALGORITHM
C00344-KR1	KR	0810782	20010085640	20010009859	DOWNLOAD OF CODEC ALGORITHM
C00377-CN1	CN	ZL01803307.5	1394310	20018003307.5	SYMTAN - SYMMETRIC TANDEM
C00377-EP1	EP			00119255.8	SYMTAN - SYMMETRIC TANDEM
C00377-EP2	EP		1317712	01971955.8	SYMTAN - SYMMETRIC TANDEM
C00377-JP1	JP	4915631	2004508635T	2002-524835T	SYMTAN - SYMMETRIC TANDEM
C00377-KR1	KR			20027005707	SYMTAN - SYMMETRIC TANDEM
C00377-WO1	WO		0221290	EP2001009735	SYMTAN - SYMMETRIC TANDEM
C00383-DE1	DE	1323243		01986379.4	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00383-EP1	EP	1323243	1323243	01986379.4	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00383-FR1	FR	1323243		01986379.4	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00383-GB1	GB	1323243		01986379.4	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00383-IT1	IT	1323243		01986379.4	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00383-JP1	JP	3920217	2004511170	2002-0533494	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00383-KR1	KR	811900	20020054360	20027006986	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00383-NL1	NL	1323243		01986379.4	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00383-PCT1	WO		0229995	EP0111404	METHOD AND APPARATUS FOR CALL DROP PREVENTION
C00397-CN1	CN	1264281	1404656	20018004525.1	SCALING OF DEMODULATED DATA IN AN INTERLEAVER
C00397-DE1	DE	60119387.3		01984808.4	SCALING OF DEMODULATED DATA IN AN INTERLEAVER
C00397-EP1	EP	1356597	1356597	01984808.4	SCALING OF DEMODULATED DATA IN AN INTERLEAVER
C00397-FR1	FR	1356597		01984808.4	SCALING OF DEMODULATED DATA IN AN INTERLEAVER
C00397-GB1	GB	1356597		01984808.4	SCALING OF DEMODULATED DATA IN AN INTERLEAVER
C00397-JP1	JP	4101653	2004515960	2002-0548881	SCALING OF DEMODULATED DATA IN AN INTERLEAVER
C00397-KR1	KR	100852083		10-2002-7009949	SCALING OF DEMODULATED DATA IN AN INTERLEAVER
C00397-PCT1	WO		0247276	EP0114383	SCALING OF DEMODULATED DATA IN AN INTERLEAVER
C00445-DE1	DE	60211338.5		02733066.1	AN INTEGRATED CIRCUIT ARRANGEMENT WITH FEATURE
C00445-EP1	EP	1399828	1399828	02733066.1	AN INTEGRATED CIRCUIT ARRANGEMENT WITH FEATURE
C00445-FR1	FR	1399828		02733066.1	AN INTEGRATED CIRCUIT ARRANGEMENT WITH FEATURE
C00445-GB1	GB	1399828		02733066.1	AN INTEGRATED CIRCUIT ARRANGEMENT WITH FEATURE
C00445-JP1	JP		2004520664	2003-500752	AN INTEGRATED CIRCUIT ARRANGEMENT WITH FEATURE
C00445-PCT1	WO		02097638	IB0201915	AN INTEGRATED CIRCUIT ARRANGEMENT WITH FEATURE
C00457-CN1	CN	02813946.1	1526215	20028013946.1	METHOD AND APPARATUS FOR EFFICIENT CALCULATING
C00457-DE1	DE	60217352.3		02741053.9	METHOD AND APPARATUS FOR EFFICIENT CALCULATING
C00457-EP1	EP	1410551	1410551	02741053.9	METHOD AND APPARATUS FOR EFFICIENT CALCULATING
C00457-FR1	FR	1410551		02741053.9	METHOD AND APPARATUS FOR EFFICIENT CALCULATING
C00457-GB1	GB	1410551		02741053.9	METHOD AND APPARATUS FOR EFFICIENT CALCULATING
C00457-JP1	JP	4023613	2004522375T	20030513171T	METHOD AND APPARATUS FOR EFFICIENT CALCULATING
C00457-KR1	KR	100876473		20047000300	METHOD AND APPARATUS FOR EFFICIENT CALCULATING
C00457-PCT1	WO		03007529	IB0202600	METHOD AND APPARATUS FOR EFFICIENT CALCULATING
C00481-CN1	CN	02821086.7	1575547	20028021086.7	ITERATIVE DECODER WITH LLR CORRECTION
C00481-DE1	DE	1446888		02760514.6	ITERATIVE DECODER WITH LLR CORRECTION
C00481-EP1	EP			01204083.8	ITERATIVE DECODER WITH LLR CORRECTION
C00481-EP2	EP	1446888	1446888	02760514.6	ITERATIVE DECODER WITH LLR CORRECTION
C00481-FR1	FR	1446888		02760514.6	ITERATIVE DECODER WITH LLR CORRECTION
C00481-GB1	GB	1446888		02760514.6	ITERATIVE DECODER WITH LLR CORRECTION
C00481-JP1	JP	4047279	2005506793T	20030539168T	ITERATIVE DECODER WITH LLR CORRECTION
C00481-KR1	KR			20047006074	ITERATIVE DECODER WITH LLR CORRECTION
C00481-WO1	WO		03036797	IB0204010	ITERATIVE DECODER WITH LLR CORRECTION
C00495-CN1	CN	ZL02826821.0	1613191	02826821.0	TRANSCEIVER WITH MULTI-STATE DDS DRIVEN PLL
C00495-DE1	DE	60215976.8		02781636.2	TRANSCEIVER WITH MULTI-STATE DDS DRIVEN PLL
C00495-EP1	EP			02075026.1	TRANSCEIVER WITH MULTI-STATE DDS DRIVEN PLL
C00495-EP2	EP	1466418	1466418	02781636.2	TRANSCEIVER WITH MULTI-STATE DDS DRIVEN PLL
C00495-FR1	FR	1466418		02781636.2	TRANSCEIVER WITH MULTI-STATE DDS DRIVEN PLL
C00495-GB1	GB	1466418		02781636.2	TRANSCEIVER WITH MULTI-STATE DDS DRIVEN PLL
C00495-JP1	JP		2005514850T	20030559033T	TRANSCEIVER WITH MULTI-STATE DDS DRIVEN PLL
C00495-KR1	KR	100926849		20047010589	TRANSCEIVER WITH MULTI-STATE DDS DRIVEN PLL

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C00495-PCT1	WO		03058833	IB0205349	TRANSCIEVER WITH MULTI-STATE DDS DRIVEN PLL
C00510-CN1	CN		1636143	20038004214.2	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT
C00510-DE1	DE		10207062	20021007062.8	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT
C00510-DE2	DE	60309844.4		03742627.7	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT
C00510-EP1	EP	1478932	1478932	03742627.7	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT
C00510-FR1	FR	1478932		03742627.7	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT
C00510-GB1	GB	1478932		03742627.7	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT
C00510-JP1	JP		2005517962T	20030570141T	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT
C00510-PCT1	WO		03071291	IB0300518	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT
C00525-CN1	CN		1656700	20038011578.6	POWER EFFICIENT OFF-LINE PROCESSING FOR UMTS IDLE MODE
C00525-DE1	DE		10222970	20021022970.8	POWER EFFICIENT OFF-LINE PROCESSING FOR UMTS IDLE MODE
C00525-DE2	DE	60315830.7		03722934.1	POWER EFFICIENT OFF-LINE PROCESSING FOR UMTS IDLE MODE
C00525-EP1	EP	1510011	1510011	03722934.1	POWER EFFICIENT OFF-LINE PROCESSING FOR UMTS IDLE MODE
C00525-FR1	FR	1510011		03722934.1	POWER EFFICIENT OFF-LINE PROCESSING FOR UMTS IDLE MODE
C00525-GB1	GB	1510011		03722934.1	POWER EFFICIENT OFF-LINE PROCESSING FOR UMTS IDLE MODE
C00525-JP1	JP		2005527148T	20040507159T	POWER EFFICIENT OFF-LINE PROCESSING FOR UMTS IDLE MODE
C00525-WO1	WO		03100998	IB2003001910	POWER EFFICIENT OFF-LINE PROCESSING FOR UMTS IDLE MODE
C00526-CN1	CN			20038011744.4	COMBINED RECONFIGURABLE VECTOR AND SCALAR PIPELINE
C00526-EP1	EP			02077034.3	COMBINED RECONFIGURABLE VECTOR AND SCALAR PIPELINE
C00526-EP2	EP			03725538.7	COMBINED RECONFIGURABLE VECTOR AND SCALAR PIPELINE
C00526-JP1	JP			2004-0507989	COMBINED RECONFIGURABLE VECTOR AND SCALAR PIPELINE
C00526-TW1	TW			092113878	COMBINED RECONFIGURABLE VECTOR AND SCALAR PIPELINE
C00526-WO1	WO			IB2003002213	COMBINED RECONFIGURABLE VECTOR AND SCALAR PIPELINE
C00546-CN1	CN	ZL20038017840.0	1672223	20038017840.0	PLANAR INDUCTANCE WITH REDUCTED MAGNETIC FIELD
C00546-DE1	DE		10233980	20021033980	PLANAR INDUCTANCE WITH REDUCTED MAGNETIC FIELD
C00546-DE2	DE	1527463		03771228.8	PLANAR INDUCTANCE
C00546-EP1	EP	1527463	1527463	03771228.8	PLANAR INDUCTANCE WITH REDUCTED MAGNETIC FIELD
C00546-GB1	GB	1527463		03771228.8	PLANAR INDUCTANCE
C00546-JP1	JP		2005534184T	2004-0524018T	PLANAR INDUCTANCE WITH REDUCTED MAGNETIC FIELD
C00546-NL1	NL	1527463		03771228.8	PLANAR INDUCTANCE
C00546-WO1	WO		2004012213	IB2003003227	PLANAR INDUCTANCE WITH REDUCTED MAGNETIC FIELD
C00554-CN1	CN	ZL03811808.4	1656445	20038011808.4	COMBINED VECTOR AND SCALAR MEMORY
C00554-DE1	DE	60316151.0		03717500.7	COMBINED VECTOR AND SCALAR MEMORY
C00554-EP1	EP			02078618.2	COMBINED VECTOR AND SCALAR MEMORY
C00554-EP2	EP	1512068	1512068	03717500.7	COMBINED VECTOR AND SCALAR MEMORY
C00554-FR1	FR	1512068		03717500.7	COMBINED VECTOR AND SCALAR MEMORY
C00554-GB1	GB	1512068		03717500.7	COMBINED VECTOR AND SCALAR MEMORY
C00554-JP1	JP		2005527035T	20040507986T	COMBINED VECTOR AND SCALAR MEMORY
C00554-PCT1	WO		03100599	IB0301891	COMBINED VECTOR AND SCALAR MEMORY
C00554-TW1	TW	291096	200407705	092113718	COMBINED VECTOR AND SCALAR MEMORY
C00555-CN1	CN	ZL03811782.7	1666174	20038011782.7	VECTORIZATION OF ACU CONFIGURATION
C00555-EP1	EP			02078619.0	VECTORIZATION OF ACU CONFIGURATION
C00555-EP2	EP		1512069	03717501.5	VECTORIZATION OF ACU CONFIGURATION
C00555-JP1	JP	4624098	2005527036T	2004-507987	VECTORIZATION OF ACU CONFIGURATION
C00555-PCT1	WO		03100600	IB0301892	VECTORIZATION OF ACU CONFIGURATION
C00555-TW1	TW		200404205	092113724	VECTORIZATION OF ACU CONFIGURATION
C00577-CN1	CN		1723669	20038105418.9	METHOD FOR PHASE-GAIN IMBALANCE ESTIMATION AND COMPENSATION
C00577-EP1	EP			02102707.3	METHOD FOR PHASE-GAIN IMBALANCE ESTIMATION AND COMPENSATION
C00577-EP2	EP		1573995	03812649.6	METHOD FOR PHASE-GAIN IMBALANCE ESTIMATION AND COMPENSATION
C00577-JP1	JP		2006509438T	20040558271T	METHOD FOR PHASE-GAIN IMBALANCE ESTIMATION AND COMPENSATION
C00577-PCT1	WO		2004054194	IB0305779	METHOD FOR PHASE-GAIN IMBALANCE ESTIMATION AND COMPENSATION
C00581-CN1	CN	ZL20038106350.6	1726684	20038106350.6	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00581-DE1	DE	1576778		03813226.2	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00581-EP1	EP	1576778	1576778	03813226.2	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C00581-FR1	FR	1576778		03813226.2	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00581-GB1	GB			0229320.7	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00581-GB2	GB	1576778		03813226.2	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00581-IT1	IT	1576778		03813226.2	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00581-JP1	JP	04495596	2006510295	2004-560033	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00581-KR1	KR	10-0993461	20050089829	20057011081	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00581-WO1	WO		2004056058	IB0305658	IMPROVED DEMODULATION OF GRAY-CODED PSK SYMBOLS
C00587-CN1	CN	ZL02160462.2	1512795	20021060462.2	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING IN TD
C00587-DE1	DE	60319433.8		03778696.9	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING IN TD
C00587-EP1	EP	1582006	1582006	03778696.9	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING IN TD
C00587-FR1	FR	1582006		03778696.9	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING IN TD
C00587-GB1	GB	1582006		03778696.9	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING IN TD
C00587-JP1	JP	4701344	2006512837	2004-563511	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING IN TD
C00587-PCT1	WO		2004059864	IB0306248	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING IN TD
C00587-TW1	TW	I374616		092135370	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING IN TD
C00596-CN1	CN		1708966	200380102508.2	CHANNEL ESTIMATION FOR UMTS RAKE RECEIVER
C00596-DE1	DE	1559251		03748443.3	CHANNEL ESTIMATION FOR UMTS RAKE RECEIVER
C00596-EP1	EP			03290294.2	CHANNEL ESTIMATION FOR UMTS RAKE RECEIVER
C00596-EP2	EP	1559251	1559251	03748443.3	CHANNEL ESTIMATION FOR UMTS RAKE RECEIVER
C00596-FR1	FR	1559251		03748443.3	CHANNEL ESTIMATION FOR UMTS RAKE RECEIVER
C00596-GB1	GB	1559251		03748443.3	CHANNEL ESTIMATION FOR UMTS RAKE RECEIVER
C00596-JP1	JP	4440211	2006505227T	20050501837T	CHANNEL ESTIMATION FOR UMTS RAKE RECEIVER
C00596-PCT1	WO		2004040869	IB0304510	CHANNEL ESTIMATION FOR UMTS RAKE RECEIVER
C00611-CN1	CN	ZL200480009166.4	1768326	20048009166.4	CONFIGURABLE MULTI-STEP LFSR
C00611-DE1	DE	602004013950.5		04724336.5	CONFIGURABLE MULTI-STEP LFSR
C00611-EP1	EP			03100935.0	CONFIGURABLE MULTI-STEP LFSR
C00611-EP2	EP	1614028	1614028	04724336.5	CONFIGURABLE MULTI-STEP LFSR
C00611-FR1	FR	1614028		04724336.5	CONFIGURABLE MULTI-STEP LFSR
C00611-GB1	GB	1614028		04724336.5	CONFIGURABLE MULTI-STEP LFSR
C00611-JP1	JP	4436830	2006526861	2006-0506793	CONFIGURABLE MULTI-STEP LFSR
C00611-PCT1	WO		2004090714	IB0450362	CONFIGURABLE MULTI-STEP LFSR
C00616-CN1	CN	ZL200480010954.5	1778035	20048010954.5	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00616-CN2	CN	ZL200810130885.1	101355343	200810130885.1	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00616-EP1	EP	1620940	1620940	04728374.2	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00616-GB1	GB	1620940		04728374.2	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00616-JP1	JP	4555898	2006525715	2006-0506490	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00616-KR1	KR	10-1023382	20060009266	20057019982	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00616-WO1	WO		2004095686	IB2004001191	QUADRATURE MODULATOR AND CALIBRATION METHOD
C00628-CN1	CN	ZL200480019032.0	1816966	200480019032.0	ADAPTIVE AFC-ALGORITHM...
C00628-EP1	EP			03101981.3	ADAPTIVE AFC-ALGORITHM...
C00628-EP2	EP		1645033	04744434.4	ADAPTIVE AFC-ALGORITHM...
C00628-JP1	JP	4607108	2007528142T	2006-0518444	ADAPTIVE AFC-ALGORITHM...
C00628-KR1	KR	10-1100128	20060031844	2006-7000106	ADAPTIVE AFC-ALGORITHM...
C00628-PCT1	WO		2005004329	IB0451065	ADAPTIVE AFC-ALGORITHM...
C00634-CN1	CN			20048021024.X	DEVICE AND METHOD FOR COMPOSING CODES
C00634-DE1	DE			04744560.6	DEVICE AND METHOD FOR COMPOSING CODES
C00634-EP1	EP			03102265.0	DEVICE AND METHOD FOR COMPOSING CODES
C00634-EP2	EP			04744560.6	DEVICE AND METHOD FOR COMPOSING CODES
C00634-FR1	FR			04744560.6	DEVICE AND METHOD FOR COMPOSING CODES
C00634-GB1	GB			04744560.6	DEVICE AND METHOD FOR COMPOSING CODES
C00634-JP1	JP			20060520951T	DEVICE AND METHOD FOR COMPOSING CODES
C00634-PCT1	WO			IB0451201	DEVICE AND METHOD FOR COMPOSING CODES
C00655-CN1	CN		1622677	20031118645.7	HANDOVER SCHEME FOR PEER_TO-PEER ENABLE WIRELESS SYSTEM
C00655-CN2	CN	ZL200480035002.9	1887021	200480035002.9	HANDOVER SCHEME FOR PEER_TO-PEER ENABLE WIRELESS SYSTEM
C00655-EP1	EP		1692908	04799086.6	HANDOVER SCHEME FOR PEER_TO-PEER ENABLE WIRELESS SYSTEM

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C00655-JP1	JP	4579251	2007512752T	2006-540689	HANDOVER SCHEME FOR PEER_TO-PEER ENABLE WIRELESS SYSTEM
C00655-TW1	TW	I367036		093134084	HANDOVER SCHEME FOR PEER_TO-PEER ENABLE WIRELESS SYSTEM
C00655-WO1	WO		2005053346	IB200452344	HANDOVER SCHEME FOR PEER_TO-PEER ENABLE WIRELESS SYSTEM
C00657-CN1	CN		1890890	20048035890.4	STATION COMPRISING A RAKE RECEIVER
C00657-EP1	EP			03104548.7	STATION COMPRISING A RAKE RECEIVER
C00657-EP2	EP		1692777	04799259.9	STATION COMPRISING A RAKE RECEIVER
C00657-JP1	JP		2007513561T	20060542083T	STATION COMPRISING A RAKE RECEIVER
C00657-WO1	WO		2005055455	IB2004052570	STATION COMPRISING A RAKE RECEIVER
C00687-CN1	CN		1998144	200580015536.X	COMMUNICATION APPARATUS IMPLEMENTING TIME DOMAIN ISOLATION WITH RESTRICTED BUS ACCESS
C00687-DE1	DE	602005010409.7		05730178.0	COMMUNICATION APPARATUS IMPLEMENTING TIME DOMAIN ISOLATION WITH RESTRICTED BUS ACCESS
C00687-EP1	EP	1738473	1738473	05730178.0	COMMUNICATION APPARATUS IMPLEMENTING TIME DOMAIN ISOLATION WITH RESTRICTED BUS ACCESS
C00687-GB1	GB	1738473		05730178.0	COMMUNICATION APPARATUS IMPLEMENTING TIME DOMAIN ISOLATION WITH RESTRICTED BUS ACCESS
C00687-JP1	JP	4287489	2007532066T	20070506502T	COMMUNICATION APPARATUS IMPLEMENTING TIME DOMAIN ISOLATION WITH RESTRICTED BUS ACCESS
C00687-WO1	WO		2005099108	US2005010641	COMMUNICATION APPARATUS IMPLEMENTING TIME DOMAIN ISOLATION WITH RESTRICTED BUS ACCESS
C00688-CN1	CN	ZL20058010194.2	1938929	20058010194.2	PARALLEL ARRANGED POWER SUPPLIES
C00688-EP1	EP			04101326.9	PARALLEL ARRANGED POWER SUPPLIES
C00688-EP2	EP		1733466	05718559.7	PARALLEL ARRANGED POWER SUPPLIES
C00688-JP1	JP	4701342	2007531488	2007-505710	PARALLEL ARRANGED POWER SUPPLIES
C00688-KR1	KR	10-1140539		2006-7020578	PARALLEL ARRANGED POWER SUPPLIES
C00688-TW1	TW		200614641A	094109590	PARALLEL ARRANGED POWER SUPPLIES
C00688-WO1	WO		2005096481	IB2005051020	PARALLEL ARRANGED POWER SUPPLIES
C00704-DE1	DE	1601129		04253148.3	METHOD OF DETERMINING TRUE ERROR VECTOR MAGNITUDE IN A WIRELESS LAN
C00704-EP1	EP	1601129	1601129	04253148.3	METHOD OF DETERMINING TRUE ERROR VECTOR MAGNITUDE IN A WIRELESS LAN
C00704-FR1	FR	1601129		04253148.3	METHOD OF DETERMINING TRUE ERROR VECTOR MAGNITUDE IN A WIRELESS LAN
C00704-GB1	GB	1601129		04253148.3	METHOD OF DETERMINING TRUE ERROR VECTOR MAGNITUDE IN A WIRELESS LAN
C00704-IT1	IT	1601129		04253148.3	METHOD OF DETERMINING TRUE ERROR VECTOR MAGNITUDE IN A WIRELESS LAN
C00704-WO1	WO		2005117322	GB2005002132	METHOD OF DETERMINING TRUE ERROR VECTOR MAGNITUDE IN A WIRELESS LAN
C00743-CN1	CN	ZL200580039840.8	101061547	20058039840.8	MEMORY CONTROL WITH SELECTIVE DATA RETENTION
C00743-DE1	DE	1794756		05783548.0	MEMORY CONTROL WITH SELECTIVE DATA RETENTION
C00743-EP1	EP			04104588.1	MEMORY CONTROL WITH SELECTIVE DATA RETENTION
C00743-EP2	EP	1794756	1794756	05783548.0	MEMORY CONTROL WITH SELECTIVE DATA RETENTION
C00743-GB1	GB	1794756		05783548.0	MEMORY CONTROL WITH SELECTIVE DATA RETENTION
C00743-JP1	JP	4774526	2008513923	2007-0531943	MEMORY CONTROL WITH SELECTIVE DATA RETENTION
C00743-KR1	KR	10-1158154	20070058514	2007-7006424	MEMORY CONTROL WITH SELECTIVE DATA RETENTION
C00743-WO1	WO		2006033070	IB2005053062	MEMORY CONTROL WITH SELECTIVE DATA RETENTION
C00763-CN1	CN	ZL200580040537.X	101065921	200580040537.X	TFCI DECODING APPARATUS AND METHOD
C00763-EP1	EP			04106098.9	TFCI DECODING APPARATUS AND METHOD
C00763-EP2	EP		1817860	05826627.1	TFCI DECODING APPARATUS AND METHOD
C00763-JP1	JP	4905720	2008522478T	2007-542476	TFCI DECODING APPARATUS AND METHOD
C00763-KR1	KR	10-1125532	20070088642	2007-7011577	TFCI DECODING APPARATUS AND METHOD
C00763-WO1	WO		2006056957	IB2005053895	TFCI DECODING APPARATUS AND METHOD
C00767-CN1	CN	ZL200580041254.7	101069357	20058041254.7	TURBO DECODER.
C00767-EP1	EP			04300839.0	TURBO DECODER.
C00767-EP2	EP		1820276	05825379.0	TURBO DECODER.
C00767-JP1	JP		2008522528	2007-0543964	TURBO DECODER.
C00767-WO1	WO		2006059280	IB2005053963	TURBO DECODER.
C00768-CN1	CN	ZL200580041261.7	101116255	20058041261.7	UMTS SYNCHRO.
C00768-DE1	DE	1864412		05825920.1	UMTS SYNCHRO.
C00768-EP1	EP			04300844.0	UMTS SYNCHRO.
C00768-EP2	EP	1864412	1864412	05825920.1	UMTS SYNCHRO.
C00768-FR1	FR	1864412		05825920.1	UMTS SYNCHRO.
C00768-GB1	GB	1864412		05825920.1	UMTS SYNCHRO.
C00768-JP1	JP	4904596	2008522529	2007-543965	UMTS SYNCHRO.

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C00768-PCT1	WO		2006059281	IB0553964	UMTS SYNCHRO.
C00784-EP1	EP		1677175	05113079.7	A SYSTEM FOR DYNAMIC POWER MANAGEMENT
C00784-IN1	IN			2616DEL2004	A SYSTEM FOR DYNAMIC POWER MANAGEMENT
C00784-IN2	IN			2616DEL2004	A SYSTEM FOR DYNAMIC POWER MANAGEMENT
C00805-EP1	EP		1952563	06734840.9	SYSTEM AND METHOD FOR EFFICIENT POWER SUPPLY REGULATION COMPATIBLE WITH RADIO FREQUENCY OPERATION
C00805-TW1	TW			095110952	SYSTEM AND METHOD FOR EFFICIENT POWER SUPPLY REGULATION COMPATIBLE WITH RADIO FREQUENCY OPERATION
C00805-WO1	WO		2006107406	US2006004885	SYSTEM AND METHOD FOR EFFICIENT POWER SUPPLY REGULATION COMPATIBLE WITH RADIO FREQUENCY OPERATION
C00815-CN1	CN		101180913	200680018112.3	HIGH SPEED CELL SEARCH FUNCTION
C00815-EP1	EP			05300413.1	HIGH SPEED CELL SEARCH FUNCTION
C00815-EP2	EP		1889505	06755985.6	HIGH SPEED CELL SEARCH FUNCTION
C00815-IN1	IN		9994DELNP2008	9994DELNP2007	HIGH SPEED CELL SEARCH FUNCTION
C00815-JP1	JP		2008543153	2008-0512980	HIGH SPEED CELL SEARCH FUNCTION
C00815-KR1	KR		20080035519	2007-7029985	HIGH SPEED CELL SEARCH FUNCTION
C00815-WO1	WO		2006126137	IB2006051524	HIGH SPEED CELL SEARCH FUNCTION
C00823-CN1	CN	ZL200680004314.2	101138189	20068004314.2	ESTIMATION OF ERROR RATES ON UNKNOWN DATA BITS
C00823-DE1	DE	60 2006 022913.5		06710859.7	METHOD AND APPARATUS FOR SIGNAL QUALITY ESTIMATION
C00823-EP1	EP			05105509.3	ESTIMATION OF ERROR RATES ON UNKNOWN DATA BITS
C00823-EP2	EP	1851895	1851895	06710859.7	ESTIMATION OF ERROR RATES ON UNKNOWN DATA BITS
C00823-GB1	GB	1851895		06710859.7	METHOD AND APPARATUS FOR SIGNAL QUALITY ESTIMATION
C00823-JP1	JP		2008530888	2007-0554715	ESTIMATION OF ERROR RATES ON UNKNOWN DATA BITS
C00823-WO1	WO		2006085275	IB2006050420	ESTIMATION OF ERROR RATES ON UNKNOWN DATA BITS
C00856-CN1	CN	ZL200680034695.9	101356517	20068034695.9	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00856-DE1	DE	1932288		06796012.0	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00856-EP1	EP			05108725.2	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00856-EP2	EP	1932288	1932288	06796012.0	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00856-FR1	FR	1932288		06796012.0	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00856-GB1	GB	1932288		06796012.0	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00856-JP1	JP		2009509226	2008-0530706	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00856-PCT1	WO		2007034368	IB0653249	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00856-TW1	TW		200811661	095134452	MASTER CLOCK HAND-OVER MECHANISM IN AN INTER-IC COMMUNICATION BUS
C00864-CN1	CN		101273589	200680035779.4	TIME REDUCTION OF INITIAL CELL SEARCH IN WB-CDMA (3G)
C00864-EP1	EP			05300784.5	TIME REDUCTION OF INITIAL CELL SEARCH IN WB-CDMA (3G)
C00864-EP2	EP		1932297	06809401.0	TIME REDUCTION OF INITIAL CELL SEARCH IN WB-CDMA (3G)
C00864-JP1	JP	4892694		2008532947	TIME REDUCTION OF INITIAL CELL SEARCH IN WB-CDMA (3G)
C00864-PCT1	WO		2007036869	IB0653478	TIME REDUCTION OF INITIAL CELL SEARCH IN WB-CDMA (3G)
C00877-CN1	CN	ZL200680041518.3	101305356	200680041518.3	FLAG REGISTERS AS AN OPTIMISED SOLUTION FOR DUAL CORE SYNCHRONIZATION
C00877-EP1	EP			05300900.7	FLAG REGISTERS AS AN OPTIMISED SOLUTION FOR DUAL CORE SYNCHRONIZATION
C00877-EP2	EP		1949249	06821324.8	FLAG REGISTERS AS AN OPTIMISED SOLUTION FOR DUAL CORE SYNCHRONIZATION
C00877-IN1	IN		4864DELNP2008	4864DELNP2008	FLAG REGISTERS AS AN OPTIMISED SOLUTION FOR DUAL CORE SYNCHRONIZATION
C00877-JP1	JP	4940436	2009514118T	2008-538485	FLAG REGISTERS AS AN OPTIMISED SOLUTION FOR DUAL CORE SYNCHRONIZATION
C00877-KR1	KR	10-1029392	20080077150	2008-7013591	FLAG REGISTERS AS AN OPTIMISED SOLUTION FOR DUAL CORE SYNCHRONIZATION
C00877-TW1	TW		200811666	095141067	FLAG REGISTERS AS AN OPTIMISED SOLUTION FOR DUAL CORE SYNCHRONIZATION
C00877-WO1	WO		2007054871	IB2006054104	FLAG REGISTERS AS AN OPTIMISED SOLUTION FOR DUAL CORE SYNCHRONIZATION
C00879-CN1	CN	ZL200680043468.2	101313505	20068043468.2	RX CLOCKING BY NOP SIGNALLING VIA A SELF-CLOCKING LINK WITHOUT SENDING DATA OR EXPLICIT CLOCK SIGNAL
C00879-EP1	EP			05111110.2	RX CLOCKING BY NOP SIGNALLING VIA A SELF-CLOCKING LINK WITHOUT SENDING DATA OR EXPLICIT CLOCK SIGNAL

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C00879-EP2	EP		1955470	06821526.8	RX CLOCKING BY NOP SIGNALLING VIA A SELF-CLOCKING LINK WITHOUT SENDING DATA OR EXPLICIT CLOCK SIGNAL
C00879-JP1	JP	4815559	2009516978T	20080541881T	RX CLOCKING BY NOP SIGNALLING VIA A SELF-CLOCKING LINK WITHOUT SENDING DATA OR EXPLICIT CLOCK SIGNAL
C00879-KR1	KR	10-0977934	20080069261	20087014957	RX CLOCKING BY NOP SIGNALLING VIA A SELF-CLOCKING LINK WITHOUT SENDING DATA OR EXPLICIT CLOCK SIGNAL
C00879-WO1	WO		2007060620	IB2006054379	RX CLOCKING BY NOP SIGNALLING VIA A SELF-CLOCKING LINK WITHOUT SENDING DATA OR EXPLICIT CLOCK SIGNAL
C00883-CN1	CN	ZL200680045525.0	101379719	200680045525.0	DETERMINATION OF ACTIVE SPREADING CODES AND THEIR POWER THROUGH FAST WALSH HADAMARD TRANSFORMATION
C00883-EP1	EP			05301010.4	DETERMINATION OF ACTIVE SPREADING CODES AND THEIR POWER THROUGH FAST WALSH HADAMARD TRANSFORMATION
C00883-EP2	EP		1961128	06832082.9	DETERMINATION OF ACTIVE SPREADING CODES AND THEIR POWER THROUGH FAST WALSH HADAMARD TRANSFORMATION
C00883-JP1	JP	4815556	2009518923T	2008-543969	DETERMINATION OF ACTIVE SPREADING CODES AND THEIR POWER THROUGH FAST WALSH HADAMARD TRANSFORMATION
C00883-WO1	WO		2007066283	IB200654595	DETERMINATION OF ACTIVE SPREADING CODES AND THEIR POWER THROUGH FAST WALSH HADAMARD TRANSFORMATION
C00904-CN1	CN	ZL200780004419.2	101379721	200780004419.2	METHOD AND APPARATUS FOR IMPROVING THE SPEED OF CONVERGENCE AND TRACKING PERFORMANCE OF SYMBOL LEVEL
C00904-EP1	EP			06300105.1	METHOD AND APPARATUS FOR IMPROVING THE SPEED OF CONVERGENCE AND TRACKING PERFORMANCE OF SYMBOL LEVEL
C00904-EP2	EP		1985027	07705755.2	METHOD AND APPARATUS FOR IMPROVING THE SPEED OF CONVERGENCE AND TRACKING PERFORMANCE OF SYMBOL LEVEL
C00904-IN1	IN		7442DEL2008	7442DEL2008	METHOD AND APPARATUS FOR IMPROVING THE SPEED OF CONVERGENCE AND TRACKING PERFORMANCE OF SYMBOL LEVEL
C00904-JP1	JP	4822143		2008-552939	METHOD AND APPARATUS FOR IMPROVING THE SPEED OF CONVERGENCE AND TRACKING PERFORMANCE OF SYMBOL LEVEL
C00904-KR1	KR	10-1029413	20080091507	20087021460	METHOD AND APPARATUS FOR IMPROVING THE SPEED OF CONVERGENCE AND TRACKING PERFORMANCE OF SYMBOL LEVEL
C00904-WO1	WO		2007088516	IB2007050330	METHOD AND APPARATUS FOR IMPROVING THE SPEED OF CONVERGENCE AND TRACKING PERFORMANCE OF SYMBOL LEVEL
C00927-TW1	TW		200825705	096114229	PROGRAMMABLE POWER MODE TRANSITION CONTROLLER
C00927-WO1	WO		2007122596	IB2007051540	PROGRAMMABLE POWER MODE TRANSITION CONTROLLER
C00928-EP1	EP			06113305.4	HIGHER UART BAUD RATES GENERATION AT LOWER XTAL OSCILLATOR FREQUENCIES USING OVER SAMPLING
C00928-EP2	EP			07735620.2	HIGHER UART BAUD RATES GENERATION AT LOWER XTAL OSCILLATOR FREQUENCIES USING OVER SAMPLING
C00928-WO1	WO		2007125472	IB2007051495	HIGHER UART BAUD RATES GENERATION AT LOWER XTAL OSCILLATOR FREQUENCIES USING OVER SAMPLING
C00929-CN1	CN			20061077447.4	NOVEL METHOD AND STRUCTURE OF SOFTWARE DEFINED RADIO DEVICE CONFIGURATION
C00929-WO1	WO		2007125459	IB2007051407	NOVEL METHOD AND STRUCTURE OF SOFTWARE DEFINED RADIO DEVICE CONFIGURATION
C00933-DE1	DE	2027539		07735893.5	MEMORY ARCHITECTURE
C00933-EP1	EP			06113975.4	LOW-COST SIMD MEMORY WITH LIMITED SCATTER GATHER
C00933-EP2	EP	2027539	2027539	07735893.5	LOW-COST SIMD MEMORY WITH LIMITED SCATTER GATHER
C00933-WO1	WO		2007135615	IB2007051822	LOW-COST SIMD MEMORY WITH LIMITED SCATTER GATHER
C00973-CN1	CN			200780032473.8	SPACE DIVERSITY RF RECEIVER FOR UTRA-FDD/WCDMA STANDARD
C00973-EP1	EP			06300906.2	SPACE DIVERSITY RF RECEIVER FOR UTRA-FDD/WCDMA STANDARD
C00973-EP2	EP		2057753	07826195.5	SPACE DIVERSITY RF RECEIVER FOR UTRA-FDD/WCDMA STANDARD
C00973-PCT1	WO		2008026176	IB0753478	SPACE DIVERSITY RF RECEIVER FOR UTRA-FDD/WCDMA STANDARD
C00974-DE1	DE	2060081		07826198.9	CORRECTION OF LINEAR FREQUENCY DEPENDENT SIGNAL PATH ERRORS
C00974-EP1	EP			06119949.3	CORRECTION OF LINEAR FREQUENCY DEPENDENT SIGNAL PATH ERRORS
C00974-EP2	EP	2060081	2060081	07826198.9	CORRECTION OF LINEAR FREQUENCY DEPENDENT SIGNAL PATH ERRORS

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C00974-GB	GB	2060081		07826198.9	CORRECTION OF LINEAR FREQUENCY DEPENDENT SIGNAL PATH ERRORS
C00974-WO1	WO		2008026178	IB2007053481	CORRECTION OF LINEAR FREQUENCY DEPENDENT SIGNAL PATH ERRORS
C00978-EP1	EP			06120821.1	IMPROVED TIMING CONCEPT FOR HS/LP OPERATION MODE SWITCHING IN MIPI PHYS
C00978-EP2	EP	2064629	2064629	07826389.4	IMPROVED TIMING CONCEPT FOR HS/LP OPERATION MODE SWITCHING IN MIPI PHYS
C00978-GB1	GB	2064629		07826389.4	IMPROVED TIMING CONCEPT FOR HS/LP OPERATION MODE SWITCHING IN MIPI PHYS
C00978-WO1	WO		2008035262	IB2007053721	IMPROVED TIMING CONCEPT FOR HS/LP OPERATION MODE SWITCHING IN MIPI PHYS
C00986-DE1	DE	2092675		07849092.7	INTEGRATED CIRCUIT TO ENCODE DATA
C00986-EP1	EP			06291775.2	MULTI-STANDARD CHANNEL ENCODER
C00986-EP2	EP	2092675	2092675	07849092.7	MULTI-STANDARD CHANNEL ENCODER
C00986-FR1	FR	2092675		07849092.7	INTEGRATED CIRCUIT TO ENCODE DATA
C00986-GB1	GB	2092675		07849092.7	INTEGRATED CIRCUIT TO ENCODE DATA
C00986-WO1	WO		2008059431	IB200754588	MULTI-STANDARD CHANNEL ENCODER
C01013-CN1	CN		CN101647208A	200880010290.0	OUTER LOOP POWER CONTROL IN UNLOADED CELLS
C01013-DE1	DE	2143211		08719749.7	WIRELESS TRANSMISSION POWER CONTROL METHOD AND SYSTEM
C01013-EP1	EP			07105142.9	OUTER LOOP POWER CONTROL IN UNLOADED CELLS
C01013-EP2	EP	2143211	2143211	08719749.7	OUTER LOOP POWER CONTROL IN UNLOADED CELLS
C01013-FR1	FR	2143211		08719749.7	WIRELESS TRANSMISSION POWER CONTROL METHOD AND SYSTEM
C01013-GB1	GB	2143211		08719749.7	WIRELESS TRANSMISSION POWER CONTROL METHOD AND SYSTEM
C01013-WO1	WO		2008117203	IB2008051019	OUTER LOOP POWER CONTROL IN UNLOADED CELLS
C01019-EP1	EP		2149239	08737859.2	DOWNSAMPLED CHANNEL ESTIMATION FOR LTE
C01019-GB1	GB			0707355.4	DOWNSAMPLED CHANNEL ESTIMATION FOR LTE
C01019-GB2	GB			0725147.3	DOWNSAMPLED CHANNEL ESTIMATION FOR LTE
C01019-WO1	WO		2008126055	IB2008051437	DOWNSAMPLED CHANNEL ESTIMATION FOR LTE
C01025-CN1	CN		CN101802749A	20880101742.6	STRATEGY FOR LOW POWER MANAGEMENT
C01025-CN1	CN			20880101742.6	STRATEGY FOR LOW POWER MANAGEMENT
C01025-EP1	EP			07109505.3	STRATEGY FOR LOW POWER MANAGEMENT
C01025-EP1	EP			07109505.3	STRATEGY FOR LOW POWER MANAGEMENT
C01025-EP2	EP			08763120.6	STRATEGY FOR LOW POWER MANAGEMENT
C01025-EP2	EP			08763120.6	STRATEGY FOR LOW POWER MANAGEMENT
C01025-JP1	JP			2010-510923	STRATEGY FOR LOW POWER MANAGEMENT
C01025-JP1	JP			2010-510923	STRATEGY FOR LOW POWER MANAGEMENT
C01025-WO1	WO			IB2008052078	STRATEGY FOR LOW POWER MANAGEMENT
C01025-WO1	WO			IB2008052078	STRATEGY FOR LOW POWER MANAGEMENT
C01030-CN1	CN		101690308	200880021780.0	IMPROVED POWER CONSUMPTION IN MOBILE PHONE USING TDMA
C01030-EP1	EP			07111141.3	IMPROVED POWER CONSUMPTION IN MOBILE PHONE USING TDMA
C01030-EP2	EP		2160911	08776434.6	IMPROVED POWER CONSUMPTION IN MOBILE PHONE USING TDMA
C01030-WO1	WO		2009001265	IB2008052449	IMPROVED POWER CONSUMPTION IN MOBILE PHONE USING TDMA
C01084-CN1	CN			200980105851.X	HIGHLY EFFICIENT AND FLEXIBLE EARLY STOPPING RULES FOR TURBO CODES/CN
C01084-EP1	EP			08290100.0	HIGHLY EFFICIENT AND FLEXIBLE EARLY STOPPING RULES FOR TURBO CODES
C01084-EP2	EP		2258065	09707145.0	HIGHLY EFFICIENT AND FLEXIBLE EARLY STOPPING RULES FOR TURBO CODES/EP
C01084-WO1	WO		2009098059	EP2009000797	HIGHLY EFFICIENT AND FLEXIBLE EARLY STOPPING RULES FOR TURBO CODES
C01127-DE1	DE	2356784		09745033.2	RECEIVER WITH CHANNEL ESTIMATION CIRCUITRY
C01127-EP1	EP	2356784		09745033.2	RECEIVER WITH CHANNEL ESTIMATION CIRCUITRY
C01127-FR1	FR	2938137	2938137	0857422	FREQUENCY INTERPOLATION FILTER FOR AN OFDM RECEIVER
C01127-GB1	GB	2356784		09745033.2	RECEIVER WITH CHANNEL ESTIMATION CIRCUITRY
C01127-IN1	IN		na	3007DELNP2011	RECEIVER WITH CHANNEL ESTIMATION CIRCUITRY
C01127-JP1	JP			2011-533731	RECEIVER WITH CHANNEL ESTIMATION CIRCUITRY
C01127-WO1	WO		2010049508	EP2009064325	FREQUENCY INTERPOLATION FILTER FOR AN OFDM RECEIVER
C01130-EP1	EP		2184851	08305787.7	OPTIMIZED AGC FOR DC TRANSIENTS REDUCTION
C01130-IN1	IN			3269DELNP2011	METHOD OF STORING DATA IN A MEMORY DEVICE AND A PROCESSING DEVICE FOR PROCESSING SUCH DATA AND CORRE

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C01130-JP1	JP			2011-535107	METHOD OF STORING DATA IN A MEMORY DEVICE AND A PROCESSING DEVICE FOR PROCESSING SUCH DATA AND CORRE
C01130-WO1	WO		2010052265	EP2009064671	OPTIMIZED AGC FOR DC TRANSIENTS REDUCTION
C01144-CN1	CN			200980153336.9	DIGITAL TRANSMITTER FOR WIRELESS APPLICATIONS
C01144-DE1	DE			08254184.8	DIGITAL TRANSMITTER FOR WIRELESS APPLICATIONS
C01144-EP1	EP			08254184.8	DIGITAL TRANSMITTER FOR WIRELESS APPLICATIONS
C01144-FR1	FR			08254184.8	DIGITAL TRANSMITTER FOR WIRELESS APPLICATIONS
C01144-GB1	GB			08254184.8	DIGITAL TRANSMITTER FOR WIRELESS APPLICATIONS
C01144-JP1	JP			2011-544026	DIGITAL TRANSMITTER FOR WIRELESS APPLICATIONS
C01144-WO1	WO			EP2009067812	DIGITAL TRANSMITTER FOR WIRELESS APPLICATIONS
C01146-EP1	EP			08012192	METHOD FOR SHORT-TIME OFDM TRANSMISSION AND APPARATUS FOR PERFORMING FLEXIBLE OFDM MODULATION MULTICARRIER ALPHA-OFDM
C01146-EP2	EP			08368023.1	METHOD FOR SHORT-TIME OFDM TRANSMISSION AND APPARATUS FOR PERFORMING FLEXIBLE OFDM MODULATION MULTICARRIER ALPHA-OFDM
C01146-EP3	EP		2314036	09793878.1	METHOD FOR SHORT-TIME OFDM TRANSMISSION AND APPARATUS FOR PERFORMING FLEXIBLE OFDM MODULATION MULTICARRIER ALPHA-OFDM
C01146-JP1	JP			2011-515245	METHOD FOR SHORT-TIME OFDM TRANSMISSION AND APPARATUS FOR PERFORMING FLEXIBLE OFDM MODULATION MULTICARRIER ALPHA-OFDM
C01146-WO1	WO		2010003597	EP2009004836	METHOD FOR SHORT-TIME OFDM TRANSMISSION AND APPARATUS FOR PERFORMING FLEXIBLE OFDM MODULATION MULTICARRIER ALPHA-OFDM
C01147-EP1	EP		2204930	08368024.9	SERIAL DIGITAL INTERFACE EMI REDUCTION TECHNIQUE BY FREQUENCY PLANNING & SLEW RATE CONTROL
C01147-IN1	IN			5693DELNP2011	DIGITAL INTERFACE BETWEEN A RF AND BASEBAND CIRCUIT AND PROCESS FOR CONTROLLING SUCH INTERFACE
C01147-JP1	JP			2011-543993	DIGITAL INTERFACE BETWEEN A RF AND BASEBAND CIRCUIT AND PROCESS FOR CONTROLLING SUCH INTERFACE
C01147-WO1	WO		2010076021	EP2009009311	SERIAL DIGITAL INTERFACE EMI REDUCTION TECHNIQUE BY FREQUENCY PLANNING & SLEW RATE CONTROL
C01148-CN1	CN		102301667A	200980155663.8	PROCESS FOR BLIND DETECTION OF A SYNCHRONIZATION SIGNAL FOR LTE
C01148-EP1	EP		2204957	08368025.6	SSC BLIND DETECTION FOR LTE STANDARD
C01148-WO1	WO		2010076022	EP2009009312	SSC BLIND DETECTION FOR LTE STANDARD
C01167-DE1	DE			10706235.8	WIRELESS COMMUNICATING APPARATUS PROCESSING UNIT CONTROL AND CORRESPONDING WIRELESS COMMUNICATING AP
C01167-EP1	EP		2404401	10706235.8	WIRELESS COMMUNICATING APPARATUS PROCESSING UNIT CONTROL AND CORRESPONDING WIRELESS COMMUNICATING AP
C01167-FR1	FR	0951385	2942932	0951385	HSDPA MIPS LIMITER
C01167-GB1	GB			10706235.8	WIRELESS COMMUNICATING APPARATUS PROCESSING UNIT CONTROL AND CORRESPONDING WIRELESS COMMUNICATING AP
C01167-IN1	IN			5627DELNP2011	WIRELESS COMMUNICATING APPARATUS PROCESSING UNIT CONTROL AND CORRESPONDING WIRELESS COMMUNICATING AP
C01167-WO1	WO		2010100140	EP2010052611	WIRELESS COMMUNICATING APPARATUS PROCESSING UNIT CONTROL AND CORRESPONDING WIRELESS COMMUNICATING AP/WO
C01178-CN1	CN		CN102461132A	201080026348.8	NOISE SUPPRESSION
C01178-EP1	EP		2242185	09305321.3	NOISE SUPPRESSOR HAVING BURST NOISE CANCELLATION CAPABILITIES
C01178-IN1	IN			7900DELNP2011	NOISE SUPPRESSION
C01178-WO1	WO		2010119074	EP2010054905	NOISE SUPPRESSOR HAVING BURST NOISE CANCELLATION CAPABILITIES
C01206-EP1	EP		2438684	10724479.0	MANAGEMENT OF RESSOURCES OF COMMUNICATION TERMINALS
C01206-FR1	FR	0953638	2946217	0953638	TIME DOMAIN ISOLATION IMPROVED USAGE
C01206-KR1	KR			2011-7028961	MANAGEMENT OF RESSOURCES OF COMMUNICATION TERMINALS
C01206-WO1	WO		2010139745	EP2010057742	TIME DOMAIN ISOLATION IMPROVED USAGE
C01217-EP1	EP		2443737	10725432.8	FREQUENCY MULTIBAND OSCILLATOR SYSTEM
C01217-FR1	FR	0954178	2947118	0954178	COUPLED-INDUCTOR, MULTI-CORE, MULTI-BAND LC OSCILLATOR.
C01217-IN1	IN			443DELNP2012	FREQUENCY MULTIBAND OSCILLATOR SYSTEM
C01217-WO1	WO		2010146038	EP2010058353	COUPLED-INDUCTOR, MULTI-CORE, MULTI-BAND LC OSCILLATOR
C01228-CN1	CN		CN102597900A	201080036749.1	LOW DROP OUT REGULATOR
C01228-EP1	EP		2454643	10732977.3	LOW DROP OUT REGULATOR
C01228-FR1	FR			0954924	LDO REGULATOR - SHORT CIRCUIT CURRENT LIMITATION WITH IMPROVED ACCURACY AND LOW CONSUMPTION
C01228-WO1	WO			EP2010060263	LOW DROP OUT REGULATOR
C01262-EP1	EP		2486651	10788409.0	PULSE WIDTH MODULATION FOR SWITCHING AMPLIFIERS

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C01262-IN1	IN			2100DEL2009	PULSE WIDTH MODULATION FOR A SWITCHING AMPLIFIER
C01262-JP1	JP			2012-532720	PULSE WIDTH MODULATION FOR SWITCHING AMPLIFIERS
C01262-WO1	WO		2011042919	IN2010000668	PULSE WIDTH MODULATION FOR SWITCHING AMPLIFIERS
C01269-EP1	EP		2315404	09368041.1	MAXIMUM-LIKELIHOOD ALAMOUTI BLOCK-CODES DETECTION IN HIGHLY SELECTIVE CHANNELS USING LATTICE REDUCTION FOR OFDM
C01269-JP1	JP			2012-534585	PROCESS FOR DECODING ALAMOUTI BLOCK CODE IN AN OFDM SYSTEM, AND RECEIVER FOR THE SAME
C01269-WO1	WO		2011050935	EP2010006508	PROCESS FOR DECODING ALAMOUTI BLOCK CODE IN AN OFDM SYSTEM, AND RECEIVER FOR THE SAME/WO
C01289-EP1	EP		2333971	09306193.5	ACCURATE 3G SERVING CELL BAND DETECTION BASED ON INCREMENTAL FFT ZOOM (ON STORED SIGNAL RECEIVED AT UE)
C01289-JP1	JP			2012-542455	TO PERFORM RAW HFO ESTIMATE
C01289-WO1	WO		2011069867	EP2010068634	METHOD AND DEVICE FOR IDENTIFYING AT LEAST ONE COMMUNICATION CHANNEL WITH AN INCIDENT SIGNAL
C02008-CN1	CN		CN102460980A	201080032368.6	METHOD AND DEVICE FOR IDENTIFYING AT LEAST ONE COMMUNICATION CHANNEL WITH AN INCIDENT SIGNAL
C02008-DE1	DE			10726013.5	Phase Compensation without Trigonometric Involvement
C02008-EP1	EP		2436121	10726013.5	Phase Compensation without Trigonometric Involvement
C02008-GB1	GB			10726013.5	Phase Compensation without Trigonometric Involvement
C02008-WO1	WO		2010136390	EP2010057020	Phase Compensation without Trigonometric Involvement
C02013-CN1	CN		102668382A	201080052719.X	VCO with Wide Tuning Range
C02013-EP1	EP		2326010	09176449.8	VCO with Wide Tuning Range
C02013-WO1	WO		2011061293	EP2010067832	VCO with Wide Tuning Range
C02017-BR1	BR			1120120064932	Flexible Power Supply Startup Sequence
C02017-CN1	CN		CN102576242	201080042750.5	Flexible Power Supply Startup Sequence
C02017-DE1	DE	2312418		09171083.0	Flexible Power Supply Startup Sequence
C02017-EP1	EP	2312418	2312418	09171083.0	Flexible Power Supply Startup Sequence
C02017-ES1	ES	2312418		09171083.0	Flexible Power Supply Startup Sequence
C02017-GB1	GB	2312418		09171083.0	Flexible Power Supply Startup Sequence
C02017-IN1	IN			1908DELNP2012	Flexible Power Supply Startup Sequence
C02017-JP1	JP			2012-530265	Flexible Power Supply Startup Sequence
C02017-KR1	KR			2012-7010260	Flexible Power Supply Startup Sequence
C02017-NL1	NL	2312418		09171083.0	Flexible Power Supply Startup Sequence
C02017-RU1	RU			2012116065	Flexible Power Supply Startup Sequence
C02017-WO1	WO		2011036227	EP2010064084	Flexible Power Supply Startup Sequence
C02020-EP1	EP		2491647	09749000.7	Bootstrapped Passive Mixer
C02020-IN1	IN			4259DELNP2012	Bootstrapped Passive Mixer
C02020-JP1	JP			2012-534545	Bootstrapped Passive Mixer
C02020-WO1	WO		2011047703	EP2009007609	Bootstrapped Passive Mixer
C02038-EP1	EP		2341676	09180937.6	Area Reduction using a Branch Processor Approach...
C02038-EP2	EP		2520055	10807440.2	Area Reduction using a Branch Processor Approach...
C02038-IN1	IN			6482DELNP2012	Area Reduction using a Branch Processor Approach...
C02038-WO1	WO		2011080326	EP2010070946	Area Reduction using a Branch Processor Approach...
C02052-CN1	CN			EP2011068278	Opportunistic comma code matching in an encoded Phy
C02052-EP1	EP			11771155.6	Opportunistic comma code matching in an encoded Phy
C02052-KR1	KR			2013-7012801	Opportunistic comma code matching in an encoded Phy
C02052-WO1	WO			EP2011068278	Opportunistic comma code matching in an encoded Phy
C02152-CN1	CN			EP2011068204	PROTOCOL TO DETECT AND COMMUNICATE SYNCHRONIZATION LOSS
C02152-EP1	EP			11773245.3	PROTOCOL TO DETECT AND COMMUNICATE SYNCHRONIZATION LOSS
C02152-IN1	IN			3749DELNP2013.	PROTOCOL TO DETECT AND COMMUNICATE SYNCHRONIZATION LOSS
C02152-WO1	WO			EP2011068204	PROTOCOL TO DETECT AND COMMUNICATE SYNCHRONIZATION LOSS
C02181-CN1	CN			EP2011073098	Vector-based matcher of address and data streams
C02181-EP1	EP			10195792.6	Vector-based matcher of address and data streams
C02181-WO1	WO			EP2011073098	Vector-based matcher of address and data streams
C02219-EP1	EP			10164182.7	UniPro error handling for errors detected in-between
C02219-WO1	WO			EP2011058763	UniPro error handling for errors detected in-between
C02255-WO1	WO		2012022785	EP2011064232	Method for Minimizing Speech Delay (2)
C02283-CN1	CN			EP2011067192	DutyCycle adjustment to improve efficiency of a digital RF-PA
C02283-EP1	EP			EP2011067192	DutyCycle adjustment to improve efficiency of a digital RF-PA

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C02283-WO1	WO		2012042048	EP2011067192	DutyCycle adjustment to improve efficiency of a digital RF-PA
C02302-WO1	WO			EP2012069657	An SIMD memory to support up and downsampling and transposition
C02305-EP1	EP			EP2011066621	PA load-line tuning with capacitors around a balun
C02305-WO1	WO		2012038545	EP2011066621	PA load-line tuning with capacitors around a balun
C02308-CN1	CN			EP2011066769	RF Divider using Direct Digital Synthesis
C02308-EP1	EP			EP2011066769	RF Divider using Direct Digital Synthesis
C02308-WO1	WO		2012041852	EP2011066769	RF Divider using Direct Digital Synthesis
C02310-CN1	CN			EP2011067191	Current saving by reduced modulation code and disabled clock buffers
C02310-EP1	EP			EP2011067191	Current saving by reduced modulation code and disabled clock buffers
C02310-WO1	WO		2012042047	EP2011067191	Current saving by reduced modulation code and disabled clock buffers
C02319-EP1	EP			EP2011066619	Calibrate output matching for correct output power
C02319-WO1	WO		2012038544	EP2011066619	Calibrate output matching for correct output power
C02382-WO1	WO			EP2012060819	High Output Power Digital TX
C02388-WO1	WO			EP2012069644	Method for the static assignment of discrete frequency/power operating points to tasks of a real-time application running on a DVFS-enabled multi....
C02389-EP1	EP			11185934.4	Improved scalar distribution in an SIMD system
C02389-WO1	WO			EP2012069589	Improved scalar distribution in an SIMD system
C02394-EP1	EP			11174261.5	Simplified Preprocessing for the Sphere Decoder..
C02394-WO1	WO			EP2012062530	Simplified Preprocessing for the Sphere Decoder..
C02397-EP1	EP			11174969.3	Power efficient branch prediction
C02397-WO1	WO			EP2012063867	Power efficient branch prediction
C02398-WO1	WO			EP2012068588	Dynamic Power Scaling of Covariance Computations
C02417-WO1	WO			EP2012058728	Hardware Controlled Switching Between DVFS Operating Points
C02482-EP1	EP			11290315.8	Method to ensure reliable data exchange closing between two chips
C02482-WO1	WO			EP2012063295	Method to ensure reliable data exchange closing between two chips
C02494-WO1	WO			EP2012060622	A Generic Multi-RAT L1 SW Architecture
C02495-WO1	WO			EP2012060623	Flexible Distributed Sequencer
C02496-WO1	WO			EP2012058734	Radio Planner
C02604-CN1	CN			200610089204.2	a method and device of channel estimation for intra cells
C02608-CN1	CN			200910078481.7	A method and device of closed loop power control
C02629-CN1	CN			200710062964.9	A method and device of SYNC-DL detection for intra-frequency cells in TD-SCDMA system
C02635-CN1	CN			200810227092.1	A method and device use in wireless telecom of frequency offset estimation
C02717-CN1	CN			200710063018.6	A UE and its corresponding processing method
C02725-CN1	CN			200710098641.5	an equalization-based receiver and technique
C02774-CN1	CN			200610093545.7	the initial cell search method in TD-SCDMA when intra-frequency deployment network
C02794-EP1	EP			11188973.9	LTE transmitter with reduced spurious emissions
C02794-WO1	WO			EP2012072558	LTE transmitter with reduced spurious emissions
C02807-EP1	EP			12154767.3	Signed polar modulator
C02807-WO1	WO			EP2013052542	Signed polar modulator
C02819-WO1	WO			EP2012064032	High Efficiency Injection-Locked Power Amplifier
C02830-WO1	WO			CN2011080775	A Method and Device for TD-SCDMA of Combat Strong Interference Joint Detection
C02836-WO1	WO			CN2011080279	A Method and Device for TD-SCDMA Joint Detection
C02935-EP1	EP			11306437.2	Receiver optimization for non contiguous carrier aggregation
C02935-EP2	EP			12175794.2	Receiver optimization for non contiguous carrier aggregation
C02935-WO1	WO			EP2012071603	Receiver optimization for non contiguous carrier aggregation
C02976-CN1	CN			ZL200810057421.2	Frequency deviation estimation method and device
C02987-WO1	WO			CN2012077751	One Method for Soft Bit Quantization before Turbo Decoder for 64QAM Wireless System
C02998-EP1	EP			12162984.4	Dual VCO
C02998-WO1	WO			EP2013056883	Dual VCO
C03021-EP1	EP			12174386.8	Dynamic List based Sphere Decoder for Turbo Equalization
C03021-WO1	WO			na	Dynamic List based Sphere Decoder for Turbo Equalization
C03028-EP1	EP			EP12171963.7	System for vector element selection

Appendix A - Patents from STE to Ericsson

Reference	Country	Registration No	PublicationNo	Application No	Title
C03028-WO1	WO			EP2013061882	System for vector element selection
C03032-EP1	EP			12156646.7	SIP B2BUA and Proxy on Modem
C03032-WO1	WO			EP2013053372	SIP B2BUA and Proxy on Modem
C03075-WO1	WO			EP2013057258	Uplink transmit diversity UE architecture optimization
C03194-WO1	WO			EP2013061865	Protection of SIP B2BUA on Modem
C03237-EP1	EP			EP13153568.4	Connecting auxiliary battery charger IC to mobile chipset
C03239-EP1	EP			13170348.0	Synchronizing Power Management ICs
C03272-EP1	EP			13169170.1	Sphere Decoder based Turbo Equalizer – symbol detection under a-priori constraints
C03288-EP1	EP			13165924.5	Semi-polar modulator
C03310-CN1	CN			201310125190.5	A Method to Improve HSPA Throughput in TD-SCDMA
C03344-EP1	EP			na	Calculating P scaling values with P/2 circuits for real-complex multiplication
C03350-EP1	EP			13172302.5	Multi-tree Search Based Soft-input Sphere Decoder for Turbo-equalization with a novel forward traversal
C03401-EP1	EP			na	Envelope tracking power supply



REGISTRE DU COMMERCE DE GENÈVE

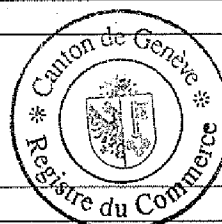
Extrait avec éventuelles radiations

EXTRAIT DU REGISTRE
No réf. 02166/2009
N° féd. CH-660-0364009-6

Ericsson Modems SA

inscrite le 12 février 2009
Société anonyme

Réf.	Raison Sociale
1	ST-Ericsson AT SA- (ST-Ericsson-AT Ltd)
13	Ericsson Modems SA (Ericsson Modems Ltd)
Siège	
1	Plan-les-Ouates
Adresse	
1	chemin du Champ-des-Filles 39, 1228 Plan-les-Ouates
Dates des Statuts	
1	02.02.2009
4	29.06.2010
5	27.10.2010 (nouv. stat.)
13	12.09.2013
But, Observations	
1	But: développement et/ou vente de logiciels et de circuits intégrés basés sur les semi-conducteurs (IC) et destinés en priorité aux composants de communication cellulaire sans fil ainsi qu'aux services y relatifs (cf. statuts pour but complet).
4	Opting-out: Selon déclaration du conseil d'administration du 29.06.2010, la société n'est pas soumise à un contrôle ordinaire et renonce à un contrôle restreint.
5	But: développement et/ou vente de logiciels et de circuits intégrés basés sur les semi-conducteurs (IC) et destinés en priorité aux composants de communication cellulaire sans fil ainsi qu'aux services y relatifs (cf. statuts pour but complet).
Fusions (LFus)	
5	Fusion: - reprise des actifs et passifs de ST-Ericsson AT Holding AG, à Zurich (CH-020-3033454-6), selon contrat de fusion du 19.10.2010 et bilan au 29.05.2010, présentant des actifs de CHF 27'085'309, parmi lesquels sont contenues toutes les actions de la société reprenante, des passifs envers les tiers de CHF 208'335, soit un actif net de CHF 26'876'974. La fusion ne donne pas lieu à une augmentation du capital, étant donné que les actionnaires de la société transférante reçoivent les actions propres de la société reprenante, acquises lors de la fusion. Conformément à l'attestation d'un expert-réviseur agréé, des créances du montant du découvert et du surendettement ont été postposées.
Organe de publication	
1	Communication aux actionnaires: lettre
1	Feuille Officielle Suisse du Commerce



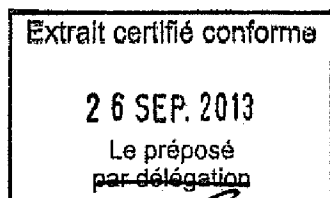
Réf.	Capital-actions		
	Nominal	Libéré	Actions
1	CHF 100'000	CHF 100'000	100 actions de CHF 1'000, nominatives

Réf.			Administration, organe de révision et personnes ayant qualité pour signer		
Inscr.	Mod.	Rad.	Nom et Prénoms, Origine, Domicile	Fonctions	Mode Signature
1		3	Dutheil Alain, de France, à Genève	adm. président	signature collective à 2
1		5	Lucie Smith Timothy, de Grande Bretagne, à Genève	adm.	signature collective à 2
1		4	PricewaterhouseCoopers SA (CH-660-1784998-4), succursale à Genève	organe de révision	
2		5	Champseix Jean-Louis, de France, à Saint-Julien-en-Genevois, F	adm.	signature collective à 2
2		3	Puskari Robert, de Suède, à Lund, SWE	adm.	signature collective à 2
3	m	5	Delfassy Gilles, de France, à Annecy-le-Vieux, F	adm. président	signature individuelle
3		5	Cetto Marc, de France, à Veyrier	adm.	signature collective à 2
5		12	Vestberg Hans Erik, de Suède, à Stocksund, SWE	adm. président	signature collective à 2
5		12	Bozotti Carlo, d'Italie, à Genève	adm. vice-président	signature collective à 2
5		6	Dutheil Alain, de France, à Genève	adm.	signature collective à 2
5		9	Ferro Carlo, de Capriasca, à Divonne les Bains, F	adm.	signature collective à 2
5		12	Frykhammar Jan, de Suède, à Djursholm, SWE	adm.	signature collective à 2
5		6	Lietar Loic, de France, à Genève	adm.	signature collective à 2
5		12	Wäreby Jan Erik, de Suède, à Stockholm, SWE	adm.	signature collective à 2
5		12	Wibergh Johan, de Suède, à Sollentuna, SWE	adm.	signature collective à 2
5	5	8	Delfassy Gilles, de France, à Annecy-le-Vieux, F	directeur général	signature individuelle
6		10	Lambinet Philippe, de France, à Cologne	adm.	signature collective à 2
6	m	8	Lamouche Didier, de France, à Veyrier	adm.	signature collective à 2
7			PricewaterhouseCoopers SA (CH-660-1784998-4), succursale à Genève	organe de révision	
8		12	Chery Jean-Marc, de France, à Le Tholonet, F	adm.	signature collective à 2
8	8	11	Lamouche Didier, de France, à Veyrier	directeur général	signature individuelle
9		12	Grandi Lorenzo, d'Italie, à Cessy, F	adm.	signature collective à 2
10		12	Penalver Georges, de France, à Maisons Laffitte, F	adm.	signature collective à 2
11		12	Ferro Carlo, de Capriasca, à Divonne les Bains, F	directeur	signature individuelle
12			Norin Mats, de Suède, à Sollentuna, S	adm. président	signature individuelle
12			Oscarsson Per, de Suède, à Segeltorp, S	adm. vice-président	signature collective à 2
12			Barbazza Michele, d'Italie, à Niederlenz	adm.	signature collective à 2
12			Ionita Gheorghe, de Flühli, à Baar	adm.	signature collective à 2

Réf.	JOURNAL		PUBLICATION FOSC		Réf.	JOURNAL		PUBLICATION FOSC	
	Numéro	Date	Date	Page/Id		Numéro	Date	Date	Page/Id
1	2166	12.02.2009	18.02.2009	9/4887302	2	3270	02.03.2009	06.03.2009	6
3	19061	07.12.2009	11.12.2009	10/5389234	4	12424	22.07.2010	28.07.2010	9/5748984
5	20403	07.12.2010	13.12.2010	10/5936754	6	3089	16.02.2011	22.02.2011	9/6044994
7	12311	26.07.2011	29.07.2011	0/6278668	8	1053	18.01.2012	23.01.2012	0/6515828
9	7495	10.05.2012	15.05.2012	0/6679018	10	18824	08.11.2012	13.11.2012	0/6930006
11	6642	17.04.2013	22.04.2013	0/7157852	12	15179	11.09.2013	16.09.2013	0/1078587
13	15962	25.09.2013							

Inscription non encore publiée mais approuvée par l'office fédéral du registre du commerce (art. 32 ORC)

Genève, le 26 septembre 2013



Fin de l'extrait

Seul un extrait certifié conforme, signé et muni du sceau du registre, a une valeur légale.