

## PATENT ASSIGNMENT COVER SHEET

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<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT	
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT	
<b>CONVEYING PARTY DATA</b>		
<b>Name</b>		<b>Execution Date</b>
NUMONYX B.V.		05/23/2012
<b>RECEIVING PARTY DATA</b>		
<b>Name:</b>	Micron Technology, Inc.	
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<b>City:</b>	Boise	
<b>State/Country:</b>	IDAHO	
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<b>PROPERTY NUMBERS Total: 1</b>		
<b>Property Type</b>	<b>Number</b>	
<b>Application Number:</b>	14034275	
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<b>DATE SIGNED:</b>	07/10/2014	
<b>Total Attachments: 50</b>		
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## PATENT ASSIGNMENT AGREEMENT

**WHEREAS**, Numonyx B.V., a company organized under the laws of Netherlands, having a place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte. De l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignor") has rights in and to the following:

1. U.S. Letters Patents Listed on Exhibit A,
2. U.S. Patent Applications listed on Exhibit B,
3. Foreign patents and patent applications listed on Exhibit C.

(said patents and patent applications listed in Exhibits A, B, and C being hereinafter referred to as "SAID PATENT RIGHTS"); and

**WHEREAS**, Micron Technology, Inc. is a corporation having a principal place of business at 8000 S. Federal Way, Boise, Idaho 83707, United States (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

**NOW, THEREFORE**, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuation-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

**IN WITNESS WHEREOF**, Assignor has caused this Assignment to be executed by its duly authorized officer effective as of the 23 day of May, 2012

Numonyx B.V.

By: Thomas L. Lewis Jr.  
Printed Name: Thomas L. Lewis Jr.  
Title: Director

Micron Technology, Inc.

By: Roderic W. Lewis  
Printed Name: Roderic W. Lewis  
Title: V.P. of Legal Affairs

L.S.

STMicroelectronics originated

**PATENT**  
**REEL: 033282 FRAME: 0339**

**Exhibit A - US Issued Patents**

Method	Device Type	Material	Series	The	Field No.	Serial No.	Issue Date	Patent No.	Country
NMXX-0001.00/RE	Utility - REIS	Expired	METHOD TO INSTALL AN ELECTRONIC COMPONENT AND ITS ELECTRICAL CONNECTIONS ON A SUPPORT, AND PRODUCT OBTAINED THEREBY		12/12/1988	08/198,208	3/20/1990	RE35,578	US
NMXX-0001.00/US	Utility - ORG	Expired	METHOD TO INSTALL AN ELECTRONIC COMPONENT AND ITS ELECTRICAL CONNECTIONS ON A SUPPORT, AND PRODUCT OBTAINED THEREBY		12/12/1988	07/283,214	3/20/1990	4,908,937	US
NMXX-0002.00/US	Utility - ORG	Abandoned	METHOD FOR FIXING AN ELECTRONIC COMPONENT AND ITS CONTACTS TO A SUPPORT		12/12/1988	07/283,305	7/17/1990	4,941,257	US
NMXX-0002.01/RE	Utility - REIS	Expired	METHOD FOR FIXING AN ELECTRONIC COMPONENT AND ITS CONTACTS TO A SUPPORT		11/9/1994	08/336,560	7/17/1990	RE35,385	US
NMXX-0003.00/US	Utility - ORG	Abandoned	ELECTRICALLY PROGRAMMABLE MEMORY WITH SEVERAL INFORMATION BITS PER CELL		4/24/1989	07/342,476	10/16/1990	4,964,079	US
NMXX-0004.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY DEVICE WITH BALANCING CIRCUIT INCLUDING FOLLOWER AMPLIFIER COUPLED TO BIT LINE		7/23/1993	08/096,684	12/3/1996	5,581,511	US
NMXX-0004.01/US	Utility - CON	Issued	INTEGRATED CIRCUIT MEMORY DEVICE WITH BALANCING CIRCUIT INCLUDING FOLLOWER AMPLIFIER COUPLED TO BIT LINE		6/7/1995	08/486,363	6/10/1997	5,638,332	US
NMXX-0004.02/US	Utility - DIV	Issued	INTEGRATED CIRCUIT MEMORY DEVICE WITH BALANCING CIRCUIT INCLUDING FOLLOWER AMPLIFIER COUPLED TO BIT LINE		6/7/1995	08/478,463	8/6/1996	5,544,114	US
NMXX-0005.00/US	Utility - ORG	Issued	DEVICE FOR THE PROTECTION OF AN INTEGRATED CIRCUIT AGAINST POWER SUPPLY CUTS		8/2/1993	08/100,867	12/5/1995	5,473,496	US
NMXX-0006.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER CIRCUIT OF THE CHARGE-PUMP TYPE WITH BOOTSTRAPPED OSCILLATOR		9/29/1993	08/128,871	12/31/1996	5,589,793	US
NMXX-0006.01/US	Utility - DIV	Issued	VOLTAGE BOOSTER CIRCUIT OF THE CHARGE-PUMP TYPE WITH A BOOTSTRAPPED OSCILLATOR		6/6/1995	08/467,297	1/7/1997	5,592,115	US
NMXX-0007.01/US	Utility - CON	Issued	POWER-ON-RESET CIRCUIT PROVIDING PROTECTION AGAINST POWER SUPPLY INTERRUPTIONS		8/8/1997	08/908,583	9/12/2000	6,118,315	US
NMXX-0008.00/US	Utility - ORG	Expired	SAFETY DEVICE FOR ELECTRICALLY PROGRAMMABLE READ-ONLY MEMORY		5/21/1987	07/052,068	3/7/1989	4,811,291	US
NMXX-0009.00/US	Utility - ORG	Expired	NON-VOLATILE SHADOW STORAGE CELL WITH REDUCED TUNNEL DEVICE COUNT FOR IMPROVED RELIABILITY		8/3/1987	07/081,032	1/31/1989	4,802,124	US
NMXX-0010.00/US	Utility - ORG	Expired	NON-VOLATILE SHADOW STORAGE CELL WITH IMPROVED LEVEL SHIFTING CIRCUIT AND REDUCED TUNNEL DEVICE COUNT FOR IMPROVED RELIABILITY		8/3/1987	07/081,037	11/22/1988	4,787,066	US
NMXX-0011.03/US	Utility - CON	Issued	CONTENT ADDRESSABLE MEMORY		6/7/1995	08/478,429	4/16/2002	6,313,737	US
NMXX-0012.00/US	Utility - ORG	Expired	REDUNDANCY FOR SERIAL MEMORY		1/12/1990	07/464,219	4/2/1991	5,005,158	US
NMXX-0013.00/RE	Utility - REIS	Expired	METHODS FOR FABRICATING MEMORY CELLS AND LOAD ELEMENTS		9/29/1994	08/316,035	9/29/1992	RE37,769	US
NMXX-0013.00/US	Utility - ORG	Abandoned	POLYCRYSTALLINE SILICON CONTACT STRUCTURE		4/30/1990	07/516,272	9/29/1992	5,151,387	US
NMXX-0013.01/US	Utility - DIV	Expired	POLYCRYSTALLINE SILICON CONTACT STRUCTURE		2/28/1992	07/843,818	1/18/1994	5,279,887	US
NMXX-0014.00/US	Utility - CIP	Expired	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE		12/21/1989	07/454,408	6/9/1992	5,121,346	US
NMXX-0014.03/US	Utility - CON	Issued	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE		4/1/1994	08/221,986	3/26/1996	5,502,655	US
NMXX-0015.00/US	Utility - ORG	Expired	PIPELINED CIRCUITRY FOR ALLOWING THE COMPARISON OF THE RELATIVE DIFFERENCE BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE		10/18/1990	07/600,201	4/27/1993	5,206,817	US
NMXX-0016.00/US	Utility - ORG	Expired	TEST CIRCUIT FOR NON-VOLATILE STORAGE CELL		9/29/1989	07/414,775	7/9/1991	5,031,152	US
NMXX-0017.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH A CLOCKED ACCESS CODE FOR TEST MODE ENTRY		8/17/1990	07/569,009	12/10/1991	5,072,137	US
NMXX-0018.00/US	Utility - ORG	Expired	DUAL STATE MEMORY STORAGE CELL WITH IMPROVED DATA TRANSFER CIRCUITRY		2/26/1991	07/661,555	10/1/1991	5,053,996	US
NMXX-0019.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH IMPROVED TEST MODE		7/13/1990	07/552,567	11/23/1993	5,265,100	US
NMXX-0019.01/US	Utility - DIV	Expired	SEMICONDUCTOR MEMORY WITH IMPROVED TEST MODE		6/2/1993	08/071,448	5/10/1994	5,311,473	US
NMXX-0020.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH MULTIPLE CLOCKING FOR TEST MODE ENTRY		8/17/1990	07/568,968	11/3/1992	5,161,159	US

**Exhibit A - US Issued Patents**

Part ID	Material	Status	Description	File Path	Serial No.	Issue Date	Part No.	Country
NMXX-0021.01/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY WITH INHIBITED TEST MODE ENTRY DURING POWER-UP	11/20/1992	07/984,233	4/18/1995	5,408,435	US
NMXX-0021.02/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY WITH MULTIPLE CLOCKING FOR TEST MODE ENTRY	4/18/1995	08/424,722	1/6/1998	5,705,232	US
NMXX-0022.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY HAVING LATCHED REPEATERS FOR MEMORY ROW LINE SELECTION	9/26/1990	07/588,601	6/2/1992	5,110,340	US
NMXX-0023.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY HAVING IMPROVED LATCHED REPEATERS FOR MEMORY ROW LINE SELECTION	9/26/1990	07/588,577	7/7/1992	5,128,897	US
NMXX-0024.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH SEQUENTIAL CLOCKED ACCESS CODES FOR TEST MODE ENTRY	8/17/1990	07/570,203	12/10/1991	5,072,138	US
NMXX-0025.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT HAVING MULTIPLE DATA OUTPUTS SHARING A RESISTOR NETWORK	10/22/1990	07/601,288	3/24/1992	5,099,148	US
NMXX-0026.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH POWER-ON RESET CONTROLLED LATCHED ROW LINE REPEATERS	9/26/1990	07/588,609	6/9/1992	5,121,358	US
NMXX-0026.02/US	Utility - CON	Issued	SEMICONDUCTOR MEMORY WITH POWER-ON RESET CONTROLLED LATCHED ROW LINE REPEATERS	1/19/1995	08/376,127	6/11/1996	5,526,318	US
NMXX-0027.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH SEQUENCED LATCHED ROW LINE REPEATERS	9/26/1990	07/588,600	6/23/1992	5,124,951	US
NMXX-0028.00/US	Utility - ORG	Expired	ADDRESS BUFFER CIRCUIT WITH TRANSITION-BASED LATCHING	10/22/1990	07/601,287	6/23/1992	5,124,584	US
NMXX-0029.00/US	Utility - ORG	Expired	VEHICLE SEAT ASSEMBLY WITH INTEGRATED CHILD SEAT	12/14/1990	07/827,403	11/23/1993	5,265,054	US
NMXX-0030.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH COLUMN DECODED BIT LINE EQUILIBRATE	12/13/1990	07/627,050	3/22/1994	5,297,090	US
NMXX-0031.00/US	Utility - ORG	Expired	SEMICONDUCTOR MEMORY WITH SEPARATE TIME-OUT CONTROL FOR READ AND WRITE OPERATIONS	12/14/1990	07/627,236	11/2/1993	5,258,952	US
NMXX-0032.01/US	Utility - CON	Expired	SEMICONDUCTOR MEMORY WITH MULTIPLEXED REDUNDANCY	8/2/1993	08/101,347	10/11/1994	5,355,340	US
NMXX-0033.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT MEMORY DEVICE HAVING FLASH CLEAR	7/17/1991	07/731,803	5/10/1994	5,311,477	US
NMXX-0034.00/US	Utility - ORG	Expired	CONTROL CIRCUIT FOR RESETTING A SNOOP VALID BIT IN A DUAL PORT CACHE TAG MEMORY	5/1/1991	07/694,451	6/7/1994	5,319,768	US
NMXX-0035.00/US	Utility - ORG	Expired	CONTROL CIRCUIT FOR DUAL PORT MEMORY	4/30/1991	07/693,670	6/27/1995	5,428,652	US
NMXX-0036.00/US	Utility - ORG	Expired	CACHE TAG PARITY DETECT CIRCUIT	3/29/1991	07/677,313	8/16/1994	5,339,322	US
NMXX-0037.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT MEMORY DEVICE WITH REDUNDANT ROWS	7/17/1991	07/731,487	3/22/1994	5,297,084	US
NMXX-0038.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT DUAL-PART MEMORY DEVICE HAVING REDUCED CAPACTANCE	7/17/1991	07/731,802	2/15/1994	5,287,322	US
NMXX-0039.01/US	Utility - CON	Expired	SIGNAL LINE PULSE ENHANCING CIRCUIT FOR INTEGRATED CIRCUITS	8/20/1993	08/109,697	6/14/1994	5,321,314	US
NMXX-0040.00/US	Utility - ORG	Issued	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	11/30/1992	07/982,988	12/27/1994	5,327,153	US
NMXX-0040.02/US	Utility - DIV	Issued	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	5/22/1996	08/651,233	3/31/1998	5,734,602	US
NMXX-0040.03/US	Utility - CON	Issued	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	8/26/1996	08/697,482	4/22/1997	5,623,438	US
NMXX-0041.00/US	Utility - ORG	Expired	READ/WRITE MEMORY WITH IMPROVED TEST MODE DATA COMPARE	12/17/1991	07/809,393	2/8/1994	5,285,419	US
NMXX-0042.01/US	Utility - CON	Issued	TESTABLE DRIVER FOR INTERNAL DATA BUS LINES	12/31/1996	08/777,836	5/5/1998	5,748,556	US
NMXX-0043.00/US	Utility - ORG	Issued	MULTIPLEXING SENSE AMPLIFIER HAVING LEVEL SHIFTER CIRCUITS	3/31/1993	08/040,916	12/27/1994	5,377,143	US
NMXX-0043.01/US	Utility - DIV	Issued	MULTIPLEXING SENSE AMPLIFIER	9/16/1994	08/307,332	1/9/1996	5,483,489	US
NMXX-0044.00/US	Utility - ORG	Issued	MULTIPLEXING SENSE AMPLIFIER	3/31/1993	08/041,321	1/23/1996	5,487,048	US
NMXX-0045.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT WITH PRECHARGED INTERNAL DATA BUS	12/17/1991	07/809,735	3/15/1994	5,295,104	US
NMXX-0046.00/US	Utility - ORG	Issued	REDUNDANCY DECODER	1/31/1992	07/830,315	11/28/1995	5,471,426	US
NMXX-0047.00/US	Utility - ORG	Expired	COLUMN REDUNDANCY ARCHITECTURE FOR A READ/WRITE MEMORY	1/31/1992	07/830,314	10/26/1993	5,257,229	US
NMXX-0048.00/US	Utility - ORG	Issued	SELECTIVE BULK WRITE OPERATION	4/7/1992	07/864,481	5/10/1994	5,311,467	US
NMXX-0049.00/US	Utility - ORG	Issued	PARALLELIZED MAGNITUDE COMPARATOR	4/30/1992	07/876,959	10/18/1994	5,357,235	US
NMXX-0050.00/US	Utility - ORG	Issued	PARALLELIZED MAGNITUDE COMPARATOR FOR COMPARING A BINARY NUMBER TO A FIXED VALUE	4/30/1992	07/876,851	6/7/1994	5,319,347	US
NMXX-0051.01/US	Utility - CON	Issued	PARALLELIZED BORROW LOOK AHEAD SUBTRACTOR	9/16/1993	08/122,804	1/31/1995	5,386,377	US
NMXX-0052.00/US	Utility - ORG	Issued	PARALLELIZED DIFFERENCE FLAG LOGIC	5/29/1992	07/890,919	10/18/1994	5,357,236	US

Exhibit A - US Issued Patents

Patent No.	Method Type	Method Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMXX-0055.00/US	Utility - ORG	Issued	SERIALIZED DIFFERENCE FLAG CIRCUIT	6/22/1992	07/901,667	10/11/1994	5,355,113	US
NMXX-0054.00/US	Utility - ORG	Issued	PROGRAMMABLE DIFFERENCE FLAG LOGIC	7/31/1992	07/923,855	1/10/1995	5,384,126	US
NMXX-0054.01/US	Utility - CON	Issued	FIFO WITH ADAPTABLE FLAGS FOR CHANGING SYSTEM SPEED REQUIREMENTS	9/8/1994	08/303,172	4/16/1996	5,508,679	US
NMXX-0055.00/US	Utility - ORG	Expired	INTEGRATED CIRCUIT WITH SELF-BIASED DIFFERENTIAL DATA LINES	12/17/1991	07/809,733	10/26/1993	5,257,226	US
NMXX-0056.00/US	Utility - ORG	Issued	PROCESS FOR REALIZING P-CHANNEL MOS TRANSISTORS HAVING A LOW THRESHOLD VOLTAGE IN SEMICONDUCTOR INTEGRATED CIRCUITS FOR ANALOG APPLICATIONS	7/28/1994	08/282,408	7/9/1996	5,534,448	US
NMXX-0056.01/US	Utility - DIV	Issued	PROCESS FOR REALIZING P-CHANNEL MOS TRANSISTORS HAVING A LOW THRESHOLD VOLTAGE IN SEMICONDUCTOR INTEGRATED CIRCUITS FOR ANALOG APPLICATIONS	6/7/1995	08/486,747	12/31/1996	5,589,701	US
NMXX-0057.02/US	Utility - CON	Issued	INPUT BUFFER CIRCUIT IMMUNE TO COMMON MODE POWER SUPPLY FLUCTUATIONS	6/7/1995	08/477,283	11/3/1998	5,831,457	US
NMXX-0058.00/US	Utility - ORG	Issued	STRUCTURE FOR USING A PORTION OF AN INTEGRATED CIRCUIT DIE	11/13/1992	07/975,628	10/11/1994	5,355,344	US
NMXX-0058.01/US	Utility - CON	Issued	STRUCTURE FOR USING A PORTION OF AN INTEGRATED CIRCUIT DIE	6/21/1994	08/263,048	6/11/1996	5,526,317	US
NMXX-0059.01/US	Utility - CON	Issued	INTEGRATED CIRCUIT OUTPUT DRIVER	12/30/1994	08/367,681	7/17/2001	6,262,617	US
NMXX-0060.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUIT WITH BODY BIAS CONTROL FOR MULTIPLE POWER SUPPLY OPERATION	1/3/1994	08/176,960	6/6/1995	5,422,591	US
NMXX-0061.01/US	Utility - CON	Issued	MULTIPLE CLOCKED DYNAMIC SENSE AMPLIFIER	9/15/1994	08/306,527	1/16/1996	5,485,430	US
NMXX-0062.00/US	Utility - ORG	Issued	DUAL DYNAMIC SENSE AMPLIFIERS FOR A MEMORY ARRAY	12/22/1992	07/995,581	10/3/1995	5,455,802	US
NMXX-0063.01/US	Utility - CON	Issued	LEADFRAME FOR INTEGRATED CIRCUITS	1/13/1994	08/180,692	2/14/1995	5,389,577	US
NMXX-0064.00/US	Utility - CIP	Issued	MULTIPLE LEVEL PARALLEL MAGNITUDE COMPARATOR	2/19/1993	08/020,045	3/21/1995	5,400,007	US
NMXX-0065.01/US	Utility - CON	Issued	STRESS TEST FOR MEMORY ARRAYS IN INTEGRATED CIRCUITS	9/30/1992	07/954,276	6/13/1995	5,424,988	US
NMXX-0066.00/US	Utility - ORG	Issued	STRESS TEST FOR MEMORY ARRAYS IN INTEGRATED CIRCUITS	6/2/1995	08/460,409	7/11/1997	5,644,542	US
NMXX-0067.00/US	Utility - ORG	Issued	SLEW RATE LIMITED OUTPUT BUFFER WITH BYPASS CIRCUITRY	8/31/1992	07/936,399	4/5/1994	5,300,828	US
NMXX-0068.00/US	Utility - ORG	Issued	TEMPERATURE-COMPENSATED VOLTAGE LEVEL SENSE CIRCUIT	4/29/1993	08/055,080	11/15/1994	5,365,129	US
NMXX-0069.01/US	Utility - ORG	Issued	ADDRESS BUFFER	4/30/1993	08/056,078	8/16/1994	5,339,277	US
NMXX-0070.00/US	Utility - ORG	Issued	METHOD FOR STRESS TESTING DECODERS AND PERIPHERY CIRCUITS	4/30/1993	08/056,376	8/23/1994	5,341,336	US
NMXX-0071.00/US	Utility - ORG	Issued	INPUT BUFFER WITH HYSTERESIS CHARACTERISTICS	12/21/1992	07/995,666	9/20/1994	5,349,246	US
NMXX-0072.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUIT	7/31/1992	07/923,135	7/19/1994	5,331,228	US
NMXX-0073.00/US	Utility - ORG	Issued	REDUNDANCY ELEMENT CHECK IN IC MEMORY WITHOUT PROGRAMMING SUBSTITUTION OF REDUNDANT ELEMENTS	7/30/1993	08/099,606	4/4/1995	5,404,331	US
NMXX-0074.01/US	Utility - DIV	Issued	TEMPERATURE-STABLE CURRENT SOURCE	5/31/1995	08/454,926	7/1/1997	5,644,216	US
NMXX-0075.00/US	Utility - CON	Issued	PROCESS FOR IMPROVING THE INTERFACE UNION AMONG DIELECTRIC MATERIALS IN AN INTEGRATED CIRCUIT MANUFACTURE	6/6/1995	08/473,552	5/6/1997	5,627,403	US
NMXX-0076.00/US	Utility - ORG	Issued	PROCESS FOR IMPROVING THE INTERFACE UNION AMONG DIELECTRIC MATERIALS IN AN INTEGRATED CIRCUIT MANUFACTURE	11/18/1996	08/751,244	8/18/1998	5,795,821	US
NMXX-0077.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR CIRCUIT PROVIDING POTENTIALS OF OPPOSITE POLARITY	9/26/1994	08/311,941	8/13/1996	5,546,044	US
NMXX-0078.00/US	Utility - ORG	Issued	PROCESS FOR THE MANUFACTURE OF AN INTEGRATED VOLTAGE LIMITER AND STABILIZER IN FLASH EEPROM MEMORY DEVICES	9/7/1994	08/301,792	1/23/1996	5,486,486	US
NMXX-0079.00/US	Utility - ORG	Issued	PROCESS FOR THE MANUFACTURE OF AN INTEGRATED VOLTAGE LIMITER AND STABILIZER IN FLASH EEPROM MEMORY DEVICES	6/7/1995	08/477,302	2/4/1997	5,600,590	US
NMXX-0080.00/US	Utility - ORG	Issued	MONOLITHICALLY INTEGRATED STORAGE DEVICES	11/30/1994	08/347,653	7/9/1996	5,535,157	US
NMXX-0081.00/US	Utility - ORG	Issued	CIRCUIT FOR COVERING INITIAL CONDITIONS WHEN STARTING-UP AN INTEGRATED CIRCUIT DEVICE	1/30/1995	08/380,309	3/18/1997	5,612,641	US
NMXX-0082.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR SUPPLYING NEGATIVE PROGRAMMING VOLTAGES TO NON-VOLATILE MEMORY CELLS IN A NON-VOLATILE MEMORY DEVICE	11/23/1994	08/344,232	6/18/1996	5,528,536	US
NMXX-0083.01/US	Utility - DIV	Issued	METHOD AND DEVICE FOR SUPPLYING NEGATIVE PROGRAMMING VOLTAGES TO NON-VOLATILE MEMORY CELLS IN A NON-VOLATILE MEMORY DEVICE	4/26/1996	08/639,931	8/19/1997	5,659,501	US

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Pat. No.	Message	Message	Title	Filed	Serial No.	Issued	Patent No.	Country
NMXXS-0079.02/US	Utility - CON	Issued	METHOD AND DEVICE FOR SUPPLYING NEGATIVE PROGRAMMING VOLTAGES TO NON-VOLATILE MEMORY CELLS IN A NON-VOLATILE MEMORY DEVICE	6/9/1997	08/871,226	10/6/1998	5,818,760	US
NMXXS-0080.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUITRY FOR A SEMICONDUCTOR MEMORY DEVICE	12/6/1994	08/349,783	10/15/1996	5,666,114	US
NMXXS-0081.01/US	Utility - CON	Issued	INTEGRATED CIRCUIT FOR THE PROGRAMMING OF A MEMORY CELL IN A NON-VOLATILE MEMORY REGISTER	4/18/1996	08/635,455	7/1/1997	5,644,529	US
NMXXS-0082.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING REDUNDANCY REGISTERS IN A ROW REDUNDANCY INTEGRATED CIRCUITRY FOR A SEMICONDUCTOR MEMORY DEVICE, AND ROW REDUNDANCY INTEGRATED CIRCUITRY	2/16/1995	08/391,999	8/19/1997	5,659,509	US
NMXXS-0083.00/US	Utility - ORG	Issued	INTEGRATED CIRCUITRY FOR CHECKING THE UTILIZATION RATE OF REDUNDANCY MEMORY ELEMENTS IN A SEMICONDUCTOR MEMORY DEVICE	12/7/1994	08/350,961	2/20/1996	5,493,531	US
NMXXS-0083.01/US	Utility - CIP	Issued	INTEGRATED CIRCUITRY FOR CHECKING THE UTILIZATION RATE OF REDUNDANCY MEMORY ELEMENTS IN A SEMICONDUCTOR MEMORY DEVICE	2/16/1996	08/602,237	1/13/1998	5,708,601	US
NMXXS-0084.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUITRY LAYOUT FOR A SEMICONDUCTOR MEMORY DEVICE	3/29/1995	08/412,550	9/24/1996	5,559,743	US
NMXXS-0085.00/US	Utility - ORG	Issued	CURRENT SOURCE HAVING VOLTAGE STABILIZING ELEMENT	1/20/1995	08/377,524	8/13/1996	5,546,054	US
NMXXS-0086.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TIMING THE LOADING OF NONVOLATILE-MEMORY OUTPUT DATA	2/21/1995	08/391,160	5/7/1996	5,515,332	US
NMXXS-0087.00/US	Utility - ORG	Issued	INTERNAL TIMING METHOD AND CIRCUIT FOR PROGRAMMABLE MEMORIES	2/21/1995	08/391,159	9/2/1997	5,663,921	US
NMXXS-0088.01/US	Utility - CON	Issued	LOAD SIGNAL GENERATING METHOD AND CIRCUIT FOR NONVOLATILE MEMORIES	2/25/1997	08/803,915	2/10/1998	5,717,642	US
NMXXS-0089.01/US	Utility - CON	Issued	COUNT UNIT FOR NONVOLATILE MEMORIES	8/20/1996	08/700,126	11/11/1997	5,687,135	US
NMXXS-0090.00/US	Utility - ORG	Issued	END-OF-COUNT DETECTING DEVICE FOR NONVOLATILE MEMORIES	12/28/1994	08/365,510	1/14/1997	5,594,703	US
NMXXS-0091.01/US	Utility - CON	Issued	VOLTAGE BOOSTER WITH AN ACCELERATION CIRCUIT	5/14/1997	08/855,922	6/16/1998	5,768,115	US
NMXXS-0092.00/US	Utility - ORG	Issued	BIAS CIRCUIT FOR A MEMORY LINE DECODER DRIVER OF NONVOLATILE MEMORIES	12/7/1994	08/348,461	3/12/1996	5,499,217	US
NMXXS-0093.00/US	Utility - ORG	Issued	READING CIRCUIT FOR AN INTEGRATED SEMICONDUCTOR MEMORY DEVICE	3/22/1995	08/408,589	5/6/1997	5,627,730	US
NMXXS-0094.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR TESTING A NETWORK WITH A PROGRAMMABLE LOGIC MATRIX	11/28/1994	08/345,530	2/10/1998	5,717,698	US
NMXXS-0095.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING AND TESTING A NONVOLATILE MEMORY	1/31/1995	08/383,530	2/4/1997	5,600,600	US
NMXXS-0096.00/US	Utility - ORG	Issued	THRESHOLD VOLTAGE MEASURING DEVICE FOR MEMORY CELLS	3/31/1995	08/412,326	2/4/1997	5,600,594	US
NMXXS-0097.00/US	Utility - ORG	Issued	CIRCUIT FOR IDENTIFYING A MEMORY CELL HAVING ERRONEOUS DATA STORED THEREIN	8/30/1995	08/521,304	11/11/1997	5,687,124	US
NMXXS-0098.00/US	Utility - ORG	Issued	DEVICE FOR DETECTING A REDUCTION IN A SUPPLY VOLTAGE	12/29/1994	08/366,211	12/10/1996	5,583,820	US
NMXXS-0098.01/US	Utility - DIV	Issued	DEVICE FOR DETECTING A REDUCTION IN A SUPPLY VOLTAGE	9/9/1996	08/711,132	5/5/1998	5,748,548	US
NMXXS-0099.00/US	Utility - ORG	Issued	STABLE REFERENCE VOLTAGE GENERATOR CIRCUIT	11/30/1994	08/347,788	5/21/2002	6,392,463	US
NMXXS-0100.00/US	Utility - ORG	Issued	REGULATION CIRCUIT AND METHOD FOR THE ERASING PHASE OF NON-VOLATILE MEMORY CELLS	2/21/1995	08/395,361	4/1/1997	5,617,356	US
NMXXS-0101.00/US	Utility - ORG	Issued	CIRCUIT DEVICE AND CORRESPONDING METHOD FOR RESETTNG NON-VOLATILE AND ELECTRICALLY PROGRAMMABLE MEMORY DEVICES	12/29/1994	08/366,212	12/17/1996	5,586,077	US
NMXXS-0102.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR PROGRAMMING NON-VOLATILE AND ELECTRICALLY PROGRAMMABLE MEMORY CELLS	12/29/1994	08/366,259	5/21/1996	5,519,656	US
NMXXS-0103.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR NON-VOLATILE SEMICONDUCTOR ELECTRICALLY PROGRAMMABLE MEMORY DEVICES	1/3/1995	08/368,211	8/19/1997	5,659,516	US
NMXXS-0103.01/US	Utility - DIV	Issued	VOLTAGE REGULATOR FOR NON-VOLATILE SEMICONDUCTOR ELECTRICALLY PROGRAMMABLE MEMORY DEVICES	4/1/1997	08/831,046	5/18/1999	5,905,677	US
NMXXS-0104.00/US	Utility - ORG	Issued	INTEGRATED PROGRAMMING CIRCUITRY FOR AN ELECTRICALLY PROGRAMMABLE SEMICONDUCTOR MEMORY DEVICE WITH REDUNDANCY	12/28/1994	08/365,154	8/20/1996	5,548,554	US
NMXXS-0105.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR SUPPRESSING DATA LOADING NOISE IN NONVOLATILE MEMORIES	2/21/1995	08/391,147	7/30/1996	5,541,884	US
NMXXS-0106.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TIMING THE READING OF NONVOLATILE MEMORIES	2/21/1995	08/391,920	7/2/1996	5,532,972	US

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Patent No.	App. No.	App. Type	Mat. Status	Pat. Title	Filed Date	Granted Date	Issued Date	Patent No.	Country
NMXS-0107.00/US	Utility - ORG	Issued	MEMORY ARRAY CELL READING CIRCUIT WITH EXTRA CURRENT BRANCH	4/17/1995	08/422,813	10/8/1996	5,563,826	US	
NMXS-0108.00/US	Utility - ORG	Issued	FLASH-EEPROM MEMORY ARRAY AND METHOD FOR BIASING THE SAME	3/28/1995	08/412,162	6/10/1997	5,638,327	US	
NMXS-0109.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING REDUNDANCY REGISTERS IN A COLUMN REDUNDANCY INTEGRATED CIRCUIT FOR A SEMICONDUCTOR MEMORY DEVICE, AND COLUMN REDUNDANCY INTEGRATED CIRCUIT	2/16/1995	08/389,599	2/11/1997	5,602,786	US	
NMXS-0110.00/US	Utility - ORG	Issued	REFERENCE SIGNAL GENERATING METHOD AND CIRCUIT FOR DIFFERENTIAL EVALUATION OF THE CONTENT OF NONVOLATILE MEMORY CELLS	3/28/1995	08/411,904	7/30/1996	5,541,880	US	
NMXS-0111.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR NON-VOLATILE SEMICONDUCTOR MEMORY DEVICES	1/3/1995	08/367,538	11/19/1996	5,576,990	US	
NMXS-0112.00/RE	Utility - REIS	Issued	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	7/12/2000	09/616,821	7/14/1998	RE39,918	US	
NMXS-0112.02/US	Utility - CON	Abandoned	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	9/18/1997	08/932,930	7/14/1998	5,781,043	US	
NMXS-0113.00/US	Utility - ORG	Issued	STRUCTURE TO RECOVER A PORTION OF A PARTIALLY FUNCTIONAL EMBEDDED MEMORY	4/22/1993	08/051,184	11/28/1995	5,471,431	US	
NMXS-0114.00/RE	Utility - REIS	Issued	STRUCTURE FOR DESELECTIVE BROKEN SELECT LINES IN MEMORY ARRAYS	11/7/1997	08/966,042	11/7/1995	RE36,319	US	
NMXS-0114.00/US	Utility - ORG	Abandoned	STRUCTURE FOR DESELECTIVE BROKEN SELECT LINES IN MEMORY ARRAYS	5/28/1993	08/069,025	11/7/1995	5,465,233	US	
NMXS-0115.00/US	Utility - ORG	Issued	LATCH CONTROLLED OUTPUT DRIVER	6/30/1993	08/085,760	9/20/1994	5,349,243	US	
NMXS-0116.00/US	Utility - ORG	Issued	FUSE CIRCUITRY TO CONTROL THE PROPAGATION DELAY OF AN IC	6/30/1993	08/085,580	6/27/1995	5,428,311	US	
NMXS-0117.01/US	Utility - CON	Issued	CLOCK GENERATION CIRCUIT HAVING COMPENSATION FOR SEMICONDUCTOR MANUFACTURING PROCESS VARIATIONS	3/27/1995	08/410,383	8/13/1996	5,546,537	US	
NMXS-0118.01/US	Utility - CON	Issued	CLOCK GENERATION CIRCUIT HAVING COMPENSATION FOR SEMICONDUCTOR MANUFACTURING PROCESS VARIATIONS	3/30/1995	08/413,789	5/6/1997	5,627,793	US	
NMXS-0119.00/US	Utility - ORG	Issued	CLOCK GENERATOR CIRCUIT WITH LOW CURRENT FREQUENCY DIVIDER	1/27/1994	08/188,153	11/21/1995	5,469,116	US	
NMXS-0120.00/US	Utility - CIP	Issued	DISABLING SENSE AMPLIFIER	7/30/1993	08/099,947	12/27/1994	5,377,150	US	
NMXS-0120.01/US	Utility - DIV	Issued	DISABLING SENSE AMPLIFIER	8/31/1994	08/298,766	12/5/1995	5,473,567	US	
NMXS-0121.00/US	Utility - ORG	Issued	PRECHARGE DEVICE FOR AN INTEGRATED CIRCUIT INTERNAL BUS	8/31/1993	08/114,749	3/28/1995	5,402,379	US	
NMXS-0122.00/US	Utility - ORG	Issued	APPARATUS FOR TESTING SIGNAL TIMING AND PROGRAMMING DELAY	1/31/1994	08/189,589	11/26/1996	5,579,326	US	
NMXS-0122.01/US	Utility - DIV	Lapsed	APPARATUS FOR TESTING SIGNAL TIMING AND PROGRAMMING DELAY	5/9/1996	08/647,222	11/24/1998	5,841,789	US	
NMXS-0123.00/US	Utility - ORG	Issued	DISTRIBUTED NOR TAG MATCH APPARATUS	8/31/1993	08/114,747	11/5/1996	5,572,456	US	
NMXS-0124.02/US	Utility - CON	Issued	VARIABLE IMPEDANCE DELAY ELEMENTS	2/18/1997	08/801,452	11/12/2000	6,014,050	US	
NMXS-0124.03/US	Utility - DIV	Issued	VARIABLE IMPEDANCE DELAY ELEMENTS	8/25/1998	09/139,225	6/28/2001	6,252,447	US	
NMXS-0125.00/US	Utility - ORG	Issued	TESTING CIRCUITRY OF INTERNAL PERIPHERAL BLOCKS IN A SEMICONDUCTOR MEMORY DEVICE AND METHOD OF TESTING THE SAME	1/27/1994	08/188,158	8/8/1995	5,440,516	US	
NMXS-0126.00/US	Utility - ORG	Issued	STRESS TEST MODE	12/22/1993	08/172,854	11/10/1998	5,835,427	US	
NMXS-0127.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR ACQUIRING REDUNDANCY INFORMATION FROM A PACKAGED MEMORY CHIP	12/22/1993	08/172,848	8/8/2000	6,101,618	US	
NMXS-0128.01/US	Utility - CON	Issued	REDUNDANT LINE DECODER MASTER ENABLE	6/19/1995	08/492,219	10/22/1996	5,568,061	US	
NMXS-0129.01/US	Utility - CON	Issued	STRUCTURE TO UTILIZE A PARTIALLY FUNCTIONAL CACHE MEMORY BY INVALIDATION OF FAULTY CACHE MEMORY LOCATIONS	9/20/1996	08/717,139	1/13/1998	5,708,789	US	
NMXS-0130.00/US	Utility - ORG	Issued	LATCH CONTROLLED OUTPUT DRIVER	9/30/1993	08/129,257	3/7/1995	5,396,108	US	
NMXS-0131.00/US	Utility - ORG	Issued	EDGE TRANSITION DETECTION DISABLE CIRCUIT TO ALTER MEMORY DEVICE OPERATING CHARACTERISTICS	9/30/1993	08/129,763	5/23/1995	5,418,756	US	
NMXS-0132.00/US	Utility - ORG	Issued	DATA COMPARING SENSE AMPLIFIER	12/22/1993	08/172,853	10/22/1996	5,568,073	US	
NMXS-0133.00/US	Utility - ORG	Issued	STATIC TEST MODE NOISE FILTER	11/30/1993	08/160,608	8/8/1995	5,440,178	US	
NMXS-0134.00/US	Utility - ORG	Issued	FUSE CIRCUIT WITH FEEDBACK DISCONNECT	12/28/1993	08/174,139	2/13/1996	5,491,444	US	
NMXS-0135.00/US	Utility - ORG	Issued	STATIC MEMORY LONG WRITE TEST	12/22/1993	08/173,197	11/19/1996	5,577,051	US	
NMXS-0136.00/US	Utility - CIP	Issued	COMPENSATING DELAY ELEMENT FOR CLOCK GENERATION IN A MEMORY DEVICE	12/20/1993	08/170,612	6/13/1995	5,424,985	US	
NMXS-0137.00/US	Utility - ORG	Issued	SEMI-CONDUCTOR MEMORY WITH EDGE TRANSITION DETECTION PULSE DISABLE	2/28/1994	08/202,830	2/20/1996	5,493,537	US	
NMXS-0138.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY WITH WRITE CHECKING	11/17/1994	08/340,940	4/21/1998	5,742,548	US	

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Patent No.	Met. Type	Met. Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMXS-0139.00/US	Utility - ORG	Issued	RESISTOR-CAPACITOR-TRANSISTOR TYPE INTEGRATED CIRCUIT, METHOD FOR THE MANUFACTURE OF SUCH A CIRCUIT AND APPLICATION TO AN OSCILLATOR	5/19/1994	08/246,070	7/4/1995	5,430,319	US
NMXS-0139.01/US	Utility - CON	Issued	RELAXATION OSCILLATOR USING INTEGRATED RTC STRUCTURE	3/8/1995	08/399,900	8/26/1997	5,661,324	US
NMXS-0140.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT CONTAINING A PROTECTED MEMORY AND SECURED SYSTEM USING SAID INTEGRATED CIRCUIT	10/27/1994	08/329,975	1/14/1997	5,594,793	US
NMXS-0141.00/US	Utility - ORG	Issued	LINE DECODER CIRCUIT FOR A MEMORY WORKING AT LOW SUPPLY VOLTAGES	12/21/1994	08/361,313	9/10/1996	5,555,216	US
NMXS-0142.00/US	Utility - ORG	Issued	MEMORY IN INTEGRATED CIRCUIT FORM WITH IMPROVED READING TIME	12/21/1994	08/361,996	7/16/1996	5,537,349	US
NMXS-0143.00/US	Utility - ORG	Issued	METHOD FOR MAKING A BIPOLAR TRANSISTOR FOR THE PROTECTION OF AN INTEGRATED CIRCUIT AGAINST ELECTROSTATIC DISCHARGES	1/27/1995	08/379,337	3/23/1999	5,886,386	US
NMXS-0143.01/US	Utility - DIV	Issued	METHOD FOR MAKING A BIPOLAR TRANSISTOR FOR THE PROTECTION OF AN INTEGRATED CIRCUIT AGAINST ELECTROSTATIC DISCHARGES	12/24/1998	09/220,635	7/24/2001	6,265,277	US
NMXS-0144.02/US	Utility - CON	Issued	BIAS CIRCUIT FOR TRANSISTOR OF A STORAGE CELL	12/16/1996	08/767,521	5/4/1999	5,900,756	US
NMXS-0145.00/US	Utility - ORG	Issued	MEMORY REDUNDANCY CIRCUIT	2/27/1995	08/393,464	11/26/1996	5,579,265	US
NMXS-0146.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY WITH COLUMN VOLTAGE HOLDING CIRCUIT	3/10/1995	08/402,519	3/11/1997	5,610,860	US
NMXS-0146.01/US	Utility - DIV	Issued	INTEGRATED CIRCUIT MEMORY WITH COLUMN VOLTAGE HOLDING CIRCUIT	3/4/1997	08/811,358	3/17/1998	5,729,162	US
NMXS-0147.00/US	Utility - ORG	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR IN PREDEFINED INITIAL STATE FOR MEMORY REDUNDANCY CIRCUIT	1/30/1995	08/380,737	2/25/1997	5,606,523	US
NMXS-0148.00/US	Utility - ORG	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR, PROGRAMMABLE BY THE SOURCE, FOR MEMORY REDUNDANCY CIRCUIT	1/30/1995	08/380,738	1/7/1997	5,592,417	US
NMXS-0148.01/US	Utility - DIV	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR, PROGRAMMABLE BY THE SOURCE, FOR MEMORY REDUNDANCY CIRCUIT	11/21/1996	08/752,814	10/14/1997	5,677,870	US
NMXS-0148.02/US	Utility - DIV	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR, PROGRAMMABLE BY THE SOURCE, FOR MEMORY REDUNDANCY CIRCUIT	11/21/1996	08/754,432	2/24/1998	5,721,706	US
NMXS-0149.00/US	Utility - ORG	Issued	NON-VOLATILE PROGRAMMABLE BISTABLE MULTIVIBRATOR WITH REDUCED PARASITICS IN READING MODE NOTABLY FOR MEMORY REDUNDANCY CIRCUIT	1/31/1995	08/381,529	10/1/1996	5,561,621	US
NMXS-0150.00/US	Utility - ORG	Issued	SWITCHING REGULATOR WITH DYSYMMETRICAL DIFFERENTIAL INPUT STAGE	4/19/1995	08/423,398	3/18/1997	5,612,611	US
NMXS-0151.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER CIRCUIT	4/19/1995	08/423,396	6/3/1997	5,636,115	US
NMXS-0152.00/US	Utility - ORG	Abandoned	METHOD FOR THE ERASURE OF A MEMORY, AND CIRCUITS FOR THE IMPLEMENTATION THEREOF	4/12/1995	08/421,671	4/8/1997	5,619,451	US
NMXS-0153.00/US	Utility - ORG	Issued	CIRCUIT FOR THE SELECTION OF REDUNDANT MEMORY ELEMENTS AND FLASH EEPROM MEMORY COMPRISING SAID CIRCUIT	2/22/1995	08/394,314	8/18/1998	5,796,653	US
NMXS-0154.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY INCORPORATING TEST FUNCTIONS	4/7/1995	08/418,636	7/1/1997	5,644,530	US
NMXS-0155.00/US	Utility - ORG	Issued	PROGRAMMABLE INTEGRATED CIRCUIT MEMORY COMPRISING EMULATION MEANS	6/7/1995	08/484,873	7/22/1997	5,651,128	US
NMXS-0156.00/US	Utility - ORG	Issued	MATRIX DEVICE WITH REDUNDANCY FUSES FOR INTEGRATED MEMORY	3/21/1995	08/408,016	1/14/1997	5,594,693	US
NMXS-0157.00/US	Utility - ORG	Issued	METHOD FOR TESTING AN ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY DEVICE	6/7/1995	08/479,081	12/31/1996	5,590,075	US
NMXS-0158.01/US	Utility - CON	Issued	PROGRAMMABLE MEMORY ELEMENT	2/4/1997	08/794,965	12/9/1997	5,686,716	US
NMXS-0159.00/US	Utility - ORG	Issued	SENSE AMPLIFIER WITH HYSTERESIS	5/2/1995	08/432,838	2/3/1998	5,715,204	US
NMXS-0160.01/US	Utility - DIV	Issued	HIGHLY-PLANAR INTERLAYER DIELECTRIC THIN FILMS IN INTEGRATED CIRCUITS	6/6/1995	08/468,282	1/28/1997	5,598,028	US
NMXS-0161.00/US	Utility - ORG	Issued	PROCESS FOR THE PRODUCTION OF A SEMICONDUCTOR DEVICE HAVING BETTER INTERFACE ADHESION BETWEEN DIELECTRIC LAYERS	12/22/1995	08/577,125	11/28/2000	6,153,537	US
NMXS-0161.01/US	Utility - DIV	Issued	PROCESS FOR THE PRODUCTION OF A SEMICONDUCTOR DEVICE HAVING BETTER INTERFACE ADHESION BETWEEN DIELECTRIC LAYERS	12/14/1998	09/211,065	3/11/2003	6,531,714	US
NMXS-0162.00/US	Utility - ORG	Issued	FAILURE TOLERANT MEMORY DEVICE, IN PARTICULAR OF THE FLASH EEPROM TYPE	5/31/1995	08/454,650	10/28/1997	5,682,349	US

PATENT

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**Exhibit A - US Issued Patents**

Model No.	Material	Material	Patent No.	File Date	Serial No.	Issue Date	Patenting	Country
NMXX-0163.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY CELL AND A METHOD FOR FORMING THE SAME	7/18/1995	08/503,303	1/27/1998	5,712,814	US
NMXX-0164.01/US	Utility - DIV	Issued	METHOD OF MAKING ASYMMETRIC NONVOLATILE MEMORY CELL	9/1/1996	08/712,373	7/6/1999	5,920,776	US
NMXX-0164.00/US	Utility - ORG	Issued	MEMORY DEVICE HAVING ERROR DETECTION AND CORRECTION FUNCTION, AND METHODS FOR READING, WRITING AND ERASING THE MEMORY DEVICE	10/2/1995	08/538,161	6/2/1998	5,761,222	US
NMXX-0165.00/US	Utility - ORG	Issued	SENSE AMPLIFIER HAVING CAPACITIVELY COUPLED INPUT FOR OFFSET COMPENSATION	4/26/1996	08/639,192	3/17/1998	5,729,492	US
NMXX-0165.01/US	Utility - CON	Issued	SENSE AMPLIFIER CIRCUIT FOR SEMICONDUCTOR MEMORY DEVICES	1/26/1998	09/013,141	11/9/1999	5,982,666	US
NMXX-0166.01/US	Utility - CON	Issued	MONOLITHICALLY INTEGRATED GENERATOR OF A PLURALITY OF VOLTAGE VALUES	9/10/1998	09/150,802	11/7/2000	6,144,588	US
NMXX-0167.00/US	Utility - ORG	Issued	HIGH-VOLTAGE N-CHANNEL MOS TRANSISTOR AND ASSOCIATED MANUFACTURING PROCESS	2/27/1996	08/607,779	12/15/1998	5,850,360	US
NMXX-0168.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH IMPROVED IMMUNITY TO LARGE METALLIZATION DEFECTS	10/2/1995	08/538,302	7/1/1997	5,644,526	US
NMXX-0169.00/US	Utility - ORG	Issued	READING CIRCUIT FOR MEMORY CELLS	6/6/1995	08/476,547	1/20/1998	5,710,739	US
NMXX-0170.00/US	Utility - ORG	Issued	SYNCHRONIZATION DEVICE FOR OUTPUT STAGES, PARTICULARLY FOR ELECTRONIC MEMORIES	11/17/1995	08/560,324	5/27/1997	5,633,834	US
NMXX-0171.00/US	Utility - ORG	Issued	PROGRAMMABLE MEMORY WITH SINGLE BIT ENCODING	4/4/1996	08/628,587	12/15/1998	5,850,361	US
NMXX-0172.00/US	Utility - ORG	Issued	DECODER WITH REDUCED ARCHITECTURE	11/17/1995	08/560,090	4/21/1998	5,742,187	US
NMXX-0173.00/US	Utility - ORG	Issued	OUTPUT STAGE FOR INTEGRATED CIRCUITS, PARTICULARLY FOR ELECTRONIC MEMORIES	5/17/1996	08/649,468	8/12/1997	5,657,276	US
NMXX-0174.00/US	Utility - ORG	Issued	PROGRAMMABLE MULTIBIT REGISTER FOR COINCIDENCE AND JUMP OPERATIONS AND COINCIDENCE FUSE CELL	1/26/1996	08/592,122	3/24/1998	5,731,716	US
NMXX-0176.00/US	Utility - ORG	Issued	EEPROM MEMORY WITH CONTACTLESS MEMORY CELLS	5/3/1996	08/642,325	2/70/1998	5,717,636	US
NMXX-0177.00/US	Utility - ORG	Issued	BYTE ERASABLE EEPROM FULLY COMPATIBLE WITH A SINGLE POWER SUPPLY FLASH-EPROM PROCESS	9/25/1995	08/533,631	3/18/1997	5,612,913	US
NMXX-0178.00/US	Utility - ORG	Issued	POWER-ON RESET CIRCUIT	8/31/1995	08/521,666	12/9/1997	5,696,461	US
NMXX-0179.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE HAVING SECTORS OF SELECTABLE SIZE AND NUMBER	5/3/1996	08/649,857	8/11/1998	5,793,676	US
NMXX-0179.01/US	Utility - CON	Issued	NONVOLATILE MEMORY DEVICE HAVING SECTORS OF SELECTABLE SIZE AND NUMBER	6/15/1998	09/094,916	9/7/1999	5,949,713	US
NMXX-0180.00/US	Utility - ORG	Issued	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON VOLATILE MEMORY CELLS AND SIDE TRANSISTORS AND CORRESPONDING IC	6/20/1996	08/667,097	12/21/1999	6,004,847	US
NMXX-0180.01/US	Utility - CON	Issued	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS AND CORRESPONDING IC	4/27/1999	09/300,029	6/19/2001	6,248,630	US
NMXX-0181.00/US	Utility - ORG	Issued	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS OF AT LEAST TWO DIFFERENT TYPES, AND CORRESPONDING IC	6/20/1996	08/670,179	1/5/1999	5,856,221	US
NMXX-0182.00/US	Utility - ORG	Issued	METHOD OF MANUFACTURING A MOS INTEGRATED CIRCUIT HAVING COMPONENTS WITH DIFFERENT DIELECTRICS	5/10/1996	08/644,892	9/5/2000	6,114,203	US
NMXX-0183.00/US	Utility - ORG	Issued	METHOD FOR IMPROVING THE PROFILE OF THE INTERMEDIATE DIELECTRIC, PARTICULARLY FOR NON VOLATILE MEMORIES	2/19/1997	08/802,619	4/13/1999	5,894,065	US
NMXX-0183.01/US	Utility - DIV	Issued	METHOD FOR IMPROVING THE INTERMEDIATE DIELECTRIC PROFILE, PARTICULARLY FOR NON-VOLATILE MEMORIES	7/22/1997	08/898,155	8/15/2000	6,104,058	US
NMXX-0184.00/US	Utility - ORG	Issued	CHARGE PUMP VOLTAGE MULTIPLIER CIRCUIT	12/5/1995	08/567,328	9/22/1998	5,812,017	US
NMXX-0185.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR-BOOSTER FOR SUPPLYING A PULSATING VOLTAGE HAVING APPROXIMATELY CONSTANT VOLTAGE LEVELS	4/12/1996	08/631,574	6/22/1999	5,914,867	US
NMXX-0186.00/US	Utility - ORG	Issued	CURRENT DETECTING CIRCUIT	8/2/1996	08/691,796	6/9/1998	5,764,570	US
NMXX-0187.00/US	Utility - ORG	Issued	HIERARCHIC MEMORY DEVICE HAVING AUXILIARY LINES CONNECTED TO WORD LINES	9/30/1996	08/724,495	11/24/1998	5,841,728	US

# PATENT

REEL: 033282 FRAME: 0346

# Mr. Dolano

UNIT DESIGN	MANUFACTURE	MAINTENANCE	TESTING	REPORTING	REPAIR DATE	SERIAL NO.	ISSUED DATE	REPAIRING	COUNTRY
S-0188.04/US	Utility - CON	Issued	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES		5/15/1997	08/940,045	5/18/1999	5,905,683	US
S-0189.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH FUSE CIRCUITRY SIMULATING FUSE BLOWING		3/31/1994	08/220,976	5/14/1996	5,517,455	US
S-0190.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH CENTRALIZED CONTROL OF EDGE TRANSITION DETECTION PULSE GENERATION		3/31/1994	08/220,834	11/28/1995	5,471,157	US
S-0191.00/US	Utility - ORG	Issued	STRUCTURE CAPABLE OF SIMULTANEOUSLY TESTING REDUNDANT AND NON-REDUNDANT MEMORY ELEMENTS DURING STRESS TESTING OF AN INTEGRATED CIRCUIT MEMORY DEVICE		4/29/1994	08/225,161	6/25/1996	5,530,674	US
S-0192.00/US	Utility - ORG	Issued	LONG WRITE TEST		6/29/1994	08/267,667	12/10/1996	5,583,816	US
S-0193.00/US	Utility - ORG	Issued	CIRCUIT WHICH PROVIDES POWER ON RESET DISABLE DURING A TEST MODE		6/29/1994	08/267,666	10/31/1995	5,455,799	US
S-0194.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY HAVING CONTROL CIRCUITRY FOR SHARED DATA BUS		5/31/1994	08/251,718	5/28/1996	5,521,880	US
S-0195.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT MEMORY WITH DISABLED EDGE TRANSITION PULSE GENERATION DURING SPECIAL TEST MODE		5/31/1994	08/251,337	2/20/1996	5,493,532	US
S-0196.00/US	Utility - ORG	Issued	VARIABLE INPUT THRESHOLD ADJUSTMENT		7/29/1994	08/282,177	12/31/1996	5,589,783	US
S-0197.00/US	Utility - ORG	Issued	TEST MODE CONTROL CIRCUIT OF AN INTEGRATED CIRCUIT DEVICE		7/29/1994	08/282,047	11/9/1999	5,982,188	US
S-0198.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR ENABLING A BUS DRIVER WHEN A DATA SIGNAL IS VALID		8/10/1994	08/288,334	10/15/1996	5,566,112	US
S-0198.01/US	Utility - DIV	Issued	APPARATUS AND METHOD FOR ENABLING A BUS DRIVER WHEN A DATA SIGNAL IS VALID		5/10/1995	08/438,276	10/29/1996	5,570,316	US
S-0199.00/US	Utility - ORG	Issued	PRE-CHARGED EXCLUSIONARY WIRED-CONNECTED PROGRAMMED REDUNDANT SELECT		9/30/1994	08/316,087	2/27/1996	5,495,446	US
S-0200.00/US	Utility - ORG	Issued	FULL MEMORY CHIP LONG WRITE TEST MODE		9/30/1994	08/315,337	3/26/1996	5,502,678	US
S-0201.00/US	Utility - ORG	Issued	LOW CURRENT DIFFERENTIAL LEVEL SHIFTER		10/31/1994	08/331,892	5/14/1996	5,517,148	US
S-0202.00/US	Utility - ORG	Issued	CIRCUIT STRUCTURE AND METHOD FOR STRESS TESTING OF BIT LINES		10/31/1994	08/331,699	3/11/1997	5,610,866	US
S-0203.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR MAPPING A REDUNDANT MEMORY COLUMN TO A DEFECTIVE MEMORY COLUMN		5/10/1995	08/438,903	11/12/1996	5,574,688	US
S-0204.00/US	Utility - ORG	Issued	POST-FABRICATION SELECTABLE REGISTERED AND NON-REGISTERED MEMORY		12/22/1994	08/362,187	11/26/1996	5,579,263	US
S-0205.00/US	Utility - ORG	Issued	CIRCUIT FOR PROVIDING A COMPENSATED BIAS VOLTAGE		12/16/1994	08/357,664	10/22/1996	5,568,084	US
S-0205.01/US	Utility - CON	Issued	CIRCUIT FOR PROVIDING A COMPENSATED BIAS VOLTAGE		4/12/1996	08/631,063	8/5/1997	5,664,663	US
S-0206.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUITRY WITH LIMITED OUTPUT HIGH VOLTAGE		12/20/1994	08/360,228	1/21/1997	5,596,297	US
S-0207.00/US	Utility - ORG	Issued	VOLTAGE REFERENCE CIRCUIT USING AN OFFSET COMPENSATING CURRENT SOURCE		12/20/1994	08/360,229	8/20/1996	5,548,241	US
S-0208.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUITRY WITH SELECTIVE LIMITED OUTPUT HIGH VOLTAGE		12/20/1994	08/359,397	1/14/1997	5,594,373	US
S-0209.00/US	Utility - ORG	Issued	ADJUSTABLE CURRENT SOURCE		12/20/1994	08/359,927	12/31/1996	5,581,209	US
S-0210.00/US	Utility - ORG	Issued	VOLTAGE REFERENCE CIRCUIT HAVING A THRESHOLD VOLTAGE SHIFT		12/20/1994	08/359,926	1/28/1997	5,598,122	US
S-0211.00/US	Utility - ORG	Issued	DYNAMICALLY CONTROLLED VOLTAGE REFERENCE CIRCUIT		12/20/1994	08/360,227	12/31/1996	5,589,794	US
S-0212.01/US	Utility - CON	Issued	CLOCK PULSE EXTENDER MODE FOR CLOCKED MEMORY DEVICES HAVING PRECHARGED DATA PATHS		4/30/1997	08/848,342	6/29/1999	5,917,353	US
S-0213.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR MAPPING A REDUNDANT MEMORY COLUMN TO A DEFECTIVE MEMORY COLUMN		5/10/1995	08/438,349	11/5/1996	5,572,470	US
S-0214.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR TESTING AN INTEGRATED CIRCUIT DEVICE		8/25/1995	08/519,192	4/18/2000	6,052,806	US
S-0215.00/US	Utility - ORG	Issued	RESIDUAL CHARGE ELIMINATION FOR A MEMORY DEVICE		12/29/1995	08/580,549	3/31/1998	5,734,608	US
S-0216.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TESTING MEMORIES IN INTEGRATED CIRCUIT FORM		12/21/1995	08/575,953	10/7/1997	5,675,539	US
S-0217.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR ADDRESS DECODING IN AN INTEGRATED CIRCUIT MEMORY		9/11/1995	08/526,500	4/21/1998	5,742,546	US
S-0218.00/US	Utility - ORG	Issued	CURRENT DETECTION CIRCUIT FOR READING A MEMORY IN INTEGRATED CIRCUIT FORM		5/17/1996	08/649,282	12/16/1997	5,699,295	US

**Exhibit A - US Issued Patents**

Filed No.	Meeting	Water Status	The	Filed	Serial No.	Issued	Patent No.	Country
NMXS-0219.00/US	Utility - ORG	Issued	AN ELECTRICALLY PROGRAMMABLE MEMORY WITH IMPROVED RETENTION OF DATA AND A METHOD OF WRITING DATA IN SAID MEMORY	12/18/1995	08/573,897	7/29/1997	5,652,720	US
NMXS-0220.00/US	Utility - ORG	Issued	CHARGE PUMP TYPE OF NEGATIVE VOLTAGE GENERATOR CIRCUIT AND METHOD	6/13/1996	08/663,524	11/24/1998	5,841,314	US
NMXS-0221.00/US	Utility - ORG	Issued	RESISTANCE REFERENCE CIRCUIT	12/21/1995	08/576,881	2/10/1998	5,717,323	US
NMXS-0222.00/US	Utility - ORG	Issued	DIGITAL RAMP GENERATOR	5/30/1996	08/652,710	6/30/1998	5,774,380	US
NMXS-0223.00/US	Utility - ORG	Issued	SERIAL DICHOTOMIC METHOD FOR SENSING MULTIPLE-LEVEL NON-VOLATILE MEMORY CELLS, AND SENSING CIRCUIT IMPLEMENTING SUCH METHOD	1/29/1996	08/593,650	12/23/1997	5,701,265	US
NMXS-0224.01/US	Utility - CON	Issued	READING CIRCUIT FOR MULTILEVEL NON VOLATILE MEMORY CELL DEVICES	6/4/1997	08/869,072	11/17/1998	5,838,612	US
NMXS-0225.00/US	Utility - ORG	Issued	CIRCUIT FOR READING NON-VOLATILE MEMORIES	7/31/1996	08/690,530	3/31/1998	5,734,610	US
NMXS-0226.00/RE	Utility - REIS	Issued	CIRCUIT AND METHOD FOR READING A MEMORY CELL THAT CAN STORE MULTIPLE BITS OF DATA	9/30/1999	09/410,164	9/30/1997	RE38,466	US
NMXS-0226.00/US	Utility - ORG	Abandoned	SENSE CIRCUIT FOR SERIES BINARY SENSING OF MULTILEVEL NONVOLATILE STORAGE ELEMENT	1/29/1996	08/592,939	9/30/1997	5,673,221	US
NMXS-0227.00/US	Utility - ORG	Issued	READING CIRCUIT FOR MEMORY CELL DEVICES HAVING A LOW SUPPLY VOLTAGE	4/25/1996	08/638,976	12/2/1997	5,694,363	US
NMXS-0228.00/US	Utility - ORG	Issued	MODULATED SLOPE SIGNAL GENERATION CIRCUIT, PARTICULARLY FOR LATCH DATA SENSING ARRANGEMENTS	7/19/1996	08/684,431	4/7/1998	5,737,268	US
NMXS-0229.00/US	Utility - ORG	Issued	THRESHOLD DETECTING DEVICE	7/31/1996	08/688,956	12/8/1998	5,847,584	US
NMXS-0230.00/US	Utility - ORG	Issued	CHARGE PUMP CIRCUIT WITH MULTIPLE BOOST STAGES	7/19/1996	08/684,192	6/2/1998	5,760,497	US
NMXS-0231.00/US	Utility - ORG	Issued	FLASH EEPROM WITH INTEGRATED DEVICE FOR LIMITING THE ERASE SOURCE VOLTAGE	7/31/1996	08/692,936	1/14/2003	6,507,067	US
NMXS-0232.00/US	Utility - ORG	Issued	NEGATIVE WORD LINE VOLTAGE REGULATION CIRCUIT FOR ELECTRICALLY ERASABLE SEMICONDUCTOR MEMORY DEVICES	6/19/1996	08/665,862	8/19/1997	5,659,502	US
NMXS-0233.01/US	Utility - CON	Issued	NEGATIVE WORD LINE VOLTAGE REGULATION CIRCUIT FOR ELECTRICALLY ERASABLE SEMICONDUCTOR MEMORY DEVICES	6/23/1997	08/881,713	7/6/1999	5,920,505	US
NMXS-0233.00/US	Utility - ORG	Issued	PARALLEL PROGRAMMING METHOD OF MEMORY WORDS AND CORRESPONDING CIRCUIT	9/30/1996	08/722,378	7/14/1998	5,781,474	US
NMXS-0234.00/US	Utility - ORG	Issued	METHOD FOR SETTING THE THRESHOLD VOLTAGE OF A REFERENCE MEMORY CELL	7/12/1996	08/679,656	7/21/1998	5,784,314	US
NMXS-0235.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR SEMICONDUCTOR NON-VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY DEVICE	9/30/1996	08/720,491	12/1/1998	5,844,404	US
NMXS-0236.00/US	Utility - ORG	Issued	ROW DECODING CIRCUIT FOR SEMICONDUCTOR NON-VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY AND CORRESPONDING METHOD	3/27/1997	08/824,616	12/8/1998	5,848,013	US
NMXS-0236.01/US	Utility - DIV	Issued	ROW DECODING CIRCUIT FOR A SEMICONDUCTOR NON VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY AND CORRESPONDING METHOD	12/2/1998	09/203,957	10/24/2000	6,137,725	US
NMXS-0236.02/US	Utility - DIV	Issued	ROW DECODING CIRCUIT FOR A SEMICONDUCTOR NON VOLATILE ELECTRICALLY PROGRAMMABLE MEMORY AND CORRESPONDING METHOD	8/7/2000	09/633,334	11/20/2001	6,320,792	US
NMXS-0237.00/US	Utility - ORG	Issued	DEVICE FOR GENERATING AND REGULATING A GATE VOLTAGE IN A NON-VOLATILE MEMORY	12/30/1996	08/775,111	10/13/1998	5,822,247	US
NMXS-0238.00/US	Utility - ORG	Issued	SYSTEM FOR DETERMINING THE PROGRAMMED/NON PROGRAMMED STATUS OF A MEMORY CELL	3/27/1997	08/827,409	1/26/1999	5,864,513	US
NMXS-0239.00/US	Utility - ORG	Issued	BOOST REGULATOR	1/21/1997	08/787,494	10/13/1998	5,821,806	US
NMXS-0240.00/US	Utility - ORG	Issued	UNBALANCED LATCH AND FLUSE CIRCUIT INCLUDING THE SAME	7/19/1996	08/684,406	8/19/1997	5,659,498	US
NMXS-0241.00/US	Utility - ORG	Issued	REFERENCE WORD LINE AND DATA PROPAGATION REPRODUCTION CIRCUIT FOR MEMORIES PROVIDED WITH HIERARCHICAL DECODERS	3/27/1997	08/835,033	5/19/1998	5,754,483	US
NMXS-0242.00/US	Utility - ORG	Issued	TIMESHARING INTERNAL BUS, PARTICULARLY FOR NON-VOLATILE MEMORIES	3/7/1997	08/813,667	8/20/2002	6,438,669	US
NMXS-0243.00/US	Utility - ORG	Issued	PARALLEL-DICHOTOMIC SERIAL SENSING METHOD FOR SENSING MULTIPLE-LEVEL NON VOLATILE MEMORY CELLS, AND SENSING CIRCUIT FOR ACTUATING SUCH METHOD	7/31/1996	08/690,059	3/17/1998	5,729,490	US

Exhibit A - US Issued Patents

Patent No.	Matter Type	Process Status	Patent Title	Filing Date	Serial No.	Issue Date	Patent No.	Country
NMXS-0244.01/US	Utility - CON	Issued	SUPPLY VOLTAGES SWITCHES CIRCUIT	7/2/1998	09/109,630	3/21/2000	6,040,734	US
NMXS-0245.00/US	Utility - ORG	Abandoned	METHOD AND PROGRAMMING DEVICE FOR DETECTING AN ERROR IN A MEMORY	12/24/1996	08/774,110	3/3/1998	5,724,290	US
NMXS-0246.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR FOR ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS	10/23/1996	08/735,709	8/11/1998	5,793,679	US
NMXS-0246.01/US	Utility - DIV	Issued	VOLTAGE GENERATOR FOR ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS	2/27/1998	09/032,119	12/5/2000	6,157,054	US
NMXS-0247.00/US	Utility - ORG	Issued	FLASH EEPROM WITH ON-CHIP ERASE SOURCE VOLTAGE GENERATOR	7/24/1996	08/687,145	2/27/2001	6,195,291	US
NMXS-0247.01/US	Utility - DIV	Issued	FLASH EEPROM WITH ON-CHIP ERASE SOURCE VOLTAGE GENERATOR	1/23/2001	09/768,744	11/19/2002	6,483,750	US
NMXS-0248.00/US	Utility - ORG	Issued	NEGATIVE CHARGE PUMP CIRCUIT FOR ELECTRICALLY ERASABLE SEMICONDUCTOR MEMORY DEVICES	10/31/1996	08/751,299	5/19/1998	5,754,476	US
NMXS-0249.00/US	Utility - ORG	Issued	ANALOG VOLTAGE-SIGNAL SELECTOR DEVICE	11/1/1996	08/742,978	5/18/1999	5,905,387	US
NMXS-0250.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH IMPROVED YIELD AND RELIABILITY	6/28/1996	08/671,848	7/7/1998	5,778,012	US
NMXS-0251.00/US	Utility - ORG	Issued	CIRCUIT FOR THE GENERATION OF A VOLTAGE AS A FUNCTION OF THE CONDUCTIVITY OF AN ELEMENTARY CELL OF A NON-VOLATILE MEMORY	3/27/1997	08/835,031	9/1/1998	5,801,988	US
NMXS-0252.00/US	Utility - ORG	Issued	ZERO CONSUMPTION POWER-ON-RESET	1/30/1997	08/790,832	10/13/1998	5,821,788	US
NMXS-0253.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR REDUNDANCY MANAGEMENT OF NON-VOLATILE MEMORIES	3/27/1997	08/828,039	11/17/1998	5,838,619	US
NMXS-0254.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING A MICROTOP CATHODE ASSEMBLY FOR A FIELD EMISSION DISPLAY PANEL	12/13/1996	08/807,113	12/14/1999	6,000,980	US
NMXS-0255.00/US	Utility - ORG	Issued	HIGH-VOLTAGE-RESISTANT MOS TRANSISTOR, AND CORRESPONDING MANUFACTURING PROCESS	3/18/1997	08/824,888	11/2/1999	5,977,591	US
NMXS-0256.01/US	Utility - CON	Issued	NONVOLATILE FLOATING-GATE MEMORY DEVICES, AND PROCESS OF FABRICATION	4/13/2000	09/548,782	9/10/2002	6,448,138	US
NMXS-0257.00/US	Utility - ORG	Issued	HIGH CAPACITY CAPACITOR AND CORRESPONDING MANUFACTURING PROCESS	10/30/1996	08/739,997	4/24/2001	6,222,245	US
NMXS-0258.00/US	Utility - ORG	Issued	ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS DEVICE FOR A REDUCED NUMBER OF PROGRAMMING CYCLES	2/27/1997	08/807,574	7/20/1999	5,926,416	US
NMXS-0259.00/US	Utility - ORG	Issued	PROCESS OF FABRICATING TUNNEL-OXIDE NONVOLATILE MEMORY DEVICES	1/31/1997	08/792,893	10/6/1998	5,817,557	US
NMXS-0260.00/US	Utility - ORG	Issued	FABRICATION OF NATURAL TRANSISTORS IN A NONVOLATILE MEMORY PROCESS	1/16/1997	08/784,967	7/13/1999	5,923,975	US
NMXS-0261.00/US	Utility - ORG	Issued	MULTI-LEVEL MEMORY CIRCUITS AND CORRESPONDING READING AND WRITING METHODS	1/30/1997	08/791,348	1/12/1999	5,859,795	US
NMXS-0262.00/US	Utility - NSPCT	Issued	MULTI-LEVEL MEMORY CIRCUIT WITH REGULATED READING VOLTAGE	10/30/1996	09/202,657	8/8/2000	6,101,121	US
NMXS-0263.00/US	Utility - ORG	Issued	CLOCK CIRCUIT FOR READING A MULTILEVEL NON VOLATILE MEMORY CELLS DEVICE	6/27/1997	08/883,822	11/16/1999	5,986,921	US
NMXS-0264.00/US	Utility - NSPCT	Issued	MULTI-LEVEL MEMORY CIRCUIT WITH REGULATED WRITING VOLTAGE	10/30/1996	09/202,656	8/1/2000	6,097,628	US
NMXS-0265.00/US	Utility - ORG	Issued	SECTORIZED ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY DEVICE WITH REDUNDANCY	3/21/1997	08/821,804	12/29/1998	5,854,764	US
NMXS-0266.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY DEVICE WITH TESTABLE REDUNDANCY CIRCUITS	5/8/1997	08/853,756	12/7/1999	5,999,450	US
NMXS-0267.00/US	Utility - ORG	Issued	SENSING CIRCUITRY FOR READING AND VERIFYING THE CONTENTS OF ELECTRICALLY PROGRAMMABLE/ERASABLE NON-VOLATILE MEMORY CELLS	4/29/1997	08/845,916	6/29/1999	5,917,753	US
NMXS-0267.01/US	Utility - CON	Issued	SENSING CIRCUITRY FOR READING AND VERIFYING THE CONTENTS OF ELECTRICALLY PROGRAMMABLE/ERASABLE NON-VOLATILE MEMORY CELLS	12/9/1998	09/209,319	4/25/2000	6,055,187	US
NMXS-0268.00/US	Utility - ORG	Issued	ERASING METHOD FOR A NON-VOLATILE MEMORY	12/24/1996	08/774,860	10/6/1998	5,818,763	US
NMXS-0269.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR GENERATING A SYNCHRONIZING AND SIGNAL	11/26/1997	08/978,665	3/23/1999	5,886,949	US
NMXS-0270.01/US	Utility - DIV	Issued	METHOD OF AVOIDING DISTURBANCE DURING THE STEP OF PROGRAMMING AND ERASING AN ELECTRICALLY PROGRAMMABLE, SEMICONDUCTOR NON-VOLATILE STORAGE DEVICE	9/27/1999	09/407,135	2/27/2001	6,195,290	US

Exhibit A - US Issued Patents

Patent No.	Patent Title	Patent Status	Patent Description	Filed	Serial No.	Issued Date	Patent No.	Country
NMXXS-0271.00/US	Utility - ORG	Issued	ERASE VOLTAGE CONTROL CIRCUIT FOR AN ELECTRICALLY ERASABLE NON-VOLATILE MEMORY CELL	1/23/1997	08/787,907	2/24/1998	5,721,707	US
NMXXS-0272.00/US	Utility - ORG	Issued	METHOD FOR ERASING AN ELECTRICALLY PROGRAMMABLE AND ERASABLE NON-VOLATILE MEMORY CELL	1/24/1997	08/788,530	7/21/1998	5,784,319	US
NMXXS-0273.00/US	Utility - ORG	Issued	MONOLITHICALLY INTEGRATED PROGRAMMABLE DEVICE HAVING ELEMENTARY MODULES CONNECTED ELECTRICALLY BY MEANS OF MEMORY CELLS OF THE FLASH TYPE	12/27/1996	08/777,296	12/21/1999	6,005,411	US
NMXXS-0274.00/US	Utility - ORG	Issued	PAGE-MODE MEMORY DEVICE WITH MULTIPLE-LEVEL MEMORY CELLS	6/5/1997	08/869,208	5/26/1998	5,757,719	US
NMXXS-0275.00/US	Utility - ORG	Issued	HIGH VOLTAGES DETECTOR CIRCUIT AND INTEGRATED CIRCUIT USING SAME	1/30/1997	08/791,700	8/18/1998	5,795,275	US
NMXXS-0276.00/US	Utility - ORG	Issued	PROCESS FOR FORMING AN EDGE STRUCTURE TO SEAL INTEGRATED ELECTRONIC DEVICES, AND CORRESPONDING DEVICE	1/27/1998	09/014,437	5/2/2000	6,057,591	US
NMXXS-0276.01/US	Utility - DIV	Issued	PROCESS FOR FORMING AN EDGE STRUCTURE TO SEAL INTEGRATED ELECTRONIC DEVICES	3/24/2000	09/534,675	4/3/2001	6,210,994	US
NMXXS-0277.00/US	Utility - ORG	Issued	MEMORY ARCHITECTURE FOR FLEXIBLE READING MANAGEMENT, PARTICULARLY FOR NON-VOLATILE MEMORIES, HAVING NOISE-IMMUNITY FEATURES, MATCHING DEVICE PERFORMANCE, AND HAVING OPTIMIZED THROUGHOUT	4/24/1997	08/847,385	6/29/1999	5,917,768	US
NMXXS-0278.00/US	Utility - ORG	Issued	CIRCUITS AND METHODS FOR READ-ENABLING MEMORY DEVICES SYNCHRONOUSLY WITH THE READING OF THE MINIMUM FUNCTIONALITY CONDITIONS OF THE MEMORY CELLS AND READING CIRCUITS, PARTICULARLY FOR NON-VOLATILE MEMORIES	4/24/1997	08/840,056	3/2/1999	5,878,049	US
NMXXS-0279.00/US	Utility - ORG	Issued	COLUMN MULTIPLEXER	5/9/1997	08/853,732	7/7/1998	5,777,941	US
NMXXS-0280.00/US	Utility - ORG	Issued	DRIVER DEVICE FOR SELECTION LINES FOR A MULTIPLEXER, TO BE USED IN A WIDE RANGE OF SUPPLY VOLTAGES, PARTICULARLY FOR NON-VOLATILE MEMORIES	3/27/1997	08/826,223	5/12/1998	5,751,654	US
NMXXS-0281.00/US	Utility - ORG	Issued	A MEMORY UNDER TEST PROGRAMMING AND READING DEVICE	3/28/1997	08/835,030	11/14/2000	6,148,413	US
NMXXS-0282.00/US	Utility - ORG	Issued	DECODING HIERARCHICAL ARCHITECTURE FOR HIGH INTEGRATION MEMORIES	1/30/1997	08/791,746	12/29/1998	5,854,770	US
NMXXS-0283.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE CAPABLE OF READING DATA WITH AN APPROPRIATE SELF-TIMING AND A REDUCED NUMBER OF REFERENCE LINES	3/27/1997	08/826,489	8/31/1999	5,946,237	US
NMXXS-0283.01/US	Utility - DIV	Issued	DATA READING PATH MANAGEMENT ARCHITECTURE FOR A MEMORY DEVICE, PARTICULARLY FOR NON-VOLATILE MEMORIES	7/12/1999	09/351,603	4/3/2001	6,212,096	US
NMXXS-0284.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE HAVING OPTIMIZED MANAGEMENT OF DATA TRANSMISSION LINES	3/7/1997	08/812,595	11/3/1998	5,831,891	US
NMXXS-0285.00/US	Utility - ORG	Issued	PULSE GENERATION CIRCUIT AND METHOD FOR SYNCHRONIZED DATA LOADING IN AN OUTPUT PRE-BUFFER	3/27/1997	08/826,009	1/12/1999	5,859,810	US
NMXXS-0286.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT WITH UNEQUALLY-SIZED, PAIRED MEMORY COUPLED TO ODD NUMBER OF INPUT/OUTPUT PADS	6/7/1995	08/477,244	4/29/1997	5,625,603	US
NMXXS-0287.01/US	Utility - CON	Issued	CONSTANT CURRENT SOURCE WITH REDUCED SENSITIVITY TO SUPPLY VOLTAGE AND PROCESS VARIATION	6/24/1996	08/667,481	8/11/1998	5,793,247	US
NMXXS-0288.00/US	Utility - ORG	Issued	CIRCUIT FOR PROVIDING A BIAS VOLTAGE COMPENSATED FOR P-CHANNEL TRANSISTOR VARIATIONS	6/5/1995	08/464,551	6/17/1997	5,640,122	US
NMXXS-0289.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR AN OUTPUT DRIVER WITH REDUCED OUTPUT IMPEDANCE	3/31/1995	08/414,103	11/19/1996	5,576,656	US
NMXXS-0290.00/RE	Utility - REIS	Issued	POWER SUPPLY SWITCH REFERENCE CIRCUITRY	5/5/2000	09/568,201	5/5/1998	RE37,876	US
NMXXS-0290.00/US	Utility - ORG	Abandoned	CIRCUIT AND METHOD FOR BIASING BIT LINES	6/5/1995	08/464,082	5/5/1998	5,747,890	US
NMXXS-0291.00/US	Utility - ORG	Issued	REDUCED PIN COUNT STRESS TEST CIRCUIT FOR INTEGRATED MEMORY DEVICES AND METHOD THEREOF	6/7/1995	08/484,491	6/16/1998	5,768,206	US
NMXXS-0292.00/US	Utility - ORG	Issued	FAULT DETECTION FOR ENTIRE WAFER STRESS TEST	6/7/1995	08/476,658	1/7/1997	5,592,422	US
NMXXS-0293.00/US	Utility - ORG	Issued	FAULT DETECTION FOR ENTIRE WAFER STRESS TEST	7/31/1995	08/509,158	4/8/1997	5,619,462	US

PATENT

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Patent No.	Title	Filed Date	Serial No.	Issued Date	Patent No.	Country
NMXS-0294.01/US	Utility - CON	Issued	INTEGRATED MEMORY CIRCUIT WITH SEQUENCED BITLINES FOR STRESS TEST	4/7/1997	08/833,582	US
NMXS-0295.01/US	Utility - CON	Issued	METHOD AND APPARATUS FOR TEST MODE ENTRY DURING POWER UP	9/12/1996	08/712,960	US
NMXS-0296.00/US	Utility - ORG	Issued	COLUMN REDUNDANCY OF A MULTIPLE BLOCK MEMORY ARCHITECTURE	7/31/1995	08/509,351	US
NMXS-0297.00/US	Utility - CIP	Issued	STRUCTURE FOR DESELECTING BROKEN SELECT LINES IN MEMORY ARRAYS	7/31/1995	08/509,211	US
NMXS-0298.00/US	Utility - ORG	Issued	WRITE CONTROLLED ADDRESS BUFFER	8/31/1995	08/521,800	US
NMXS-0299.00/US	Utility - ORG	Issued	REDUNDANCY ARCHITECTURE	12/29/1995	08/582,424	US
NMXS-0300.00/US	Utility - ORG	Issued	TEST MODE ACTIVATION AND DATA OVERRIDE	1/19/1996	08/587,709	US
NMXS-0300.02/US	Utility - CON	Issued	TEST MODE ACTIVATION AND DATA OVERRIDE	12/9/1999	09/457,558	US
NMXS-0301.00/US	Utility - ORG	Issued	PIPELINED CHIP ENABLE CONTROL CIRCUITRY AND METHODOLOGY	1/19/1996	08/538,730	US
NMXS-0301.01/US	Utility - DIV	Issued	PIPELINED CHIP ENABLE CONTROL CIRCUITRY AND METHODOLOGY	6/19/1997	08/878,612	US
NMXS-0302.00/US	Utility - ORG	Issued	OUTPUT DRIVER CIRCUITRY HAVING A SINGLE SLEW RATE RESISTOR	1/19/1996	08/538,988	US
NMXS-0303.00/US	Utility - ORG	Issued	REDUNDANCY CONTROL	12/29/1995	08/580,827	US
NMXS-0304.00/US	Utility - ORG	Issued	WRITE PASS THROUGH CIRCUIT	1/19/1996	08/538,662	US
NMXS-0305.00/US	Utility - ORG	Issued	DATA-INPUT DEVICE FOR GENERATING TEST SIGNALS ON BIT AND BIT-COMPLEMENT LINES	1/19/1996	08/538,762	US
NMXS-0306.00/US	Utility - ORG	Issued	WRITE DRIVER HAVING A TEST FUNCTION	1/19/1996	08/539,141	US
NMXS-0307.01/US	Utility - CON	Issued	CIRCUIT AND METHOD FOR TRACKING THE START OF A WRITE TO A MEMORY CELL	5/19/1997	08/858,295	US
NMXS-0308.01/US	Utility - CON	Issued	CIRCUIT AND METHOD FOR TERMINATING A WRITE TO A MEMORY CELL	5/19/1997	08/858,788	US
NMXS-0309.00/US	Utility - ORG	Issued	CLOCKED SENSE AMPLIFIER WITH WORDLINE TRACKING	1/19/1996	08/537,728	US
NMXS-0309.01/US	Utility - DIV	Issued	CLOCKED SENSE AMPLIFIER WITH WORDLINE TRACKING	10/6/1997	08/944,649	US
NMXS-0310.01/US	Utility - CON	Lapsed	MEMORY-ROW SELECTOR HAVING A TEST FUNCTION	9/10/1997	08/926,833	US
NMXS-0311.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR SETTING THE TIME DURATION OF A WRITE TO A MEMORY CELL	1/19/1996	08/537,711	US
NMXS-0311.01/US	Utility - DIV	Issued	CIRCUIT AND METHOD FOR SETTING THE TIME DURATION OF A WRITE TO A MEMORY CELL	2/9/1998	09/020,430	US
NMXS-0312.00/US	Utility - ORG	Issued	LOW-POWER READ CIRCUIT AND METHOD FOR CONTROLLING A SENSE AMPLIFIER	1/19/1996	08/539,024	US
NMXS-0313.01/US	Utility - DIV	Issued	DEVICE AND METHOD FOR DRIVING A CONDUCTIVE PATH WITH A SIGNAL	9/15/1997	08/929,987	US
NMXS-0313.02/US	Utility - CON	Issued	DEVICE AND METHOD FOR DRIVING A CONDUCTIVE PATH WITH A SIGNAL	11/19/1997	08/974,747	US
NMXS-0314.00/US	Utility - ORG	Issued	CIRCUITRY AND METHODOLOGY TO TEST SINGLE BIT FAILURES OF INTEGRATED CIRCUIT MEMORY DEVICES	8/24/1995	08/519,075	US
NMXS-0315.00/US	Utility - ORG	Issued	ENTIRE WAFER STRESS TEST METHOD FOR INTEGRATED MEMORY DEVICES AND CIRCUIT THEREFOR	8/21/1995	08/484,609	US
NMXS-0316.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR THE PROTECTION OF NON-VOLATILE MEMORY ZONES	3/28/1996	08/623,044	US
NMXS-0317.00/US	Utility - ORG	Issued	PHASE GENERATOR CIRCUIT FOR CHARGE PUMP TYPE OF NEGATIVE SUPPLY CIRCUIT	6/13/1996	08/664,083	US
NMXS-0318.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR THE PROGRAMMING AND ERASURE OF A MEMORY	8/27/1996	08/703,811	US
NMXS-0318.01/US	Utility - DIV	Issued	METHOD AND CIRCUIT FOR THE PROGRAMMING AND ERASURE OF A MEMORY	11/24/1998	09/198,431	US
NMXS-0319.00/US	Utility - ORG	Issued	HIGH VOLTAGE GENERATOR	12/11/1996	08/762,677	US
NMXS-0320.00/US	Utility - ORG	Issued	METHODS AND APPARATUS FOR PROGRAMMING CONTENT-ADDRESSABLE MEMORIES USING FLOATING-GATE MEMORY CELLS	10/25/1996	08/738,231	US
NMXS-0321.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	2/12/1997	08/797,948	US
NMXS-0322.00/US	Utility - ORG	Issued	DEVICE TO NEUTRALIZE AN ELECTRONIC CIRCUIT WHEN IT IS BEING POWERED OR DISCONNECTED	1/24/1997	08/792,962	US

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Modeling	Material Type	Material Status	The	Modeling	Serial No.	Submitter	Patent No.	Country
NMXXS-0322.01/US	Utility - CON	Issued	DEVICE TO NEUTRALIZE AN ELECTRONIC CIRCUIT WHEN IT IS BEING POWERED OR DISCONNECTED	11/17/1998	09/193,735	9/26/2000	6,125,022	US
NMXXS-0323.00/US	Utility - ORG	Issued	FLASH-EPROM WITH EMBEDDED EEPROM	4/10/1997	08/833,925	12/15/1998	5,850,092	US
NMXXS-0323.01/US	Utility - DIV	Issued	FLASH-EPROM WITH EMBEDDED EEPROM	10/1/1998	09/164,761	6/13/2000	6,074,916	US
NMXXS-0324.00/US	Utility - ORG	Issued	DATA SENSING TIMING MODULATING CIRCUIT, PARTICULARLY FOR NON-VOLATILE MEMORIES	3/27/1997	08/825,098	3/9/1999	5,881,001	US
NMXXS-0325.00/US	Utility - ORG	Issued	SYNCHRONIZATION SIGNAL GENERATION CIRCUIT AND METHOD	5/30/1997	08/865,748	9/28/1999	5,969,935	US
NMXXS-0326.00/US	Utility - ORG	Issued	POWER ON RESET CIRCUIT WITH AUTO TURN OFF	4/30/1997	08/846,757	7/27/1999	5,929,674	US
NMXXS-0327.00/US	Utility - ORG	Issued	UPROM CELL FOR LOW VOLTAGE SUPPLY	4/30/1997	08/846,755	10/13/1998	5,822,259	US
NMXXS-0328.00/US	Utility - ORG	Issued	BIASING CIRCUIT FOR UPROM CELLS WITH LOW VOLTAGE SUPPLY	4/30/1997	08/846,753	1/12/1999	5,859,797	US
NMXXS-0329.00/US	Utility - ORG	Issued	LINE DECODER FOR MEMORY DEVICES	5/23/1997	08/862,563	1/25/2000	6,018,255	US
NMXXS-0329.01/US	Utility - DIV	Issued	LINE DECODER FOR MEMORY DEVICES	11/2/1999	09/432,642	7/25/2000	6,094,073	US
NMXXS-0330.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR READING LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY CELLS	6/18/1997	08/879,017	10/3/2000	6,128,225	US
NMXXS-0331.00/US	Utility - ORG	Issued	LOW-SUPPLY-VOLTAGE NONVOLATILE MEMORY DEVICE WITH VOLTAGE BOOSTING	6/18/1997	08/877,927	5/11/1999	5,903,498	US
NMXXS-0332.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR GENERATING A READ REFERENCE SIGNAL FOR NONVOLATILE MEMORY CELLS	6/18/1997	08/877,921	9/8/1998	5,805,500	US
NMXXS-0333.01/US	Utility - CON	Issued	MOS CAPACITOR WITH WIDE VOLTAGE AND FREQUENCY OPERATING RANGES	7/27/2001	09/916,954	7/8/2003	6,590,247	US
NMXXS-0334.00/US	Utility - ORG	Issued	BIDIRECTIONAL CHARGE PUMP GENERATING EITHER A POSITIVE OR NEGATIVE VOLTAGE	7/28/1997	08/900,165	2/6/2001	6,184,741	US
NMXXS-0335.00/US	Utility - ORG	Issued	BICMOS NEGATIVE CHARGE PUMP	11/5/1997	08/965,068	1/18/2000	6,016,073	US
NMXXS-0336.00/US	Utility - ORG	Issued	POSITIVE CHARGE PUMP	10/8/1997	08/946,727	6/13/2000	6,075,402	US
NMXXS-0337.00/US	Utility - ORG	Issued	OUTPUT STAGE FOR A MEMORY DEVICE AND FOR LOW VOLTAGE APPLICATIONS	7/23/1997	08/899,228	4/10/2001	6,215,329	US
NMXXS-0338.00/US	Utility - ORG	Issued	LOW NOISE OUTPUT BUFFER FOR SEMICONDUCTOR ELECTRONIC CIRCUITS	7/8/1997	08/889,653	5/9/2000	6,060,753	US
NMXXS-0339.00/US	Utility - ORG	Issued	HIGH VOLTAGE TOLERANCE OUTPUT STAGE	7/23/1997	08/898,811	11/21/2000	6,150,844	US
NMXXS-0340.00/US	Utility - CON	Issued	SINGLE-CELL REFERENCE SIGNAL GENERATING CIRCUIT FOR READING NONVOLATILE MEMORY	6/17/1997	08/877,066	8/31/1999	5,946,238	US
NMXXS-0341.00/US	Utility - ORG	Issued	INTEGRATED DEVICE WITH PADS	3/5/1997	08/811,577	7/13/1999	5,923,076	US
NMXXS-0342.00/US	Utility - ORG	Issued	NMOS NEGATIVE CHARGE PUMP	1/23/1998	09/012,331	10/10/2000	6,130,572	US
NMXXS-0343.00/US	Utility - ORG	Issued	ADDRESS TRANSITION DETECTION CIRCUIT	3/5/1997	08/811,869	9/29/1998	5,815,464	US
NMXXS-0344.00/US	Utility - ORG	Issued	METHOD FOR RECOVERING FAILED MEMORY DEVICES	3/18/1997	08/816,766	4/25/2000	6,055,665	US
NMXXS-0345.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR GENERATING A POWER-ON RESET SIGNAL	3/27/1997	08/828,791	9/28/1999	5,959,476	US
NMXXS-0346.00/US	Utility - ORG	Issued	GAIN MODULATED SENSE AMPLIFIER	3/27/1997	08/828,790	5/4/1999	5,901,087	US
NMXXS-0346.01/US	Utility - DIV	Issued	GAIN MODULATED SENSE AMPLIFIER, PARTICULARLY FOR MEMORY DEVICES	3/26/1998	09/048,941	6/20/2000	6,078,523	US
NMXXS-0347.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD TO ADJUST MEMORY TIMING	3/28/1997	08/825,138	3/23/1999	5,886,945	US
NMXXS-0348.00/US	Utility - ORG	Issued	STANDY VOLTAGE BOOSTING STAGE AND METHOD FOR A MEMORY DEVICE	3/27/1997	08/826,008	3/20/1999	5,889,723	US
NMXXS-0349.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER FOR MEMORY DEVICES	3/27/1997	08/824,958	9/8/1998	5,805,435	US
NMXXS-0350.00/US	Utility - ORG	Issued	CIRCUIT FOR THE SWITCHING OF SUPPLY VOLTAGES IN ELECTRICALLY PROGRAMMABLE AND CANCELABLE NON-VOLATILE SEMICONDUCTOR MEMORY DEVICES	4/7/1997	08/835,294	5/19/1998	5,754,473	US
NMXXS-0351.00/US	Utility - ORG	Issued	AUTO-SAVING CIRCUIT FOR PROGRAMMING CONFIGURATION ELEMENTS IN NON-VOLATILE MEMORY DEVICES	4/7/1997	08/835,296	1/26/1999	5,864,500	US
NMXXS-0352.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR PROGRAMMING ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY CELLS IN A CELL MATRIX	4/4/1997	08/833,336	1/16/2001	6,175,521	US
NMXXS-0353.00/US	Utility - ORG	Issued	CIRCUIT FOR DETECTING THE COINCIDENCE BETWEEN A BINARY INFORMATION UNIT STORED THEREIN AND AN EXTERNAL DATUM	3/17/1997	08/819,519	9/28/1999	5,959,917	US
NMXXS-0354.00/US	Utility - ORG	Issued	METHOD FOR DETECTING REDUNDANT DEFECTIVE ADDRESSES IN A MEMORY DEVICE WITH REDUNDANCY	4/17/1997	08/841,904	11/17/1998	5,838,623	US



**Exhibit A - US Issued Patents**

Patent No.	Patent Title	Patent Status	Patent Type	Patent Description	Patent Date	Patent Inventor	Patent Assignee	Patent Country
US-2019-012345	Method for detecting redundant defective addresses in a memory device	Issued	Utility - CON	Method for detecting redundant defective addresses in a memory device with redundancy	10/30/1998	09/183,469	Intel Corporation	US
US-2019-012346	Semiconductor memory device with row redundancy	Issued	Utility - ORG	Semiconductor memory device with row redundancy	4/17/1997	08/442,835	Intel Corporation	US
US-2019-012347	Circuit for transferring redundancy data of a redundancy circuit inside a memory device by means of a time-shared approach	Issued	Utility - ORG	Circuit for transferring redundancy data of a redundancy circuit inside a memory device by means of a time-shared approach	4/17/1997	08/844,903	Intel Corporation	US
US-2019-012348	Semiconductor memory device with row and column redundancy	Issued	Utility - ORG	Semiconductor memory device with row and column redundancy	6/5/1997	08/869,859	Intel Corporation	US
US-2019-012349	Semiconductor memory device with clocked column redundancy and time-shared redundancy data transfer approach	Issued	Utility - ORG	Semiconductor memory device with clocked column redundancy and time-shared redundancy data transfer approach	6/3/1997	08/868,214	Intel Corporation	US
US-2019-012350	Memory device with clocked column redundancy	Issued	Utility - ORG	Memory device with clocked column redundancy	6/3/1997	08/868,213	Intel Corporation	US
US-2019-012351	Pre-charge step determining circuit of a generic bit line particularly for non-volatile memories	Issued	Utility - ORG	Pre-charge step determining circuit of a generic bit line particularly for non-volatile memories	4/7/1997	08/835,347	Intel Corporation	US
US-2019-012352	Read circuit and method for non-volatile memory cells with an equalizing structure	Issued	Utility - ORG	Read circuit and method for non-volatile memory cells with an equalizing structure	6/18/1997	08/877,922	Intel Corporation	US
US-2019-012353	Multi-level non-volatile memory device	Issued	Utility - ORG	Multi-level non-volatile memory device	8/22/1997	08/916,874	Intel Corporation	US
US-2019-012354	Floating gate MOS transistor charge injection circuit and computation devices incorporating it	Issued	Utility - ORG	Floating gate MOS transistor charge injection circuit and computation devices incorporating it	9/30/1997	08/940,278	Intel Corporation	US
US-2019-012355	Floating gate MOS transistor charge injection circuit and computation devices incorporating it	Issued	Utility - CON	Floating gate MOS transistor charge injection circuit and computation devices incorporating it	8/13/1999	09/373,813	Intel Corporation	US
US-2019-012356	Floating gate MOS transistor charge injection circuit and computation devices incorporating it	Issued	Utility - CON	Floating gate MOS transistor charge injection circuit and computation devices incorporating it	8/9/2000	09/636,397	Intel Corporation	US
US-2019-012357	Asymmetrical pulse delay network	Issued	Utility - ORG	Asymmetrical pulse delay network	7/25/1997	08/900,424	Intel Corporation	US
US-2019-012358	Control circuit of an output buffer	Issued	Utility - ORG	Control circuit of an output buffer	9/19/1997	08/934,499	Intel Corporation	US
US-2019-012359	Protection circuit for redundancy register set-up cells of electrically programmable non-volatile memory devices	Issued	Utility - ORG	Protection circuit for redundancy register set-up cells of electrically programmable non-volatile memory devices	10/30/1997	08/961,368	Intel Corporation	US
US-2019-012360	Process for realizing an intermediate dielectric layer for enhancing the planarity in semiconductor electronic devices	Issued	Utility - ORG	Process for realizing an intermediate dielectric layer for enhancing the planarity in semiconductor electronic devices	12/9/1997	08/967,454	Intel Corporation	US
US-2019-012361	Process for realizing an intermediate dielectric layer for enhancing the planarity in semiconductor electronic devices	Issued	Utility - DIV	Process for realizing an intermediate dielectric layer for enhancing the planarity in semiconductor electronic devices	8/24/2000	09/645,711	Intel Corporation	US
US-2019-012362	Floating gate non-volatile memory cell with low erasing voltage and manufacturing method	Issued	Utility - ORG	Floating gate non-volatile memory cell with low erasing voltage and manufacturing method	9/29/1997	08/940,856	Intel Corporation	US
US-2019-012363	Floating gate non-volatile memory cell with low erasing voltage and having different potential barriers	Issued	Utility - DIV	Floating gate non-volatile memory cell with low erasing voltage and having different potential barriers	3/8/2000	09/521,867	Intel Corporation	US
US-2019-012364	Method of making floating gate non-volatile memory cell with low erasing voltage	Issued	Utility - DIV	Method of making floating gate non-volatile memory cell with low erasing voltage	3/8/2000	09/521,876	Intel Corporation	US
US-2019-012365	Method of making floating gate non-volatile memory cell with low erasing voltage having double layer gate dielectric	Issued	Utility - DIV	Method of making floating gate non-volatile memory cell with low erasing voltage having double layer gate dielectric	5/30/2002	10/158,706	Intel Corporation	US
US-2019-012366	Method of making floating gate non-volatile memory cell with low erasing voltage having double layer gate dielectric	Issued	Utility - DIV	Method of making floating gate non-volatile memory cell with low erasing voltage having double layer gate dielectric	3/3/2004	10/792,308	Intel Corporation	US
US-2019-012367	Process for depositing a stratified dielectric structure for enhancing the planarity of semiconductor electronic devices	Issued	Utility - ORG	Process for depositing a stratified dielectric structure for enhancing the planarity of semiconductor electronic devices	12/23/1997	08/996,920	Intel Corporation	US
US-2019-012368	Method of planarizing a semiconductor device by depositing a dielectric film structure	Abandoned	Utility - ORG	Method of planarizing a semiconductor device by depositing a dielectric film structure	12/23/1997	08/997,403	Intel Corporation	US
US-2019-012369	Planarization structure and method for dielectric layers	Abandoned	Utility - DIV	Planarization structure and method for dielectric layers	10/13/2000	09/687,217	Intel Corporation	US
US-2019-012370	Reading circuit for semiconductor memory cells	Issued	Utility - ORG	Reading circuit for semiconductor memory cells	9/29/1997	08/940,115	Intel Corporation	US
US-2019-012371	Reading circuit for semiconductor memory cells	Issued	Utility - DIV	Reading circuit for semiconductor memory cells	12/1/1998	09/203,798	Intel Corporation	US
US-2019-012372	Starcase adaptive voltage generator circuit	Issued	Utility - ORG	Starcase adaptive voltage generator circuit	2/26/1998	09/032,282	Intel Corporation	US
US-2019-012373	Method and circuit architecture for testing a non-volatile memory device	Issued	Utility - ORG	Method and circuit architecture for testing a non-volatile memory device	5/30/1997	08/865,642	Intel Corporation	US

Exhibit A - US Issued Patents

Patent No.	Issue Date	Patent No.	Issue Date	Patent No.	Issue Date	Patent No.	Issue Date	Country
NMXS-0375.00/US	Utility - ORG	Issued	SELF-REGULATED EQUALIZER, PARTICULARLY FOR SENSE AMPLIFIERS	10/30/1997	08/960,926	11/16/1999	5,966,954	US
NMXS-0376.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT FOR GENERATING INITIALIZATION SIGNALS FOR MEMORY CELL SENSING CIRCUITS	5/30/1997	08/866,283	6/22/1999	5,914,901	US
NMXS-0377.00/US	Utility - ORG	Issued	CIRCUIT FOR MINIMIZING AN INTEGRATED CIRCUIT FROM NOISE AFFECTING ENABLE SIGNALS OF THE INTEGRATED CIRCUIT	3/4/1997	08/811,548	5/11/1999	5,903,166	US
NMXS-0378.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH A MEMORY CELL ARRAY IN TRIPLE WELL, AND RELATED MANUFACTURING PROCESS	2/20/1998	09/027,343	11/23/1999	5,990,526	US
NMXS-0378.01/US	Utility - DIV	Issued	MEMORY DEVICE WITH A MEMORY CELL ARRAY IN TRIPLE WELL, AND RELATED MANUFACTURING PROCESS	9/3/1999	09/389,955	6/6/2000	6,071,778	US
NMXS-0379.00/US	Utility - ORG	Issued	HIGH VOLTAGE CAPACITOR	7/20/1998	09/119,115	2/13/2001	6,188,121	US
NMXS-0380.00/US	Utility - ORG	Issued	CIRCUIT FOR THE GENERATION AND RESET OF TIMING SIGNAL USED FOR READING A MEMORY DEVICE	3/4/1997	08/811,386	8/11/1998	5,793,699	US
NMXS-0381.00/US	Utility - ORG	Issued	CIRCUIT FOR SELECTIVELY ENABLING ONE AMONG A PLURALITY OF CIRCUIT ALTERNATIVES OF AN INTEGRATED CIRCUIT	10/24/1997	08/957,685	8/8/2000	6,100,740	US
NMXS-0381.01/US	Utility - CON	Issued	CIRCUIT FOR SELECTIVELY ENABLING ONE AMONG A PLURALITY OF CIRCUIT ALTERNATIVES OF AN INTEGRATED CIRCUIT	4/25/2000	09/558,956	2/25/2003	6,525,591	US
NMXS-0381.02/US	Utility - DIV	Issued	METHOD FOR SELECTIVELY ENABLING ALTERNATIVE CIRCUITS	7/6/2000	09/611,258	11/20/2001	6,320,440	US
NMXS-0382.00/US	Utility - ORG	Issued	METHOD FOR VERIFYING ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY AFTER CELLS OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY DEVICE AFTER PROGRAMMING	5/30/1997	08/866,531	1/26/1999	5,864,508	US
NMXS-0383.00/US	Utility - ORG	Issued	ALIGNMENT ETCHING PROCESS FOR REALIZING WORD LINES IN MEMORY DEVICES	12/23/1997	08/997,499	10/10/2000	6,130,165	US
NMXS-0383.01/US	Utility - DIV	Issued	AUTOMATED ETCHING PROCESS FOR REALIZING WORD LINES IN MEMORY DEVICES	3/17/2000	09/528,406	4/30/2002	6,380,582	US
NMXS-0384.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY DEVICE HAVING BIT LINES WITH BLOCK SELECTOR SWITCHES	12/29/1997	08/998,854	10/19/1999	5,969,977	US
NMXS-0384.01/US	Utility - CON	Issued	MEMORY BLOCK FOR REALIZING SEMICONDUCTOR MEMORY DEVICES AND CORRESPONDING MANUFACTURING PROCESS	6/7/1999	09/327,111	8/8/2000	6,101,124	US
NMXS-0384.02/US	Utility - DIV	Issued	ELECTRONIC MEMORY DEVICE HAVING BIT LINES WITH BLOCK SELECTOR SWITCHES	8/13/1999	09/387,103	9/4/2001	6,284,585	US
NMXS-0385.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING AN INTEGRATED CIRCUIT COMPRISING AN ARRAY OF MEMORY CELLS	7/24/1997	08/897,799	11/2/1999	5,976,933	US
NMXS-0385.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING AN INTEGRATED CIRCUIT COMPRISING AN ARRAY OF MEMORY CELLS	7/16/1999	09/356,080	3/5/2002	6,353,243	US
NMXS-0386.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR INCREASING THE INTERNAL ADDRESS OF A MEMORY DEVICE USING MULTIFUNCTIONAL TERMINALS	3/27/1998	09/049,858	9/5/2000	6,115,801	US
NMXS-0387.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR CHECKING MULTILEVEL PROGRAMMING OF FLOATING-GATE NONVOLATILE MEMORY CELLS PARTICULARLY FLASH CELLS	9/30/1997	08/941,882	3/9/1999	5,880,993	US
NMXS-0388.00/US	Utility - ORG	Abandoned	METHOD OF PRODUCING MOSFET TRANSISTORS BY MEANS OF TILTED IMPLANTS	4/20/1998	09/062,859	6/22/1999	5,915,185	US
NMXS-0389.00/US	Utility - ORG	Issued	METHOD FOR SAVING DATA IN THE EVENT OF UNWANTED INTERRUPTIONS IN THE PROGRAMMING CYCLE OF A NONVOLATILE MEMORY, AND A NONVOLATILE MEMORY	4/29/1999	09/302,231	9/5/2000	6,115,313	US
NMXS-0390.00/US	Utility - ORG	Issued	PROCESS FOR THE REPAIR OF FLOATING-GATE NON-VOLATILE MEMORIES DAMAGED BY PLASMA TREATMENT	12/15/1997	08/990,328	3/30/1999	5,888,836	US
NMXS-0391.00/US	Utility - ORG	Issued	VOLTAGE LEVEL SHIFTER DEVICE, PARTICULARLY FOR A NONVOLATILE MEMORY	2/25/1998	09/030,604	9/28/1999	5,959,902	US
NMXS-0392.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING A NATIVE MOS P-CANAL TRANSISTOR WITH A PROCESS FOR MANUFACTURING NON-VOLATILE MEMORIES	8/26/1998	09/139,909	5/16/2000	6,063,663	US

PATENT

REEL: 033282 FRAME: 0354

**Exhibit A - US Issued Patents**

Part Number	Material Type	Material Status	Title	File	File Path	File Date	Serial No.	Issued Date	Part Name	Country
NMXXS-0394.00/US	Utility - ORG	Issued	METHOD OF MANUFACTURING AN EPROM MEMORY DEVICE HAVING MEMORY CELLS ORGANIZED IN A TABLECLOTH MATRIX			8/27/1998	09/141,849	12/4/2001	6,326,266	US
NMXXS-0395.00/US	Utility - ORG	Issued	SWITCHING MASTER SLAVE CIRCUIT			1/19/1996	08/588,648	7/21/1998	5,783,958	US
NMXXS-0396.00/US	Utility - ORG	Issued	MULTIPLE ACCESS MEMORY DEVICE			12/31/1996	08/775,664	7/21/1998	5,784,331	US
NMXXS-0397.00/US	Utility - CIP	Issued	LOW CURRENT CRYSTAL OSCILLATOR WITH FAST START-UP TIME			5/3/1996	08/642,271	9/8/1998	5,805,027	US
NMXXS-0398.00/US	Utility - ORG	Issued	EXTERNAL WRITE PULSE CONTROL METHOD AND STRUCTURE			12/21/1996	08/771,642	6/9/1998	5,764,592	US
NMXXS-0399.00/US	Utility - ORG	Issued	BURN-IN STRESS TEST MODE			12/21/1996	08/771,645	3/14/2000	6,037,792	US
NMXXS-0399.01/US	Utility - DIV	Issued	INTEGRATED CIRCUIT DEVICE HAVING A BURN-IN MODE FOR WHICH ENTRY INTO AND EXIT FROM CAN BE CONTROLLED			1/27/2000	09/491,664	10/30/2001	6,310,485	US
NMXXS-0399.02/US	Utility - DIV	Issued	INTEGRATED CIRCUIT DEVICE HAVING A BURN-IN MODE FOR WHICH ENTRY INTO AND EXIT FROM CAN BE CONTROLLED			8/17/2001	09/931,820	2/11/2003	6,518,746	US
NMXXS-0400.00/US	Utility - ORG	Issued	INPUT DELAY CONTROL			12/23/1996	08/780,053	2/17/1998	5,719,445	US
NMXXS-0401.00/US	Utility - ORG	Issued	SENSE AMPLIFIER CONTROL OF A MEMORY DEVICE			4/25/1997	08/844,696	9/28/1999	5,959,910	US
NMXXS-0402.00/US	Utility - CIP	Issued	CIRCUIT AND METHOD FOR SELECTING A SIGNAL			11/27/1996	08/758,587	3/14/2000	6,037,799	US
NMXXS-0403.00/RE	Utility - REIS	Issued	EDGE TRANSITION DETECTION CIRCUITRY FOR USE WITH TEST MODE OPERATION OF AN INTEGRATED CIRCUIT MEMORY DEVICE			5/9/2002	10/145,606	5/9/2000	RE-40,282	US
NMXXS-0403.00/US	Utility - ORG	Issued	EDGE TRANSITION DETECTION CIRCUITRY FOR USE WITH TEST MODE OPERATION OF AN INTEGRATED CIRCUIT MEMORY DEVICE			12/21/1996	08/771,643	5/9/2000	6,059,450	US
NMXXS-0404.00/US	Utility - CIP	Issued	INTEGRATED CIRCUIT THAT SUPPORTS AND METHOD FOR WATER-LEVEL TESTING			9/17/1996	08/710,357	9/15/1998	5,808,947	US
NMXXS-0405.00/US	Utility - ORG	Issued	DEVICE FOR READING CELLS OF A MEMORY			6/12/1997	08/873,502	7/13/1999	5,923,590	US
NMXXS-0406.00/US	Utility - ORG	Issued	PARALLEL-ACCESS MEMORY AND METHOD			5/9/1997	08/853,526	7/4/2000	6,085,280	US
NMXXS-0407.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE MULTILEVEL NON-VOLATILE MEMORY COMPRISING INTERNAL REFRESH MEANS			10/24/1997	08/957,715	4/11/2000	6,049,497	US
NMXXS-0408.00/US	Utility - ORG	Issued	READ CIRCUIT FOR NON-VOLATILE MEMORY WORKING WITH A LOW SUPPLY VOLTAGE			9/72/1997	08/934,696	1/12/1999	5,859,798	US
NMXXS-0409.00/US	Utility - ORG	Issued	DEVICE FOR THE PROTECTION OF STORED DATA			8/21/1997	08/916,054	7/28/1998	5,787,040	US
NMXXS-0410.00/US	Utility - ORG	Issued	CURRENT REFERENCE DEVICE IN INTEGRATED CIRCUIT FORM			1/30/1997	08/791,383	5/11/1999	5,903,141	US
NMXXS-0411.00/US	Utility - ORG	Issued	DEVICE FOR THE PROTECTION OF STORED DATA USING A TIME DELAY CIRCUIT			8/21/1997	08/915,855	9/12/2000	6,119,210	US
NMXXS-0412.00/US	Utility - ORG	Issued	MEMORY WITH IMPROVED READING TIME			10/24/1997	08/957,666	2/9/1999	5,870,336	US
NMXXS-0413.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR POWER-ON/POWER-OFF CHECKING OF AN INTEGRATED CIRCUIT			12/18/1997	08/993,071	8/28/2001	6,281,723	US
NMXXS-0414.00/US	Utility - ORG	Issued	OSCILLATOR AND SWITCH-OVER CONTROL CIRCUIT FOR A HIGH-VOLTAGE GENERATOR			12/22/1997	08/995,667	11/14/2000	6,147,566	US
NMXXS-0414.01/US	Utility - DIV	Issued	OSCILLATOR AND SWITCH-OVER CONTROL CIRCUIT FOR A HIGH-VOLTAGE GENERATOR.			5/25/2000	09/578,778	4/16/2002	6,373,311	US
NMXXS-0415.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE FLOATING-GATE MEMORY ORGANIZED IN WORDS			3/24/1999	09/275,327	8/1/2000	6,097,631	US
NMXXS-0416.00/US	Utility - ORG	Abandoned	METHOD AND DEVICE FOR THE PREDICTIVE READING OF A MEMORY			12/16/1997	08/991,693	10/24/2000	6,138,220	US
NMXXS-0417.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR THE INCREMENTAL READING OF A MEMORY			12/16/1997	08/991,428	1/23/2001	6,178,490	US
NMXXS-0418.00/US	Utility - ORG	Issued	MEMORY WITH READ PROTECTED ZONES			12/19/1997	08/994,889	12/14/1999	6,002,619	US
NMXXS-0419.00/US	Utility - ORG	Issued	A READ CIRCUIT FOR MEMORY ADAPTED TO THE MEASUREMENT OF LEAKAGE CURRENTS			2/27/1998	09/031,748	3/30/1999	5,889,702	US
NMXXS-0420.00/US	Utility - ORG	Issued	LOAD PUMP TYPE OF VOLTAGE GENERATOR CIRCUIT			4/13/1998	09/059,088	10/19/1999	5,969,961	US
NMXXS-0421.00/US	Utility - ORG	Issued	DIFFERENTIAL AMPLIFIER IN A MOS TRANSISTORS STRUCTURE			11/6/1998	09/188,071	10/31/2000	6,140,876	US
NMXXS-0422.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR THE FILTERING OF A PULSE SIGNAL			5/29/1998	09/087,478	10/12/1999	5,966,034	US
NMXXS-0423.00/US	Utility - ORG	Issued	RING OSCILLATOR USING CMOS TECHNOLOGY			1/12/1998	09/005,722	10/3/2000	6,127,898	US
NMXXS-0424.00/US	Utility - ORG	Issued	PAGE-WRITE INDICATOR FOR NON-VOLATILE MEMORY			1/20/1998	09/009,088	9/28/1999	5,959,886	US
NMXXS-0425.00/US	Utility - ORG	Issued	WORD ADDRESSABLE FLOATING-GATE MEMORY COMPRISING A REFERENCE VOLTAGE GENERATOR CIRCUIT FOR THE VERIFICATION OF THE CONTENTS OF A WORD			3/6/1998	09/035,654	9/14/1999	5,953,253	US

PATENT  
3282 FRAME: 0356

**REEL: 033282 FRAME: 0356**

Item Details	Material Type	Water Status	HS Code	Patent No.	Issue Date	Serial No.	Issue Date	Patent No.	Country
NMXXS-0455.00/US	Utility - ORG	Issued	HIGH VOLTAGE DRIVER CIRCUIT FOR A DECODING CIRCUIT IN MULTILEVEL NON-VOLATILE MEMORY DEVICES		12/31/1998	09/224,330	2/72/2000	6,028,793	US
NMXXS-0456.00/US	Utility - ORG	Issued	MONOETHRICALLY INTEGRATED SELECTOR FOR ELECTRICALLY PROGRAMMABLE MEMORY CELL DEVICES		5/28/1999	09/321,961	6/5/2001	6,242,971	US
NMXXS-0457.00/US	Utility - ORG	Issued	CONTROLLED HOT-ELECTRON WRITING METHOD FOR NON-VOLATILE MEMORY CELLS		10/8/1998	09/169,239	1/9/2001	6,172,908	US
NMXXS-0458.00/US	Utility - ORG	Issued	SELF-TEST AND CORRECTION OF LOSS OF CHARGE ERRORS IN A FLASH MEMORY, ERASABLE AND PROGRAMMABLE BY SECTORS THEREOF		12/16/1998	09/712,896	8/14/2001	6,275,960	US
NMXXS-0459.00/US	Utility - ORG	Issued	COMPENSATED VOLTAGE REGULATOR		9/30/1998	09/163,755	11/9/1999	5,982,677	US
NMXXS-0460.00/US	Utility - ORG	Issued	METHOD FOR MULTILEVEL PROGRAMMING OF A NONVOLATILE MEMORY, AND A MULTILEVEL NONVOLATILE MEMORY		11/3/1998	09/185,906	1/4/2000	6,011,715	US
NMXXS-0461.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR GENERATING AN A/D SIGNAL TO REGULATE THE ACCESS TO A NON-VOLATILE MEMORY		11/4/1998	09/186,497	6/13/2000	6,075,750	US
NMXXS-0462.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR REGULATING THE LENGTH OF AN A/D PULSE SIGNAL		11/4/1998	09/186,496	1/2/2001	6,169,423	US
NMXXS-0463.00/US	Utility - ORG	Issued	BOOSTING CIRCUIT PARTICULAR FOR A MEMORY DEVICE		11/5/1998	09/186,758	11/7/2000	6,144,589	US
NMXXS-0464.00/US	Utility - ORG	Issued	IMPROVED OUTPUT CIRCUIT FOR INTEGRATED CIRCUITS		10/13/1998	09/170,788	11/28/2000	6,153,914	US
NMXXS-0465.00/US	Utility - ORG	Issued	MEMORY CELL VOLTAGE REGULATOR WITH TEMPERATURE CORRELATED VOLTAGE GENERATOR CIRCUIT		11/4/1998	09/186,498	2/6/2001	6,184,670	US
NMXXS-0467.00/US	Utility - ORG	Issued	HIGH-VOLTAGE PUMP ARCHITECTURE FOR INTEGRATED ELECTRONIC DEVICES		1/29/1999	09/739,908	8/14/2001	6,275,099	US
NMXXS-0468.00/US	Utility - ORG	Issued	CIRCUIT FOR DISCHARGING A NEGATIVE POTENTIAL NODE TO GROUND, INCLUDING CONTROL OF THE DISCHARGE CURRENT		3/24/1999	09/775,200	6/19/2001	6,249,172	US
NMXXS-0469.00/US	Utility - ORG	Issued	SWITCHING CIRCUIT		3/24/1999	09/775,694	5/16/2000	6,064,988	US
NMXXS-0470.00/US	Utility - ORG	Issued	DATA PROTECTION METHOD FOR A SEMICONDUCTOR MEMORY AND CORRESPONDING PROTECTED MEMORY DEVICE		12/23/1998	09/720,127	9/4/2001	6,286,086	US
NMXXS-0471.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR SINGLE FEED VOLTAGE MEMORY CIRCUITS, AND FLASH TYPE MEMORY IN PARTICULAR		11/20/1998	09/196,204	8/8/2000	6,101,118	US
NMXXS-0471.01/US	Utility - DIV	Issued	VOLTAGE REGULATOR FOR SINGLE FEED VOLTAGE MEMORY CIRCUITS, AND FLASH TYPE MEMORIES IN PARTICULAR		6/26/2000	09/602,669	9/4/2001	6,285,614	US
NMXXS-0472.00/US	Utility - ORG	Issued	ELECTRONIC CIRCUIT FOR GENERATING A STABLE VOLTAGE SIGNAL FOR POLARIZING DURING A READING STEP UPROM MEMORY CELLS OPERATING AT LOW FEED VOLTAGE		12/21/1998	09/718,796	3/20/2001	6,204,722	US
NMXXS-0473.00/US	Utility - ORG	Issued	ROW DECODER CIRCUIT FOR AN ELECTRONIC MEMORY DEVICE, PARTICULARLY FOR LOW VOLTAGE APPLICATIONS		12/29/1998	09/722,022	5/30/2000	6,069,837	US
NMXXS-0474.00/US	Utility - ORG	Issued	METHOD AND A RELATED CIRCUIT FOR ADJUSTING THE DURATION OF A SYNCHRONIZATION SIGNAL A/D FOR TIMING THE ACCESS TO A NON-VOLATILE MEMORY		12/29/1998	09/722,070	5/22/2001	6,237,104	US
NMXXS-0475.00/US	Utility - ORG	Issued	DRIVING CIRCUIT WITH THREE OUTPUT LEVELS, ONE OUTPUT LEVEL BEING A BOOSTED LEVEL		1/19/1999	09/724,016	12/5/2000	6,157,225	US
NMXXS-0476.00/US	Utility - ORG	Abandoned	MEMORY CELL INTEGRATED STRUCTURE AND CORRESPONDING BIASING DEVICE		4/21/1999	09/729,667	11/21/2000	6,151,251	US
NMXXS-0477.00/US	Utility - DIV	Abandoned	MEMORY CELL INTEGRATED STRUCTURE WITH CORRESPONDING BIASING DEVICE		9/29/2000	09/675,985	10/16/2001	6,304,490	US
NMXXS-0478.00/US	Utility - ORG	Issued	SWITCHING CIRCUIT HAVING AN OUTPUT VOLTAGE VARYING BETWEEN A REFERENCE VOLTAGE AND A NEGATIVE VOLTAGE		3/24/1999	09/725,255	2/29/2000	6,031,761	US
NMXXS-0479.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TRIMMING THE INTERNAL TIMING CONDITIONS OF A SEMICONDUCTOR MEMORY DEVICE		2/26/1998	09/032,272	12/28/1999	6,009,041	US
NMXXS-0480.00/US	Utility - ORG	Issued	STRUCTURE AND METHOD FOR EVALUATING AN INTEGRATED ELECTRONIC DEVICE		12/9/1998	09/709,049	11/6/2001	6,313,480	US
NMXXS-0480.00/US	Utility - ORG	Issued	LOW CONSUMPTION TTL-CMOS INPUT BUFFER STAGE		12/30/1998	09/731,130	10/23/2001	6,307,396	US

REEL: 033282 FRAME: 0357

Exhibit A - US Issued Patents

Patent No.	Material	Material	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMMS-0481.00/US	Utility - NSPCT	Issued	MONOLITHICALLY INTEGRATED SELECTOR FOR ELECTRICALLY PROGRAMMABLE MEMORY CELLS DEVICES	6/30/1999	09/674,395	9/11/2001	6,288,594	US
NMMS-0482.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY CAPABLE OF AUTONOMOUSLY EXECUTING A PROGRAM	7/8/1999	09/349,702	7/11/2003	6,587,914	US
NMMS-0483.00/US	Utility - ORG	Issued	OPERATING VOLTAGE SELECTION CIRCUIT FOR NON-VOLATILE SEMICONDUCTOR MEMORIES	1/13/1999	09/729,474	8/8/2000	6,101,127	US
NMMS-0484.00/US	Utility - ORG	Issued	METHOD FOR CONTROLLED ERASING MEMORY DEVICES, IN PARTICULAR ANALOG AND MULTI-LEVEL FLASH-EEPROM DEVICES	1/21/1999	09/734,942	7/18/2000	6,093,642	US
NMMS-0485.00/US	Utility - ORG	Issued	ESD PROTECTION NETWORK FOR CIRCUIT STRUCTURES FORMED IN A SEMICONDUCTOR	12/30/1998	09/723,621	7/24/2001	6,266,222	US
NMMS-0486.00/US	Utility - ORG	Issued	METHOD AND A CIRCUIT FOR IMPROVING THE EFFECTIVENESS OF ESD PROTECTION IN CIRCUIT STRUCTURES FORMED IN A SEMICONDUCTOR	12/30/1998	09/731,129	6/5/2001	6,242,793	US
NMMS-0486.01/US	Utility - DIV	Issued	METHOD FOR IMPROVING THE EFFECTIVENESS OF ESD PROTECTION IN CIRCUIT STRUCTURES FORMED IN A SEMICONDUCTOR SUBSTRATE	4/17/2001	09/837,137	4/16/2002	6,372,597	US
NMMS-0487.00/US	Utility - ORG	Issued	HIGH STORAGE CAPACITY NON-VOLATILE MEMORY	5/26/1999	09/320,315	11/14/2000	6,147,902	US
NMMS-0488.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING A SEMICONDUCTOR NON-VOLATILE MEMORY DEVICE WITH SHALLOW TRENCH ISOLATION (STI)	6/18/1999	09/336,089	4/17/2001	6,218,265	US
NMMS-0489.00/US	Utility - ORG	Issued	CIRCUIT DEVICE AND CORRESPONDING METHOD FOR PROGRAMMING A NONVOLATILE MEMORY CELL HAVING A SINGLE VOLTAGE SUPPLY	5/28/1999	09/322,644	8/29/2000	6,111,791	US
NMMS-0490.00/US	Utility - ORG	Issued	ELECTRONIC COUNTER FOR A NON-VOLATILE MEMORY DEVICE INTEGRATED ON A SEMICONDUCTOR	3/4/1999	09/262,500	3/27/2001	6,208,705	US
NMMS-0491.00/US	Utility - ORG	Issued	EDGE TRANSITION DETECTION CONTROL OF A MEMORY DEVICE	12/30/1997	08/000,547	11/30/1999	5,995,444	US
NMMS-0492.01/US	Utility - CON	Abandoned	VSS SWITCHING SCHEME FOR BATTERY BACKED-UP SEMICONDUCTOR DEVICES	10/29/1999	09/429,964	8/22/2000	6,107,865	US
NMMS-0493.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR READING A MEMORY CELL	5/29/1998	09/087,399	11/30/1999	5,995,421	US
NMMS-0494.00/US	Utility - ORG	Issued	READ CIRCUIT FOR NON VOLATILE MEMORIES	10/29/1998	09/182,843	8/1/2000	6,097,633	US
NMMS-0495.00/US	Utility - ORG	Issued	IN-SITU DEPOSITION AND DOPING PROCESS FOR POLYCRYSTALLINE SILICON LAYERS AND THE RESULTING DEVICE	11/13/1998	09/191,743	3/15/2005	6,867,113	US
NMMS-0496.00/US	Utility - ORG	Issued	TEMPERATURE STABLE CURRENT GENERATION	2/5/1999	09/246,029	10/17/2000	6,133,718	US
NMMS-0497.00/US	Utility - ORG	Issued	BITLENE BIAS CIRCUIT FOR NON-VOLATILE MEMORY DEVICES	2/23/1999	09/256,648	4/11/2000	6,049,491	US
NMMS-0498.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR NON-VOLATILE MEMORY DEVICES	2/23/1999	09/256,603	7/25/2000	6,094,394	US
NMMS-0499.00/US	Utility - ORG	Issued	UPROM MEMORY CELLS FOR NON-VOLATILE MEMORY DEVICES INTEGRATED ON SEMICONDUCTORS	3/25/1999	09/276,263	8/7/2001	6,271,571	US
NMMS-0500.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR LOW VOLTAGE MEMORIES	2/12/1999	09/249,834	10/15/2002	6,466,059	US
NMMS-0501.00/US	Utility - ORG	Issued	LOW CONSUMPTION BOOSTED VOLTAGE DRIVING CIRCUIT	2/26/1999	09/257,682	10/10/2000	6,130,844	US
NMMS-0502.00/US	Utility - ORG	Issued	VOLTAGE BOOSTING CIRCUIT FOR GENERATING BOOSTED VOLTAGE PHASES	2/26/1999	09/258,224	5/16/2000	6,064,594	US
NMMS-0503.00/US	Utility - ORG	Issued	VOLTAGE PHASE GENERATOR WITH INCREASED DRIVING CAPACITY	2/26/1999	09/257,684	3/6/2001	6,198,672	US
NMMS-0504.00/US	Utility - ORG	Issued	PIPELINED NON-BLOCKING LEVEL TWO CACHE SYSTEM WITH INHERENT TRANSACTION COLLISION-AVOIDANCE	12/4/1998	09/205,598	2/11/2003	6,519,682	US
NMMS-0505.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY HAVING A PROTECTABLE ZONE AND AN ELECTRONIC SYSTEM INCLUDING THE MEMORY	10/23/1998	09/177,889	3/7/2000	6,034,889	US
NMMS-0506.00/US	Utility - ORG	Issued	METHOD FOR WRITING IN AN ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY	10/27/1998	09/179,621	3/21/2000	6,040,994	US
NMMS-0507.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR THE GENERATION OF PROGRAMMING AND ERASURE VOLTAGE IN A NON-VOLATILE MEMORY	9/18/1998	09/156,945	11/30/1999	5,995,416	US
NMMS-0508.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORIES PROGRAMMABLE BY ""HOT CARRIER"" TUNNEL EFFECT AND ERASABLE BY TUNNEL EFFECT	10/15/1998	09/173,136	10/3/2000	6,128,222	US
NMMS-0509.00/US	Utility - ORG	Issued	NON-VOLATILE ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY	11/25/1998	09/199,671	12/7/1999	5,999,447	US
NMMS-0510.00/US	Utility - ORG	Issued	DETECTOR OF RADIO FREQUENCY SIGNALS FOR CONTACTLESS CHIP CARDS	9/18/1998	09/157,060	11/28/2000	6,152,373	US

PATENT

REEL: 033282 FRAME: 0358



**PATENT**  
**3282 FRAME: 0359**

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**3282 FRAME: 0359**

Exhibit A - US Issued Patents

Patent No.	Patent Title	Patent Status	Patent Description	Filed Date	Serial No.	Issued Date	Patent No.	Country
NMXS-0535.00/US	Utility - ORG	Abandoned	PROCESS FOR THE DEFINITION OF OPENINGS IN A DIELECTRIC LAYER	9/28/1999	09/406,903	11/6/2001	6,313,040	US
NMXS-0536.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NON-VOLATILE MEMORY CELLS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	9/9/1999	09/392,937	8/28/2001	6,281,077	US
NMXS-0536.01/US	Utility - DIV	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NON-VOLATILE MEMORY CELLS AND LV TRANSISTORS WITH SALICIDED JUNCTIONS	4/16/2001	09/836,590	5/28/2002	6,396,101	US
NMXS-0537.00/US	Utility - ORG	Issued	SELECTIVE SALICIDATION PROCESS FOR ELECTRONIC DEVICES INTEGRATED IN A SEMICONDUCTOR SUBSTRATE	12/28/1999	09/473,367	8/5/2003	6,602,774	US
NMXS-0538.00/US	Utility - ORG	Issued	METHOD FOR CORRECTION OF ERRORS IN A BINARY WORD STORED IN MULTILEVEL MEMORY CELLS, NOT REQUIRING ADDITIONAL CELLS	2/24/2000	09/513,286	3/4/2003	6,530,058	US
NMXS-0539.00/US	Utility - ORG	Issued	NONVOLATILE MULTILEVEL MEMORY AND READING METHOD THEREFORE	2/9/2000	09/501,131	9/24/2002	6,456,527	US
NMXS-0540.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TESTING MEMORY CELLS IN A MULTILEVEL MEMORY DEVICE	10/7/1999	09/415,024	10/9/2001	6,301,157	US
NMXS-0541.00/US	Utility - ORG	Issued	DEVICE FOR READING NON-VOLATILE MEMORY CELLS, IN PARTICULAR ANALOG FLASH MEMORY CELLS	10/19/1999	09/421,616	11/27/2001	6,323,799	US
NMXS-0542.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR PROGRAMMING NONVOLATILE MEMORY CELLS WITH AUTOMATIC GENERATION OF PROGRAMMING VOLTAGE	11/12/1999	09/438,232	10/15/2002	6,466,481	US
NMXS-0543.00/US	Utility - ORG	Issued	CIRCUIT FOR PARALLEL PROGRAMMING NONVOLATILE MEMORY CELLS, WITH ADJUSTABLE PROGRAMMING SPEED	11/23/1999	09/447,531	12/19/2000	6,163,483	US
NMXS-0544.00/US	Utility - ORG	Issued	CIRCUIT FOR HIGH-PRECISION ANALOG READING OF NONVOLATILE MEMORY CELLS, IN PARTICULAR ANALOG OR MULTILEVEL FLASH OR EEPROM MEMORY CELLS	11/12/1999	09/438,823	10/3/2000	6,128,228	US
NMXS-0545.00/US	Utility - ORG	Issued	PROCESS FOR INTEGRATING IN A SAME CHIP A NON-VOLATILE MEMORY AND A HIGH-PERFORMANCE LOGIC CIRCUITRY	11/24/1999	09/449,044	6/25/2002	6,410,387	US
NMXS-0545.01/US	Utility - DIV	Issued	PROCESS FOR INTEGRATING IN A SAME CHIP A NON-VOLATILE MEMORY AND A HIGH-PERFORMANCE LOGIC CIRCUITRY	5/29/2002	10/158,424	3/30/2004	6,713,347	US
NMXS-0546.00/US	Utility - ORG	Issued	METHOD FOR MAINTAINING THE MEMORY CONTENT OF NON-VOLATILE MEMORY CELLS	9/15/1999	09/397,387	1/2/2001	6,169,691	US
NMXS-0547.00/US	Utility - ORG	Issued	METHOD OF FABRICATION OF A NO-FIELD MOS TRANSISTOR	4/5/2000	09/543,400	2/26/2002	6,350,637	US
NMXS-0548.00/US	Utility - CIP	Issued	DEVICE FOR READING ANALOG NONVOLATILE MEMORY CELLS, IN PARTICULAR FLASH CELLS	10/22/1999	09/425,446	2/27/2001	6,195,289	US
NMXS-0549.00/US	Utility - ORG	Issued	CAPACITIVE BOOSTING CIRCUIT FOR THE REGULATION OF THE WORD LINE READING VOLTAGE IN NON-VOLATILE MEMORIES	1/19/2000	09/491,476	7/10/2001	6,259,635	US
NMXS-0550.00/US	Utility - ORG	Issued	CAPACITIVE COMPENSATION CIRCUIT FOR THE REGULATION OF THE WORD LINE READING VOLTAGE IN NON-VOLATILE MEMORIES	1/19/2000	09/491,475	7/10/2001	6,259,632	US
NMXS-0551.00/US	Utility - ORG	Issued	CHIP OUTLINE BAND (COB) STRUCTURE FOR INTEGRATED CIRCUITS	1/14/2000	09/483,656	10/8/2002	6,462,400	US
NMXS-0552.00/US	Utility - ORG	Issued	IMPROVED FIELD-EFFECT TRANSISTOR AND CORRESPONDING MANUFACTURING METHOD	11/18/1999	09/442,834	5/14/2002	6,387,763	US
NMXS-0552.01/US	Utility - DIV	Issued	FIELD-EFFECT TRANSISTOR AND CORRESPONDING MANUFACTURING METHOD	3/4/2002	10/090,978	5/18/2004	6,737,715	US
NMXS-0553.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR DRIVING PLURAL LOADS BASED ON THE NUMBER OF LOADS BEING DRIVEN	12/20/1999	09/467,726	5/15/2001	6,232,753	US
NMXS-0554.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC DEVICES, COMPRISING NON-SALICIDED NON-VOLATILE MEMORY CELLS, NON-SALICIDED HV TRANSISTORS, AND LV TRANSISTORS WITH SALICIDED JUNCTIONS WITH FEW MASKS	12/21/1999	09/469,849	8/14/2001	6,214,411	US
NMXS-0555.00/US	Utility - ORG	Issued	METHOD FOR HIGH PRECISION PROGRAMMING NONVOLATILE MEMORY CELLS, WITH OPTIMIZED PROGRAMMING SPEED	11/24/1999	09/449,188	5/21/2002	6,392,931	US
NMXS-0556.00/US	Utility - ORG	Issued	PROCESS OF MANUFACTURE OF A NON VOLATILE MEMORY WITH ELECTRIC CONTINUITY OF THE COMMON SOURCE LINES	4/12/2000	09/547,520	9/25/2001	6,294,431	US
NMXS-0557.00/US	Utility - ORG	Issued	FLASH COMPATIBLE EEPROM	6/21/2000	09/599,356	4/24/2001	6,222,775	US

PATENT

REEL: 033282 FRAME: 0360



Exhibit A - US issued Patents

Pat. No.	Metaphor	Notes	Abstract	Pub. No.	Serial No.	Issue Date	Priority	Country
NMXS-0558.00/US	Utility - ORG	Issued	METHOD FOR READING A MEMORY, PARTICULARLY A NON-VOLATILE MEMORY	12/29/1999	09/474,932	11/5/2002	6,477,625	US
NMXS-0559.00/US	Utility - ORG	Issued	SYNCHRONOUS OUTPUT BUFFER, PARTICULARLY FOR NON-VOLATILE MEMORIES	11/20/2000	09/716,759	7/23/2002	6,424,575	US
NMXS-0560.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SEMICONDUCTOR INTEGRATED MEMORY DEVICES WITH CELLS HAVING VIRTUAL GROUND	2/18/2000	09/507,777	4/2/2002	6,365,456	US
NMXS-0561.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SEMICONDUCTOR INTEGRATED MEMORY DEVICES WITH CELLS HAVING VIRTUAL GROUND	2/25/2000	09/512,900	10/9/2001	6,300,195	US
NMXS-0562.00/US	Utility - ORG	Abandoned	BIT COUNTER STAGE, PARTICULARLY FOR MEMORIES	12/15/1999	09/464,631	11/27/2001	6,324,238	US
NMXS-0563.00/US	Utility - ORG	Issued	PULSE GENERATOR CIRCUIT, PARTICULARLY FOR NON-VOLATILE MEMORIES	12/27/1999	09/472,496	8/14/2001	6,275,416	US
NMXS-0564.00/US	Utility - ORG	Issued	PRIORITY DETERMINING CIRCUIT FOR NON-VOLATILE MEMORY	2/8/2000	09/499,799	4/16/2002	6,373,781	US
NMXS-0565.00/US	Utility - ORG	Issued	METHOD FOR AUTOALIGNING OVERLAPPED LINES OF A CONDUCTIVE MATERIAL PROVIDED IN INTEGRATED ELECTRONIC CIRCUITS	5/26/2000	09/579,778	2/26/2002	6,350,671	US
NMXS-0566.00/US	Utility - ORG	Issued	METHOD OF ADJUSTING PROGRAM VOLTAGE IN NON-VOLATILE MEMORIES, AND PROCESS FOR FABRICATING A NON-VOLATILE MEMORY DEVICE	4/20/2000	09/552,933	3/18/2003	6,535,431	US
NMXS-0566.01/US	Utility - DIV	Issued	METHOD OF ADJUSTING PROGRAM VOLTAGE IN NON-VOLATILE MEMORIES, AND PROCESS FOR FABRICATING A NON-VOLATILE MEMORY DEVICE	1/24/2003	10/350,745	10/12/2004	6,803,630	US
NMXS-0567.00/US	Utility - ORG	Issued	ADDRESS LATCH ENABLE SIGNAL CONTROL CIRCUIT FOR ELECTRONIC MEMORIES	12/8/1999	09/457,500	6/19/2001	6,249,463	US
NMXS-0569.00/US	Utility - ORG	Issued	SEMICONDUCTOR DEVICE WITH IMPROVED INTERCONNECTIONS BETWEEN THE CHIP AND THE TERMINALS, AND PROCESS FOR ITS MANUFACTURE	2/10/2000	09/502,070	5/28/2002	6,396,132	US
NMXS-0570.00/US	Utility - ORG	Issued	Method for reading a multilevel nonvolatile memory and multilevel nonvolatile memory	2/25/2000	09/513,598	10/9/2001	6,301,149	US
NMXS-0571.00/US	Utility - ORG	Issued	VOLTAGE REGULATING CIRCUIT FOR A CAPACITIVE LOAD	6/29/2000	09/608,445	6/19/2001	6,249,112	US
NMXS-0572.00/US	Utility - ORG	Issued	APPARATUS AND METHOD FOR SWITCHING BETWEEN TWO POWER SUPPLIES OF AN INTEGRATED CIRCUIT	12/21/1998	09/217,321	9/12/2000	6,118,188	US
NMXS-0573.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR INCREASING COMPARATOR GAIN WITHOUT AFFECTING STANDBY CURRENT	12/21/1998	09/217,323	7/4/2000	6,084,390	US
NMXS-0574.00/US	Utility - ORG	Issued	METHOD AND STRUCTURE FOR ENHANCING THE ACCESS TIME OF INTEGRATED MEMORY DEVICES	10/30/1998	09/183,231	8/31/1999	5,946,264	US
NMXS-0575.00/US	Utility - ORG	Issued	CONTROL CIRCUIT FOR TERMINATING A MEMORY ACCESS CYCLE IN A MEMORY BLOCK OF AN ELECTRONIC STORAGE DEVICE	10/30/1998	09/183,589	3/7/2000	6,034,917	US
NMXS-0576.00/US	Utility - ORG	Issued	STRESS TEST MODE ENTRY AT POWER UP FOR LOW/ZERO POWER MEMORIES	10/30/1998	09/183,451	6/27/2000	6,081,466	US
NMXS-0577.00/US	Utility - ORG	Issued	INITIALIZATION FOR FUSE CONTROL	10/30/1998	09/183,840	3/21/2000	6,041,000	US
NMXS-0578.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT STRUCTURE COMPRISING CAPACITOR ELEMENT AND CORRESPONDING MANUFACTURING PROCESS	10/12/1999	09/415,992	9/25/2001	6,294,798	US
NMXS-0578.01/US	Utility - DIV	Issued	INTEGRATED CIRCUIT STRUCTURE COMPRISING CAPACITOR ELEMENT AND CORRESPONDING MANUFACTURING PROCESS	7/24/2001	09/912,638	1/28/2003	6,511,874	US
NMXS-0579.00/US	Utility - ORG	Issued	CONTENT ADDRESSABLE MEMORY PROTECTION CIRCUIT AND METHOD	4/28/2000	09/561,271	5/29/2001	6,240,002	US
NMXS-0580.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR FAST ADDRESSING REDUNDANT COLUMNS IN A NON VOLATILE MEMORY	4/13/2000	09/548,783	10/30/2001	6,310,801	US
NMXS-0581.00/US	Utility - ORG	Issued	READING CIRCUIT FOR NONVOLATILE MEMORY CELLS WITHOUT LIMITATION OF THE SUPPLY VOLTAGE	4/11/2000	09/546,957	11/27/2001	6,324,098	US
NMXS-0582.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR AUTOMATICALLY REGULATING THE EQUALIZATION DURATION WHEN READING A NONVOLATILE MEMORY	4/11/2000	09/546,975	6/5/2001	6,243,310	US
NMXS-0583.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING SOI INTEGRATED CIRCUITS AND CIRCUITS MADE THEREBY	4/28/2000	09/561,272	6/25/2002	6,410,404	US
NMXS-0584.00/US	Utility - ORG	Issued	COMMUNICATIONS SYSTEM AND METHOD FOR REDUCING THE EFFECTS OF TRANSMITTER NON-LINEAR DISTORTION ON A RECEIVED SIGNAL	6/17/1999	09/335,024	8/13/2002	6,434,189	US
NMXS-0585.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT COMPRISING AT LEAST TWO MEMORIES	5/18/1999	09/313,926	7/23/2002	6,425,066	US

**Exhibit A - US Issued Patents**

MEMO ID NO	Material Type	Material Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMMS-0586.00/US	Utility - ORG	Issued	MULTIPLE LEVEL FLOATING-GATE MEMORY DEVICE AND METHOD FOR GENERATING A HIGH VOLTAGE	12/2/1999	09/453,753	6/11/2002	6,404,673	US
NMMS-0587.00/US	Utility - ORG	Issued	METHOD FOR VERIFYING THE MEMORY CIRCUIT INTEGRITY	8/10/1999	09/371,549	12/5/2000	6,157,249	US
NMMS-0588.00/US	Utility - ORG	Abandoned	SECURED EEPROM MEMORY COMPRISING MEANS FOR THE DETECTION OF ERASURE BY ULTRAVIOLET RADIATION	12/2/1999	09/452,446	4/3/2001	6,212,112	US
NMMS-0589.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR THE PROGRAMMING OF AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE MEMORY FOR IMPROVING RELIABILITY	11/24/1999	09/448,632	6/19/2001	6,249,456	US
NMMS-0590.00/US	Utility - ORG	Issued	METHOD FOR ERASING AND REWRITING NON VOLATILE MEMORY CELLS AND PARTICULARLY FLASH CELLS	4/21/2000	09/556,476	9/18/2001	6,292,396	US
NMMS-0591.00/US	Utility - ORG	Issued	READING METHOD FOR NON-VOLATILE MEMORIES WITH SENSING RATIO VARIABLE WITH THE READING VOLTAGE, AND DEVICE TO REALIZE SAID METHOD	4/20/2000	09/553,526	8/28/2001	6,282,125	US
NMMS-0592.00/US	Utility - ORG	Issued	ERASING AND PARALLEL REWRITING CIRCUIT FOR MEMORY CELL BLOCKS, PARTICULARLY FOR ANALOG FLASH CELLS, AND RELATED OPERATING METHOD	6/8/2000	09/589,723	3/26/2002	6,363,015	US
NMMS-0593.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE, IN PARTICULAR A FLASH-EEPROM	6/13/2000	09/592,796	11/6/2001	6,314,043	US
NMMS-0594.00/US	Utility - ORG	Issued	METHOD FOR THE IN-WRITING VERIFICATION OF THE THRESHOLD VALUE IN NON-VOLATILE MEMORIES	4/20/2000	09/552,945	2/26/2002	6,351,413	US
NMMS-0595.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE WITH HIERARCHICAL SECTOR DECODING	5/11/2000	09/569,232	9/18/2001	6,292,298	US
NMMS-0596.00/US	Utility - ORG	Issued	SYNCHRONOUS MULTILEVEL NON-VOLATILE MEMORY AND RELATED READING METHOD	6/26/2000	09/602,680	9/24/2002	6,456,530	US
NMMS-0597.00/US	Utility - ORG	Issued	CIRCUIT FOR READING A SEMICONDUCTOR MEMORY	5/17/2000	09/572,127	3/6/2001	6,198,660	US
NMMS-0598.00/US	Utility - ORG	Issued	BIASING STAGE FOR BIASING THE DRAIN TERMINAL OF A NONVOLATILE MEMORY CELL DURING THE READ PHASE	6/26/2000	09/603,275	12/25/2001	6,333,885	US
NMMS-0599.00/US	Utility - ORG	Issued	METHOD AND A DEVICE FOR MEASURING AN ANALOG VOLTAGE IN A NON-VOLATILE MEMORY	6/2/2000	09/586,399	11/20/2001	6,320,790	US
NMMS-0600.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR GENERATING FROM A SINGLE SUPPLY LINE VOLTAGES INTERNAL TO A FLASH MEMORY WITH REDUCED SETTLING TIMES	6/29/2000	09/608,847	1/14/2003	6,507,183	US
NMMS-0601.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH LOW POWER CONSUMPTION AND AND RELATIVE WRITING, READING AND ERASING METHODS	6/30/2000	09/608,239	5/21/2002	6,392,396	US
NMMS-0602.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING MULTI-LEVEL NON-VOLATILE MEMORIES BY CONTROLLING THE GATE VOLTAGE	7/21/2000	09/620,765	9/18/2001	6,292,400	US
NMMS-0603.00/US	Utility - ORG	Issued	CMOS SWITCH CIRCUIT FOR TRANSFERRING HIGH VOLTAGES, IN PARTICULAR FOR LINE VOLTAGE	8/2/2000	09/631,187	4/2/2002	6,366,496	US
NMMS-0604.00/US	Utility - ORG	Issued	ROW DECODER FOR SELECTIVELY BIASING A WORDLINE TO A POSITIVE OR NEGATIVE VOLTAGE	6/2/2000	09/585,916	8/13/2002	6,433,583	US
NMMS-0605.00/US	Utility - ORG	Abandoned	METHOD FOR READING DATA FROM A NON-VOLATILE MEMORY DEVICE WITH AUTOERASE BURST MODE READING AND CORRESPONDING READING CIRCUIT	6/16/2000	09/595,054	3/12/2002	6,356,481	US
NMMS-0606.00/US	Utility - ORG	Issued	SOFT PROGRAMMING METHOD FOR NON-VOLATILE MEMORY CELLS	11/20/2000	09/716,746	2/19/2002	6,349,059	US
NMMS-0607.00/US	Utility - ORG	Issued	BIPOLAR TRANSISTOR PRODUCED USING PROCESSES COMPATIBLE WITH THOSE EMPLOYED IN THE MANUFACTURE OF MOS DEVICES	12/14/2000	09/738,253	10/22/2002	6,469,934	US
NMMS-0608.01/US	Utility - DIV	Issued	BUFFER DEVICE WITH DUAL SUPPLY VOLTAGE FOR LOW SUPPLY VOLTAGE APPLICATIONS	2/15/2002	10/077,288	12/30/2003	6,670,229	US
NMMS-0610.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH FUNCTIONAL CAPABILITY OF SIMULTANEOUS MODIFICATION OF THE CONTENT AND BURST MODE READ OR PAGE MODE READ AND PAGE MODE READ DURING SUSPENSION OF AN OPERATION OF ELECTRICAL ALTERATION	12/13/2000	09/736,984	11/20/2001	6,320,361	US
NMMS-0611.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH FUNCTIONAL CAPABILITY OF BURST MODE READ AND	7/28/2000	09/627,703	6/28/2005	6,912,598	US
NMMS-0612.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH FUNCTIONAL CAPABILITY OF BURST MODE READ AND	7/19/2000	09/619,589	8/27/2002	6,442,068	US

**Exhibit A - US Issued Patents**

Item ID	Material Type	Status	Description	Date	Setting	Issued Date	Patent No.	Country
NMXX-0613.00/US	Utility - ORG	Issued	METHOD FOR CONTROLLED SOFT PROGRAMMING OF NON-VOLATILE MEMORY CELLS, IN PARTICULAR OF THE FLASH EEPROM AND EPROM TYPE	10/27/2000	09/699,309	4/30/2002	6,384,177	US
NMXX-0614.00/US	Utility - ORG	Issued	MANUFACTURE OF AN HY-AMOS TRANSISTOR WITH LOW MULTIPLICATION FACTOR IN A DUAL-GATE CMOS PROCESS	11/14/2000	09/713,144	12/31/2002	6,501,147	US
NMXX-0615.00/US	Utility - ORG	Issued	READ CIRCUIT FOR A NONVOLATILE MEMORY	7/21/2000	09/621,019	12/4/2001	6,327,184	US
NMXX-0617.00/US	Utility - ORG	Issued	ENABLING CIRCUIT FOR OUTPUT DEVICES IN ELECTRONIC MEMORIES	7/31/2001	09/629,229	8/20/2002	6,438,040	US
NMXX-0618.00/US	Utility - ORG	Issued	PROCESS FOR THE FABRICATION OF INTEGRATED DEVICES WITH REDUCTION OF DAMAGE FROM PLASMA	3/9/2001	09/804,693	10/28/2003	6,638,833	US
NMXX-0619.00/US	Utility - ORG	Issued	NON VOLATILE MEMORY WITH DETECTION OF SHORT CIRCUITS BETWEEN WORD LINES	9/8/2000	09/658,236	10/23/2001	6,307,778	US
NMXX-0620.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR TIMING MULTI-LEVEL NON-VOLATILE MEMORIES	12/15/2000	09/738,889	6/3/2003	6,574,146	US
NMXX-0621.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH BURST MODE READING AND CORRESPONDING READING METHOD	11/23/2000	09/717,938	2/8/2005	6,854,040	US
NMXX-0622.00/US	Utility - ORG	Issued	VOLTAGE GENERATOR SWITCHING BETWEEN ALTERNATING, FIRST AND SECOND VOLTAGE VALUES, IN PARTICULAR FOR PROGRAMMING MULTILEVEL CELLS	11/15/2000	09/714,837	7/23/2002	6,424,121	US
NMXX-0623.00/US	Utility - ORG	Lapsed	ANTI-DECRYPTING CONTACTS	10/1/2001	09/968,682	3/4/2003	6,528,885	US
NMXX-0624.00/US	Utility - ORG	Issued	READING CIRCUIT FOR A NON-VOLATILE MEMORY	10/27/2000	09/699,304	6/4/2002	6,400,607	US
NMXX-0625.00/US	Utility - ORG	Issued	READING METHOD AND CIRCUIT FOR A NON-VOLATILE MEMORY	10/27/2000	09/699,043	10/29/2002	6,473,340	US
NMXX-0626.00/US	Utility - ORG	Issued	COLUMN DECODER CIRCUIT FOR PAGE READING OF A SEMICONDUCTOR MEMORY	2/27/2001	09/797,471	1/4/2003	6,507,534	US
NMXX-0627.00/US	Utility - ORG	Issued	FLOW PROCESS FOR PRODUCING NON-VOLATILE MEMORIES WITH DIFFERENTIATED REMOVAL OF SACRIFICIAL OXIDE	8/2/2000	09/630,933	3/26/2002	6,362,053	US
NMXX-0628.00/US	Utility - ORG	Issued	ARCHITECTURE FOR HANDLING INTERNAL VOLTAGES IN A NON-VOLATILE MEMORY, PARTICULARLY IN A SINGLE-VOLTAGE SUPPLY TYPE OF DUAL-WORK FLASH MEMORY	11/9/2000	09/710,067	5/7/2002	6,385,107	US
NMXX-0629.00/US	Utility - ORG	Issued	CONTROL AND TIMING STRUCTURE FOR A MEMORY	10/5/2001	09/972,753	4/15/2003	6,549,485	US
NMXX-0630.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NONVOLATILE MEMORY CELLS OF REDUCED DIMENSIONS	11/22/2000	09/718,971	1/21/2003	6,509,222	US
NMXX-0630.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING ELECTRONIC DEVICES COMPRISING NONVOLATILE MEMORY CELLS OF REDUCED DIMENSIONS	8/20/2002	10/725,315	8/5/2003	6,603,171	US
NMXX-0631.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR NON-VOLATILE FLOATING GATE MEMORY CELLS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND COMPRISED IN A CELL MATRIX WITH AN ASSOCIATED CONTROL CIRCUITY	12/5/2000	09/730,518	7/16/2002	6,420,223	US
NMXX-0632.00/US	Utility - ORG	Issued	SINGLE SUPPLY VOLTAGE NONVOLATILE MEMORY DEVICE WITH ROW DECODING	7/28/2000	09/627,273	4/16/2002	6,373,780	US
NMXX-0633.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY TEST STRUCTURE AND NONVOLATILE MEMORY RELIABILITY TEST METHOD	10/27/1999	09/428,683	10/3/2000	6,128,219	US
NMXX-0635.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR PROGRAMMING A MULTILEVEL, NON-VOLATILE MEMORY	9/12/2001	09/952,957	2/25/2003	6,525,861	US
NMXX-0637.00/US	Utility - ORG	Issued	SERIAL-FLASH, EPROM, EEPROM AND FLASH EEPROM NONVOLATILE MEMORY IN AMG CONFIGURATION	10/10/2000	09/686,362	4/30/2002	6,381,173	US
NMXX-0638.00/US	Utility - ORG	Issued	LOW CONSUMPTION VOLTAGE BOOST DEVICE	12/22/2000	09/747,312	8/20/2002	6,437,636	US
NMXX-0639.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH CONFIGURABLE ROW REDUNDANCY	2/14/2001	09/785,079	7/9/2002	6,418,051	US
NMXX-0640.00/US	Utility - ORG	Abandoned	INTEGRATED DEVICE WITH OPERATIVITY TESTING	3/21/2001	09/798,347	5/24/2005	6,898,745	US
NMXX-0641.00/US	Utility - ORG	Issued	VOLTAGE BOOSTING DEVICE, IN PARTICULAR FOR SPEEDING POWER-UP OF MULTILEVEL, NONVOLATILE MEMORIES	2/6/2001	09/778,330	8/6/2002	6,429,634	US
NMXX-0642.00/US	Utility - ORG	Issued	NON-VOLATILE HIGH-PERFORMANCE MEMORY DEVICE AND RELATIVE MANUFACTURING PROCESS	12/19/2000	09/740,407	1/13/2004	6,677,206	US
NMXX-0643.00/US	Utility - ORG	Issued	SYNCHRONOUS COUNTER FOR ELECTRONIC MEMORIES	1/23/2001	09/767,762	2/26/2002	6,351,334	US

Exhibit A - US Issued Patents

Patent No.	Patent Title	Patent Status	Patent Description	Filed Date	Serial No.	Issue Date	Agent No.	Country
NMXX-0644.00/US	Utility - ORG	Issued	SYNCHRONIZATION CIRCUIT FOR READ PATHS OF AN ELECTRONIC MEMORY	4/5/2001	09/827,359	10/12/2004	6,804,756	US
NMXX-0645.00/US	Utility - ORG	Issued	INTERNAL ADDRESSING STRUCTURE OF A SEMICONDUCTOR MEMORY	10/9/2001	09/974,737	12/31/2002	6,501,700	US
NMXX-0646.00/US	Utility - ORG	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	3/20/2001	09/817,363	7/2/2002	6,414,875	US
NMXX-0646.01/US	Utility - DIV	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	6/24/2002	10/179,553	1/27/2004	6,683,808	US
NMXX-0646.02/US	Utility - DIV	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	12/19/2003	10/742,429	7/4/2006	7,072,212	US
NMXX-0646.03/US	Utility - DIV	Issued	STRING PROGRAMMABLE NONVOLATILE MEMORY WITH NOR ARCHITECTURE	12/19/2003	10/742,181	10/11/2005	6,954,395	US
NMXX-0647.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR THE INTEGRATION IN A SEMICONDUCTOR CHIP OF AN INTEGRATED CIRCUIT INCLUDING A HIGH-DENSITY INTEGRATED CIRCUIT COMPONENTS PORTION AND A HIGH-PERFORMANCE LOGIC INTEGRATED CIRCUIT COMPONENTS PORTION	5/15/2001	09/858,335	9/17/2002	6,451,653	US
NMXX-0648.00/US	Utility - ORG	Issued	LOGIC PARTITIONING OF A NONVOLATILE MEMORY ARRAY	3/26/2001	09/821,804	6/17/2003	6,581,134	US
NMXX-0649.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY MATRIX ARCHITECTURE WITH VERTICAL INSULATION STRIPS BETWEEN ADJACENT MEMORY BLOCKS	7/3/2001	09/898,744	10/15/2002	6,466,479	US
NMXX-0650.00/US	Utility - ORG	Issued	PROGRAMMABLE VOLTAGE GENERATOR	11/15/2000	09/714,852	11/18/2003	6,650,173	US
NMXX-0651.00/US	Utility - ORG	Issued	METHOD OF FORMING LOW-RESISTIVITY CONNECTIONS IN NON-VOLATILE MEMORIES	3/12/2001	09/798,778	2/3/2004	6,686,241	US
NMXX-0652.00/US	Utility - ORG	Issued	METHOD AND A DEVICE FOR TESTING ELECTRONIC MEMORY DEVICES	3/30/2001	09/823,926	1/23/2007	7,168,016	US
NMXX-0653.00/US	Utility - ORG	Issued	CIRCUITAL STRUCTURE FOR PROGRAMMING DATA IN A NON-VOLATILE MEMORY DEVICE	5/30/2001	09/871,235	1/14/2003	6,507,517	US
NMXX-0654.00/US	Utility - ORG	Issued	CIRCUITAL STRUCTURE FOR READING DATA IN A NON-VOLATILE MEMORY DEVICE	5/30/2001	09/871,234	4/15/2003	6,549,473	US
NMXX-0655.00/US	Utility - ORG	Abandoned	INTEGRATED DEVICE WITH VOLTAGE SELECTOR	3/29/2001	09/823,262	11/15/2002	6,476,664	US
NMXX-0656.00/US	Utility - ORG	Abandoned	LOW-CONSUMPTION CHARGE PUMP FOR A NONVOLATILE MEMORY	3/28/2001	09/821,827	5/6/2003	6,559,709	US
NMXX-0657.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING A NON-VOLATILE MEMORY CELL WITH A FLOATING GATE REGION AUTOALIGNED TO THE ISOLATION AND WITH A HIGH COUPLING COEFFICIENT	7/6/2001	09/900,501	3/25/2003	6,537,879	US
NMXX-0657.01/US	Utility - DIV	Issued	NON-VOLATILE MEMORY CELL WITH A FLOATING GATE REGION AUTOALIGNED TO THE ISOLATION AND WITH A HIGH COUPLING COEFFICIENT	1/7/2003	10/337,556	6/15/2004	6,750,505	US
NMXX-0658.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY ARCHITECTURE	10/5/2001	09/972,769	6/17/2003	6,580,637	US
NMXX-0659.00/US	Utility - ORG	Issued	REFERENCE VOLTAGE ADJUSTMENT	12/31/1999	09/476,036	8/28/2001	6,281,734	US
NMXX-0659.01/US	Utility - DIV	Issued	INTEGRATED VOLTAGE ADJUSTMENT	7/10/2001	09/902,206	11/5/2002	6,476,669	US
NMXX-0660.00/US	Utility - ORG	Issued	INTEGRATED VOLTAGE AND NON-VOLATILE MEMORY	8/31/2000	09/653,495	7/15/2003	6,594,192	US
NMXX-0660.01/US	Utility - DIV	Issued	INTEGRATED VOLTAGE AND NON-VOLATILE MEMORY	5/20/2003	10/442,844	8/24/2004	6,781,916	US
NMXX-0661.00/US	Utility - ORG	Issued	CMOS TECHNOLOGY VOLTAGE BOOSTER	12/15/2000	09/738,890	7/16/2002	6,420,926	US
NMXX-0662.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY AND HIGH SPEED MEMORY TEST METHOD	9/26/2000	09/670,248	8/20/2002	6,438,048	US
NMXX-0663.00/US	Utility - ORG	Issued	Memory test method and nonvolatile memory with low error masking probability	9/26/2000	09/670,471	8/28/2001	6,282,134	US
NMXX-0664.00/US	Utility - ORG	Issued	ROW SELECTION CIRCUIT FOR FAST MEMORY DEVICES	12/21/2000	09/745,286	8/27/2002	6,442,072	US
NMXX-0665.00/US	Utility - ORG	Issued	INTERLEAVED DATA PATH AND OUTPUT MANAGEMENT ARCHITECTURE FOR AN INTERLEAVED MEMORY AND LOAD PULSER CIRCUIT FOR OUTPUTTING THE READ DATA	1/31/2001	09/774,542	10/22/2002	6,470,431	US
NMXX-0666.00/US	Utility - ORG	Issued	INTERLEAVED MEMORY DEVICE FOR SEQUENTIAL ACCESS SYNCHRONOUS READING WITH SIMPLIFIED ADDRESS COUNTERS	1/31/2001	09/774,539	9/17/2002	6,452,864	US
NMXX-0667.00/US	Utility - ORG	Issued	REDUNDANCY ARCHITECTURE FOR AN INTERLEAVED MEMORY	1/31/2001	09/773,272	10/29/2002	6,473,339	US
NMXX-0668.00/US	Utility - ORG	Issued	ACCELERATED CARRY GENERATION	1/31/2001	09/774,878	4/2/2002	6,366,634	US
NMXX-0669.00/US	Utility - ORG	Issued	ATD GENERATION IN A SYNCHRONOUS MEMORY	1/31/2001	09/773,760	9/23/2003	6,625,706	US
NMXX-0670.00/US	Utility - ORG	Issued	INTERLEAVED MEMORY DEVICE FOR BURST TYPE ACCESS IN SYNCHRONOUS READ MODE WITH THE TWO SEMI-ARRAYS INDEPENDENTLY READABLE IN RANDOM ACCESS ASYNCHRONOUS MODE	1/31/2001	09/773,300	7/1/2003	6,587,913	US
NMXX-0671.00/US	Utility - ORG	Issued	COLOR IMAGE RESTORATION WITH ANTI-ALIAS	11/2/2000	09/705,145	1/11/2005	6,842,191	US

PATENT

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Patent No.	Applicant	Inventor	Issue Date	Patent No.	Country			
NMXXS-0672.00/US	Utility - NSPCT	Issued	VOLTAGE PRODUCTION CIRCUIT	6/26/2000	10/019,771	9/16/2003	6,621,720	US
NMXXS-0673.00/US	Utility - ORG	Issued	METHOD FOR PAGE MODE WRITING IN AN ELECTRICALLY ERASABLE/PROGRAMMABLE NON-VOLATILE MEMORY AND CORRESPONDING ARCHITECTURE	9/12/2000	09/660,303	1/7/2003	6,504,791	US
NMXXS-0674.00/US	Utility - ORG	Issued	MEMORY INCORPORATING COLUMN REGISTER AND METHOD OF WRITING IN SAID MEMORY	9/29/2000	09/675,366	10/23/2001	6,307,792	US
NMXXS-0674.01/US	Utility - DIV	Issued	MEMORY INCORPORATING COLUMN REGISTER AND METHOD OF WRITING IN SAID MEMORY	9/13/2001	09/952,904	5/7/2002	6,385,096	US
NMXXS-0675.00/US	Utility - ORG	Issued	ARCHITECTURE OF A NON-VOLATILE ELECTRICALLY ERASABLE AND PROGRAMMABLE MEMORY	9/7/2000	09/657,319	8/14/2001	6,275,413	US
NMXXS-0677.00/US	Utility - ORG	Issued	PAGE BY PAGE PROGRAMMABLE FLASH MEMORY	12/14/2000	09/737,170	1/4/2005	6,839,285	US
NMXXS-0678.00/US	Utility - ORG	Issued	METHOD FOR THE CORRECTION OF A BIT IN A STRING OF BITS	12/15/2000	09/737,827	5/11/2004	6,735,733	US
NMXXS-0679.00/US	Utility - ORG	Issued	HIGH-EFFICIENCY BIDIRECTIONAL VOLTAGE BOOSTING DEVICE	2/13/2001	09/788,623	9/10/2002	6,448,842	US
NMXXS-0680.00/US	Utility - ORG	Issued	CHARGE PUMP BOOSTER DEVICE WITH TRANSFER AND RECOVERY OF THE CHARGE	2/13/2001	09/785,042	6/11/2002	6,404,272	US
NMXXS-0681.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY CELL WITH HIGH PROGRAMMING EFFICIENCY	7/30/2001	09/919,341	5/11/2004	6,734,490	US
NMXXS-0682.00/US	Utility - ORG	Issued	METHOD AND A CIRCUIT STRUCTURE FOR MODIFYING THE THRESHOLD VOLTAGES OF NON-VOLATILE MEMORY CELLS	7/18/2001	09/908,386	2/11/2003	6,519,183	US
NMXXS-0683.00/US	Utility - ORG	Issued	CONTROL CIRCUIT FOR A VARIABLE-VOLTAGE REGULATOR OF A NONVOLATILE MEMORY WITH HIERARCHICAL ROW DECODING	9/21/2001	09/960,851	1/7/2003	6,504,758	US
NMXXS-0684.00/US	Utility - ORG	Issued	CIRCUIT ARRANGEMENT FOR THE LOWERING OF THE THRESHOLD VOLTAGE OF A DIODE CONFIGURED TRANSISTOR	6/13/2001	09/881,661	12/31/2002	6,501,673	US
NMXXS-0685.00/US	Utility - ORG	Issued	DIRECT-COMPARISON READING CIRCUIT FOR A NONVOLATILE MEMORY ARRAY	8/15/2001	09/930,875	10/8/2002	6,462,987	US
NMXXS-0686.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY WITH A CHARGE PUMP WITH REGULATED VOLTAGE	7/19/2001	09/909,467	11/12/2002	6,480,436	US
NMXXS-0687.00/US	Utility - ORG	Issued	CHARGE PUMP WITH EFFICIENT SWITCHING TECHNIQUES	12/11/2000	09/735,277	3/19/2002	6,359,500	US
NMXXS-0688.00/US	Utility - ORG	Issued	High-voltage bidirectional switch made using high-voltage MOS transistors	6/16/2000	09/595,680	9/11/2001	6,288,603	US
NMXXS-0689.00/US	Utility - ORG	Issued	READING CIRCUIT FOR SEMICONDUCTOR NON-VOLATILE MEMORIES	9/13/2001	09/953,070	5/13/2003	6,563,737	US
NMXXS-0691.00/US	Utility - ORG	Issued	LATERAL DMOS TRANSISTOR WITH FIRST AND SECOND DRAIN ELECTRODES IN RESPECTIVE CONTACT WITH HIGH-AND LOW-CONCENTRATION PORTIONS OF A DRAIN REGION	9/20/2001	09/960,254	9/23/2003	6,624,471	US
NMXXS-0692.00/US	Utility - ORG	Issued	VOLTAGE BOOSTER WITH A LOW OUTPUT RESISTANCE	2/13/2001	09/785,083	6/11/2002	6,404,273	US
NMXXS-0693.00/US	Utility - ORG	Issued	MULTIPURPOSE METHOD FOR CONSTRUCTING AN ERROR-CONTROL CODE FOR MULTILEVEL MEMORY CELLS OPERATING WITH A VARIABLE NUMBER OF STORAGE LEVELS, AND MULTIPURPOSE ERROR-CONTROL METHOD USING SAID ERROR-CONTROL CODE	11/2/2001	10/015,949	5/16/2006	7,047,478	US
NMXXS-0694.00/US	Utility - ORG	Issued	FOLDING TOP FOR A MOTOR VEHICLE	10/31/2001	09/002,599	8/5/2003	6,603,681	US
NMXXS-0695.00/US	Utility - ORG	Issued	NONVOLATILE SEMICONDUCTOR MEMORY CAPABLE OF SELECTIVELY ERASING A PLURALITY OF ELEMENTAL MEMORY UNITS	7/31/2001	09/919,789	3/11/2003	6,532,171	US
NMXXS-0696.00/US	Utility - ORG	Issued	ELECTRICALLY MODIFIABLE, NON-VOLATILE, SEMICONDUCTOR MEMORY WHICH CAN KEEP A DATUM STORED UNTIL AN OPERATION TO MODIFY THE DATUM IS COMPLETED	12/28/2001	10/036,088	1/4/2005	6,839,818	US
NMXXS-0697.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR FOR LOW-CONSUMPTION CIRCUITS	11/7/2001	09/008,540	5/6/2003	6,559,627	US
NMXXS-0698.00/US	Utility - ORG	Issued	SMALL SIZE, LOW CONSUMPTION, MULTILEVEL NONVOLATILE MEMORY	10/4/2001	09/972,726	4/1/2003	6,542,404	US
NMXXS-0699.00/US	Utility - ORG	Issued	METHOD FOR STORING AND READING DATA IN A MULTILEVEL NONVOLATILE MEMORY	10/11/2001	09/976,473	11/11/2003	6,646,913	US
NMXXS-0700.00/US	Utility - ORG	Abandoned	NONVOLATILE MEMORY DEVICE, HAVING PARTS WITH DIFFERENT ACCESS TIME, RELIABILITY, AND CAPACITY	9/19/2001	09/957,628	12/10/2002	6,493,260	US
NMXXS-0701.00/US	Utility - ORG	Issued	CONTROL CIRCUIT FOR AN OUTPUT DRIVING STAGE OF AN INTEGRATED CIRCUIT	11/21/2001	09/991,493	5/20/2003	6,567,318	US

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Part No.	Part Name	Part Description	Part Category	Part Status	Part Date	Part Qty	Part Unit	Part Price	Part Country
NMXXS-0702.00/US	Utility - ORG	Issued	BIASING CIRCUIT FOR MULTILEVEL MEMORY CELLS	6/1/2001	09/882,535	3/18/2003	6,535,428	US	
NMXXS-0703.00/US	Utility - ORG	Issued	CIRCUIT DEVICE FOR PERFORMING HIERARCHIC ROW DECODING IN NON-VOLATILE MEMORY DEVICES	7/12/2001	09/905,163	12/10/2002	6,493,268	US	
NMXXS-0704.01/US	Utility - CON	Issued	ANALOG-TO-DIGITAL CONVERSION METHOD AND DEVICE, IN HIGH-DENSITY MULTILEVEL, NON-VOLATILE MEMORY DEVICES	1/29/2002	10/060,076	1/6/2004	6,674,385	US	
NMXXS-0705.00/US	Utility - ORG	Issued	POWER-ON RESET CIRCUIT FOR DUAL SUPPLY VOLTAGES	10/25/2001	09/003,474	11/12/2002	6,480,421	US	
NMXXS-0706.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING NONVOLATILE MEMORY CELLS WITH PROGRAM AND VERIFY ALGORITHM USING A STAIRCASE VOLTAGE WITH VARYING STEP AMPLITUDE	4/9/2002	10/119,523	9/7/2004	6,788,579	US	
NMXXS-0707.00/US	Utility - ORG	Issued	CONDUCTION LINE DECOUPLING CIRCUIT	7/26/2001	09/917,613	9/7/2004	6,788,517	US	
NMXXS-0708.00/US	Utility - ORG	Issued	MULTI-EMITTER BIPOlar TRANSISTOR FOR BANDGAP REFERENCE CIRCUITS	12/27/2001	10/035,006	3/2/2004	6,700,226	US	
NMXXS-0710.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY SYSTEM	5/30/2002	10/158,554	7/15/2003	6,594,180	US	
NMXXS-0711.00/US	Utility - ORG	Issued	OUTPUT BUFFER FOR A NONVOLATILE MEMORY WITH OUTPUT SIGNAL SWITCHING NOISE REDUCTION, AND NONVOLATILE MEMORY COMPRISING THE SAME	5/30/2002	10/161,053	9/7/2004	6,788,586	US	
NMXXS-0712.00/US	Utility - ORG	Issued	OUTPUT BUFFER FOR A NONVOLATILE MEMORY WITH OPTIMIZED SLEW-RATE CONTROL	5/30/2002	10/161,055	12/7/2004	6,829,177	US	
NMXXS-0713.00/US	Utility - ORG	Issued	EEPROM FLASH MEMORY ERASABLE LINE BY LINE	8/20/2002	10/225,513	2/3/2004	6,687,167	US	
NMXXS-0714.00/US	Utility - ORG	Issued	METHOD OF PROGRAMMING A PLURALITY OF MEMORY CELLS CONNECTED IN PARALLEL, AND A PROGRAMMING CIRCUIT THEREFOR	12/19/2001	10/036,337	2/3/2004	6,687,159	US	
NMXXS-0715.00/US	Utility - ORG	Issued	COLUMN MULTIPLEXER FOR SEMICONDUCTOR MEMORIES	5/30/2002	10/158,553	4/22/2003	6,552,952	US	
NMXXS-0716.00/US	Utility - ORG	Issued	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	12/14/2001	10/017,502	12/23/2003	6,667,903	US	
NMXXS-0717.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR DYNAMIC READING OF A MEMORY CELL, IN PARTICULAR A MULTILEVEL, NONVOLATILE MEMORY CELL	1/14/2002	10/047,918	11/4/2003	6,643,179	US	
NMXXS-0718.00/US	Utility - ORG	Issued	METHOD FOR STORING DATA IN A NONVOLATILE MEMORY	12/19/2001	10/035,909	12/2/2003	6,655,758	US	
NMXXS-0720.00/US	Utility - ORG	Issued	AUTOTESTING METHOD OF A MEMORY CELL MATRIX, PARTICULARLY OF THE NON-VOLATILE TYPE	12/23/2002	10/328,721	11/8/2005	6,963,512	US	
NMXXS-0721.00/US	Utility - ORG	Issued	READING CIRCUIT AND METHOD FOR A MULTILEVEL, NON-VOLATILE MEMORY	4/8/2002	10/118,660	12/2/2003	6,657,895	US	
NMXXS-0722.00/US	Utility - ORG	Abandoned	METHOD OF REFRESHING AN ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY	6/20/2002	10/176,387	5/4/2004	6,731,557	US	
NMXXS-0723.00/US	Utility - ORG	Issued	HIGH-EFFICIENCY POWER CHARGE PUMP SUPPLYING HIGH DC OUTPUT CURRENTS	6/3/2002	10/162,135	8/12/2003	6,605,985	US	
NMXXS-0724.00/US	Utility - ORG	Issued	NON-VOLATILE, ELECTRICALLY ALTERABLE SEMICONDUCTOR MEMORY	1/24/2002	10/057,769	9/9/2003	6,618,315	US	
NMXXS-0725.00/US	Utility - ORG	Issued	METHOD FOR ERASING AN ELECTRICALLY ERASABLE NONVOLATILE MEMORY DEVICE, IN PARTICULAR AN EEPROM-FLASH MEMORY DEVICE, AND AN ELECTRICALLY ERASABLE NONVOLATILE MEMORY DEVICE, IN PARTICULAR AN EEPROM-FLASH MEMORY DEVICE	5/30/2002	10/159,780	3/22/2005	6,871,258	US	
NMXXS-0726.00/US	Utility - ORG	Issued	METHOD FOR REFRESHING STORED DATA IN AN ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY	1/24/2002	10/057,768	12/23/2003	6,668,303	US	
NMXXS-0727.00/US	Utility - ORG	Issued	MEMORY WITH IMPROVED DIFFERENTIAL READING SYSTEM	6/20/2002	10/176,954	3/2/2004	6,700,819	US	
NMXXS-0728.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR TIMING DYNAMIC READING OF A MEMORY CELL WITH CONTROL OF THE INTEGRATION TIME	4/16/2002	10/123,874	4/27/2004	6,728,141	US	
NMXXS-0729.00/US	Utility - CIP	Issued	METHOD AND CIRCUIT FOR DYNAMIC READING OF A MEMORY CELL AT LOW SUPPLY VOLTAGE AND WITH LOW OUTPUT DYNAMICS	2/13/2002	10/076,023	10/28/2003	6,639,833	US	
NMXXS-0730.00/US	Utility - CIP	Issued	METHOD AND CIRCUIT FOR GENERATING REFERENCE VOLTAGES FOR READING A MULTILEVEL MEMORY CELL	4/26/2002	10/133,231	4/20/2004	6,724,658	US	
NMXXS-0731.00/US	Utility - ORG	Issued	METHOD FOR ERROR CONTROL IN MULTILEVEL CELLS WITH CONFIGURABLE NUMBER OF STORED BITS	5/30/2002	10/159,782	3/21/2006	7,017,089	US	
NMXXS-0732.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS OF AN INTERPOLY DIELECTRIC STRUCTURE FOR NON-VOLATILE SEMICONDUCTOR INTEGRATED MEMORIES	1/30/2003	10/356,351	8/1/2006	7,084,032	US	



Exhibit A - US Issued Patents

APPLICATION NO.	Material Type	Patent Status	Title	Filing Date	Serial No.	Issue Date	Patent No.	Country
NMXXS-0733.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS OF A SEMICONDUCTOR NON-VOLATILE MEMORY CELL	12/18/2002	10/323,615	8/28/2007	7,262,098	US
NMXXS-0734.00/US	Utility - ORG	Issued	PROCESS FOR REMOVING POLYMERS DURING THE FABRICATION OF SEMICONDUCTOR DEVICES	7/12/2002	10/189,152	4/13/2004	6,720,271	US
NMXXS-0735.00/US	Utility - ORG	Issued	LOW POWER CHARGE PUMP CIRCUIT	11/7/2002	10/290,030	11/9/2004	6,816,001	US
NMXXS-0736.00/US	Utility - ORG	Issued	CONTENT ADDRESSABLE MEMORY (CAM) WITH BATTERY BACK-UP	6/29/2001	09/895,491	12/17/2002	6,496,439	US
NMXXS-0737.00/US	Utility - ORG	Issued	Data output buffer with precharge	8/11/2000	09/636,363	9/18/2001	6,292,405	US
NMXXS-0738.00/US	Utility - ORG	Issued	INTERLACED MEMORY DEVICE WITH RANDOM OR SEQUENTIAL ACCESS	10/15/2001	09/977,561	3/12/2004	6,701,419	US
NMXXS-0739.00/US	Utility - ORG	Issued	OUTPUT BUFFER AND METHOD OF DRIVING	8/8/2001	09/925,842	12/3/2002	6,489,807	US
NMXXS-0740.00/US	Utility - ORG	Issued	GENERATOR CIRCUIT FOR VOLTAGE RAMP AND CORRESPONDING VOLTAGE GENERATION METHOD	12/28/2001	10/033,711	11/18/2003	6,650,153	US
NMXXS-0741.00/US	Utility - ORG	Issued	BOOST DEVICE FOR NONVOLATILE MEMORIES WITH AN INTEGRATED STAND-BY CHARGE PUMP	2/13/2002	10/076,134	11/18/2003	6,650,569	US
NMXXS-0742.00/US	Utility - ORG	Abandoned	CHARGE PUMP FOR A NONVOLATILE MEMORY WITH READ VOLTAGE REGULATION IN THE PRESENCE OF ADDRESS SKEW, AND NONVOLATILE MEMORY COMPRISING SUCH A CHARGE PUMP	2/5/2002	10/068,560	5/6/2003	6,560,145	US
NMXXS-0743.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR TIMING THE READING OF A NONVOLATILE MEMORY WITH REDUCED SWITCHING NOISE	2/15/2002	10/077,687	1/6/2004	6,674,666	US
NMXXS-0744.00/US	Utility - ORG	Issued	DEVICE AND METHOD FOR AUTOMATICALLY GENERATING AN APPROPRIATE NUMBER OF WAIT CYCLES WHILE READING A NONVOLATILE MEMORY	4/3/2002	10/115,888	7/12/2005	6,917,994	US
NMXXS-0745.00/US	Utility - ORG	Issued	CIRCUIT FOR GENERATING A PULSE SIGNAL INDEPENDENT OF VOLTAGE AND TEMPERATURE VARIATIONS	10/31/2001	09/998,903	4/15/2003	6,549,486	US
NMXXS-0746.00/US	Utility - ORG	Issued	NEGATIVE CHARGE PUMP ARCHITECTURE WITH SELF-GENERATED BOOSTED PHASES	10/31/2001	09/998,902	4/13/2004	6,720,822	US
NMXXS-0748.00/US	Utility - ORG	Issued	THRESHOLD AMPLIFIER	8/9/2001	09/925,979	4/15/2003	6,549,048	US
NMXXS-0749.00/US	Utility - NSPCT	Issued	PAGE-ERASABLE FLASH MEMORY	11/14/2001	10/438,733	10/19/2004	6,807,103	US
NMXXS-0750.00/US	Utility - ORG	Issued	CIRCUIT FOR THE DETECTION OF A DEFECTIVE POWER SUPPLY CONNECTION	1/29/2002	10/060,105	8/10/2004	6,774,663	US
NMXXS-0751.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR SEQUENTIAL READOUT OF A MEMORY WITH ADDRESS JUMP	2/22/2002	10/081,740	8/9/2005	6,928,530	US
NMXXS-0752.00/US	Utility - ORG	Issued	FLASH MEMORY INCLUDING MEANS OF CHECKING MEMORY CELL THRESHOLD VOLTAGES	11/15/2001	09/997,214	5/27/2003	6,568,510	US
NMXXS-0752.01/US	Utility - DIV	Issued	FLASH MEMORY INCLUDING MEANS OF CHECKING MEMORY CELL THRESHOLD VOLTAGES	1/28/2003	10/352,581	3/30/2004	6,714,453	US
NMXXS-0753.00/US	Utility - NSPCT	Issued	CONTACTLESS IC CARD WITH OPERATING SYSTEM USED IN CONTACT TYPE CARDS AND READER FOR SUCH CONTACTLESS CARDS	2/14/2002	10/471,883	1/16/2007	7,163,154	US
NMXXS-0754.00/US	Utility - ORG	Issued	FLASH EEPROM AND EPROM ARRAYS WITH SELECT TRANSISTORS WITHIN THE BIT LINE PITCH	3/11/1994	08/212,176	9/17/1996	5,557,124	US
NMXXS-0755.00/US	Utility - ORG	Issued	METHOD OF RE-PROGRAMMING AN ARRAY OF NON-VOLATILE MEMORY CELLS, IN PARTICULAR OF THE NOR ARCHITECTURE FLASH TYPE, AFTER AN ERASE OPERATION, AND A CORRESPONDING MEMORY DEVICE	6/12/2002	10/171,078	11/25/2003	6,654,287	US
NMXXS-0756.00/US	Utility - ORG	Issued	MEMORY DEVICE	10/24/2001	10/041,684	5/20/2003	6,567,296	US
NMXXS-0757.00/US	Utility - ORG	Issued	WORD LINE SELECTOR FOR A SEMICONDUCTOR MEMORY	2/20/2003	10/372,626	3/8/2005	6,865,114	US
NMXXS-0758.00/US	Utility - ORG	Issued	DESIGN FAILURE MODE EFFECT ANALYSIS (DFMEA)	12/26/2002	10/330,485	4/25/2006	7,053,769	US
NMXXS-0759.00/US	Utility - ORG	Issued	POWER SUPPLY CIRCUIT STRUCTURE FOR A ROW DECODER OF A MULTILEVEL, NON-VOLATILE MEMORY DEVICE	12/30/2002	10/334,126	12/17/2004	6,829,168	US
NMXXS-0760.00/US	Utility - ORG	Issued	BINARY ENCODING CIRCUIT	12/20/2002	10/325,707	2/24/2004	6,696,990	US
NMXXS-0761.00/US	Utility - ORG	Issued	REGULATION METHOD FOR THE DRAIN, BODY AND SOURCE TERMINALS VOLTAGES IN A NON-VOLATILE MEMORY CELL DURING A PROGRAM PHASE AND CORRESPONDING PROGRAM CIRCUIT	12/27/2002	10/331,116	10/26/2004	6,809,961	US

PATENT

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**Exhibit A - US Issued Patents**

MT-Device	Material Type	Materials	Title	Release	Setting	Issued Date	Patent No.	Country
NMXXS-0762.00/US	Utility - ORG	Issued	METHOD FOR STORING AND READING DATA IN A MULTILEVEL NONVOLATILE MEMORY, AND ARCHITECTURE THEREOF	9/26/2002	10/259,252	11/9/2004	6,816,407	US
NMXXS-0762.01/US	Utility - DIV	Issued	MULTILEVEL MEMORY DEVICE WITH MEMORY CELLS STORING NON-POWER OF TWO VOLTAGE LEVELS	10/12/2004	10/964,160	11/15/2005	6,965,523	US
NMXXS-0763.00/US	Utility - ORG	Issued	ELECTRICALLY-PROGRAMMABLE NON-VOLATILE MEMORY CELL	2/20/2003	10/372,044	4/19/2005	6,882,001	US
NMXXS-0764.00/US	Utility - ORG	Issued	DECODING STRUCTURE FOR A MEMORY DEVICE WITH A CONTROL CODE	12/27/2002	10/331,177	10/25/2005	6,958,949	US
NMXXS-0765.00/US	Utility - ORG	Issued	SENSE AMPLIFIER STRUCTURE FOR MULTILEVEL NON-VOLATILE MEMORY DEVICES AND CORRESPONDING READING METHOD	12/26/2002	10/331,158	11/23/2004	6,822,906	US
NMXXS-0766.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING A DUAL CHARGE STORAGE LOCATION MEMORY CELL	11/14/2002	10/794,999	10/25/2005	6,958,510	US
NMXXS-0767.00/US	Utility - ORG	Issued	FINITE STATE MACHINE INTERFACE HAS ARBITRATION STRUCTURE TO STORE COMMAND GENERATED BY INTERNAL CIRCUITS DURING EVALUATION PHASE OF STATE MACHINE FOR FLASH EEPROM DEVICE	12/4/2002	10/309,759	10/16/2007	7,284,144	US
NMXXS-0768.00/US	Utility - ORG	Issued	SINGLE SUPPLY VOLTAGE, NONVOLATILE PHASE CHANGE MEMORY DEVICE WITH CASCADED COLUMN SELECTION AND SIMULTANEOUS WORD READ/WRITE OPERATIONS	12/27/2002	10/331,185	6/22/2004	6,754,107	US
NMXXS-0769.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING A DUAL CHARGE STORAGE LOCATION MEMORY CELL	10/7/2002	10/267,033	11/30/2004	6,825,523	US
NMXXS-0769.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING A DUAL CHARGE STORAGE LOCATION MEMORY CELL	10/12/2004	10/964,049	10/3/2006	7,115,472	US
NMXXS-0770.00/US	Utility - ORG	Issued	ARCHITECTURE FOR A FLASH-EEPROM SIMULTANEOUSLY READABLE IN OTHER SECTIONS WHILE ERASING AND/OR PROGRAMMING ONE OR MORE SECTORS	1/10/2003	10/340,207	5/10/2005	6,891,755	US
NMXXS-0771.00/US	Utility - ORG	Issued	FAST PROGRAMMING METHOD FOR NONVOLATILE MEMORIES, IN PARTICULAR FLASH MEMORIES, AND RELATIVE MEMORY ARCHITECTURE	10/24/2002	10/281,078	12/27/2005	6,981,107	US
NMXXS-0772.00/US	Utility - ORG	Issued	ARCHITECTURE OF A PHASE-CHANGE NONVOLATILE MEMORY ARRAY	12/12/2002	10/319,439	11/9/2004	6,816,404	US
NMXXS-0773.00/US	Utility - ORG	Issued	MEMORY SYSTEM COMPRISING A SEMICONDUCTOR MEMORY	12/12/2003	10/735,250	5/22/2007	7,221,602	US
NMXXS-0774.00/US	Utility - ORG	Issued	SELF-REPAIR METHOD FOR NONVOLATILE MEMORY DEVICES WITH ERASING/PROGRAMMING FAILURE, AND RELATIVE NONVOLATILE MEMORY DEVICE	5/15/2003	10/440,043	9/13/2005	6,944,072	US
NMXXS-0775.00/US	Utility - ORG	Issued	INTEGRATED RESISTOR, PHASE-CHANGE MEMORY ELEMENT INCLUDING THIS RESISTOR, AND PROCESS FOR THE FABRICATION THEREOF	1/14/2003	10/345,129	9/20/2005	6,946,673	US
NMXXS-0776.00/US	Utility - ORG	Issued	METHOD FOR AVOIDING THE EFFECTS OF LACK OF UNIFORMITY IN TRENCH ISOLATED INTEGRATED CIRCUITS	11/29/2001	09/997,227	7/1/2003	6,586,313	US
NMXXS-0777.00/US	Utility - ORG	Issued	SMALL AREA CONTACT REGION, HIGH EFFICIENCY PHASE CHANGE MEMORY CELL AND FABRICATION METHOD THEREOF	12/5/2002	10/313,991	6/5/2007	7,227,171	US
NMXXS-0778.00/US	Utility - ORG	Issued	PROGRAMMING METHOD OF THE MEMORY CELLS IN A MULTILEVEL NON-VOLATILE MEMORY DEVICE	5/13/2003	10/438,175	7/19/2005	6,920,066	US
NMXXS-0779.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE	3/14/2003	10/390,556	12/12/2006	7,149,844	US
NMXXS-0780.00/US	Utility - CIP	Issued	PHASE CHANGE MEMORY CELL AND MANUFACTURING METHOD THEREOF USING MAINTRENCHES	2/20/2003	10/372,761	5/10/2005	6,891,747	US
NMXXS-0780.01/US	Utility - DIV	Issued	PHASE CHANGE MEMORY CELL AND MANUFACTURING METHOD THEREOF USING MAINTRENCHES	1/27/2005	11/045,170	8/9/2011	7,993,957	US
NMXXS-0782.00/US	Utility - CIP	Issued	SUBLITHOGRAPHIC CONTACT STRUCTURE, PHASE CHANGE MEMORY CELL WITH OPTIMIZED HEATER SHAPE, AND MANUFACTURING METHOD THEREOF	2/20/2003	10/371,154	12/6/2005	6,972,430	US
NMXXS-0782.01/US	Utility - DIV	Issued	SUBLITHOGRAPHIC CONTACT STRUCTURE, PHASE CHANGE MEMORY CELL WITH OPTIMIZED HEATER SHAPE, AND MANUFACTURING METHOD THEREOF	10/24/2005	11/258,340	5/13/2008	7,372,166	US
NMXXS-0783.00/US	Utility - CIP	Issued	CONTACT STRUCTURE, PHASE CHANGE MEMORY CELL, AND MANUFACTURING METHOD THEREOF WITH ELIMINATION OF DOUBLE CONTACTS	2/20/2003	10/372,639	8/16/2005	6,930,913	US



Exhibit A - US Issued Patents

Patent No.	App. No.	Inventor	Assignor	Title	Filed	Pub. Date	Patent No.	Country
NMXXS-0783.01/US	Utility - DIV	Issued	MANUFACTURING METHOD OF A CONTACT STRUCTURE AND PHASE CHANGE MEMORY CELL WITH ELIMINATION OF DOUBLE CONTACTS	6/20/2005	11/156,989	7/22/2008	7,402,455	US
NMXXS-0784.00/US	Utility - ORG	Issued	BASIC STAGE FOR A CHARGE PUMP CIRCUIT	3/28/2003	10/402,852	2/21/2006	7,002,399	US
NMXXS-0785.00/US	Utility - ORG	Issued	ELECTRICALLY ERASABLE AND PROGRAMMABLE NON-VOLATILE MEMORY CELL	6/25/2003	10/606,164	4/5/2005	6,876,033	US
NMXXS-0786.00/US	Utility - ORG	Issued	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	12/26/2002	10/331,161	8/9/2006	7,088,614	US
NMXXS-0787.00/US	Utility - ORG	Issued	ADJUSTABLE FREQUENCY OSCILLATOR WITH PROGRAMMABLE CALIBRATING CIRCUIT AND RELATED SYSTEM AND METHOD	4/2/2003	10/406,628	4/19/2005	6,882,231	US
NMXXS-0788.00/US	Utility - ORG	Issued	TEST STRUCTURE FOR THE MEASUREMENT OF CONTACT TO GATE DISTANCE IN NON-VOLATILE MEMORY DEVICES AND CORRESPONDING TEST METHOD	5/30/2003	10/449,761	6/17/2005	6,903,995	US
NMXXS-0789.00/US	Utility - ORG	Abandoned	METHOD FOR MANUFACTURING ELECTRONIC CIRCUITS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE	5/1/2003	10/428,338	2/14/2006	6,998,348	US
NMXXS-0790.00/US	Utility - ORG	Issued	SINGLE ENDED OUTPUT SENSE AMPLIFIER CIRCUIT WITH REDUCED POWER CONSUMPTION AND NOISE	7/8/2002	10/190,917	11/23/2004	6,822,919	US
NMXXS-0791.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUIT AND METHOD FOR SEMICONDUCTOR MEMORY DEVICES	5/31/2002	10/161,501	5/4/2004	6,731,550	US
NMXXS-0792.00/US	Utility - ORG	Issued	CIRCUIT FOR CONTROLLING A REFERENCE NODE IN A SENSE AMPLIFIER	12/27/2002	10/331,147	10/5/2004	6,801,466	US
NMXXS-0793.00/US	Utility - ORG	Issued	NONVOLATILE MEMORY DEVICE WITH DOUBLE SERIAL/PARALLEL COMMUNICATION INTERFACE	10/15/2002	10/271,352	5/10/2005	6,892,269	US
NMXXS-0794.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH SELF-REFRESH CAPABILITY	3/11/2003	10/387,141	5/8/2007	7,216,212	US
NMXXS-0796.00/US	Utility - ORG	Issued	MEMORY DEVICE OUTPUTTING READ DATA IN A TIME STARTING FROM A RISING EDGE OF AN EXTERNAL CLOCK THAT IS SHORTER THAN THAT OF KNOWN DEVICES	12/19/2002	10/325,486	1/24/2006	6,990,596	US
NMXXS-0797.00/US	Utility - ORG	Issued	DEVICE INTEGRATING A NONVOLATILE MEMORY ARRAY AND A VOLATILE MEMORY ARRAY	2/7/2003	10/360,840	5/23/2006	7,050,322	US
NMXXS-0798.00/US	Utility - ORG	Issued	METHOD OF WRITING A GROUP OF DATA BYTES IN A MEMORY AND MEMORY DEVICE	2/21/2003	10/371,221	2/7/2006	6,996,697	US
NMXXS-0799.00/US	Utility - ORG	Issued	NONVOLATILE STORAGE DEVICE AND SELF-REDUNDANCY METHOD FOR THE SAME	8/11/2003	10/639,240	4/8/2008	7,355,908	US
NMXXS-0800.00/US	Utility - ORG	Issued	BUILT-IN TESTING METHODOLOGY IN FLASH MEMORY	2/27/2004	10/789,443	5/23/2006	7,050,343	US
NMXXS-0801.00/US	Utility - ORG	Issued	SECTOR PROTECTION CIRCUIT AND METHOD FOR FLASH MEMORY DEVICES	8/2/2001	09/922,043	12/3/2002	6,490,197	US
NMXXS-0802.00/US	Utility - ORG	Issued	DUAL BANK FLASH MEMORY DEVICE AND METHOD	8/2/2001	09/922,044	4/22/2003	6,552,935	US
NMXXS-0803.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUIT AND METHOD FOR FLASH MEMORY DEVICES	8/2/2001	09/922,068	5/13/2003	6,563,732	US
NMXXS-0804.00/US	Utility - ORG	Issued	REDUNDANCY CIRCUIT AND METHOD FOR REPLACING DEFECTIVE MEMORY CELLS IN A FLASH MEMORY DEVICE	8/2/2001	09/922,176	7/15/2003	6,594,177	US
NMXXS-0805.00/US	Utility - ORG	Issued	CIRCUIT AND ASSOCIATED METHOD FOR THE ERASURE OR PROGRAMMING OF A MEMORY CELL	3/11/2002	10/096,531	9/16/2003	6,621,737	US
NMXXS-0806.00/US	Utility - ORG	Issued	EEPROM MEMORY COMPRISING MEANS FOR SIMULTANEOUS READING OF SPECIAL BITS OF A FIRST AND SECOND TYPE	10/21/2002	10/277,183	5/18/2004	6,738,266	US
NMXXS-0807.00/US	Utility - ORG	Issued	EEPROM MEMORY PROTECTED AGAINST THE EFFECTS FROM A BREAKDOWN OF AN ACCESS TRANSISTOR	6/24/2002	10/178,796	8/23/2005	6,994,192	US
NMXXS-0808.00/US	Utility - ORG	Issued	READ AMPLIFIER WITH A LOW CURRENT CONSUMPTION DIFFERENTIAL OUTPUT STAGE	11/19/2002	10/299,965	7/6/2004	6,760,265	US
NMXXS-0809.00/US	Utility - ORG	Issued	METHOD FOR ERASING NON-VOLATILE MEMORY CELLS AND CORRESPONDING MEMORY DEVICE	9/30/2003	10/675,224	2/27/2007	7,184,319	US
NMXXS-0810.00/US	Utility - ORG	Issued	NON-VOLATILE LATCH CIRCUIT	5/7/2003	10/434,395	4/18/2006	7,031,189	US
NMXXS-0811.00/US	Utility - ORG	Issued	METHOD FOR REDUCING SPURIOUS ERASING DURING PROGRAMMING OF A NONVOLATILE NROM	4/29/2003	10/426,924	11/30/2004	6,826,083	US
NMXXS-0812.00/US	Utility - CIP	Issued	METHOD FOR POST ETCH CLEANS	5/1/2002	10/137,096	6/24/2008	7,390,755	US

PATENT

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Exhibit A - US Issued Patents

Patent No.	Met. Type	Met. Status	Title	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMXX-0813.00/US	Utility - ORG	Issued	REGULATION METHOD FOR THE SOURCE TERMINAL VOLTAGE IN A NON-VOLATILE MEMORY CELL DURING A PROGRAM PHASE AND CORRESPONDING PROGRAM CIRCUIT	12/27/2002	10/331,106	11/23/2004	6,822,905	US
NMXX-0815.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT STRUCTURES	7/30/2003	10/631,463	1/16/2007	7,163,898	US
NMXX-0816.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR WITH VERY QUICK RESPONSE	6/26/2003	10/608,998	6/21/2005	6,909,264	US
NMXX-0817.00/US	Utility - ORG	Issued	ARRAY OF CELLS INCLUDING A SELECTION BIPOLAR TRANSISTOR AND FABRICATION METHOD THEREOF	10/7/2003	10/680,727	11/14/2006	7,135,756	US
NMXX-0817.01/US	Utility - DIV	Issued	ARRAY OF CELLS INCLUDING A SELECTION BIPOLAR TRANSISTOR AND FABRICATION METHOD THEREOF	10/19/2006	11/551,170	11/14/2008	7,446,011	US
NMXX-0818.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING AN ARRAY OF CELLS INCLUDING SELECTION BIPOLAR JUNCTION TRANSISTORS	10/7/2003	10/680,721	1/24/2006	6,989,580	US
NMXX-0818.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING AN ARRAY OF CELLS INCLUDING SELECTION BIPOLAR JUNCTION TRANSISTORS	11/1/2005	11/264,084	7/21/2009	7,563,684	US
NMXX-0819.00/US	Utility - ORG	Issued	SELF-REPAIR METHOD VIA ECC FOR NONVOLATILE MEMORY DEVICES, AND RELATIVE NONVOLATILE MEMORY DEVICE	4/15/2003	10/417,416	5/31/2005	6,901,011	US
NMXX-0820.00/US	Utility - ORG	Issued	SELF-REPAIR METHOD FOR NONVOLATILE MEMORY DEVICES USING A SUPERSECURE ARCHITECTURE, AND NONVOLATILE MEMORY DEVICE	4/24/2003	10/423,845	7/26/2005	6,922,366	US
NMXX-0821.00/US	Utility - ORG	Issued	LINE SELECTOR FOR A MATRIX OF MEMORY ELEMENTS	7/8/2003	10/616,414	8/16/2005	6,930,927	US
NMXX-0822.00/US	Utility - ORG	Issued	MANUFACTURING PROCESS FOR A HIGH VOLTAGE TRANSISTOR INTEGRATED ON A SEMICONDUCTOR SUBSTRATE WITH NON-VOLATILE MEMORY CELLS AND CORRESPONDING TRANSISTOR	9/29/2003	10/675,245	9/27/2005	6,949,803	US
NMXX-0823.00/US	Utility - ORG	Issued	PROCESS FOR MANUFACTURING A BYTE SELECTION TRANSISTOR FOR A MATRIX OF NON VOLATILE MEMORY CELLS AND CORRESPONDING STRUCTURE	11/18/2003	10/715,887	12/6/2005	6,972,454	US
NMXX-0823.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING A BYTE SELECTION TRANSISTOR FOR A MATRIX OF NON VOLATILE MEMORY CELLS AND CORRESPONDING STRUCTURE	10/25/2005	11/258,675	11/29/2008	7,456,467	US
NMXX-0824.00/US	Utility - ORG	Issued	NON-IMPLANTATION MACHINE, CONTROL METHOD THEREOF, AND PROCESS FOR MANUFACTURING INTEGRATED DEVICES	9/8/2003	10/657,801	6/13/2006	7,060,995	US
NMXX-0826.00/US	Utility - ORG	Issued	METHOD AND DEVICE FOR PROGRAMMING AN ELECTRICALLY PROGRAMMABLE NON-VOLATILE SEMICONDUCTOR MEMORY	12/5/2003	10/729,875	4/18/2006	7,031,193	US
NMXX-0827.00/US	Utility - ORG	Issued	CIRCUIT FOR PROGRAMMING A NON-VOLATILE MEMORY DEVICE WITH ADAPTIVE PROGRAM LOAD CONTROL	11/12/2003	10/706,306	10/18/2005	6,956,773	US
NMXX-0828.00/US	Utility - ORG	Issued	SINGLE CELL ERASING METHOD FOR RECOVERING MEMORY CELLS UNDER PROGRAMMING DISTURBS IN NON VOLATILE SEMICONDUCTOR MEMORY DEVICES	11/26/2003	10/724,022	9/13/2005	6,944,061	US
NMXX-0829.00/US	Utility - ORG	Issued	CIRCUIT FOR BIASING AN INPUT NODE OF A SENSE AMPLIFIER WITH A PRE-CHARGE STAGE	9/16/2003	10/664,606	2/15/2005	6,856,547	US
NMXX-0830.00/US	Utility - ORG	Issued	INTEGRATED RESISTIVE ELEMENTS WITH SILICIDATION PROTECTION	9/26/2003	10/672,293	2/13/2007	7,176,553	US
NMXX-0830.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING INTEGRATED RESISTIVE ELEMENTS WITH SILICIDATION PROTECTION	1/30/2006	11/343,593	7/28/2009	7,566,610	US
NMXX-0831.00/US	Utility - ORG	Issued	SELF-ALIGNED INTEGRATED ELECTRONIC DEVICES	11/14/2003	10/713,538	12/23/2008	7,468,535	US
NMXX-0831.01/US	Utility - DIV	Issued	PROCESS FOR SELF-ALIGNED MANUFACTURE OF INTEGRATED ELECTRONIC DEVICES	1/4/2008	12/006,706	8/10/2010	7,772,084	US
NMXX-0832.00/US	Utility - ORG	Issued	VOLTAGE BOOST DEVICE AND MEMORY SYSTEM	7/7/2003	10/614,693	12/28/2004	6,856,442	US
NMXX-0833.00/US	Utility - ORG	Issued	METHOD OF PROGRAMMING A MULTI-LEVEL, ELECTRICALLY PROGRAMMABLE NON-VOLATILE SEMICONDUCTOR MEMORY	2/19/2004	10/782,725	9/19/2006	7,110,300	US
NMXX-0834.00/US	Utility - ORG	Issued	STRUCTURE AND METHOD FOR DETECTING ERRORS IN A MULTILEVEL MEMORY DEVICE WITH IMPROVED PROGRAMMING GRANULARITY	12/18/2003	10/741,815	1/13/2009	7,478,292	US

PATENT

REEL: 033282 FRAME: 0370

[illegible]

## PATENT

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**Exhibit A - US Issued Patents**

WTR-000	WTR-001	WTR-002	WTR-003	WTR-004	WTR-005	WTR-006	WTR-007	WTR-008	WTR-009	WTR-010	WTR-011	WTR-012	WTR-013	WTR-014	WTR-015	WTR-016	WTR-017	WTR-018	WTR-019	WTR-020	WTR-021	WTR-022	WTR-023	WTR-024	WTR-025	WTR-026	WTR-027	WTR-028	WTR-029	WTR-030	WTR-031	WTR-032	WTR-033	WTR-034	WTR-035	WTR-036	WTR-037	WTR-038	WTR-039	WTR-040	WTR-041	WTR-042	WTR-043	WTR-044	WTR-045	WTR-046	WTR-047	WTR-048	WTR-049	WTR-050	WTR-051	WTR-052	WTR-053	WTR-054	WTR-055	WTR-056	WTR-057	WTR-058	WTR-059	WTR-060	WTR-061	WTR-062	WTR-063	WTR-064	WTR-065	WTR-066	WTR-067	WTR-068	WTR-069	WTR-070	WTR-071	WTR-072	WTR-073	WTR-074	WTR-075	WTR-076	WTR-077	WTR-078	WTR-079	WTR-080	WTR-081	WTR-082	WTR-083	WTR-084	WTR-085	WTR-086	WTR-087	WTR-088	WTR-089	WTR-090	WTR-091	WTR-092	WTR-093	WTR-094	WTR-095	WTR-096	WTR-097	WTR-098	WTR-099	WTR-100	WTR-101	WTR-102	WTR-103	WTR-104	WTR-105	WTR-106	WTR-107	WTR-108	WTR-109	WTR-110	WTR-111	WTR-112	WTR-113	WTR-114	WTR-115	WTR-116	WTR-117	WTR-118	WTR-119	WTR-120	WTR-121	WTR-122	WTR-123	WTR-124	WTR-125	WTR-126	WTR-127	WTR-128	WTR-129	WTR-130	WTR-131	WTR-132	WTR-133	WTR-134	WTR-135	WTR-136	WTR-137	WTR-138	WTR-139	WTR-140	WTR-141	WTR-142	WTR-143	WTR-144	WTR-145	WTR-146	WTR-147	WTR-148	WTR-149	WTR-150	WTR-151	WTR-152	WTR-153	WTR-154	WTR-155	WTR-156	WTR-157	WTR-158	WTR-159	WTR-160	WTR-161	WTR-162	WTR-163	WTR-164	WTR-165	WTR-166	WTR-167	WTR-168	WTR-169	WTR-170	WTR-171	WTR-172	WTR-173	WTR-174	WTR-175	WTR-176	WTR-177	WTR-178	WTR-179	WTR-180	WTR-181	WTR-182	WTR-183	WTR-184	WTR-185	WTR-186	WTR-187	WTR-188	WTR-189	WTR-190	WTR-191	WTR-192	WTR-193	WTR-194	WTR-195	WTR-196	WTR-197	WTR-198	WTR-199	WTR-200	WTR-201	WTR-202	WTR-203	WTR-204	WTR-205	WTR-206	WTR-207	WTR-208	WTR-209	WTR-210	WTR-211	WTR-212	WTR-213	WTR-214	WTR-215	WTR-216	WTR-217	WTR-218	WTR-219	WTR-220	WTR-221	WTR-222	WTR-223	WTR-224	WTR-225	WTR-226	WTR-227	WTR-228	WTR-229	WTR-230	WTR-231	WTR-232	WTR-233	WTR-234	WTR-235	WTR-236	WTR-237	WTR-238	WTR-239	WTR-240	WTR-241	WTR-242	WTR-243	WTR-244	WTR-245	WTR-246	WTR-247	WTR-248	WTR-249	WTR-250	WTR-251	WTR-252	WTR-253	WTR-254	WTR-255	WTR-256	WTR-257	WTR-258	WTR-259	WTR-260	WTR-261	WTR-262	WTR-263	WTR-264	WTR-265	WTR-266	WTR-267	WTR-268	WTR-269	WTR-270	WTR-271	WTR-272	WTR-273	WTR-274	WTR-275	WTR-276	WTR-277	WTR-278	WTR-279	WTR-280	WTR-281	WTR-282	WTR-283	WTR-284	WTR-285	WTR-286	WTR-287	WTR-288	WTR-289	WTR-290	WTR-291	WTR-292	WTR-293	WTR-294	WTR-295	WTR-296	WTR-297	WTR-298	WTR-299	WTR-300	WTR-301	WTR-302	WTR-303	WTR-304	WTR-305	WTR-306	WTR-307	WTR-308	WTR-309	WTR-310	WTR-311	WTR-312	WTR-313	WTR-314	WTR-315	WTR-316	WTR-317	WTR-318	WTR-319	WTR-320	WTR-321	WTR-322	WTR-323	WTR-324	WTR-325	WTR-326	WTR-327	WTR-328	WTR-329	WTR-330	WTR-331	WTR-332	WTR-333	WTR-334	WTR-335	WTR-336	WTR-337	WTR-338	WTR-339	WTR-340	WTR-341	WTR-342	WTR-343	WTR-344	WTR-345	WTR-346	WTR-347	WTR-348	WTR-349	WTR-350	WTR-351	WTR-352	WTR-353	WTR-354	WTR-355	WTR-356	WTR-357	WTR-358	WTR-359	WTR-360	WTR-361	WTR-362	WTR-363	WTR-364	WTR-365	WTR-366	WTR-367	WTR-368	WTR-369	WTR-370	WTR-371	WTR-372	WTR-373	WTR-374	WTR-375	WTR-376	WTR-377	WTR-378	WTR-379	WTR-380	WTR-381	WTR-382	WTR-383	WTR-384	WTR-385	WTR-386	WTR-387	WTR-388	WTR-389	WTR-390	WTR-391	WTR-392	WTR-393	WTR-394	WTR-395	WTR-396	WTR-397	WTR-398	WTR-399	WTR-400	WTR-401	WTR-402	WTR-403	WTR-404	WTR-405	WTR-406	WTR-407	WTR-408	WTR-409	WTR-410	WTR-411	WTR-412	WTR-413	WTR-414	WTR-415	WTR-416	WTR-417	WTR-418	WTR-419	WTR-420	WTR-421	WTR-422	WTR-423	WTR-424	WTR-425	WTR-426	WTR-427	WTR-428	WTR-429	WTR-430	WTR-431	WTR-432	WTR-433	WTR-434	WTR-435	WTR-436	WTR-437	WTR-438	WTR-439	WTR-440	WTR-441	WTR-442	WTR-443	WTR-444	WTR-445	WTR-446	WTR-447	WTR-448	WTR-449	WTR-450	WTR-451	WTR-452	WTR-453	WTR-454	WTR-455	WTR-456	WTR-457	WTR-458	WTR-459	WTR-460	WTR-461	WTR-462	WTR-463	WTR-464	WTR-465	WTR-466	WTR-467	WTR-468	WTR-469	WTR-470	WTR-471	WTR-472	WTR-473	WTR-474	WTR-475	WTR-476	WTR-477	WTR-478	WTR-479	WTR-480	WTR-481	WTR-482	WTR-483	WTR-484	WTR-485	WTR-486	WTR-487	WTR-488	WTR-489	WTR-490	WTR-491	WTR-492	WTR-493	WTR-494	WTR-495	WTR-496	WTR-497	WTR-498	WTR-499	WTR-500	WTR-501	WTR-502	WTR-503	WTR-504	WTR-505	WTR-506	WTR-507	WTR-508	WTR-509	WTR-510	WTR-511	WTR-512	WTR-513	WTR-514	WTR-515	WTR-516	WTR-517	WTR-518	WTR-519	WTR-520	WTR-521	WTR-522	WTR-523	WTR-524	WTR-525	WTR-526	WTR-527	WTR-528	WTR-529	WTR-530	WTR-531	WTR-532	WTR-533	WTR-534	WTR-535	WTR-536	WTR-537	WTR-538	WTR-539	WTR-540	WTR-541	WTR-542	WTR-543	WTR-544	WTR-545	WTR-546	WTR-547	WTR-548	WTR-549	WTR-550	WTR-551	WTR-552	WTR-553	WTR-554	WTR-555	WTR-556	WTR-557	WTR-558	WTR-559	WTR-560	WTR-561	WTR-562	WTR-563	WTR-564	WTR-565	WTR-566	WTR-567	WTR-568	WTR-569	WTR-570	WTR-571	WTR-572	WTR-573	WTR-574	WTR-575	WTR-576	WTR-577	WTR-578	WTR-579	WTR-580	WTR-581	WTR-582	WTR-583	WTR-584	WTR-585	WTR-586	WTR-587	WTR-588	WTR-589	WTR-590	WTR-591	WTR-592	WTR-593	WTR-594	WTR-595	WTR-596	WTR-597	WTR-598	WTR-599	WTR-600	WTR-601	WTR-602	WTR-603	WTR-604	WTR-605	WTR-606	WTR-607	WTR-608	WTR-609	WTR-610	WTR-611	WTR-612	WTR-613	WTR-614	WTR-615	WTR-616	WTR-617	WTR-618	WTR-619	WTR-620	WTR-621	WTR-622	WTR-623	WTR-624	WTR-625	WTR-626	WTR-627	WTR-628	WTR-629	WTR-630	WTR-631	WTR-632	WTR-633	WTR-634	WTR-635	WTR-636	WTR-637	WTR-638	WTR-639	WTR-640	WTR-641	WTR-642	WTR-643	WTR-644	WTR-645	WTR-646	WTR-647	WTR-648	WTR-649	WTR-650	WTR-651	WTR-652	WTR-653	WTR-654	WTR-655	WTR-656	WTR-657	WTR-658	WTR-659	WTR-660	WTR-661	WTR-662	WTR-663	WTR-664	WTR-665	WTR-666	WTR-667	WTR-668	WTR-669	WTR-670	WTR-671	WTR-672	WTR-673	WTR-674	WTR-675	WTR-676	WTR-677	WTR-678	WTR-679	WTR-680	WTR-681	WTR-682	WTR-683	WTR-684	WTR-685	WTR-686	WTR-687	WTR-688	WTR-689	WTR-690	WTR-691	WTR-692	WTR-693	WTR-694	WTR-695	WTR-696	WTR-697	WTR-698	WTR-699	WTR-700	WTR-701	WTR-702	WTR-703	WTR-704	WTR-705	WTR-706	WTR-707	WTR-708	WTR-709	WTR-710	WTR-711	WTR-712	WTR-713	WTR-714	WTR-715	WTR-716	WTR-717	WTR-718	WTR-719	WTR-720	WTR-721	WTR-722	WTR-723	WTR-724	WTR-725	WTR-726	WTR-727	WTR-728	WTR-729	WTR-730	WTR-731	WTR-732	WTR-733	WTR-734	WTR-735	WTR-736	WTR-737	WTR-738	WTR-739	WTR-740	WTR-741	WTR-742	WTR-743	WTR-744	WTR-745	WTR-746	WTR-747	WTR-748	WTR-749	WTR-750	WTR-751	WTR-752	WTR-753	WTR-754	WTR-755	WTR-756	WTR-757	WTR-758	WTR-759	WTR-760	WTR-761	WTR-762	WTR-763	WTR-764	WTR-765	WTR-766	WTR-767	WTR-768	WTR-769	WTR-770	WTR-771	WTR-772	WTR-773	WTR-774	WTR-775	WTR-776	WTR-777	WTR-778	WTR-779	WTR-780	WTR-781	WTR-782	WTR-783	WTR-784	WTR-785	WTR-786	WTR-787	WTR-788	WTR-789	WTR-790	WTR-791	WTR-792	WTR-793	WTR-794	WTR-795	WTR-796	WTR-797	WTR-798	WTR-799	WTR-800	WTR-801	WTR-802	WTR-803	WTR-804	WTR-805	WTR-806	WTR-807	WTR-808	WTR-809	WTR-810	WTR-811	WTR-812	WTR-813	WTR-814	WTR-815	WTR-816	WTR-817	WTR-818	WTR-819	WTR-820	WTR-821	WTR-822	WTR-823	WTR-824	WTR-825	WTR-826	WTR-827	WTR-828	WTR-829	WTR-830	WTR-831	WTR-832	WTR-833	WTR-834	WTR-835	WTR-836	WTR-837	WTR-838	WTR-839	WTR-840	WTR-841	WTR-842	WTR-843	WTR-844	WTR-845	WTR-846	WTR-847	WTR-848	WTR-849	WTR-850	WTR-851	WTR-852	WTR-853	WTR-854	WTR-855	WTR-856	WTR-857	WTR-858	WTR-859	WTR-860	WTR-861	WTR-862	WTR-863	WTR-864	WTR-865	WTR-866	WTR-867	WTR-868	WTR-869	WTR-870	WTR-871	WTR-872	WTR-873	WTR-874	WTR-875	WTR-876	WTR-877	WTR-878	WTR-879	WTR-880	WTR-881	WTR-882	WTR-883	WTR-884	WTR-885	WTR-886	WTR-887	WTR-888	WTR-889	WTR-890	WTR-891	WTR-892	WTR-893	WTR-894	WTR-895	WTR-896	WTR-897	WTR-898	WTR-899	WTR-900	WTR-901	WTR-902	WTR-903	WTR-904	WTR-905	WTR-906	WTR-907	WTR-908	WTR-909	WTR-910	WTR-911	WTR-912	WTR-913	WTR-914	WTR-915	WTR-916	WTR-917	WTR-918	WTR-919	WTR-920	WTR-921	WTR-922	WTR-923	WTR-924	WTR-925	WTR-926	WTR-927	WTR-928	WTR-929	WTR-930	WTR-931	WTR-932	WTR-933	WTR-934	WTR-935	WTR-936	WTR-937	WTR-938	WTR-939	WTR-940	WTR-941	WTR-942	WTR-943	WTR-944	WTR-945	WTR-946	WTR-947	WTR-948	WTR-949	WTR-950	WTR-951	WTR-952	WTR-953	WTR-954	WTR-955	WTR-956	WTR-957	WTR-958	WTR-959	WTR-960	WTR-961	WTR-962	WTR-963	WTR-964	WTR-965	WTR-966	WTR-967	WTR-968	WTR-969	WTR-970	WTR-971	WTR-972	WTR-973	WTR-974	WTR-975	WTR-976	WTR-977	WTR-978	WTR-979	WTR-980	WTR-981	WTR-982	WTR-983	WTR-984	WTR-985	WTR-986	WTR-987	WTR-988	WTR-989	WTR-990	WTR-991	WTR-992	WTR-993	WTR-994	WTR-995	WTR-996	WTR-997	WTR-998	WTR-999	WTR-1000	WTR-1001	WTR-1002	WTR-1003	WTR-1004	WTR-1005	WTR-1006	WTR-1007	WTR-1008	WTR-1009	WTR-1010	WTR-1011	WTR-1012	WTR-1013	WTR-1014	WTR-1015	WTR-1016	WTR-1017	WTR-1018	WTR-1019	WTR-1020	WTR-1021	WTR-1022	WTR-1023	WTR-1024	WTR-1025	WTR-1026	WTR-1027	WTR-1028	WTR-1029	WTR-1030	WTR-1031	WTR-1032	WTR-1033	WTR-1034	WTR-1035	WTR-1036	WTR-1037	WTR-1038	WTR-1039	WTR-1040	WTR-1041	WTR-1042	WTR-1043	WTR-1044	WTR-1045	WTR-1046	WTR-1047	WTR-1048	WTR-1049	WTR-1050	WTR-1051	WTR-1052	WTR-1053	WTR-1054	WTR-1055	WTR-1056	WTR-1057	WTR-1058	WTR-1059	WTR-1060	WTR-1061	WTR-1062	WTR-1063	WTR-1064	WTR-1065	WTR-1066	WTR-1067	WTR-1068	WTR-1069	WTR-1070	WTR-1071	WTR-1072	WTR-1073	WTR-1074	WTR-1075	WTR-1076	WTR-1077	WTR-1078	WTR-1079	WTR-1080	WTR-1081	WTR-1082	WTR-1083	WTR-1084	WTR-1085	WTR-1086	WTR-1087	WTR-1088	WTR-1089	WTR-1090	WTR-1091	WTR-1092	WTR-1093	WTR-1094	WTR-1095	WTR-1096	WTR-1097	WTR-1098	WTR-1099	WTR-1100	WTR-1101	WTR-1102	WTR-1103	WTR-1104	WTR-1105	WTR-1106	WTR-1107	WTR-1108	WTR-1109	WTR-1110	WTR-1111	WTR-1112	WTR-1113	WTR-1114
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Exhibit A - US Issued Patents

Patent No.	Patent Title	Patent Status	Patent Description	Filed Date	Granted Date	Issued Date	Patent No.	Quantity
NMXS-0895.00/US	Utility - ORG	Issued	METHOD FOR REDUCING NON-UNIFORMITY OR TOPOGRAPHY VARIATION BETWEEN AN ARRAY AND CIRCUITRY IN A PROCESS FOR MANUFACTURING SEMICONDUCTOR INTEGRATED NON-VOLATILE MEMORY DEVICES	2/16/2005	11/059,294	3/20/2007	7,192,820	US
NMXS-0896.00/US	Utility - ORG	Issued	EXPLOITING A STATISTICAL DISTRIBUTION OF THE VALUES OF AN ELECTRICAL CHARACTERISTIC IN A POPULATION OF AUXILIARY MEMORY CELLS FOR OBTAINING REFERENCE CELLS	12/23/2005	11/318,053	12/8/2009	7,630,263	US
NMXS-0897.00/US	Utility - ORG	Issued	SENSE AMPLIFIER WITH EQUALIZER	8/6/2004	10/913,788	11/14/2006	7,136,305	US
NMXS-0898.00/US	Utility - ORG	Issued	METHOD FOR REDUCING DEFECTS AFTER A METAL ETCHING IN SEMICONDUCTOR DEVICES	12/10/2004	11/009,667	10/30/2007	7,288,427	US
NMXS-0899.00/US	Utility - ORG	Issued	METHOD FOR PERFORMING ERROR CORRECTIONS OF DIGITAL INFORMATION CODED AS A SYMBOL SEQUENCE	3/19/2004	10/805,168	2/5/2008	7,328,397	US
NMXS-0900.00/US	Utility - ORG	Issued	INTEGRATED MEMORY SYSTEM	3/19/2004	10/805,182	6/1/2010	7,330,357	US
NMXS-0901.00/US	Utility - ORG	Issued	PHASE CHANGE MEMORY DEVICE WITH BIASING OF DISELECTED BIT LINES	8/25/2004	10/926,784	8/15/2006	7,092,277	US
NMXS-0902.00/US	Utility - ORG	Issued	MANUFACTURING METHOD FOR NON-ACTIVE ELECTRICALLY STRUCTURES IN ORDER TO OPTIMIZE THE DEFINITION OF ACTIVE ELECTRICALLY STRUCTURES IN AN ELECTRONIC CIRCUIT INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT	8/3/2004	10/911,220	2/21/2006	7,001,800	US
NMXS-0902.01/US	Utility - DIV	Issued	MANUFACTURING METHOD FOR NON-ACTIVE ELECTRICALLY STRUCTURES IN ORDER TO OPTIMIZE THE DEFINITION OF ACTIVE ELECTRICALLY STRUCTURES IN AN ELECTRONIC CIRCUIT INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT	1/19/2006	11/334,986	1/22/2008	7,320,904	US
NMXS-0903.00/US	Utility - ORG	Issued	SEALING METHOD FOR ELECTRONIC DEVICES FORMED ON A COMMON SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT STRUCTURE	10/22/2004	10/971,774	7/18/2006	7,078,294	US
NMXS-0904.00/US	Utility - ORG	Issued	SENSING CIRCUIT FOR A SEMICONDUCTOR MEMORY	8/6/2004	10/913,128	9/18/2007	7,272,053	US
NMXS-0905.00/US	Utility - ORG	Issued	BIT LINE DISCHARGE CONTROL METHOD AND CIRCUIT FOR A SEMICONDUCTOR MEMORY	10/22/2004	10/971,776	8/14/2007	7,257,039	US
NMXS-0906.00/US	Utility - ORG	Issued	SENSING CIRCUIT FOR A SEMICONDUCTOR MEMORY	8/1/2005	11/194,739	2/27/2007	7,184,348	US
NMXS-0908.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING NON-VOLATILE MEMORY DEVICES INTEGRATED IN A SEMICONDUCTOR SUBSTRATE	12/27/2005	11/319,798	9/2/2008	7,419,876	US
NMXS-0910.00/US	Utility - ORG	Issued	DISTRIBUTION OF AN ELECTRIC QUANTITY THROUGH A CIRCUIT	7/20/2005	11/185,905	4/23/2009	7,521,989	US
NMXS-0912.00/US	Utility - ORG	Issued	PAGE BUFFER CIRCUIT AND METHOD FOR A PROGRAMMABLE MEMORY DEVICE	6/24/2005	11/166,354	7/28/2009	7,567,456	US
NMXS-0913.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH INFORMATION LOSS SELF-DETECT CAPABILITY	5/1/2006	11/415,879	8/19/2008	7,414,902	US
NMXS-0914.00/US	Utility - ORG	Issued	NONVOLATILE PHASE CHANGE MEMORY DEVICE AND BIASING METHOD THEREFOR	8/2/2005	11/195,359	9/11/2007	7,269,080	US
NMXS-0916.00/US	Utility - ORG	Issued	PHASE CHANGE MEMORY CELL WITH JUNCTION SELECTOR AND MANUFACTURING METHOD THEREOF	12/19/2005	11/312,253	10/14/2008	7,436,692	US
NMXS-0917.00/US	Utility - ORG	Issued	READ/VERIFY CIRCUIT FOR MULTILEVEL MEMORY CELLS WITH RAMP READ VOLTAGE, AND READ/VERIFY METHOD THEREOF	7/8/2005	11/178,240	7/8/2008	7,397,702	US
NMXS-0919.00/US	Utility - ORG	Issued	METHOD OF MAKING A FLOATING GATE NON-VOLATILE MOS SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CAPACITIVE COUPLING AND DEVICE THUS OBTAINED	12/22/2005	11/317,641	8/30/2011	8,006,701	US
NMXS-0920.00/US	Utility - ORG	Issued	PROGRAMMABLE NAND MEMORY	7/14/2005	11/183,229	6/8/2010	7,733,697	US
NMXS-0921.00/US	Utility - ORG	Issued	DATA CONTROL UNIT CAPABLE OF CORRECTING BOOT ERRORS, AND CORRESPONDING SELF-CORRECTION METHOD	6/9/2005	11/149,948	10/28/2008	7,444,543	US
NMXS-0923.00/US	Utility - ORG	Issued	TUNING FUNCTIONAL PARAMETERS IN INTEGRATED CIRCUITS	4/25/2005	11/113,818	5/22/2007	7,221,212	US
NMXS-0924.00/US	Utility - ORG	Issued	METHOD FOR MANUFACTURING ELECTRONIC NON-VOLATILE MEMORY DEVICES INTEGRATED IN A SEMICONDUCTOR SUBSTRATE	12/27/2005	11/319,750	2/5/2008	7,326,615	US

Exhibit A - US Issued Patents

Exhibit A - US Issued Patents								
Patent No.	Patent Title	Patent Status	Patent Description	Filed Date	Serialing	Issued Date	Patenting	Country
NMXXS-0926.00/US	Utility - ORG	Issued	FLASH MEMORY DEVICE WITH NAND ARCHITECTURE WITH REDUCED CAPACITIVE COUPLING EFFECT	5/31/2006	11/445,491	7/1/2008	7,394,694	US
NMXXS-0928.00/US	Utility - ORG	Issued	INTEGRATED ELECTRONIC DEVICE HAVING A LOW VOLTAGE ELECTRIC SUPPLY	4/11/2006	11/401,523	11/10/2009	7,616,515	US
NMXXS-0929.00/US	Utility - ORG	Issued	REGULATOR CIRCUITRY AND METHOD	10/27/2003	10/695,294	6/20/2006	7,064,534	US
NMXXS-0930.00/US	Utility - ORG	Issued	MAGNITUDE CONTENT ADDRESSABLE MEMORY	10/21/2003	10/690,368	6/12/2007	7,230,839	US
NMXXS-0931.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR WITH STRESS MODE	10/21/2003	10/690,332	3/14/2006	7,012,417	US
NMXXS-0932.00/US	Utility - ORG	Issued	USER RAM FLASH CLEAR	2/27/2004	10/788,581	2/6/2007	7,173,845	US
NMXXS-0933.00/US	Utility - ORG	Issued	RESET INITIALIZATION	11/30/2004	10/999,751	4/24/2007	7,208,987	US
NMXXS-0934.00/US	Utility - ORG	Issued	TAMPER MEMORY CELL	2/20/2004	10/783,935	5/29/2007	7,224,600	US
NMXXS-0935.00/US	Utility - ORG	Issued	RESET RAMP CONTROL	12/15/2004	11/012,693	8/12/2008	7,411,433	US
NMXXS-0937.00/US	Utility - ORG	Issued	ESD BONDING PAD	12/15/2004	11/013,123	2/19/2008	7,333,310	US
NMXXS-0938.00/US	Utility - ORG	Issued	VOLTAGE TRANSLATING CONTROL STRUCTURE	12/15/2004	11/012,533	5/6/2008	7,368,947	US
NMXXS-0939.00/US	Utility - ORG	Abandoned	RESET CIRCUIT	12/15/2004	11/012,712	10/12/2007	7,276,948	US
NMXXS-0940.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR READING A CELL OF A NON-VOLATILE MEMORY DEVICE	5/4/2005	11/121,616	1/23/2007	7,167,394	US
NMXXS-0941.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR VERIFYING AND EVENTUALLY SUBSTITUTING DEFECTIVE REFERENCE CELLS OF A MEMORY	5/4/2005	11/121,615	2/13/2007	7,177,217	US
NMXXS-0943.01/US	Utility - CON	Issued	CIRCUIT FOR SELECTING/DESELECTING A BITLINE OF A NON-VOLATILE MEMORY	5/3/2005	11/120,766	8/7/2007	7,254,062	US
NMXXS-0944.00/US	Utility - ORG	Issued	METHOD FOR MANAGING BAD MEMORY BLOCKS IN A NONVOLATILE-MEMORY DEVICE, AND NONVOLATILE-MEMORY DEVICE IMPLEMENTING THE MANAGEMENT METHOD	2/19/2008	12/033,855	10/13/2009	7,603,593	US
NMXXS-0945.00/US	Utility - ORG	Issued	ROW DECODER FOR NAND MEMORIES	8/11/2005	11/202,632	2/20/2007	7,180,786	US
NMXXS-0946.00/US	Utility - ORG	Issued	METHOD OF GENERATING AN ENABLE SIGNAL OF A STANDARD MEMORY CORE AND RELATIVE MEMORY DEVICE	7/17/2004	10/886,003	4/14/2009	7,519,751	US
NMXXS-0947.00/US	Utility - ORG	Issued	INTEGRATED MEMORY DEVICE WITH MULTI-SECTOR SELECTION COMMANDS	11/8/2004	10/984,372	11/25/2008	7,457,908	US
NMXXS-0948.00/US	Utility - ORG	Issued	SYNCHRONOUS MEMORY DEVICE WITH REDUCED POWER CONSUMPTION	5/25/2005	11/138,884	4/29/2008	7,366,012	US
NMXXS-0949.00/US	Utility - ORG	Issued	FLASH MEMORY COMPRISING MEANS FOR CHECKING AND REFRESHING MEMORY CELLS IN THE ERASED STATE	6/14/2004	10/867,378	5/23/2006	7,050,335	US
NMXXS-0950.00/US	Utility - ORG	Issued	MEMORY CIRCUIT WITH NON-VOLATILE IDENTIFICATION MEMORY AND ASSOCIATED METHOD	8/18/2004	10/921,365	6/27/2006	7,068,538	US
NMXXS-0952.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR A CONTINUOUS READ COMMAND IN AN EXTENDED MEMORY ARRAY	12/9/2004	11/008,586	2/12/2008	7,330,381	US
NMXXS-0952.01/US	Utility - DIV	Issued	SERIAL MEMORY COMPRISING MEANS FOR PROTECTING AN EXTENDED MEMORY ARRAY DURING A WRITE OPERATION	12/9/2004	11/008,588	10/30/2007	7,290,078	US
NMXXS-0953.00/US	Utility - ORG	Issued	SERIAL MEMORY COMPRISING MEANS FOR PROTECTING AN EXTENDED MEMORY ARRAY DURING A WRITE OPERATION	9/10/2007	11/852,937	9/7/2010	7,793,033	US
NMXXS-0954.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH UNIPOLAR AND BIPOLAR SELECTORS	9/22/2005	11/233,464	1/27/2009	7,483,296	US
NMXXS-0957.00/US	Utility - ORG	Issued	ELECTRONIC NON-VOLATILE MEMORY DEVICE HAVING A NAND STRUCTURE AND BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/401,521	3/9/2010	7,675,788	US
NMXXS-0958.00/US	Utility - ORG	Issued	MEMORY DEVICE	10/13/2005	11/249,763	3/27/2007	7,196,943	US
NMXXS-0959.00/US	Utility - ORG	Issued	CHARGE-PUMP DEVICE WITH INCREASED CURRENT OUTPUT	11/9/2005	11/270,308	5/12/2009	7,532,060	US
NMXXS-0961.00/US	Utility - ORG	Issued	INTEGRATED ELECTRONIC NON-VOLATILE MEMORY DEVICE HAVING NAND STRUCTURE	4/11/2006	11/279,384	11/13/2007	7,295,472	US
NMXXS-0963.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/279,378	9/25/2007	7,274,594	US
NMXXS-0964.00/US	Utility - ORG	Issued	PROGRAMMING METHOD OF MULTILEVEL MEMORIES AND CORRESPONDING CIRCUIT	10/27/2005	11/261,903	1/8/2008	7,317,637	US
NMXXS-0965.00/US	Utility - ORG	Issued	MEMORY DEVICE	10/13/2005	11/250,176	4/1/2008	7,352,645	US
NMXXS-0965.00/US	Utility - ORG	Issued	MEMORY DEVICE AND METHOD FOR OPERATING THE SAME WITH HIGH REJECTION OF THE NOISE ON THE HIGH-VOLTAGE SUPPLY LINE	9/30/2005	11/241,729	1/29/2008	7,324,379	US

PATENT

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**PATENT**  
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UNIT NO.	MEMO TYPE	MEMO STATUS	MEMO TITLE	FILE DATE	SYSTEM NO.	ISSUE DATE	PATENT NO.	COUNTRY
NNMXS-0966.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY DEVICE HAVING HIGH DENSITY NON-VOLATILE MEMORY CELLS AND A REDUCED CAPACITIVE INTERFERENCE CELL-TO-CELL	12/14/2005	11/300,053	1/15/2008	7,319,604	US
NNMXS-0968.00/US	Utility - ORG	Issued	ROW DECODER CIRCUIT AND RELATED SYSTEM AND METHOD	5/15/2006	11/434,564	4/28/2009	7,525,851	US
NNMXS-0969.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/279,381	7/8/2008	7,397,700	US
NNMXS-0971.00/US	Utility - ORG	Issued	FLASH MEMORY DEVICE WITH REDUCED DRAIN STRESSES	2/6/2007	11/703,535	5/19/2009	7,535,770	US
NNMXS-0972.00/US	Utility - ORG	Issued	REMOVABLE DATA STORAGE DEVICE AND RELATED ASSEMBLING METHOD	1/31/2007	11/344,519	2/2/2010	7,656,676	US
NNMXS-0974.00/US	Utility - ORG	Issued	METHOD FOR CONFIGURING A VOLTAGE REGULATOR	11/16/2005	11/280,803	6/17/2008	7,388,793	US
NNMXS-0974.01/US	Utility - DIV	Issued	VOLTAGE REGULATOR FOR THE PROGRAMMING CIRCUIT OF A MEMORY CELL	12/26/2007	11/964,250	4/27/2010	7,706,193	US
NNMXS-0975.00/US	Utility - ORG	Issued	PROCESS FOR DIGGING A DEEP TRENCH IN A SEMICONDUCTOR BODY AND SEMICONDUCTOR BODY SO OBTAINED	12/28/2006	11/648,838	6/9/2009	7,544,620	US
NNMXS-0976.00/US	Utility - ORG	Issued	METHOD AND SYSTEM FOR CONTROLLING MRAM WRITE CURRENT TO REDUCE POWER CONSUMPTION	3/31/2004	10/816,271	9/19/2006	7,110,289	US
NNMXS-0977.00/US	Utility - ORG	Issued	MAGNETIC MEMORY CELL WITH PLURAL READ TRANSISTORS	10/31/2003	10/698,747	3/14/2006	7,012,832	US
NNMXS-0978.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING A MEMORY DEVICE SUITABLE TO MINIMIZE THE LATERAL COUPLING EFFECTS BETWEEN MEMORY CELLS	2/6/2006	11/348,513	12/30/2008	7,471,571	US
NNMXS-0979.00/US	Utility - ORG	Issued	DUAL RESISTANCE HEATER FOR PHASE CHANGE DEVICES AND MANUFACTURING METHOD THEREOF	12/19/2005	11/312,231	2/1/2011	7,880,123	US
NNMXS-0983.00/US	Utility - ORG	Issued	HIGH-VOLTAGE SWITCH WITH LOW OUTPUT RIPPLE FOR NON-VOLATILE FLOATING-GATE MEMORIES	5/19/2006	11/437,405	4/21/2009	7,521,983	US
NNMXS-0984.00/US	Utility - ORG	Issued	CHARGE PUMP TYPE, VOLTAGE-BOOSTING DEVICE WITH REDUCED RIPPLE, IN PARTICULAR FOR NON-VOLATILE FLASH MEMORIES	5/19/2006	11/437,268	5/12/2009	7,532,051	US
NNMXS-0985.00/US	Utility - ORG	Issued	PHASE CHANGE MEMORY CELL WITH TUBULAR HEATER AND MANUFACTURING METHOD THEREOF	4/6/2006	11/398,858	10/21/2008	7,439,536	US
NNMXS-0986.00/US	Utility - ORG	Issued	ELECTRONIC MEMORY DEVICE HAVING HIGH INTEGRATION DENSITY NON-VOLATILE MEMORY CELLS AND A REDUCED CAPACITIVE COUPLING	12/14/2005	11/300,145	9/22/2009	7,593,247	US
NNMXS-0987.00/US	Utility - ORG	Issued	METHOD FOR ACCESSING IN READING, WRITING AND PROGRAMMING TO A NAND NON-VOLATILE MEMORY ELECTRONIC DEVICE MONOLITHICALLY INTEGRATED ON SEMICONCTOR	11/20/2006	11/561,799	5/5/2009	7,529,881	US
NNMXS-0987.01/US	Utility - DIV	Issued	METHOD FOR ACCESSING IN READING, WRITING AND PROGRAMMING TO A NAND NON-VOLATILE MEMORY ELECTRONIC DEVICE MONOLITHICALLY INTEGRATED ON SEMICONCTOR	3/24/2009	12/409,740	1/19/2010	7,649,778	US
NNMXS-0988.00/US	Utility - ORG	Issued	VERTICAL MOSFET TRANSISTOR, IN PARTICULAR OPERATING AS A SELECTOR IN NONVOLATILE MEMORY DEVICES	4/26/2006	11/411,982	12/7/2010	7,847,329	US
NNMXS-0989.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY IMPLEMENTED WITH LOW-VOLTAGES TRANSISTORS AND RELATED SYSTEM AND METHOD	11/27/2006	11/605,209	3/3/2009	7,499,345	US
NNMXS-0990.00/US	Utility - ORG	Issued	CONTROL OF VOLTAGES DURING ERASE AND RE-PROGRAM OPERATIONS OF MEMORY CELLS	1/18/2006	11/334,205	10/30/2007	7,289,368	US
NNMXS-0991.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH A RAMP-LIKE VOLTAGE BIASING STRUCTURE BASED ON A CURRENT GENERATOR	1/26/2006	11/340,414	4/15/2008	7,359,246	US
NNMXS-0992.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR RETRIEVING DATA STORED IN SEMICONDUCTOR MEMORY CELLS	5/31/2006	11/444,892	1/6/2009	7,474,577	US
NNMXS-0992.01/US	Utility - DIV	Issued	CIRCUIT AND METHOD FOR RETRIEVING DATA STORED IN SEMICONDUCTOR MEMORY CELLS	10/9/2008	12/248,843	2/1/2011	7,889,586	US
NNMXS-0993.00/US	Utility - ORG	Issued	DATA STORING METHOD FOR A NON-VOLATILE MEMORY CELL ARRAY HAVING AN ERROR CORRECTION CODE	4/25/2006	11/411,010	1/26/2010	7,653,863	US
NNMXS-0995.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH A RAMP-LIKE VOLTAGE BIASING STRUCTURE AND REDUCED NUMBER OF REFERENCE CELLS	3/3/2006	11/568,363	6/30/2009	7,554,861	US
NNMXS-0996.00/US	Utility - ORG	Issued	METHOD OF MANAGING FAILS IN A NON-VOLATILE MEMORY DEVICE AND RELATIVE MEMORY DEVICE	11/8/2006	11/557,786	8/4/2009	7,571,362	US

Exhibit A - US Issued Patents

Patent No.	Patent Title	Filed Date	Serial No.	Issued Date	Patent No.	Country		
NMXXS-0997.00/US	Utility - NSPECT	Issued	METHOD FOR MULTILEVEL PROGRAMMING OF PHASE CHANGE MEMORY CELLS USING A PERCOLATION ALGORITHM	6/1/2006	11/949,598	12/29/2009	7,639,526	US
NMXXS-0998.00/US	Utility - ORG	Issued	FLASH MEMORY DEVICE WITH IMPROVED MANAGEMENT OF PROTECTION INFORMATION	5/10/2006	11/432,301	7/20/2010	7,761,675	US
NMXXS-0999.00/US	Utility - ORG	Issued	NAND FLASH MEMORY WITH ERASE VERIFY BASED ON SHORTER EVALUATION TIME	7/28/2006	11/495,886	4/22/2008	7,362,616	US
NMXXS-1000.00/US	Utility - ORG	Issued	TRANSISTOR STRUCTURE WITH HIGH INPUT IMPEDANCE AND HIGH CURRENT CAPABILITY	11/27/2006	11/605,190	7/14/2009	7,560,782	US
NMXXS-1001.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH TIME-SHIFTING BASED EMULATION OF REFERENCE CELLS	3/2/2006	11/367,707	3/18/2008	7,345,905	US
NMXXS-1002.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING A MEMORY DEVICE SUITABLE TO MINIMIZE FLOATING GATE COUPLING AND MEMORY DEVICE	4/2/2007	11/732,486	3/30/2010	7,688,633	US
NMXXS-1003.00/US	Utility - ORG	Issued	NON VOLATILE MEMORY DEVICE ARCHITECTURE AND CORRESPONDING PROGRAMMING METHOD	2/28/2007	11/713,081	1/19/2010	7,649,791	US
NMXXS-1004.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE WITH MULTIPLE REFERENCES AND CORRESPONDING CONTROL METHOD	7/27/2006	11/460,531	1/12/2010	7,646,644	US
NMXXS-1006.00/US	Utility - ORG	Issued	RAMP GENERATOR AND RELATIVE ROW DECODER FOR FLASH MEMORY DEVICE	5/3/2006	11/381,426	1/22/2008	7,321,512	US
NMXXS-1007.00/US	Utility - ORG	Issued	ROW SELECTOR WITH REDUCED AREA OCCUPATION FOR SEMICONDUCTOR MEMORY DEVICES	2/8/2007	11/672,857	6/1/2011	7,965,561	US
NMXXS-1008.00/US	Utility - ORG	Issued	BUILT-IN SELF TEST FOR A COUNTER SYSTEM	3/10/2005	11/076,785	12/13/2005	6,975,696	US
NMXXS-1009.00/US	Utility - ORG	Issued	READING CIRCUIT AND METHOD FOR A NONVOLATILE MEMORY DEVICE	9/28/2005	11/238,137	7/10/2007	7,242,619	US
NMXXS-1009.01/US	Utility - CON	Issued	READING CIRCUIT AND METHOD FOR A NONVOLATILE MEMORY DEVICE	6/8/2007	11/811,394	11/11/2008	7,450,428	US
NMXXS-1010.00/US	Utility - ORG	Issued	SELF-ADAPTIVE OUTPUT BUFFER BASED ON CHARGE SHARING	7/7/2006	11/482,524	9/8/2009	7,586,331	US
NMXXS-1011.00/US	Utility - ORG	Issued	CIRCUIT FOR GENERATING AN INTERNAL ENABLING SIGNAL FOR AN OUTPUT BUFFER OF A MEMORY	1/20/2006	11/337,030	5/19/2009	7,535,774	US
NMXXS-1014.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR SIMULTANEOUSLY PROGRAMMING MEMORY CELLS	4/13/2006	11/279,663	12/4/2007	7,304,896	US
NMXXS-1015.00/US	Utility - ORG	Issued	SEQUENTIAL PROGRAM-VERIFY METHOD WITH RESULT BUFFERING	3/29/2005	11/093,012	2/27/2007	7,184,310	US
NMXXS-1017.00/US	Utility - ORG	Issued	OPTIMIZING WRITE/ERASE OPERATIONS IN MEMORY DEVICES	1/13/2005	11/035,013	4/22/2008	7,363,421	US
NMXXS-1018.00/US	Utility - ORG	Issued	CONTROLLING OPERATION OF FLASH MEMORIES	1/14/2005	11/036,179	11/25/2008	7,457,909	US
NMXXS-1026.00/US	Utility - ORG	Issued	MEMORY ARCHITECTURE	9/1/2006	11/469,754	7/28/2009	7,567,475	US
NMXXS-1033.00/US	Utility - ORG	Issued	MEMORY ARCHITECTURE WITH SERIAL PERIPHERAL INTERFACE	9/8/2006	11/530,199	9/7/2010	7,793,031	US
NMXXS-1038.00/US	Utility - ORG	Issued	VOLTAGE REGULATOR OR NON-VOLATILE MEMORIES IMPLEMENTED WITH LOW-VOLTAGE TRANSISTORS	8/24/2007	11/844,470	8/17/2010	7,777,466	US
NMXXS-1040.00/US	Utility - ORG	Issued	READING CIRCUIT FOR SEMICONDUCTOR MEMORY	4/27/2007	11/740,941	3/24/2009	7,508,717	US
NMXXS-1043.00/US	Utility - ORG	Issued	SENSING CIRCUIT FOR SEMICONDUCTOR MEMORIES	4/24/2007	11/739,167	4/7/2009	7,515,493	US
NMXXS-1044.00/US	Utility - ORG	Issued	ELECTRONIC DEVICE CONTAINING SEMICONDUCTOR POLYMERS AND CORRESPONDING MANUFACTURING PROCESS	1/30/2007	11/700,541	10/18/2011	8,039,831	US
NMXXS-1046.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY DEVICE WITH A PAGE BUFFER HAVING AN IMPROVED LAYOUT ARRANGEMENT	7/25/2006	11/459,831	8/5/2008	7,408,819	US
NMXXS-1047.00/US	Utility - ORG	Issued	REDUCTION OF THE TIME FOR EXECUTING AN EXTERNALLY COMMANDED TRANSFER OF DATA IN AN INTEGRATED DEVICE	3/16/2007	11/687,353	7/28/2009	7,567,107	US
NMXXS-1048.00/US	Utility - ORG	Issued	SYNCHRONIZATION OF OPERATIONS IN DISTINCT MEMORY PARTITIONS	3/14/2007	11/686,133	4/7/2009	7,515,464	US
NMXXS-1050.00/US	Utility - ORG	Issued	OPTIMIZED FLASH MEMORY ACCESS METHOD AND DEVICE	4/13/2007	11/787,101	9/6/2011	8,015,345	US
NMXXS-1052.00/US	Utility - ORG	Issued	METHOD AND CIRCUIT FOR PROGRAMMING A MEMORY CELL, IN PARTICULAR OF THE NOR FLASH TYPE	4/16/2008	12/104,118	2/2/2010	7,656,712	US
NMXXS-1053.00/US	Utility - ORG	Issued	HIGH VOLTAGE GENERATOR OF THE DAC-CONTROLLED TYPE	8/8/2006	11/463,260	4/27/2010	7,706,160	US

PATENT

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Patent No.	Utility Type	Patent Status	The Invention	Filing Date	Serial No.	Issuance Date	Patent No.	Country
NMXS-1054.00/US	Utility - ORG	Issued	READING METHOD OF A MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE AND MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE	3/1/2007	11/713,376	3/15/2011	7,908,543	US
NMXS-1055.00/US	Utility - ORG	Issued	FAST PROGRAMMING MEMORY DEVICE	5/19/2008	12/123,359	9/13/2011	8,018,771	US
NMXS-1056.00/US	Utility - ORG	Issued	OUTPUT BUFFER	12/13/2006	11/658,321	12/9/2008	7,463,051	US
NMXS-1056.01/US	Utility - DIV	Issued	OUTPUT BUFFER	10/30/2008	12/261,101	9/28/2010	7,804,322	US
NMXS-1062.00/US	Utility - ORG	Issued	REFERENCE CELL LAYOUT WITH ENHANCED RTN IMMUNITY	4/27/2007	11/741,462	6/23/2009	7,551,465	US
NMXS-1066.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE	10/12/2006	11/549,067	1/27/2009	7,483,300	US
NMXS-1067.00/US	Utility - ORG	Issued	METHOD FOR PROGRAMMING OF MEMORY CELLS, IN PARTICULAR OF THE FLASH TYPE, AND CORRESPONDING PROGRAMMING ARCHITECTURE	12/8/2006	11/636,382	10/20/2009	7,606,078	US
NMXS-1067.01/US	Utility - DIV	Issued	METHOD FOR PROGRAMMING OF MEMORY CELLS, IN PARTICULAR OF THE FLASH TYPE, AND CORRESPONDING PROGRAMMING ARCHITECTURE	9/20/2009	12/556,970	5/17/2011	7,944,751	US
NMXS-1068.00/US	Utility - ORG	Issued	AUTOMATIC REGULATION METHOD FOR THE REFERENCE SOURCES IN A NON-VOLATILE MEMORY DEVICE AND CORRESPONDING MEMORY DEVICE	6/29/2007	11/771,677	7/28/2009	7,567,470	US
NMXS-1070.00/US	Utility - ORG	Issued	METHOD FOR ACCESSING A MULTILEVEL NONVOLATILE MEMORY DEVICE OF THE FLASH NAND TYPE	7/20/2006	11/458,904	6/3/2008	7,382,660	US
NMXS-1071.00/US	Utility - ORG	Issued	CONFIGURATION OF A MULTILEVEL FLASH MEMORY DEVICE	7/28/2006	11/460,777	5/3/2011	7,937,576	US
NMXS-1075.00/US	Utility - ORG	Issued	ELECTRONIC DEVICE COMPRISING NON VOLATILE MEMORY CELLS WITH OPTIMIZED PROGRAMMING AND CORRESPONDING PROGRAMMING METHOD	2/28/2007	11/713,074	3/31/2009	7,512,032	US
NMXS-1079.00/US	Utility - CIP	Issued	SEALING METHOD FOR ELECTRONIC DEVICES FORMED ON A COMMON SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT STRUCTURE	7/17/2006	11/457,966	8/12/2008	7,410,872	US
NMXS-1084.00/US	Utility - ORG	Issued	METHOD FOR COMPACTING THE ERASED THRESHOLD VOLTAGE DISTRIBUTION OF FLASH MEMORY DEVICES DURING WAITING OPERATIONS	8/24/2007	11/844,480	5/5/2009	7,529,136	US
NMXS-1085.00/US	Utility - ORG	Issued	CIRCUIT AND METHOD FOR ELECTRICALLY PROGRAMMING A NON-VOLATILE	6/21/2007	11/766,493	3/3/2009	7,499,332	US
NMXS-1089.00/US	Utility - ORG	Issued	METHOD OF MANAGING A MULTILEVEL MEMORY DEVICE AND RELATED DEVICE	4/25/2008	12/109,525	5/4/2010	7,710,772	US
NMXS-1090.00/US	Utility - ORG	Issued	METHOD OF MANAGING A MEMORY DEVICE EMPLOYING THREE-LEVEL CELLS	2/28/2008	12/039,268	8/24/2010	7,782,665	US
NMXS-1091.00/US	Utility - ORG	Issued	MEMORY DEVICE EMPLOYING THREE-LEVEL CELLS AND RELATED METHODS OF MANAGING	11/12/2007	11/934,144	9/29/2009	7,596,023	US
NMXS-1093.00/US	Utility - ORG	Issued	MEMORY DEVICE WITH FAIL SEARCH AND REDUNDANCY	7/20/2007	11/780,581	1/12/2010	7,646,655	US
NMXS-1094.00/US	Utility - ORG	Issued	NON-VOLATILE MEMORY DEVICE AND METHOD OF HANDLING A DATUM READ FROM A MEMORY CELL	8/3/2007	11/833,400	5/12/2009	7,532,512	US
NMXS-1095.00/US	Utility - ORG	Issued	ADDRESS COUNTER FOR NONVOLATILE MEMORY DEVICE	7/27/2007	11/829,527	7/7/2009	7,558,152	US
NMXS-1104.01/US	Utility - CON	Expired	REDUCED-BEAK PLANOX PROCESS FOR THE FORMATION OF INTEGRATED ELECTRONIC COMPONENTS	11/23/1987	07/124,440	1/30/1990	4,897,365	US
NMXS-1105.01/US	Utility - DIV	Expired	A METHOD FOR MANUFACTURING MOS/CMOS MONOLITHIC INTEGRATED CIRCUITS INCLUDING SILICIDE AND POLYSILICON PATTERNING	12/15/1988	07/789,391	11/6/1990	4,968,645	US
NMXS-1106.00/US	Utility - ORG	Expired	ELECTRICALLY ALTERABLE, NONVOLATILE FLOATING GATE MEMORY DEVICE	5/27/1987	07/054,712	4/18/1989	4,873,175	US
NMXS-1107.00/US	Utility - ORG	Expired	NONVOLATILE, SEMICONDUCTOR MEMORY DEVICE	6/22/1987	07/064,480	3/28/1989	4,816,883	US
NMXS-1108.00/US	Utility - ORG	Expired	GENERATOR OF RESET PULSES UPON THE RISE OF THE POWER SUPPLY FOR CMOS-TYPE INTEGRATED CIRCUITS	4/28/1988	07/187,581	12/19/1989	4,888,497	US
NMXS-1109.00/US	Utility - ORG	Expired	PROCESS FOR MANUFACTURING CMOS INTEGRATED DEVICES WITH REDUCED GATE LENGTHS	12/14/1988	07/784,272	11/6/1990	4,868,639	US
NMXS-1110.00/US	Utility - ORG	Expired	CIRCUIT FOR SENSING THE STATE OF MATRIX CELLS IN MOS EPROM MEMORIES INCLUDING AN OFFSET CURRENT GENERATOR	1/18/1989	07/728,487	8/14/1990	4,949,307	US
NMXS-1111.00/US	Utility - ORG	Expired	CMOS VOLTAGE MULTIPLIER	6/28/1989	07/372,493	5/1/1990	4,972,402	US
NMXS-1112.00/US	Utility - ORG	Expired	CMOS LOGIC CIRCUIT FOR HIGH VOLTAGE OPERATION	6/30/1989	07/313,203	9/11/1990	4,956,569	US
NMXS-1113.00/US	Utility - ORG	Expired	FABRICATION OF CMOS DEVICES WITH REDUCED GATE LENGTH	7/18/1989	07/381,283	1/22/1991	4,987,088	US
NMXS-1114.00/US	Utility - ORG	Expired	FABRICATION OF CMOS INTEGRATED DEVICES WITH REDUCED GATE LENGTH AND LIGHTLY DOPED DRAIN	7/28/1989	07/386,189	3/5/1991	4,997,782	US

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Exhibit A - US Issued Patents

Patent No.	Patent Title	Patent Status	Patent Description	Filed Date	Serial No.	Issue Date	Patent No.	Country
NMXXS-1115.00/US	Utility - ORG	Expired	GRIPPER FOR THE ADVANTAGEOUSLY ROBOTIZED HANDLING OF ONE OR MORE SILICON WAFERS AND/OR OF A SUPPORT FOR SUCH WAFERS	12/5/1989	07/446,196	10/8/1991	5,054,834	US
NMXXS-1116.00/US	Utility - ORG	Expired	PROGRAMMABLE LOGIC DEVICE HAVING A PLURALITY OF PROGRAMMABLE LOGIC ARRAYS ARRANGED IN A MOSAIC LAYOUT TOGETHER WITH A PLURALITY OF INTERMINGLINGLY ARRANGED INTERFACING BLOCKS	12/27/1989	07/456,782	2/12/1991	4,992,680	US
NMXXS-1117.00/US	Utility - ORG	Expired	METHOD OF MANUFACTURING INTEGRATED CIRCUITS HAVING ELECTRONIC COMPONENTS OF TWO DIFFERENT TYPES EACH HAVING PAIRS OF ELECTRODES OBTAINED FROM THE SAME POLYCRYSTALLINE SILICON LAYERS AND SEPARATED BY DIFFERENT DIELECTRIC MATERIALS	12/13/1990	07/625,764	12/24/1991	5,075,246	US
NMXXS-1118.00/US	Utility - ORG	Expired	VOLTAGE-BOOSTED PHASE OSCILLATOR FOR DRIVING A VOLTAGE MULTIPLIER	2/14/1991	07/655,049	3/17/1992	5,097,226	US
NMXXS-1119.00/RE	Utility - REIS	Expired	SENSE CIRCUIT FOR READING DATA STORED IN NONVOLATILE MEMORY CELLS	6/8/1995	08/488,718	6/8/1993	RE36,579	US
NMXXS-1119.00/US	Utility - ORG	Abandoned	READING CIRCUIT FOR NONVOLATILE MEMORY CELL	2/19/1991	07/657,083	6/8/1993	5,218,570	US
NMXXS-1120.01/US	Utility - CIP	Issued	METHOD FOR ADJUSTING THE THRESHOLD OF A READ-ONLY MEMORY TO ACHIEVE LOW CAPACITANCE AND HIGH BREAKDOWN VOLTAGE	10/19/1993	08/139,800	1/23/1996	5,486,487	US
NMXXS-1121.02/US	Utility - CON	Issued	METHOD FOR CARRYING OUT A BOOLEAN OPERATION BETWEEN ANY TWO BITS OF ANY TWO REGISTERS	12/27/1994	08/364,505	8/12/1997	5,657,484	US
NMXXS-1122.00/US	Utility - ORG	Expired	DATA OUTPUT STAGE HAVING FEEDBACK LOOPS TO PRECHARGE THE OUTPUT NODE	4/26/1991	07/691,768	1/12/1993	5,179,300	US
NMXXS-1123.00/US	Utility - ORG	Expired	READING DEVICE FOR EPROM MEMORY CELLS WITH THE OPERATIONAL FIELD INDEPENDENT OF THE THRESHOLD JUMP OF THE WRITTEN CELLS WITH RESPECT TO THE VIRGIN CELLS	7/5/1991	07/725,987	11/30/1993	5,267,202	US
NMXXS-1124.00/US	Utility - ORG	Expired	METHOD FOR FORMATION OF CONTACT PLUGS UTILIZING ETCHBACK	10/1/1991	07/769,600	7/27/1993	5,231,051	US
NMXXS-1125.01/US	Utility - CON	Expired	MONOSTABILIZED DYNAMIC PROGRAMMABLE LOGIC ARRAY IN CMOS TECHNOLOGY	10/30/1992	07/970,609	12/28/1993	5,274,282	US
NMXXS-1126.00/US	Utility - ORG	Abandoned	TWO PHASES SCAN PATH WITH SINGLECLOCK	10/22/1991	07/781,360	7/6/1993	5,225,724	US
NMXXS-1126.01/RE	Utility - REIS	Expired	OPERATIONAL ANALYSIS DEVICE OF THE SCAN PATH TYPE HAVING A SINGLE SCANNING CLOCK AND A SINGLE OUTPUT PHASE FOR AN INTEGRATED CIRCUIT	6/23/1997	08/876,130	7/6/1993	RE36,292	US
NMXXS-1127.00/US	Utility - ORG	Expired	COMPENSATED SENSE CIRCUIT FOR STORAGE DEVICES	11/13/1991	07/791,973	1/25/1994	5,282,169	US
NMXXS-1128.00/US	Utility - ORG	Expired	SENSE CIRCUIT FOR STORAGE DEVICES SUCH AS NON-VOLATILE MEMORIES, WITH COMPENSATED OFFSET CURRENT	11/13/1991	07/791,453	1/4/1994	5,276,644	US
NMXXS-1129.00/US	Utility - ORG	Expired	SENSE CIRCUIT FOR STORAGE DEVICES SUCH A NON-VOLATILE MEMORIES, WITH ENHANCED SENSING DISCRIMINATION	12/12/1991	07/806,118	12/14/1993	5,270,590	US
NMXXS-1130.00/US	Utility - ORG	Expired	FAST CAPACITIVE-LOAD DRIVING CIRCUIT PARTICULARLY MEMORIES	12/20/1991	07/811,323	2/1/1994	5,283,478	US
NMXXS-1131.00/US	Utility - ORG	Expired	MEMORY CELL READING CIRCUIT	12/19/1991	07/810,480	11/2/1993	5,258,959	US
NMXXS-1132.00/US	Utility - ORG	Issued	CURRENT OFFSET SENSE AMPLIFIER OF A MODULATED CURRENT OR CURRENT UNBALANCE TYPE FOR PROGRAMMABLE MEMORIES	5/4/1992	07/878,823	7/5/1994	5,327,379	US
NMXXS-1132.01/US	Utility - CIP	Issued	CURRENT OFFSET SENSE AMPLIFIER OF A MODULATED CURRENT OR CURRENT UNBALANCE TYPE FOR PROGRAMMABLE MEMORIES WITH A VIRTUALLY ENHANCED SENSE AMPLIFIER FOR PROGRAMMABLE MEMORIES	7/5/1994	08/270,498	10/24/1995	5,461,713	US
NMXXS-1133.00/US	Utility - ORG	Issued	SENSE AMPLIFIER FOR PROGRAMMABLE MEMORIES WITH A VIRTUALLY ENHANCED SOURCE OF SIGNAL	7/24/1992	07/919,606	4/18/1995	5,408,148	US
NMXXS-1134.00/US	Utility - ORG	Issued	ZERO-CONSUMPTION POWER-ON RESET CIRCUIT	8/22/1992	07/936,857	6/14/1994	5,321,317	US
NMXXS-1135.00/US	Utility - ORG	Issued	ROW DECODER FOR NAND-TYPE ROM	8/31/1992	07/938,731	9/13/1994	5,347,493	US
NMXXS-1136.01/US	Utility - CON	Issued	METHOD OF FORMING HIGH-STABILITY METALLIC CONTACTS IN AN INTEGRATED CIRCUIT WITH ONE OR MORE METALLIZED LAYERS	3/28/1995	08/411,385	2/26/2002	6,350,676	US
NMXXS-1137.01/US	Utility - DIV	Issued	PROCESS FOR MANUFACTURING MOS-TYPE INTEGRATED CIRCUITS	6/7/1995	08/475,555	12/9/1997	5,696,399	US
NMXXS-1137.02/US	Utility - CON	Issued	DYNAMIC REFERENCE SYSTEM FOR SENSE AMPLIFIER	9/6/1995	08/524,080	9/2/1997	5,663,080	US
NMXXS-1138.01/US	Utility - CON	Issued	METALLIZATION OVER TUNGSTEN PLUGS	10/12/1993	08/135,842	10/11/1994	5,355,333	US
NMXXS-1139.00/US	Utility - ORG	Issued	METALLIZATION OVER TUNGSTEN PLUGS	5/26/1993	08/068,139	4/18/1995	5,407,861	US
NMXXS-1139.01/US	Utility - CON	Issued	METALLIZATION OVER TUNGSTEN PLUGS	4/18/1995	08/423,397	7/26/1998	5,786,272	US

PATENT

REEL: 033282 FRAME: 0378

Exhibit A - US issued Patents

File No.	App. No.	App. Title	Pub. No.	Serial No.	Issue Date	Patent No.	Country
NMXX-1140.00/US	Utility - ORG	Issued	LOW SWITCHING NOISE OUTPUT BUFFER	6/28/1993	08/084,727	5/30/1995	5,420,525 US
NMXX-1141.00/US	Utility - ORG	Issued	INTEGRATED CIRCUIT ENTIRELY PROTECTED AGAINST ULTRAVIOLET RAYS	6/30/1993	08/086,342	8/8/1995	5,440,510 US
NMXX-1142.02/US	Utility - CON	Issued	VOLTAGE REGULATOR FOR MEMORY DEVICE	3/13/1996	08/615,727	1/6/1998	5,706,240 US
NMXX-1143.01/US	Utility - DIV	Issued	A NON-VOLATILE MEMORY IN AN INTEGRATED CIRCUIT	5/23/1995	08/447,772	10/22/1996	5,568,418 US
NMXX-1143.02/US	Utility - CON	Issued	METHOD OF FABRICATING NON-VOLATILE MEMORIES WITH OVERLAPPING LAYERS	6/6/1995	08/469,431	8/25/1998	5,798,279 US
NMXX-1144.00/US	Utility - ORG	Issued	METHOD OF MAKING NOR-TYPE ROM WITH LDD CELLS	6/28/1993	08/084,971	4/18/1995	5,407,852 US
NMXX-1144.02/US	Utility - CON	Issued	NON-TYPE ROM WITH LDD CELLS AND PROCESS OF FABRICATION	12/23/1996	08/772,301	8/11/1998	5,793,086 US
NMXX-1145.01/US	Utility - DIV	Issued	METHOD OF EVALUATING THE GATE OXIDE OF NON-VOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	6/2/1995	08/460,540	5/7/1996	5,513,318 US
NMXX-1145.02/US	Utility - CON	Issued	METHOD OF EVALUATING THE GATE OXIDE OF NON-VOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	4/1/1997	08/829,935	8/11/1998	5,793,675 US
NMXX-1146.00/US	Utility - ORG	Issued	METHOD FOR MAKING DIRECT CONTACTS IN HIGH DENSITY MOS/CMOS PROCESSES	11/17/1993	08/153,620	12/13/1994	5,372,956 US
NMXX-1147.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR FILTERING HIGH RESOLUTION DIGITAL SIGNALS	1/28/1994	08/189,271	1/14/1997	5,594,677 US
NMXX-1148.00/US	Utility - ORG	Issued	METHOD AND APPARATUS FOR FILTERING DIGITAL SIGNALS	1/28/1994	08/188,569	3/3/1998	5,721,395 US
NMXX-1149.00/US	Utility - ORG	Issued	PROCESS FOR FABRICATING INTEGRATED DEVICES INCLUDING FLASH-EEPROM MEMORIES AND TRANSISTORS	2/10/1994	08/195,369	6/10/1997	5,637,520 US
NMXX-1150.00/US	Utility - ORG	Abandoned	PROCESS FOR FABRICATING INTEGRATED DEVICES INCLUDING NONVOLATILE MEMORIES AND TRANSISTORS WITH TUNNEL OXIDE PROTECTION	2/15/1994	08/196,634	11/14/1995	5,466,622 US
NMXX-1150.01/US	Utility - CON	Issued	PROCESS FOR FABRICATING INTEGRATED DEVICES INCLUDING NONVOLATILE MEMORIES AND TRANSISTORS WITH TUNNEL OXIDE PROTECTION	7/13/1995	08/502,329	6/15/1999	5,913,120 US
NMXX-1151.00/US	Utility - ORG	Issued	NONVOLATILE EPROM, EPROM OF FLASH-EEPROM MEMORY WITH TUNNEL OXIDE PROTECTION	2/15/1994	08/196,572	3/5/1996	5,497,345 US
NMXX-1152.00/US	Utility - ORG	Issued	NONVOLATILE FLASH-EEPROM MEMORY ARRAY WITH SOURCE CONTROL TRANSISTORS	3/15/1994	08/214,049	4/16/1996	5,508,956 US
NMXX-1153.00/US	Utility - ORG	Issued	METHOD OF READING, ERASING AND PROGRAMMING A NONVOLATILE FLASH-EEPROM MEMORY ARRAY USING SOURCE LINE SWITCHING TRANSISTORS	3/15/1994	08/212,907	12/24/1996	5,587,946 US
NMXX-1153.01/US	Utility - DIV	Issued	METHOD OF PROGRAMMING A NONVOLATILE FLASH-EEPROM MEMORY ARRAY USING SOURCE LINE SWITCHING TRANSISTORS	6/2/1995	08/458,346	5/27/1997	5,633,822 US
NMXX-1154.00/US	Utility - ORG	Issued	SEMICONDUCTOR MEMORY WITH MEMORY MATRIX COMPRISING REDUNDANCY CELL COLUMNS ASSOCIATED WITH SINGLE MATRIX SECTORS	3/29/1994	08/219,204	11/21/1995	5,469,389 US
NMXX-1155.01/US	Utility - DIV	Issued	METHOD OF EVALUATING THE DIELECTRIC LAYER OF NONVOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	5/31/1995	08/454,854	2/18/1997	5,604,699 US
NMXX-1155.02/US	Utility - CON	Issued	METHOD FOR EVALUATING THE DIELECTRIC LAYER OF NONVOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	7/24/1996	08/685,782	1/27/1998	5,712,816 US
NMXX-1157.00/US	Utility - NSPCT	Issued	METHOD AND APPARATUS FOR CHECKING POST-ERASURE CONTENTS OF AN ERASABLE PERMANENT MEMORY	10/9/1992	08/075,543	1/9/1996	5,483,492 US
NMXX-1158.00/US	Utility - ORG	Issued	SUPPLY VOLTAGE DISTRIBUTION SYSTEM WITH REDUCED RESISTANCE FOR SEMICONDUCTOR DEVICES	7/27/2007	11/881,505	9/27/2011	8,027,217 US

PATENT

REEL: 033282 FRAME: 0379

Exhibit B - US Applications

Pat. No.	Material	Material	Title	File Date	Serial No.	Country
NMXX-0002.00/RE	Utility - REIS	Abandoned	METHOD FOR FIXING AN ELECTRONIC COMPONENT AND ITS CONTACTS TO A SUPPORT	7/13/1992	07/912,867	US
NMXX-0007.00/US	Utility - ORG	Abandoned	POWER-ON-RESET CIRCUIT PROVIDING PROTECTION AGAINST POWER SUPPLY CUTS FOR INTEGRATED CIRCUIT	12/14/1993	08/166,970	US
NMXX-0011.00/US	Utility - ORG	Abandoned	CONTENT ADDRESSABLE MEMORY	5/31/1990	07/531,011	US
NMXX-0011.01/US	Utility - CON	Abandoned	CONTENT ADDRESSABLE MEMORY	3/15/1994	08/213,474	US
NMXX-0011.02/US	Utility - CON	Abandoned	CONTENT ADDRESSABLE MEMORY	12/6/1994	08/350,516	US
NMXX-0014.01/US	Utility - CON	Abandoned	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	4/20/1992	07/870,878	US
NMXX-0014.02/US	Utility - CON	Abandoned	DIFFERENCE COMPARISON BETWEEN TWO ASYNCHRONOUS POINTERS AND A PROGRAMMABLE VALUE	8/2/1993	08/101,281	US
NMXX-0020.01/US	Utility - CON	Abandoned	SEMICONDUCTOR MEMORY WITH MULTIPLE CLOCKING FOR TEST MODE ENTRY	10/20/1992	07/964,103	US
NMXX-0021.00/US	Utility - ORG	Abandoned	SEMICONDUCTOR MEMORY WITH INHIBITED TEST MODE ENTRY DURING POWER-UP	8/17/1990	07/570,148	US
NMXX-0026.01/US	Utility - DIV	Abandoned	SEMICONDUCTOR MEMORY WITH POWER-ON RESET CONTROLLED LATCHED ROW LINE REPEATERS	2/12/1992	07/834,632	US
NMXX-0032.00/US	Utility - ORG	Abandoned	SEMICONDUCTOR MEMORY WITH MULTIPLEXED REDUNDANCY	12/14/1990	07/627,823	US
NMXX-0039.00/US	Utility - ORG	Abandoned	SIGNAL LINE PULSE ENHANCING CIRCUIT FOR INTEGRATED CIRCUITS	1/21/1992	07/823,680	US
NMXX-0040.01/US	Utility - DIV	Abandoned	VIRTUAL GROUND READ ONLY MEMORY CIRCUIT	9/16/1994	08/307,519	US
NMXX-0042.00/US	Utility - ORG	Abandoned	TRISTABLE DRIVER FOR INTERNAL DATA BUS LINES	12/17/1991	07/809,392	US
NMXX-0051.00/US	Utility - ORG	Abandoned	PARALLELIZED BORROW LOOK AHEAD SUBTRACTOR	3/31/1992	07/860,981	US
NMXX-0057.00/US	Utility - ORG	Abandoned	INPUT BUFFER CIRCUIT IMMUNE TO COMMON MODE POWER SUPPLY FLUCTUATIONS	11/2/1992	07/970,266	US
NMXX-0057.01/US	Utility - CON	Abandoned	INPUT BUFFER CIRCUIT IMMUNE TO COMMON MODE POWER SUPPLY FLUCTUATIONS	5/9/1994	08/240,091	US
NMXX-0059.00/US	Utility - ORG	Abandoned	INTEGRATED CIRCUIT OUTPUT DRIVER	8/31/1992	07/938,401	US
NMXX-0061.00/US	Utility - ORG	Abandoned	MULTIPLE CLOCKED DYNAMIC SENSE AMPLIFIER	12/22/1992	07/995,580	US
NMXX-0063.00/US	Utility - ORG	Abandoned	LEADFRAME FOR INTEGRATED CIRCUITS	8/31/1992	07/938,396	US
NMXX-0063.02/US	Utility - DIV	Abandoned	LEADFRAME FOR INTEGRATED CIRCUITS	10/7/1994	08/319,799	US
NMXX-0074.00/US	Utility - ORG	Abandoned	PROCESS FOR IMPROVING THE INTERFACE UNION AMONG DIELECTRIC MATERIALS IN AN INTEGRATED CIRCUIT MANUFACTURE	4/28/1994	08/235,173	US
NMXX-0081.00/US	Utility - ORG	Abandoned	INTEGRATED CIRCUIT FOR THE PROGRAMMING OF A MEMORY CELL IN A NON-VOLATILE MEMORY REGISTER	11/30/1994	08/347,267	US
NMXX-0088.00/US	Utility - ORG	Abandoned	LOAD SIGNAL GENERATING METHOD AND CIRCUIT FOR NONVOLATILE MEMORIES	2/21/1995	08/391,146	US
NMXX-0089.00/US	Utility - ORG	Abandoned	COUNT UNIT FOR NONVOLATILE MEMORIES	12/28/1994	08/365,155	US
NMXX-0091.00/US	Utility - ORG	Abandoned	VOLTAGE BOOSTER WITH AN ACCELERATION CIRCUIT	12/28/1994	08/365,284	US
NMXX-0112.00/US	Utility - ORG	Abandoned	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	4/30/1993	08/056,301	US
NMXX-0112.01/US	Utility - CON	Abandoned	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	2/23/1996	08/606,233	US
NMXX-0112.03/US	Utility - CON	Abandoned	DIRECT CURRENT SUM BANDGAP VOLTAGE COMPARATOR	2/9/1998	09/020,980	US

PATENT

REEL: 033282 FRAME: 0380



Exhibit B - US Applications

Patent No.	Material	Material Status	Title	Filed Date	Serial No.	Country
NMXXS-0117.00/US	Utility - ORG	Abandoned	METHOD AND APPARATUS FOR PARALLEL TESTING OF MEMORY	6/30/1993	08/085,980	US
NMXXS-0118.00/US	Utility - ORG	Abandoned	CLOCK GENERATION CIRCUIT HAVING COMPENSATION FOR SEMICONDUCTOR MANUFACTURING PROCESS VARIATIONS	6/30/1993	08/085,751	US
NMXXS-0124.00/US	Utility - ORG	Abandoned	VARIABLE IMPEDANCE DELAY ELEMENTS	7/30/1993	08/100,624	US
NMXXS-0124.01/US	Utility - CON	Abandoned	VARIABLE IMPEDANCE DELAY ELEMENTS	7/25/1995	08/507,023	US
NMXXS-0128.00/US	Utility - ORG	Abandoned	REDUNDANT LINE DECODER MASTER ENABLE	9/30/1993	08/129,766	US
NMXXS-0129.00/US	Utility - ORG	Abandoned	STRUCTURE TO UTILIZE A PARTIALLY FUNCTIONAL CACHE MEMORY BY INVALIDATION OF FAULTY CACHE MEMORY LOCATIONS	11/1/1993	08/146,228	US
NMXXS-0144.00/US	Utility - ORG	Abandoned	BIAS CIRCUIT FOR TRANSISTOR OF A STORAGE CELL	2/27/1995	08/393,463	US
NMXXS-0144.01/US	Utility - CON	Abandoned	BIAS CIRCUIT FOR TRANSISTOR OF A STORAGE CELL	7/1/1996	08/674,283	US
NMXXS-0158.00/US	Utility - ORG	Abandoned	PROGRAMMABLE MEMORY ELEMENT	3/31/1995	08/413,969	US
NMXXS-0160.00/US	Utility - ORG	Abandoned	HIGHLY-PLANAR INTERLAYER DIELECTRIC THIN FILMS IN INTEGRATED CIRCUITS	4/11/1995	08/419,826	US
NMXXS-0161.02/US	Utility - DIV	Abandoned	PROCESS FOR THE PRODUCTION OF A SEMICONDUCTOR DEVICE HAVING BETTER INTERFACE ADHESION BETWEEN DIELECTRIC LAYERS	9/27/2000	09/670,723	US
NMXXS-0166.00/US	Utility - ORG	Abandoned	MONOLITHICALLY INTEGRATED GENERATOR OF A PLURALITY OF VOLTAGE VALUES	11/29/1995	08/563,758	US
NMXXS-0175.00/US	Utility - ORG	Abandoned	TWIN-CELL MANUFACTURING PROCESS FOR AN EEPROM INTEGRATED DEVICE WITH REDUCED BODY EFFECT	11/22/1995	08/561,840	US
NMXXS-0180.02/US	Utility - DIV	Abandoned	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS AND CORRESPONDING IC	7/6/1999	09/347,905	US
NMXXS-0181.01/US	Utility - DIV	Abandoned	PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS OF AT LEAST TWO DIFFERENT TYPES, AND CORRESPONDING IC	7/28/1998	09/123,295	US
NMXXS-0188.00/US	Utility - ORG	Abandoned	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	2/28/1994	08/202,827	US
NMXXS-0188.01/US	Utility - DIV	Abandoned	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	5/25/1995	08/450,087	US
NMXXS-0188.02/US	Utility - CON	Abandoned	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	8/24/1995	08/518,959	US
NMXXS-0188.03/US	Utility - CON	Abandoned	METHOD AND STRUCTURE FOR RECOVERING SMALLER DENSITY MEMORIES FROM LARGER DENSITY MEMORIES	3/26/1997	08/826,269	US
NMXXS-0212.00/US	Utility - ORG	Abandoned	CLOCK PULSE EXTENDER MODE FOR Clocked MEMORY DEVICES HAVING PRECHARGED DATA PATHS	2/15/1995	08/388,755	US
NMXXS-0224.00/US	Utility - ORG	Abandoned	READING CIRCUIT FOR MULTILEVEL NON-VOLATILE MEMORY CELL DEVICES	4/1/1996	08/625,895	US
NMXXS-0244.00/US	Utility - ORG	Abandoned	SUPPLY VOLTAGES SWITCHES CIRCUIT	12/30/1996	08/775,109	US
NMXXS-0249.01/US	Utility - CON	Abandoned	ANALOG VOLTAGE-SIGNAL SELECTOR DEVICE	12/11/1998	09/210,179	US
NMXXS-0256.00/US	Utility - ORG	Abandoned	NONVOLATILE FLOATING-GATE MEMORY DEVICES, AND PROCESS OF FABRICATION	1/31/1997	08/792,621	US
NMXXS-0256.02/US	Utility - DIV	Abandoned	NONVOLATILE FLOATING-GATE MEMORY DEVICES, AND PROCESS OF FABRICATION	6/6/2002	10/165,010	US

PATENT

REEL: 033282 FRAME: 0381

**Exhibit B - US Applications**

NTI-DataNo	Material type	MaterialStatus	Title	Filedate	SerialNo	Country
NMXS-0257.01/US	Utility - DIV	Abandoned	HIGH CAPACITY CAPACITOR AND CORRESPONDING MANUFACTURINGPROCESS	7/12/2000	09/614,955	US
NMXS-0270.00/US	Utility - ORG	Abandoned	METHOD TO PREVENT DISTURBS DURING THE PROGRAMMING AND ERASING PHASES IN A NON-VOLATILE MEMORY DEVICE	12/30/1996	08/775,110	US
NMXS-0287.00/US	Utility - CIP	Abandoned	CONSTANT CURRENT SOURCE WITH REDUCED SENSITIVITY TO SUPPLY VOLTAGE AND PROCESS VARIATION	3/8/1995	08/399,079	US
NMXS-0294.00/US	Utility - ORG	Abandoned	INTEGRATED MEMORY CIRCUIT WITH SEQUENCED BITLINES FOR STRESS TEST	7/31/1995	08/509,196	US
NMXS-0295.00/US	Utility - ORG	Abandoned	METHOD AND APPARATUS FOR TEST MODE ENTRY DURING POWER UP	6/6/1995	08/466,107	US
NMXS-0300.01/US	Utility - DIV	Abandoned	TEST MODE ACTIVATION AND DATA OVERRIDE	12/3/1999	09/454,800	US
NMXS-0307.00/US	Utility - ORG	Abandoned	CIRCUIT AND METHOD FOR TRACKING THE START OF A WRITE TO A MEMORY CELL	1/19/1996	08/589,139	US
NMXS-0308.00/US	Utility - ORG	Abandoned	CIRCUIT AND METHOD FOR TERMINATING A WRITE TO A MEMORY CELL	1/19/1996	08/588,737	US
NMXS-0310.00/US	Utility - ORG	Abandoned	MEMORY-ROW SELECTOR HAVING A TEST FUNCTION	1/19/1996	08/589,140	US
NMXS-0313.00/US	Utility - ORG	Abandoned	DEVICE AND METHOD FOR DRIVING A CONDUCTIVE PATH WITH A SIGNAL	1/19/1996	08/587,708	US
NMXS-0321.00/RE	Utility - REIS	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	9/7/2001	09/949,290	US
NMXS-0321.01/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	2/19/2002	10/078,169	US
NMXS-0321.02/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	8/16/2002	10/222,940	US
NMXS-0321.03/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	3/10/2003	10/385,189	US
NMXS-0321.04/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	8/21/2003	10/645,344	US
NMXS-0321.05/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	3/8/2004	10/796,635	US
NMXS-0321.06/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	9/13/2004	10/939,960	US
NMXS-0321.07/US	Utility - CON	Abandoned	ELECTRICALLY MODIFIABLE NON-VOLATILE MEMORY CIRCUIT HAVING MEANS FOR AUTONOMOUS REFRESHING DEPENDENT UPON ON PERIODIC CLOCK PULSES	3/17/2005	11/083,367	US
NMXS-0333.00/US	Utility - ORG	Abandoned	MOS CAPACITOR WITH WIDE VOLTAGE AND FREQUENCY OPERATING RANGES	7/25/1997	08/900,328	US
NMXS-0393.00/US	Utility - ORG	Abandoned	DEPOSITION METHOD OF A DIELECTRIC STRUCTURE FOR PLANARIZING MEMORY DEVICES	6/30/1998	09/107,535	US

**PATENT**

**REEL: 033282 FRAME: 0382**

Exhibit B - US Applications

Patent No.	Material Type	Material Status	Title	Filed Date	Ser. No.	Country
NMXXS-0430.01/US	Utility - CON	Abandoned	METHOD FOR PROGRAMMING A FLASH EPROM-TYPE FOR MEMORY	10/22/1998	09/177,324	US
NMXXS-0432.00/US	Utility - ORG	Abandoned	PROCESS OF FINAL PASSIVATION OF AN INTEGRATED CIRCUIT DEVICE	4/14/1998	09/060,192	US
NMXXS-0432.01/US	Utility - DIV	Abandoned	PROCESS OF FINAL PASSIVATION OF AN INTEGRATED CIRCUIT DEVICE	7/30/1999	09/365,355	US
NMXXS-0466.00/US	Utility - ORG	Abandoned	HIGH VOLTAGE REGULATOR AND CORRESPONDING VOLTAGE REGULATION METHOD	11/4/1998	09/186,504	US
NMXXS-0479.01/US	Utility - DIV	Abandoned	METHOD FOR EVALUATING AN INTEGRATED ELECTRONIC DEVICE	4/19/2001	09/838,946	US
NMXXS-0479.02/US	Utility - CON	Abandoned	METHOD FOR EVALUATING AN INTEGRATED ELECTRONIC DEVICE	5/21/2001	09/862,665	US
NMXXS-0492.00/US	Utility - ORG	Abandoned	VSS SWITCHING SCHEME FOR BATTERY BACKED-UP SEMICONDUCTOR DEVICES	10/31/1997	08/962,977	US
NMXXS-0523.00/US	Utility - ORG	Abandoned	CIRCUIT STRUCTURE FOR PROVIDING A HIERARCHICAL DECODING IN SEMICONDUCTOR MEMORY DEVICES	9/29/1999	09/408,157	US
NMXXS-0527.00/US	Utility - ORG	Abandoned	METHOD FOR ENHANCING SELECTIVITY BETWEEN A FILM OF A LIGHT-SENSITIVE MATERIAL AND A LAYER TO BE ETCHED IN ELECTRONIC SEMICONDUCTOR DEVICE FABRICATION PROCESSES	6/29/1999	09/342,318	US
NMXXS-0568.00/US	Utility - ORG	Abandoned	ESD PROTECTION DEVICE FOR SEMICONDUCTOR INTEGRATED CIRCUIT STRUCTURE	7/28/2000	09/627,876	US
NMXXS-0608.00/US	Utility - ORG	Abandoned	BIPOLAR TRANSISTOR PRODUCED USING PROCESSES COMPATIBLE WITH THOSE EMPLOYED IN THE MANUFACTURE OF MOS DEVICES	7/20/2000	09/619,720	US
NMXXS-0609.00/US	Utility - ORG	Abandoned	NON-VOLATILE MEMORY DEVICE AND MANUFACTURING PROCESS THEREOF	12/6/2000	09/731,065	US
NMXXS-0616.00/US	Utility - ORG	Abandoned	LOW RATE REMOVAL ETCH PROCESS IN THE MANUFACTURE OF SEMICONDUCTOR INTEGRATED DEVICES USING A DIELECTRIC FILM DEPOSITION CHAMBER	1/11/2001	09/759,125	US
NMXXS-0634.00/US	Utility - ORG	Abandoned	INTEGRATED MOS TRANSISTOR WITH A HIGH THRESHOLD VOLTAGE AND LOW MULTIPLICATION COEFFICIENT	12/14/2000	09/737,842	US
NMXXS-0704.00/US	Utility - ORG	Abandoned	PROCESS FOR PRODUCING OPTICALLY ACTIVE TITANIUM ALKOXIDE COMPLEXES	10/31/2001	09/001,918	US
NMXXS-0719.00/US	Utility - ORG	Abandoned	METHOD AND DEVICE FOR REDUCING AVERAGE ACCESS TIME OF A NON-VOLATILE MEMORY DURING READING	12/28/2001	10/033,358	US
NMXXS-0732.01/US	Utility - DIV	Abandoned	MANUFACTURING PROCESS OF AN INTERPOLY DIELECTRIC STRUCTURE FOR NON-VOLATILE SEMICONDUCTOR INTEGRATED MEMORIES	6/27/2006	11/476,361	US
NMXXS-0753.01/US	Utility - CON	Abandoned	CONTACT LESS IC CARD WITH OPERATING SYSTEM USED IN CONTACT TYPE CARDS AND READER FOR SUCH CONTACTLESS CARDS	11/30/2006	11/564,864	US
NMXXS-0758.01/US	Utility - DIV	Abandoned	Design failure mode effect analysis (DFMEA)	12/5/2005	11/294,044	US
NMXXS-0775.01/US	Utility - DIV	Abandoned	PROCESS FOR MANUFACTURING INTEGRATED RESISTOR AND PHASE-CHANGE MEMORY ELEMENT INCLUDING THIS RESISTOR	8/11/2005	11/201,790	US
NMXXS-0780.02/US	Utility - CON	Pending	PHASE CHANGE MEMORY CELL AND MANUFACTURING METHOD THEREOF USING MINITRENCHES	6/10/2011	13/158,291	US
NMXXS-0789.01/US	Utility - CON	Abandoned	METHOD FOR MANUFACTURING ELECTRONIC CIRCUITS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE	12/6/2005	11/294,763	US
NMXXS-0795.00/US	Utility - ORG	Abandoned	MEMORY DEVICE AND METHOD FOR READING SEQUENTIALLY GROUPS OF BITS FROM A MEMORY DEVICE	2/7/2003	10/360,515	US

PATENT

REEL: 033282 FRAME: 0383

Exhibit B - US Applications

Application No.	Category	Material Status	Title	File Date	Serial No.	Country
NMXXS-0814.00/US	Utility - ORG	Abandoned	PROCESS FOR FORMING TRENCHES WITH OBLIQUE PROFILE AND ROUNDED TOP CORNERS	6/27/2003	10/608,855	US
NMXXS-0824.01/US	Utility - DIV	Abandoned	ION-IMPLANTATION MACHINE, CONTROL METHOD THEREOF, AND PROCESS FOR MANUFACTURING INTEGRATED DEVICES	6/7/2006	11/449,129	US
NMXXS-0841.00/US	Utility - ORG	Abandoned	METHOD AND APPARATUS FOR DETECTING A LEAK OF EXTERNAL AIR INTO A PLASMA REACTOR	8/29/2003	10/652,862	US
NMXXS-0846.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING A MEMORY DEVICE HAVING SELECTOR TRANSISTORS FOR STORAGE ELEMENTS AND MEMORY DEVICE FABRICATED THEREBY	4/30/2004	10/836,651	US
NMXXS-0848.00/US	Utility - ORG	Abandoned	PROCESS FOR THE FORMATION OF DIELECTRIC ISOLATION STRUCTURES IN SEMICONDUCTOR DEVICES	5/25/2004	10/853,565	US
NMXXS-0848.01/US	Utility - DIV	Abandoned	PROCESS FOR THE FORMATION OF DIELECTRIC ISOLATION STRUCTURES IN SEMICONDUCTOR DEVICES	1/16/2008	12/014,883	US
NMXXS-0852.00/US	Utility - ORG	Abandoned	NON-VOLATILE MEMORY CELL COMPRISING DIELECTRIC LAYERS HAVING A LOW DIELECTRIC CONSTANT AND CORRESPONDING MANUFACTURING PROCESS	12/30/2003	10/749,130	US
NMXXS-0867.00/US	Utility - NSPCT	Abandoned	ELEKTRONSPEKTROSKOP MIT EMISSION, DIE DURCH EINEN MONOCHROMATISCHEN ELEKTROENSTRALH. INDUZIERT WIRD	10/6/2004	10/574,868	US
NMXXS-0870.01/US	Utility - DIV	Abandoned	MOS DEVICE AND A PROCESS FOR MANUFACTURING MOS DEVICES USING A DUAL-POLYSILICON LAYER TECHNOLOGY WITH SIDE CONTACT	6/29/2006	11/479,369	US
NMXXS-0898.01/US	Utility - DIV	Pending	METHOD FOR REDUCING DEFECTS AFTER A METAL ETCHING IN SEMICONDUCTOR DEVICES	9/14/2007	11/855,229	US
NMXXS-0899.01/US	Utility - DIV	Pending	METHOD FOR PERFORMING ERROR CORRECTIONS OF DIGITAL INFORMATION CODIFIED AS A SYMBOL SEQUENCE	12/10/2007	12/001,294	US
NMXXS-0903.01/US	Utility - CIP	Abandoned	SEALING METHOD FOR ELECTRONIC DEVICES FORMED ON A COMMON SEMICONDUCTOR SUBSTRATE AND CORRESPONDING CIRCUIT STRUCTURE	7/17/2006	11/457,948	US
NMXXS-0907.00/US	Utility - ORG	Pending	METHOD OF MAKING A FLOATING-GATE NON-VOLATILE MOS SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CAPACITIVE COUPLING	12/22/2005	11/317,679	US
NMXXS-0909.00/US	Utility - ORG	Abandoned	PROCESS FOR DEFINING INTEGRATED CIRCUITS IN SEMICONDUCTOR ELECTRONIC DEVICES	11/16/2005	11/280,186	US
NMXXS-0915.00/US	Utility - ORG	Abandoned	METHOD AND SYSTEM FOR CORRECTING LOW LATENCY ERRORS IN READ AND WRITE NON VOLATILE MEMORIES, PARTICULARLY OF THE FLASH TYPE	6/30/2005	11/173,896	US
NMXXS-0918.00/US	Utility - ORG	Abandoned	NONVOLATILE MEMORY HAVING CONDUCTIVE FILM BETWEEN ADJACENT MEMORY CELLS	11/2/2006	11/592,020	US
NMXXS-0918.01/US	Utility - DIV	Abandoned	PROCESS FOR MANUFACTURING A NONVOLATILE MEMORY HAVING CONDUCTIVE FILM BETWEEN ADJACENT MEMORY CELLS	5/21/2010	12/785,346	US
NMXXS-0919.01/US	Utility - CON	Pending	METHOD OF MAKING A FLOATING GATE NON-VOLATILE MOS SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CAPACITIVE COUPLING AND DEVICE THUS OBTAINED	8/26/2011	13/219,491	US
NMXXS-0922.00/US	Utility - ORG	Abandoned	METHOD AND SYSTEM FOR CORRECTING ERRORS IN ELECTRONIC MEMORY DEVICES	6/29/2005	11/169,497	US

**Exhibit B - US Applications**

App. No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Country
NMXXS-0927.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING A NON-VOLATILE MEMORY ELECTRONIC DEVICE INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING DEVICE	12/29/2006	11/618,370	US
NMXXS-0927.01/US	Utility - DIV	Pending	PROCESS FOR MANUFACTURING A NON-VOLATILE MEMORY ELECTRONIC DEVICE INTEGRATED ON A SEMICONDUCTOR SUBSTRATE AND CORRESPONDING DEVICE	5/13/2010	12/779,150	US
NMXXS-0933.00/PR	Prov - ORG	Abandoned	RESET INITIALIZATION	12/18/2003	60/530,726	US
NMXXS-0934.00/PR	Prov - ORG	Abandoned	TAMPER MEMORY CELL	1/8/2004	60/535,064	US
NMXXS-0935.00/PR	Prov - ORG	Completed	RESET RAMP CONTROL	12/18/2003	60/530,727	US
NMXXS-0936.00/PR	Prov - ORG	Abandoned	ESD BONDING PAD	12/18/2003	60/530,736	US
NMXXS-0937.00/PR	Prov - ORG	Abandoned	VOLTAGE TRANSLATING CONTROL STRUCTURE	12/18/2003	60/530,576	US
NMXXS-0938.00/PR	Prov - ORG	Abandoned	RESET CIRCUIT	12/18/2003	60/530,848	US
NMXXS-0939.00/RE	Utility - REIS	Abandoned	SENSE AMPLIFIER FOR READING A CELL OF A NON-VOLATILE MEMORY DEVICE	1/23/2009	12/359,221	US
NMXXS-0942.00/US	Utility - ORG	Abandoned	MASS MEMORY DEVICE BASED ON A FLASH MEMORY WITH MULTIPLE BUFFERS	6/4/2004	10/861,341	US
NMXXS-0943.00/US	Utility - ORG	Abandoned	METHOD FOR MANAGING BAD MEMORY BLOCKS IN A NONVOLATILE-MEMORY DEVICE, AND NONVOLATILE-MEMORY DEVICE IMPLEMENTING THE MANAGEMENT METHOD	6/14/2005	11/152,675	US
NMXXS-0955.00/US	Utility - ORG	Abandoned	PHASE CHANGE MEMORY DEVICE AND MANUFACTURING PROCESS THEREOF	1/23/2006	11/337,787	US
NMXXS-0960.00/US	Utility - ORG	Abandoned	EMBEDDED STORAGE DEVICE WITH INTEGRATED DATA-MANAGEMENT FUNCTIONS AND STORAGE SYSTEM INCORPORATING IT	8/16/2005	11/205,766	US
NMXXS-0962.00/US	Utility - ORG	Pending	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	4/11/2006	11/279,385	US
NMXXS-0962.01/US	Utility - CON	Pending	NON-VOLATILE MEMORY ELECTRONIC DEVICE WITH NAND STRUCTURE BEING MONOLITHICALLY INTEGRATED ON SEMICONDUCTOR	8/5/2011	13/198,978	US
NMXXS-0970.00/US	Utility - ORG	Abandoned	NON-VOLATILE MEMORY ELECTRONIC DEVICE	12/28/2006	11/617,472	US
NMXXS-0973.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING A MEMORY WITH LOCAL ELECTRICAL CONTACT BETWEEN THE SOURCE LINE AND THE WELL	1/12/2006	11/331,826	US
NMXXS-0979.01/US	Utility - DIV	Pending	DUAL RESISTANCE HEATER FOR PHASE CHANGE DEVICES AND MANUFACTURING METHOD THEREOF	12/28/2010	12/980,141	US
NMXXS-0981.00/US	Utility - ORG	Abandoned	PROCESS FOR PHYSICAL VAPOR DEPOSITION OF A CHALCOGENIDE MATERIAL LAYER AND CHAMBER FOR PHYSICAL VAPOR DEPOSITION OF A CHALCOGENIDE MATERIAL LAYER OF A PHASE CHANGE MEMORY DEVICE	4/5/2006	11/398,849	US
NMXXS-0982.00/US	Utility - ORG	Abandoned	LOW-RIPPLE BOOSTED VOLTAGE GENERATOR	12/30/2005	11/373,937	US
NMXXS-0988.01/US	Utility - CON	Pending	VERTICAL MOSFET TRANSISTOR, IN PARTICULAR OPERATING AS A SELECTOR IN NONVOLATILE MEMORY DEVICES	8/24/2010	12/862,624	US
NMXXS-0994.00/US	Utility - ORG	Abandoned	NON VOLATILE MEMORY DEVICE SUPPORTING HIGH-PARALLELISM TEST AT WATER LEVEL	12/15/2005	11/304,488	US
NMXXS-0996.00/RE	Utility - REIS	Pending	METHOD OF MANAGING FAILS IN A NON-VOLATILE MEMORY DEVICE AND RELATIVE MEMORY DEVICE	8/4/2011	13/198,665	US
NMXXS-1005.00/US	Utility - ORG	Abandoned	MEMORY DEVICE AND RELATIVE CONTROL METHOD	2/21/2007	11/677,411	US

**PATENT**

**REEL: 033282 FRAME: 0385**

Exhibit B - US Applications

Patent No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Country
NMXS-1012.00/US	Utility - ORG	Abandoned	METHOD AND ARCHITECTURE FOR RESTRICTING ACCESS TO A MEMORY DEVICE	1/19/2006	11/336,411	US
NMXS-1013.00/US	Utility - ORG	Pending	ENHANCED SECURITY MEMORY ACCESS METHOD AND ARCHITECTURE	1/19/2006	11/337,085	US
NMXS-1015.00/US	Utility - ORG	Abandoned	BASIC SEMICONDUCTOR ELECTRONIC CIRCUIT WITH REDUCED SENSITIVITY TO PROCESS VARIATIONS	6/19/2006	11/455,896	US
NMXS-1020.00/US	Utility - ORG	Pending	FLASH MEMORY DEVICE WITH A LOW PIN COUNT (LPC) COMMUNICATION INTERFACE	10/28/2005	11/261,131	US
NMXS-1025.00/US	Utility - ORG	Abandoned	READING METHOD OF A NON-VOLATILE ELECTRONIC DEVICE AND CORRESPONDING DEVICE	5/24/2007	11/753,368	US
NMXS-1031.00/US	Utility - ORG	Abandoned	METHOD FOR PROGRAMMING/ERASING A NON VOLATILE MEMORY CELL DEVICE, IN PARTICULAR FOR FLASH TYPE MEMORIES	3/9/2007	11/684,052	US
NMXS-1032.00/US	Utility - ORG	Abandoned	NON-ACTIVE ELECTRICALLY STRUCTURES OF INTEGRATED ELECTRONIC CIRCUIT	5/29/2007	11/754,494	US
NMXS-1034.00/US	Utility - ORG	Abandoned	CHARGE PUMP SYSTEMS AND METHODS	11/27/2006	11/605,193	US
NMXS-1035.00/US	Utility - ORG	Abandoned	VOLTAGE SWITCHING CIRCUITS AND METHODS	3/21/2007	11/726,707	US
NMXS-1036.00/US	Utility - ORG	Abandoned	ROW SELECTOR FOR A SEMICONDUCTOR MEMORY DEVICE	3/21/2007	11/726,726	US
NMXS-1037.00/US	Utility - ORG	Abandoned	COLUMN DECODING SYSTEM FOR SEMICONDUCTOR MEMORY DEVICES IMPLEMENTED WITH LOW VOLTAGE TRANSISTORS	4/12/2007	11/787,220	US
NMXS-1039.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING A NON-VOLATILE MEMORY DEVICE	9/27/2006	11/528,500	US
NMXS-1041.00/US	Utility - NSPCT	Pending	SEMICONDUCTOR FIELD-EFFECT TRANSISTOR, MEMORY CELL AND MEMORY DEVICE	12/23/2009	12/293,534	US
NMXS-1042.00/US	Utility - ORG	Abandoned	TEST CARTRIDGE WITH INTERNAL GENERATION OF THE TEST SIGNALS	12/20/2007	11/961,266	US
NMXS-1045.00/US	Utility - ORG	Abandoned	METHOD FOR MANUFACTURING NON VOLATILE MEMORY CELLS INTEGRATED ON A SEMICONDUCTOR SUBSTRATE	12/27/2006	11/647,504	US
NMXS-1049.00/US	Utility - ORG	Abandoned	METHOD FOR LOCALLY SUPPRESSING A DISTURBANCE OF A REFERENCE LINE	2/27/2007	11/712,239	US
NMXS-1050.01/US	Utility - CON	Pending	OPTIMIZED FLASH MEMORY ACCESS METHOD AND DEVICE	8/3/2011	13/197,056	US
NMXS-1054.01/US	Utility - CON	Pending	READING METHOD OF A MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE AND MEMORY DEVICE WITH EMBEDDED ERROR-CORRECTING CODE	3/14/2011	13/047,678	US
NMXS-1055.01/US	Utility - DIV	Pending	FAST PROGRAMMING MEMORY DEVICE	6/7/2011	13/155,347	US
NMXS-1058.00/US	Utility - ORG	Abandoned	Process for manufacturing an electronic device integrated on semiconductor substrate comprising non volatile floating gate memories and an associated circuitry and corresponding electronic device	2/14/2008	12/070,175	US
NMXS-1060.00/US	Utility - ORG	Abandoned	SEMICONDUCTOR-INTEGRATED ELECTRONIC DEVICE HAVING A PLURALITY OF LEADS	7/6/2007	11/774,416	US
NMXS-1061.00/US	Utility - ORG	Abandoned	MANUFACTURING METHOD OF AN INTEGRATED CIRCUIT FORMED ON A SEMICONDUCTOR SUBSTRATE	9/4/2007	11/899,275	US
NMXS-1065.00/US	Utility - ORG	Abandoned	Process for manufacturing integrated circuits formed on a semiconductor substrate and comprising tungsten layers	3/5/2008	12/074,776	US

PATENT

REEL: 033282 FRAME: 0386



Exhibit B - US Applications

MT-Defno	Mattertype	MatterStatus	Title	Filedate	SerialNo	Country
NMMS-1069.00/US	Utility - ORG	Allowed	METHOD AND DEVICE FOR DETECTING POSSIBLE CORRUPTION OF SECTOR PROTECTION INFORMATION OF A NON-VOLATILE MEMORY STORED IN AN ON BOARD VOLATILE MEMORY ARRAY AT POWER-ON	3/29/2007	11/693,360	US
NMMS-1071.01/US	Utility - CON	Pending	CONFIGURATION OF A MULTILEVEL FLASH MEMORY DEVICE	3/15/2011	13/048,760	US
NMMS-1072.00/US	Utility - ORG	Abandoned	VOLTAGE REGULATOR MADE OF HIGH VOLTAGE TRANSISTORS	12/3/2007	11/949,517	US
NMMS-1074.00/US	Utility - ORG	Abandoned	METHOD OF PROGRAMMING A FOUR-LEVEL FLASH MEMORY DEVICE AND A RELATED PAGE BUFFER	7/28/2006	11/460,835	US
NMMS-1074.01/US	Utility - DIV	Pending	METHOD OF PROGRAMMING A MULTILEVEL MEMORY DEVICE	8/31/2009	12/551,383	US
NMMS-1075.01/US	Utility - CON	Pending	ELECTRONIC DEVICE COMPRISING NON VOLATILE MEMORY CELLS WITH OPTIMIZED PROGRAMMING AND CORRESPONDING PROGRAMMING METHOD	2/19/2009	12/388,656	US
NMMS-1075.02/US	Utility - CON	Pending	ELECTRONIC DEVICE COMPRISING NON VOLATILE MEMORY CELLS WITH OPTIMIZED PROGRAMMING AND CORRESPONDING PROGRAMMING METHOD	3/22/2011	13/053,723	US
NMMS-1076.00/US	Utility - ORG	Abandoned	METHOD OF FIXING READ EVALUATION TIME IN A NON VOLATILE NAND TYPE MEMORY DEVICE	9/13/2007	11/854,713	US
NMMS-1077.00/US	Utility - ORG	Abandoned	PLASMA DEPOSITION PROCESS WITH VARIABLE PROCESS PARAMETERS	11/26/2007	11/944,791	US
NMMS-1078.00/US	Utility - NSPCT	Pending	PHASE CHANGE MEMORY DEVICE	2/22/2010	12/442,392	US
NMMS-1080.00/US	Utility - ORG	Abandoned	MANUFACTURING PROCESS OF AN ORGANIC MASK FOR MICROELECTRONIC INDUSTRY	4/13/2007	11/734,818	US
NMMS-1086.00/US	Utility - ORG	Abandoned	MEMORY DEVICE	8/24/2007	11/895,609	US
NMMS-1097.00/US	Utility - ORG	Pending	NON-VOLATILE MEMORY CIRCUIT, SYSTEM AND METHOD	4/17/2008	12/148,521	US
NMMS-1104.00/US	Utility - ORG	Abandoned	REDUCED-BEAK PLANOX PROCESS FOR THE FORMATION OF INTEGRATED ELECTRONIC COMPONENTS	10/4/1985	06/784,324	US
NMMS-1105.00/US	Utility - ORG	Abandoned	METHOD FOR MANUFACTURING MOS/CMOS MONOLITHIC INTEGRATED CIRCUITS INCLUDING SILICIDE AND POLYSILICON PATTERNING	12/11/1986	06/940,501	US
NMMS-1120.00/US	Utility - ORG	Abandoned	METHOD FOR ADJUSTING THE THRESHOLD OF A READ-ONLY MEMORY TO ACHIEVE LOW CAPACITANCE AND HIGH BREAKDOWN VOLTAGE	3/29/1991	07/678,557	US
NMMS-1121.00/US	Utility - ORG	Abandoned	METHOD FOR CARRYING OUT A BOOLEAN OPERATION BETWEEN ANY TWO BITS OF ANY TWO REGISTERS	4/24/1991	07/690,495	US
NMMS-1121.01/US	Utility - CON	Abandoned	METHOD FOR CARRYING OUT A BOOLEAN OPERATION BETWEEN ANY TWO BITS OF ANY TWO REGISTERS	2/9/1993	08/015,307	US
NMMS-1124.00/RE	Utility - REIS	Abandoned	METHOD FOR FORMATION OF CONTACT PLUGS UTILIZING ETCHBACK	7/26/1995	08/507,545	US
NMMS-1124.01/US	Utility - CON	Abandoned	METHOD FOR FORMATION OF CONTACT PLUGS UTILIZING ETCHBACK	1/16/1997	08/784,040	US
NMMS-1125.00/US	Utility - ORG	Abandoned	MONOSTABILIZED DYNAMIC PROGRAMMABLE LOGIC ARRAY IN CMOS TECHNOLOGY	9/27/1991	07/767,141	US
NMMS-1126.00/RE	Utility - REIS	Abandoned	OPERATIONAL ANALYSIS DEVICE OF THE SCAN PATH TYPE HAVING A SINGLE SCANNING CLOCK AND A SINGLE OUTPUT PHASE FOR AN INTEGRATED CIRCUIT	7/6/1995	08/498,856	US
NMMS-1135.01/US	Utility - DIV	Abandoned	ROW DECODER FOR NAND-TYPE ROM	10/26/1995		US
NMMS-1136.00/US	Utility - ORG	Abandoned	PLANARIZED CONTACT WITH A SANDWICH OF TUNGSTEN AND ALUMINIUM.	11/23/1992	07/980,550	US
NMMS-1137.00/US	Utility - ORG	Abandoned	PROCESS FOR MANUFACTURING MOS-TYPE INTEGRATED CIRCUITS	11/23/1992	07/980,453	US
NMMS-1138.00/US	Utility - ORG	Abandoned	DYNAMIC REFERENCE SYSTEM FOR SENSE AMPLIFIER	9/22/1992	07/949,678	US

PATENT

REEL: 033282 FRAME: 0387

**Exhibit B - US Applications**

NTID/DCING	Material Type	Material Status	Title	Filed Date	Serial No.	Country
NMXXS-1142.00/US	Utility - ORG	Abandoned	VOLTAGE REGULATOR FOR MEMORY DEVICE	6/28/1993	08/083,377	US
NMXXS-1142.01/US	Utility - CON	Abandoned	VOLTAGE REGULATOR FOR MEMORY DEVICE	5/23/1995	08/447,736	US
NMXXS-1143.00/US	Utility - ORG	Abandoned	METHOD OF FABRICATING NON-VOLATILE MEMORIES WITH OVERLAPPING LAYERS	9/30/1993	08/129,776	US
NMXXS-1144.01/US	Utility - DIV	Abandoned	NOR-TYPE ROM WITH LDD CELLS AND PROCESS OF FABRICATION	1/3/1995	08/367,830	US
NMXXS-1145.00/US	Utility - ORG	Abandoned	METHOD OF EVALUATING THE GATE OXIDE OF NON-VOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	10/21/1993	08/140,629	US
NMXXS-1153.02/US	Utility - DIV	Abandoned	METHOD OF PROGRAMMING A NONVOLATILE FLASH-EEPROM MEMORY ARRAY USING SOURCE LINE SWITCHING TRANSISTORS	6/2/1995	08/460,227	US
NMXXS-1155.00/US	Utility - ORG	Abandoned	METHOD FOR EVALUATING THE DIELECTRIC LAYER OF NONVOLATILE EPROM, EEPROM AND FLASH-EEPROM MEMORIES	10/21/1993	08/140,628	US
NMXXS-1158.01/US	Utility - CON	Pending	SUPPLY VOLTAGE DISTRIBUTION SYSTEM WITH REDUCED RESISTANCE FOR SEMICONDUCTOR DEVICES	9/26/2011	13/245,392	US

**PATENT**

**REEL: 033282 FRAME: 0388**

**RECORDED: 07/10/2014**