

<b>PATENT ASSIGNMENT COVER SHEET</b>
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<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
RF MICRO DEVICES, INC.	07/21/2014
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<b>PROPERTY NUMBERS Total: 10</b>	
<b>Property Type</b>	<b>Number</b>
Patent Number:	6804502
Patent Number:	7123898
Patent Number:	7460852
Patent Number:	7796969
Patent Number:	7860499
Patent Number:	8405147
Patent Number:	5920233
Patent Number:	6653885
Application Number:	60328353
Application Number:	60650033
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<b>ATTORNEY DOCKET NUMBER:</b>	034918.00011

PATENT

<b>NAME OF SUBMITTER:</b>	JENNIFER AUGSBURGER
<b>SIGNATURE:</b>	/jenn augsburger/
<b>DATE SIGNED:</b>	07/23/2014
<b>Total Attachments: 3</b> source=Peregrine-Assignment#page1.tif source=Peregrine-Assignment#page2.tif source=Peregrine-Assignment#page3.tif	


## ASSIGNMENT

In exchange for good and valuable consideration between RFMD and Peregrine, the sufficiency of which is hereby acknowledged, RFMD hereby irrevocably assigns, conveys, transfers and delivers to Peregrine all rights, title and interest to all intellectual property that RFMD acquired from Robert H. Benton and/or Joyce Benton including without limitation any rights in and to the patents and patent applications listed in Appendix A-1 hereto, as well as all provisionals, substitutes, renewals, continuations, continuations-in-part, divisionals, foreign and other counterparts, reissues, oppositions (i.e., patents in opposition), continued examinations, reexaminations, claims allowed after inter partes review, and extensions thereof, or any patents or applications claiming priority thereto.

RFMD further hereby irrevocably assigns, conveys, transfers and delivers to Peregrine all causes of action, rights, and remedies arising under any such patent or application prior to or after September 26, 2013, including the right to sue for past damages and to collect royalties.

RF Micro Devices, Inc.

Date: July 21, 2014

By Its:   
Steven Eric Creviston  
President, Cellular Products Group

# APPENDIX A-1

**APPENDIX A-1**

<b>Serial No.</b>	<b>Filing Date</b>	<b>Patent No.</b>	<b>Issue date</b>	<b>Title</b>	<b>Named Inventors</b>
60/328,353	10/10/2001	N/A	N/A	Silicon-On-Insulator RF Switches	Mark L. Burgener James S. Cable
10/267,531	10/08/2002	6,804,502	10/12/2004	Switch Circuit and Method of Switching Radio Frequency Signals	Mark L. Burgener James S. Cable
10/922,135	08/18/2004	7,123,898	10/17-2006	Switch Circuit and Method of Switching Radio Frequency Signals	Mark L. Burgener James S. Cable
11/582,206	10/16/2006	7,460,852	12/02/2008	Switch Circuit and Method of Switching Radio Frequency Signals	Mark L. Burgener James S. Cable
60/650,033	02/03/2005	N/A	N/A	Symmetrically and Asymmetrically Stacked Transistor Grouping RF Switch	Dylan Kelly Mark L. Burgener
11/347,014	02/03/2006	7,796,969	09/14/2010	Symmetrically and Asymmetrically Stacked Transistor Group RF Switch	Dylan Kelly Mark L. Burgener
12/315,395	12/01/2008	7,860,499	12/28/2010	Switch Circuit and Method of Switching Radio Frequency Signals	Mark L. Burgener James S. Cable
13,412,529	03/05/2013	8,405,147	03/26/2013	Method and Apparatus for Use in Improving Linearity of MOSFETs Using an Accumulated Charge Sink	Christopher N. Brindle; Michael A. Stuber; Dylan J. Kelly; Clint L. Kemerling; George P. Imthurn; Robert B. Welstand; Mark L. Burgener Paul A. Denny
08/751,571	11/18/1996	5,920,233	07/06/1999	Phase Locked Loop Including a Sampling Circuit for Reducing Spurious Side Bands	Paul A. Denny
10/001,250	10/30/2001	6,653,885	11/25/2003	On-chip Integrated mixer balun circuit and method of making the same	John C. Wu; Paul L. Rodgers; Jeff T. Mohr; David E. Kelly