502906277 07/24/2014

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT2952874

SUBMISSION TYPE:	NEW ASSIGNMENT				
NATURE OF CONVEYANCE:	ASSIGNMENT	ASSIGNMENT			
CONVEYING PARTY DATA	·				
	Name Executio	n Date			
VITESSE SEMICONDUCTOR CO					
		<u> </u>			
RECEIVING PARTY DATA					
Name: III HOLI	III HOLDINGS 2, LLC				
Street Address: 2711 Cl	2711 CENTERVILLE RD				
Internal Address: SUITE	SUITE 400				
City: WILMIN					
State/Country: DELAWARE					
Postal Code: 19808					
PROPERTY NUMBERS Total: 6					
Property Type	Number				
Patent Number:	7428599				
Patent Number:	7200176				
Patent Number:	7340662				
Patent Number:	6604206				
Patent Number:	6665347				
Patent Number:	7817674				
CORRESPONDENCE DATA					
	(512)853-8801				
	the e-mail address first; if that is unsuccessful, it will be ; if that is unsuccessful, it will be sent via US Mail.	sent			
• · ·	5128538800				
Email:	dkramer@intprop.com				
Correspondent Name:	MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZE				
Address Line 1:	1120 SOUTH CAPITAL OF TEXAS HIGHWAY				
Address Line 2:	BUILDING 2, SUITE 300				
Address Line 4:	AUSTIN, TEXAS 78746				
ATTORNEY DOCKET NUMBER:	6757-68200				
NAME OF SUBMITTER:	DEAN M. MUNYON				
BIGNATURE:	/Dean M. Munyon/				

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PATENT REEL: 033385 FRAME: 0571

Total Attachments: 3
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Appendix 2 Form of Assignment (Beginning on following page)

ASSIGNMENT

WHEREAS, VITESSE SEMICONDUCTOR CORPORATION, a Delaware corporation, having a place of business at 741 Calle Plano, Camarillo, CA 93012 (hereafter called "ASSIGNOR") is the assignee of record for the following U.S. and/or foreign patents:

Patent or Application No.	Country	Filing Date	<u>Title of Patent and</u> First Named Inventor
7428599	US	01/09/2004	Method for detecting link partner state during auto negotiation and switching local state to establish link
			Vikas Shahdadpuri
7200176	US	07/03/2002	Transformerless ethernet controller
			John Paulos
7340662	US	04/30/2003	GBit/s transceiver with
			built-in self test features
	US	05/30/2001	James Francis Mcelwee Reduced GMII with
6604206	03	05/50/2001	internal timing compensation
			Mandeep Singh Chadha
6665347	US	06/28/2001	Output driver for high speed Ethernet transceiver
			Nicholas van Bavel
7817674	US	01/09/2004	Output clock adjustment for a digital I/O between physical layer device and media access controller
		r.	Marty Pflum

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PATENT REEL: 033385 FRAME: 0573 **AND WHEREAS**, III Holdings 2, LLC, a Delaware limited liability company, with an address at 2711 Centerville Rd, Suite 400, Wilmington, DE 19808 (hereafter "ASSIGNEE") wants to acquire all right, title and interest in and to said above-listed patents and all the inventions therein, as well as the right of action and all other rights, including the right to sue or otherwise bring action and to collect and receive damages therefrom, for past, present, and future infringement thereof, and ASSIGNOR is willing to enter into such assignment.

NOW, THEREFORE, in accordance with and for good and valuable consideration conveyed pursuant to an agreement between the parties, the receipt and sufficiency of which is hereby acknowledged, ASSIGNOR did and hereby does sell, assign, transfer and set over to ASSIGNEE (subject to a retained free license to ASSIGNOR and its affiliates) all right, title and interest in and to the above-listed patents and all continuations, divisions, reexaminations, reissues and substitutes thereof, and all inventions therein, including the right to sue or otherwise bring action and to collect and receive damages therefrom, for past, present, and future infringement thereof;

IN TESTIMONY WHEREOF, I hereunto set my hand this <u>25</u> day of <u>5002</u>, 2014.

Vitesse Semiconductor Corporation

Bv: Name: CRGAROMA

Title: CED (Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.

2. <u>Unit</u> <u>(unit)</u> (or proved to me on the basis of satisfactory evidence) and appeared before me on <u>Une</u> K, 2014 to execute the above Assignment of Patent Rights on behalf of Vitesse Semiconductor Corporation.

3. <u>Ours Surdher</u> subscribed to the above Assignment of Patent Rights on behalf of Vitesse Semiconductor Corporation.

PATENT REEL: 033385 FRAME: 0574

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on	lene R. 2014 (date)
Prin	Wame: Jampy Madesh

PATENT REEL: 033385 FRAME: 0575

RECORDED: 07/24/2014