PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
RYAN T. HIROSE	12/23/2011
BOGDAN GEORGESCU	12/27/2011
LEONARD GITLAN	12/22/2011
ASHISH AMONKAR	12/27/2011
GARY MOSCALUK	12/23/2011
JOHN TIEDE	12/22/2011

RECEIVING PARTY DATA

Name:	Cypress Semiconductor Corporation	
Street Address:	198 Champion Court	
City:	San Jose	
State/Country:	CALIFORNIA	
Postal Code:	95134	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	14286497

CORRESPONDENCE DATA

Fax Number: (408)545-6911

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

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CYPRESS SEMICONDUCTOR CORPORATION Correspondent Name:

Address Line 1: 198 CHAMPION COURT

Address Line 4: SAN JOSE, CALIFORNIA 95134

ATTORNEY DOCKET NUMBER:	CD11092C1
NAME OF SUBMITTER:	ANDREW J. BATEMAN
SIGNATURE:	/Andrew J. Bateman/
DATE SIGNED:	07/29/2014

Total Attachments: 3

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ASSIGNMENT OF PATENT APPLICATION	Attorney Docket No.: CD11092	
For good and valuable consideration, the receipt and	d sufficiency of which is hereby	
acknowledged, I/we, the undersigned, Ryan T. Hirose, Ashis	h Amonkar, Bogdan Georgescu,	
Leonard Gitlan, Gary Moscaluk, and John Tiede		
hereby sell, assign, and transfer to <u>Cypress Semiconduc</u>	tor Corporation	
 a <u>corporation</u> (Type of Assignee: e.g., corporation, company, partnership, university, e 	of <u>Delaware</u> ,	
having a principal place of business at 198 Champion Cou	ırt, San Jose, California 95134-1709	
("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all inventions or improvements that are disclosed in the application (provisional or non-provisional) for the United States patent which may have a declaration executed by the undersigned prior hereto or concurrently herewith and is entitled		
SYSTEMS AND METHODS FOR PROVIDING	HIGH VOLTAGE TO	
MEMORY DEVICES		
said patent application also identified as follows (when know	vn):	
[I/we hereby authorize an attorney or agent for said number and filing date of said patent application wh		
United States Patent Application Number 13/340,248	filed <u>2011-12-29</u> ,	
and in and to said application (provisional or non-provisional provisional applications, utility applications, design applications applications, continued prosecution applications, continuate applications, renewal applications, reissue applications, ree patent applications that have been or shall be filed in the Urany of said inventions or improvements; and in and to all or reexamination certificates, and extensions, that have been all foreign countries on said inventions or improvements; ar from the filing of said United States application;	ions, divisional applications, continuation on-in-part applications, substitute examinations, extensions, and all other nited States and all foreign countries on iginal patents, reissued patents, or shall be issued in the United States and	
agree that said Assignee may apply for and receive improvements in its own name; and that, when requested, very said Assignee, its successors, assigns, and legal represent	without charge to, but at the expense of,	

agree that said Assignee may apply for and receive a patent or patents for said inventions or improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all provisional applications, non-provisional applications, utility applications, design applications, divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications on any and all said inventions or improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said inventions or improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said inventions or improvements and for vesting title to said inventions or improvements, and all applications for patents and all patents on said inventions or improvements, in said Assignee, its successors, assigns, and legal representatives; and

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covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Each Inventor: Please Sign and Date Below:

7	•
12/23, 2011	Ryan J. I. Mose Signature
Date	ິນ Signature
	Ryan T. HIROSE
	Printed/Typed Name
12/27/ , 20 ¹¹	39
Date	Signature
	Bogdan GEORGESCU
	Printed/Typed Name
12/22 , 20 ¹¹ Date	L'Fet Can
Date	Signature
	Leonard GITLAN
	Printed/Typed Name

Assignment Document Return Address:
CYPRESS SEMICONDUCTOR CORPORATION
198 Champion Court
San Jose, CA 95134
(408) 545-7194

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covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Each Inventor: Please Sign and Date Below:

12/27 . 20¹¹

AshishiA . Amorton

Ashish AMONKAR

12/23 , 20¹¹

Printed/Typed Name

Gary MOSCALUK

Dec 22, 20 11
Date

Printed/Typed Name

Signature

John TIEDE

Printed/Typed Name

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