

<b>PATENT ASSIGNMENT COVER SHEET</b>
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Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT2978056

<b>SUBMISSION TYPE:</b>	CORRECTIVE ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	Corrective Assignment to correct the ASSIGNOR FROM GEORGE MARR previously recorded on Reel 032715 Frame 0879. Assignor(s) hereby confirms the ASSIGNOR AS THINSILICON CORPORATION.
<b>RESUBMIT DOCUMENT ID:</b>	502923298

**CONVEYING PARTY DATA**

Name	Execution Date
THINSILICON CORPORATION	03/26/2014

**RECEIVING PARTY DATA**

<b>Name:</b>	SOLARBASE GROUP, INC.
<b>Street Address:</b>	533 AIRPORT BOULEVARD
<b>Internal Address:</b>	SUITE 503
<b>City:</b>	BURLINGAME
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	94010

**PROPERTY NUMBERS Total: 1**

Property Type	Number
<b>Patent Number:</b>	8444766

**CORRESPONDENCE DATA****Fax Number:**

*Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.*

**Email:** shyshenQ@gmail.com  
**Correspondent Name:** SOLARBASE GROUP, INC.  
**Address Line 1:** 533 AIRPORT BOULEVARD  
**Address Line 2:** SUITE 503  
**Address Line 4:** BURLINGAME, CALIFORNIA 94010

<b>NAME OF SUBMITTER:</b>	CHRISTOPHER R. CARROLL
<b>SIGNATURE:</b>	/Christopher R. Carroll/
<b>DATE SIGNED:</b>	08/12/2014

**Total Attachments: 5**

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## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

EPAS ID: PAT2821506

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT	
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT	
<b>CONVEYING PARTY DATA</b>		
	<b>Name</b>	<b>Execution Date</b>
	<del>GEORGE MARR</del> Thinsilicon Corporation	03/26/2014
<b>RECEIVING PARTY DATA</b>		
<b>Name:</b>	SOLARBASE GROUP, INC	
<b>Street Address:</b>	533 AIRPORT BOULEVARD	
<b>Internal Address:</b>	SUITE 503	
<b>City:</b>	BURLINGAME	
<b>State/Country:</b>	CALIFORNIA	
<b>Postal Code:</b>	94010	
<b>PROPERTY NUMBERS Total: 1</b>		
	<b>Property Type</b>	<b>Number</b>
	Patent Number:	8444766
<b>CORRESPONDENCE DATA</b>		
<b>Fax Number:</b>	(314)584-4061	
	<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	314-584-4097	
<b>Email:</b>	docket@splglaw.com	
<b>Correspondent Name:</b>	THE SMALL PATENT LAW GROUP LLC	
<b>Address Line 1:</b>	225 S. MERAMEC AVE.	
<b>Address Line 2:</b>	SUITE 725T	
<b>Address Line 4:</b>	CLAYTON, MISSOURI 63105	
<b>ATTORNEY DOCKET NUMBER:</b>	312-0002US	
<b>NAME OF SUBMITTER:</b>	CHRISTOPHER R. CARROLL	
<b>SIGNATURE:</b>	/Christopher R. Carroll/	
<b>DATE SIGNED:</b>	04/21/2014	
<b>Total Attachments: 2</b>		
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**ASSIGNMENT  
FOR UTILITY PATENT APPLICATIONS**

This agreement is between ThinSilicon Corporation, having an address at 1400 North Shoreline Boulevard, #B-3, Mountain View, California 94043 (referred to herein as "Assignor"), and SolarBase Group, Inc., having an address at 633 Airport Boulevard, Suite 503, Burlingame, California 94010 (referred to herein as "Assignee").

In exchange for good and valuable consideration of which Assignor acknowledges receipt, Assignor hereby sells and assigns to Assignee, its successors, and assigns, the entire right, title, and interest in and to the invention and improvements described in the patents and application listed in the following table:

Country of Filing	Patent or Application Number	Title
US	8,444,766	System and Method for Recycling a Gas Used to Deposit a Semiconductor Layer
CN	CN102246272B	System and Method for Recycling a Gas Used to Deposit a Semiconductor Layer
KR	10-1308324	Monolithically-Integrated Solar Module
CN	CN102272944B	Photovoltaic Cells and Methods to Enhance Light Trapping
KR	10-1319674	Photovoltaic Cells and Methods to Enhance Light Trapping
KR	10-1247916	Photovoltaic Modules and Methods of Manufacturing Photovoltaic Modules Having Multiple Semiconductor Layer Stacks
KR	10-1245037	Photovoltaic Module and Method of Manufacturing a Photovoltaic Module Having Multiple Semiconductor Layer Stacks
KR	10-1319750	Photovoltaic Module and Method of Manufacturing a Photovoltaic Module Having Multiple Semiconductor Layer Stacks
US	61/879,363	Heterojunction Solar Cell And Method Of Manufacture

and any and all applications for patent and patents therefrom in any and all countries, including all divisions, continuations, continuations-in-part, reexaminations, renewals, and reissues thereof, and all rights of priority resulting from the filing of the application(s), and authorize and request any official whose duty it is to issue patents, to issue any patent on the inventions and improvements resulting therefrom to the Assignee, or its successors or assigns, and agree that on request and without further consideration, Assignor will communicate to Assignee or its representatives or nominees any facts known to Assignor respecting the inventions and improvements and testify in any legal proceeding, make all rightful oaths, sign all lawful papers and execute all instruments or documents required or requested for the making and prosecution of any applications of any type for patent, utility model, or other similar rights in all countries including, but not limited to, any provisional, non-provisional, continuation, continuation-in-part, divisional, renewal or substitute thereof, for any derivation proceedings relating thereto, and, as to any patents that issue from such applications, for any supplemental examination, derivation

proceeding, opposition, post grant review, reissue, re-examination, inter partes review, or extension thereof, and generally do everything possible to aid the Assignee, its successors, assigns, and nominees to obtain and enforce proper patent protection for the invention and its improvements in all countries.

Assignor also assigns all right, title, and interest in and to damages for past infringement of any of the patents listed above.

ASSIGNOR ThinSilicon Corp.

Signature: George Marr

Date: March 26, 2014

Printed Name: GEORGE MARR

Title: Chief Executive Officer

ASSIGNEE SolarBase Group, Inc.

Signature: Shy Sheng Lin

Date: March 28, 2014

Printed Name: Shy Sheng Lin

Title: Co-President