

## PATENT ASSIGNMENT COVER SHEET

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<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	National Science Foundation
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<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
Application Number:	14267190
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Invention Title: COMPUTER ARCHITECTURE HAVING SELECTABLE, PARALLEL AND SERIAL COMMUNICATION CHANNELS BETWEEN PROCESSORS AND MEMORY

Inventor(s): Nam Sung Kim, Hao Wang

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Patent or Application Serial No.: 14/267190

Grant/Contract Number(s): National Science Foundation 1217102

Foreign Applications filed/intended in (countries):

The invention identified above is a Subject Invention under **35 U.S.C. 200, et seq.**, and the Standard Patent Rights clause at **37 CFR 401.14, FAR 52.227-11** or **FAR 52.227-12** (if applicable) which are included among the terms of the above identified grant or contract award from the United State Government. This document is confirmatory of:

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Signed this 14 August, 2014,

By Michael Falk

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