# PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date
KEXIN LUO	05/13/2014
KAI ZHOU	05/13/2014
SHENGGUO CAO	05/13/2014
LINGFENG YUE	05/13/2014
FANQING CHU	05/13/2014
SHEN YU	05/13/2014
ZHI WU	05/13/2014

### **RECEIVING PARTY DATA**

Name:	SILICON IMAGE, INC.	
Street Address:	1140 EAST ARQUES AVENUE	
City:	SUNNYVALE	
State/Country:	CALIFORNIA	
Postal Code:	94085	

### **PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	14383865

## **CORRESPONDENCE DATA**

**Fax Number:** (650)938-5200

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

**Phone:** 650-335-7291

Email: lburshteyn@fenwick.com

Correspondent Name: DOHYUN AHN

Address Line 1: 801 CALIFORNIA STREET Address Line 2: FENWICK & WEST LLP

Address Line 4: MOUNTAIN VIEW, CALIFORNIA 94041

ATTORNEY DOCKET NUMBER:	30850-25761
NAME OF SUBMITTER:	DOHYUN AHN
SIGNATURE:	/Dohyun Ahn/
DATE SIGNED:	09/09/2014

502968435 PATENT REEL: 033701 FRAME: 0698

## **Total Attachments: 3**

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PATENT REEL: 033701 FRAME: 0699

#### ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the person(s) named below (referred to as "INVENTOR" whether singular or plural) has sold, assigned, and transferred and does hereby sell, assign, and transfer to Silicon Image, Inc., a Delaware corporation, having a place of business at 1140 East Arques Avenue, Sunnyvale, CA 94085, United States of America ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

- 1. The entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the following non-provisional application filed under 35 U.S.C. § 111(a), international application filed according to the Patent Cooperation Treaty (PCT), or U.S. national phase application filed under 35 U.S.C. § 371 ("APPLICATION"):
  - U.S. Application No. 14/383,865 , entitled "Techniques for Fractional-N Phase Locked Loops," which is a National Phase Entry of International Application No. PCT/CN2014/077643 , filed on May 16, 2014
  - 2. The entire worldwide right, title, and interest in and to:
- (a) the APPLICATION; (b) all applications claiming priority from the APPLICATION; (c) all provisional, utility, divisional, continuation, substitute, renewal, reissue, and other applications related thereto that have been or may be filed in the United States or elsewhere in the world; (d) all patents (including reissues and re-examinations) that may be granted on the applications set forth in (a), (b), and (c) above; and (e) all right of priority in the APPLICATION and in any underlying provisional or foreign application, together with all rights to recover damages for infringement of provisional rights.

INVENTOR agrees that ASSIGNEE may apply for and receive patents for SUBJECT MATTER in ASSIGNEE's own name.

INVENTOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all oaths, assignments, powers of attorney, applications, and other papers necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests herein conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the SUBJECT MATTER and for vesting in ASSIGNEE the rights, titles, and interests herein conveyed. INVENTOR further agrees to provide any successor, assign, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

INVENTOR represents that INVENTOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the INVENTOR has not made and will not hereafter make any assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

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30850/25755/DOC\$/3395625,1

Title: Filed: Techniques for Fractional N-Phase Locked Loops

September 8, 2014

Application No.:

14/383,865

INVENTOR grants the attorney of record the power to insert on this Assignment any further identification that may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office or other authority for recordation of this document.

This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

By signing below, INVENTOR further attests to the following:

- The APPLICATION was made or authorized to be made by INVENTOR.
- INVENTOR believes that INVENTOR is the original inventor or an original joint inventor of a claimed invention in the APPLICATION.

INVENTOR hereby acknowledges that any willful false statement made in this document is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

	Date of Signature
Name and Signature	2
Kexin Luo	2014-5-13
Kexin Luo	
Name and Signature	Date of Signature
Kari shon	2014-5-13
Kai Zhou	
Name and Signature	Date of Signature
Shengguo Cao	JO14-5-13
Shengguo Cao	
Name and Signature	Date of Signature
Inflore her	+/13/2014
Lingfeng Yue	
Name and Signature	Date of Signature
fang ~~	5/13/2014
Fangging Chu	

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30850/25755/DOCS/3395625.1

Title:	Techniques for Fractional N-Phase Locked Loops	
Filed: Application No.:	September 8, 2014 14/383,865	DO FOR FOR COMMODITY AND AN AND AN AND AN AND AN AND AN AND AN AND AND
Name and Signature		Date of Signature
Shen Yu		US/13/2014
Name and Signature		Date of Signature
Zhi wa		2014-5-13

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