503019095 10/15/2014

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT3065696

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
MING CHUEN ALVAN LAM	07/31/2014
WEIMING SUN	08/11/2014
EMMA WANG	10/10/2014
PENG ZHU	10/13/2014

RECEIVING PARTY DATA

Name:	FAIRCHILD SEMICONDUCTOR CORPORATION
Street Address:	3030 ORCHARD PARKWAY
City:	SAN JOSE
State/Country:	CALIFORNIA
Postal Code:	95134

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	14109161

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 6123736900

Email: USPTO@SLWIP.COM

Correspondent Name: SCHWEGMAN LUNDBERG & WOESSNER / FAIRCHIL

Address Line 1: P.O. BOX 2938

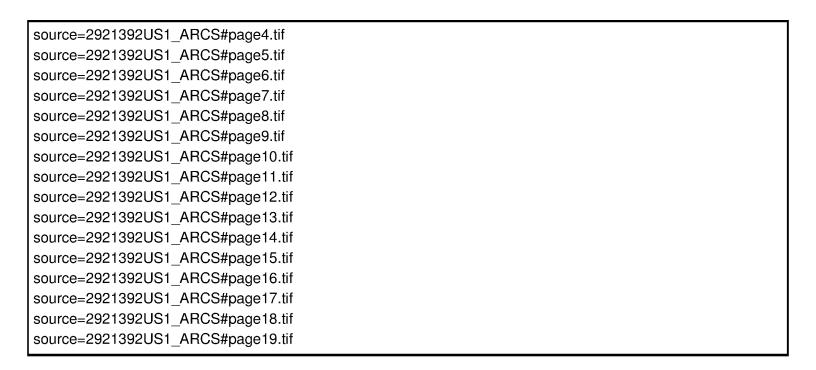
Address Line 4: MINNEAPOLIS, MINNESOTA 55402

ATTORNEY DOCKET NUMBER:	2921.392US1
NAME OF SUBMITTER:	KAREN MOON
SIGNATURE:	/Karen Moon/
DATE SIGNED:	10/15/2014

Total Attachments: 19

source=2921392US1_ARCS#page1.tif source=2921392US1_ARCS#page2.tif source=2921392US1_ARCS#page3.tif

PATENT 503019095 REEL: 033952 FRAME: 0263



RECORDATION FORM COVER SHEET PATENTS ONLY

Atty Ref/Docket No.: 2921.392US1 Patent and Trademark Office To the Director of the U.S. Patent and Trademark Office: Please record the attached original documents or copy thereof. 2. Name and address of receiving party(ies): 1. Name of conveying party(ies): Name: Fairchild Semiconductor Corporation Ming Chuen Alvan Lam, Weiming Sun, Emma Wang, Peng Zhu Street Address: 3030 Orchard Parkway City: <u>San Jose</u> State: <u>CA</u> Zip: <u>95134</u> Country: <u>United States of America</u> Additional name(s) of conveying party(ies) attached? []Yes [X]No Additional name(s) & address(es) attached? []Yes [X]No 3. Nature of conveyance: [X] Assignment [] Merger [] Security Agreement [] Change of Name [] Other Execution Date: <u>July 31, 2014</u>, <u>August 11, 2014</u>, <u>October 10, 2014</u>, <u>October 13, 2014</u> 4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is: B. Patent No.(s) A. Patent Application No.(s) Serial No. 14/109,161 Additional numbers attached? []Yes [X]No 6. Total number of applications and patents involved: 1 5. Name and address of party to whom correspondence concerning document should be mailed: 7. Total fee (37 CFR 3.41):\$ 0.00 Name: Edward J. Sandor []Enclosed []Authorized to be charged to deposit account 19-Address: Schwegman Lundberg & Woessner, P.A. P.O. Box 2938 Please charge any additional fees or credit any over Minneapolis, MN 55402--0938 payments to our Deposit Account No.: 19-0743 DO NOT USE THIS SPACE Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Edward J. Sandor/Reg. No. 60,826 Name of Person Signing Total number of pages including cover sheet: 19

Mail documents to be recorded with required cover sheet information to:

Commissioner of Patents and Trademarks Mail Stop Assignment Recordation Services P.O. Box 1450 Alexandria, VA 22313-1450

ASSIGNMENT

WHEREAS, Alvan Lam, Weiming Sun, Emma Wang and Peng Zhu (hereinafter the "Undersigned") have made one or more inventions and other subject matter (hereinafter collectively referred to as the "Invention") which are described in a patent application filed on December 17, 2013, which application was assigned US patent application serial number 14/109,161, and which is titled DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT; which are described in a patent application filed on December 17, 2012, which application was assigned Chinese patent application serial number 201210562262.8, and which is titled DELAY CIRCUIT AND METHOD AND INTEGRATED CIRCUIT.

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency, and adequacy of which are hereby acknowledged by the Undersigned, the Undersigned do hereby irrevocably and unconditionally:

CONVEY, ASSIGN, AND TRANSFER to Fairchild Semiconductor Corporation (the "Assignee"), a corporation of the State of Delaware, having a place of business at 3030 Orchard Parkway, San Jose, CA 95134, the Undersigned's entire right, title, and interest for the United States and all foreign countries and jurisdictions in and to:

the Invention which is disclosed in the above-identified application or applications;

such application or applications, and all divisional, continuing (including continuation-in-part), substitute, renewal, reissue, and all other applications for a patent or patents which have been or shall be filed in the United States (including all provisional and non-provisional applications), and in all foreign countries and jurisdictions based in whole or in part on any of such Invention (including any application for a utility model or an innovation patent application);

all original and reissued patents which have been or shall be issued in the United States and all foreign countries and jurisdictions based in whole or in part on any of such Invention;

including the right to claim priority to the above-identified patent application or applications in relation to subject matter based in whole or in part on the above-identified patent application or applications and any of the foregoing including the right to file foreign applications under the provisions of any convention or treaty;

and including the right to all causes of action, remedies, and other enforcement rights related to the above-identified application or applications, including without limitation the right to sue for past, present, or future infringement, misappropriation, or violation of any and all rights related to the above-identified patent application or applications and any of the foregoing, including the right to obtain and collect damages for past, present, or future infringement;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such Invention to the Assignee;

AUTHORIZE AND REQUEST that any attorney associated with U.S. Patent and Trademark Office (USPTO) Customer No. 13488 may (directly or through his/her designee) delete, insert, or alter any

Assignment

Assignors: Alvan Lam et al.

Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT

Page 2 of 6

Docket No: 2921.392US1 Client Ref. No. 74939US.01

information related to the above-identified patent application or applications or any of the foregoing, after execution of this Assignment;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or shall be made to others by the Undersigned, and that the full right to convey the same as herein expressed is possessed by the Undersigned;

COVENANT, that when requested and without compensation, but at the expense of the Assignee, in order to carry out in good faith the intent and purpose of this Assignment, the Undersigned shall (1) execute all provisional, non-provisional, divisional, continuing (including continuation-in-part), substitute, renewal, reissue, and all other patent applications for the Invention; (2) execute all rightful oaths, declarations, assignments, powers of attorney and other papers for the Invention; (3) communicate to the Assignee all facts known to the Undersigned relating to the Invention and the history thereof; (4) cooperate with the Assignee in any interference, reexamination, reissue, opposition, dispute, or litigation involving any of the applications or patents for the Invention; and (5) take such further actions as the Assignee shall reasonably consider necessary or desirable for vesting title to such Invention in the Assignee, or for securing, maintaining and enforcing proper patent protection for the Invention;

COVENANT, that should any provision of this agreement be held unenforceable by an authority of competent jurisdiction, such a ruling shall not affect the validity and enforceability of the remaining provisions. To the extent that any such provision is found to be unenforceable, the Undersigned, when requested and without compensation shall act in good faith to substitute for such provision a new provision with content and purpose as close as possible to the provision deemed unenforceable.

THIS AGREEMENT IS TO BE BINDING on the heirs, assigns, representatives, and successors of the Undersigned, and is to extend to the benefit of the successors, assigns, and nominees of the Assignee.

AGREED as of the date of my signature below:

Assignment
Assignors: Alvan Lam et al.
Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT

Docket No: 2921.392US1 Client Ref. No. 74939US.01

Page 3 of 6

Assignor:

(Signature):

1 Ming Chuen Alvan Lam

Name: Alvan Lam

City/State: Scarborough, ME

Date:

Jul 31, 2014

Assignment

Assignors: Alvan Lam et al.
Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT

Page 4 of 6

Docket No: 2921.392US1 Client Ref. No. 74939US.01

Assignor:

(Signature):

1 Weining Sun

Name: Weiming Sun

City/State: Beijing, China

Date:

Aug 11, 2014

Assignment Assignors: Alvan Lam et al. Bitle: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT Page 5 of 6			Docket No: 2921.392US1 Client Ref. No. 74939US.01
Assignor:			
(Signature):	/	1	
	Name: <u>Emma V</u> City/State: <u>Beiji</u>		
Date:			

Assignment Assignors: Alvan Lam et al. Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT Page 6 of 6			Docket No: 2921.392US1 Client Ref. No. 74939US.01
Assignor:			
(Signature):	/	1	
	Name: <u>Peng Zl</u> City/State: <u>Tia</u>		
Date:			

PATENT REEL: 033952 FRAME: 0271

Docket No: 2921.392US1

ASSIGNMENT

WHEREAS, Alvan Lam, Weiming Sun, Emma Wang and Peng Zhu (hereinafter the "Undersigned") have made one or more inventions and other subject matter (hereinafter collectively referred to as the "Invention") which are described in a patent application filed on December 17, 2013, which application was assigned US patent application serial number 14/109,161, and which is titled DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT; which are described in a patent application filed on December 17, 2012, which application was assigned Chinese patent application serial number 201210562262.8, and which is titled DELAY CIRCUIT AND METHOD AND INTEGRATED CIRCUIT.

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency, and adequacy of which are hereby acknowledged by the Undersigned, the Undersigned do hereby irrevocably and unconditionally:

CONVEY, ASSIGN, AND TRANSFER to Fairchild Semiconductor Corporation (the "Assignee"), a corporation of the State of Delaware, having a place of business at 3030 Orchard Parkway, San Jose, CA 95134, the Undersigned's entire right, title, and interest for the United States and all foreign countries and jurisdictions in and to:

the Invention which is disclosed in the above-identified application or applications;

such application or applications, and all divisional, continuing (including continuation-in-part), substitute, renewal, reissue, and all other applications for a patent or patents which have been or shall be filed in the United States (including all provisional and non-provisional applications), and in all foreign countries and jurisdictions based in whole or in part on any of such Invention (including any application for a utility model or an innovation patent application);

all original and reissued patents which have been or shall be issued in the United States and all foreign countries and jurisdictions based in whole or in part on any of such Invention;

including the right to claim priority to the above-identified patent application or applications in relation to subject matter based in whole or in part on the above-identified patent application or applications and any of the foregoing including the right to file foreign applications under the provisions of any convention or treaty:

and including the right to all causes of action, remedies, and other enforcement rights related to the above-identified application or applications, including without limitation the right to sue for past, present, or future infringement, misappropriation, or violation of any and all rights related to the above-identified patent application or applications and any of the foregoing, including the right to obtain and collect damages for past, present, or future infringement;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such Invention to the Assignee;

AUTHORIZE AND REQUEST that any attorney associated with U.S. Patent and Trademark Office (USPTO) Customer No. 13488 may (directly or through his/her designee) delete, insert, or alter any

information related to the above-identified patent application or applications or any of the foregoing, after execution of this Assignment;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or shall be made to others by the Undersigned, and that the full right to convey the same as herein expressed is possessed by the Undersigned;

COVENANT, that when requested and without compensation, but at the expense of the Assignee, in order to carry out in good faith the intent and purpose of this Assignment, the Undersigned shall (1) execute all provisional, non-provisional, divisional, continuing (including continuation-in-part), substitute, renewal, reissue, and all other patent applications for the Invention; (2) execute all rightful oaths, declarations, assignments, powers of attorney and other papers for the Invention; (3) communicate to the Assignee all facts known to the Undersigned relating to the Invention and the history thereof; (4) cooperate with the Assignee in any interference, reexamination, reissue, opposition, dispute, or litigation involving any of the applications or patents for the Invention; and (5) take such further actions as the Assignee shall reasonably consider necessary or desirable for vesting title to such Invention in the Assignee, or for securing, maintaining and enforcing proper patent protection for the Invention;

COVENANT, that should any provision of this agreement be held unenforceable by an authority of competent jurisdiction, such a ruling shall not affect the validity and enforceability of the remaining provisions. To the extent that any such provision is found to be unenforceable, the Undersigned, when requested and without compensation shall act in good faith to substitute for such provision a new provision with content and purpose as close as possible to the provision deemed unenforceable.

THIS AGREEMENT IS TO BE BINDING on the heirs, assigns, representatives, and successors of the Undersigned, and is to extend to the benefit of the successors, assigns, and nominees of the Assignee.

AGREED as of the date of my signature below:

Assignment Assignors: Alvan Lam et al. Fitle: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT Page 3 of 6			Docket No. 2921.392US1 Client Ref. No. 74939US.01
Assignor:			
(Signature):		/	
	Name: Alvan City/State: Sca	Lam arborough, ME	

Date:

Assignment Assignors: Alvan Lam et al. Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT Page 4 of 6			Docket No: 2921.392US Client Ref. No. 74939US.0
Assignor:			
(Signature):	/		
	Name: Weiming Sun City/State: Beijing, China		
Date:			

Assignment
Assignors: Alvan Lam et al.
Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT
Page 5 of 6

Docket No: 2921.392US1 Client Ref. No. 74939US.01

Assignor:

(Signature):

/Emma Wang

__/

Name: Emma Wang

City/State: Beijing, China

Date:

Oct 10, 2014

Assignment
Assignors: Alvan Lam et al.

Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT
Page 6 of 6

Assignor:

(Signature): / /

Name: Peng Zhu
City/State: Tianjin, China

Date:

ASSIGNMENT

WHEREAS, Alvan Lam, Weiming Sun, Emma Wang and Peng Zhu (hereinafter the "Undersigned") have made one or more inventions and other subject matter (hereinafter collectively referred to as the "Invention") which are described in a patent application filed on December 17, 2013, which application was assigned US patent application serial number 14/109,161, and which is titled DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT; which are described in a patent application filed on December 17, 2012, which application was assigned Chinese patent application serial number 201210562262.8, and which is titled DELAY CIRCUIT AND METHOD AND INTEGRATED CIRCUIT.

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the Invention which is disclosed in the above-identified application or applications;

such application or applications, and all divisional, continuing (including continuation-in-part), substitute, renewal, reissue, and all other applications for a patent or patents which have been or shall be filed in the United States (including all provisional and non-provisional applications), and in all foreign countries and jurisdictions based in whole or in part on any of such Invention (including any application for a utility model or an innovation patent application);

all original and reissued patents which have been or shall be issued in the United States and all foreign countries and jurisdictions based in whole or in part on any of such Invention;

including the right to claim priority to the above-identified patent application or applications in relation to subject matter based in whole or in part on the above-identified patent application or applications and any of the foregoing including the right to file foreign applications under the provisions of any convention or treaty;

and including the right to all causes of action, remedies, and other enforcement rights related to the above-identified application or applications, including without limitation the right to sue for past, present, or future infringement, misappropriation, or violation of any and all rights related to the above-identified patent application or applications and any of the foregoing, including the right to obtain and collect damages for past, present, or future infringement;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such Invention to the Assignee;

AUTHORIZE AND REQUEST that any attorney associated with U.S. Patent and Trademark Office (USPTO) Customer No. 13488 may (directly or through his/her designee) delete, insert, or alter any

information related to the above-identified patent application or applications or any of the foregoing, after execution of this Assignment;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or shall be made to others by the Undersigned, and that the full right to convey the same as herein expressed is possessed by the Undersigned;

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AGREED as of the date of my signature below:

Assignment Docket No: 2921.392US1
Assignors: Alvan Lam et al. Client Ref. No. 74939US.01
Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT
Page 3 of 6

Assignor:

(Signature): / /

Name: Alvan Lam

City/State: Scarborough, ME

Date:

Assignment Docket No: 2921.392US1
Assignors: Alvan Lam et al. Client Ref. No. 74939US.01
Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT
Page 4 of 6

Assignor:

(Signature): / /

Name: Weiming Sun
City/State: Beijing, China

Date:

Assignment
Assignors: Alvan Lam et al.
Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT
Page 5 of 6

Docket No: 2921.392US1 Client Ref. No. 74939US.01

Assignor:			
(Signature):	/	1	
	Name: <u>Emma Wa</u> City/State: <u>Beijin</u>		
Date:			

Assignment
Assignors: Alvan Lam et al.
Title: DELAY METHOD, CIRCUIT AND INTEGRATED CIRCUIT

Page 6 of 6

Docket No: 2921.392US1 Client Ref. No. 74939US.01

Assignor:

(Signature):

, Peng Zhu

Name: Peng Zhu

City/State: <u>Tianjin, China</u>

Date:

Oct 13, 2014

PATENT REEL: 033952 FRAME: 0283

RECORDED: 10/15/2014