

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
FREESCALE SEMICONDUCTOR, INC.	12/09/2013

RECEIVING PARTY DATA

Name:	ZENITH INVESTMENTS, LLC
Street Address:	1209 ORANGE STREET
City:	WILMINGTON
State/Country:	DELAWARE
Postal Code:	19801

PROPERTY NUMBERS Total: 14

Property Type	Number
Patent Number:	7409502
Patent Number:	5689714
Patent Number:	5737584
Patent Number:	7228401
Patent Number:	6775727
Patent Number:	7096307
Patent Number:	7099973
Patent Number:	7107436
Patent Number:	7130943
Patent Number:	7249223
Patent Number:	7340542
Patent Number:	7805590
Patent Number:	5920690
Patent Number:	6519684

CORRESPONDENCE DATA

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PATENT

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NAME OF SUBMITTER: B. NOEL KIVLIN

SIGNATURE: /B. Noel Kivlin/

DATE SIGNED: 10/21/2014

Total Attachments: 6

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ASSIGNMENT

This Assignment by **Freescale Semiconductor, Inc.**, a corporation duly authorized under the laws of the State of Delaware (hereinafter the "Assignor") witnesseth:

WHEREAS, Assignor is the owner of certain rights, title and interest in and to the patents and patent applications set forth on Schedule A hereto (hereinafter the "Patents and Applications");

WHEREAS, **Zenith Investments, LLC**, a corporation duly organized under and pursuant to the laws of the State of California, and having a principal place of business at 1209 Orange Street, Wilmington, DE 19801 (hereinafter the "Assignee"), is desirous of acquiring the entire right, title and interest in and to said Patents and Applications for Letters Patent of the United States or foreign, and in and to any Letters Patent or Patents, United States or foreign, to be obtained therefor and thereon;

NOW, THEREFORE, for good and valuable consideration, the receipt and adequacy of which is hereby acknowledged, said Assignor has sold, assigned, transferred and set over, and by these presents does sell, assign, transfer and set over, unto said Assignee, its successors, legal representatives and assigns, Assignor's interest in the entire right, title and interest in and to the above-mentioned Patents and Applications, application for Letters Patent, and any and all Letters Patent or Patents in the United States of America and all foreign countries which may be granted therefor and thereon, and in and to any and all divisions, continuations, and continuations-in-part of said application, or reissues or extensions of said Letters Patent or Patents, and all rights under the International Convention for the Protection of Industrial Property, the same to be held and enjoyed by said Assignee, for its own use and behalf and the use and behalf of its successors, legal representatives and assigns, to the full end of the term or terms for which Letters Patent or Patents may be granted, as fully and entirely as the same would have been held and enjoyed by the Assignor, had this sale and assignment not been made. Assignor does hereby sell, assign, transfer, and convey to Assignee, its successors, legal representatives, and assigns all claims for damages and all remedies arising out of any violation of the rights assigned hereby that may have accrued prior to the date of assignment to Assignee, or may accrue hereafter including, but not limited to, the right to sue for, collect, and retain damages for past infringement of the said Patents and Applications before or after issuance.

IN TESTIMONY WHEREOF, 9th day of December 2013.

Freescale Semiconductor, Inc.

By: 

Changhae Park, PhD
Vice President
Freescale Semiconductor, Inc.

SCHEDULE "A" to Assignment

Patent Number	Case Number	Country	Application Number	Filing Date	Inventors	Application Title	Application Status
7409502	NC10046TH-US	US	11/982900	11-May-2006	MOYER, WILLIAM C.; SCOTT, JEFFREY W.	SELECTIVE CACHE LINE ALLOCATION INSTRUCTION EXECUTION AND CIRCUITRY	Granted
5689714	SC02739A-US	US	09/520121	28-Aug-1995	MOYER, WILLIAM C.	METHOD AND APPARATUS FOR PROVIDING LOW POWER CONTROL OF PERIPHERAL DEVICES USING THE REGISTER FILE OF A MICROPROCESSOR	Granted
5737584	SC03034A-US	US	08/599699	22-Jan-1996	MOYER, WILLIAM C.	DATA PROCESSING SYSTEM HAVING PROGRAMMABLE EXTERNAL TERMINALS SUCH THAT THE EXTERNAL TERMINALS ARE SELECTIVELY SUBJECTED TO BUS ARBITRATION	Granted
ZL02826499.1	SC11370TH-CN	CN	02826499.1	29-Jun-2004	MOYER, WILLIAM C.	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR	Granted
4998241	SC11370TH-EP	EP	02789344.5	30-Oct-2002	MOYER, WILLIAM C.	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR	Dropped
10-981033	SC11370TH-JP	JP	2003-544586	13-May-2004	MOYER, WILLIAM C.	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR	Granted
1313622	SC11370TH-KR	KR	10-2004-7607338	13-May-2004	MOYER, WILLIAM C.	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR	Granted
	SC11370TH-TW	TW	091132761	07-Nov-2002	MOYER, WILLIAM C.	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR	Granted
7228401	SC11370TH-US	US	10/954577	13-Nov-2001	MOYER, WILLIAM C.	INTERFACING A PROCESSOR TO A COPROCESSOR IN WHICH THE PROCESSOR SELECTIVELY ALTERS AN EXECUTION MODE OF THE COPROCESSOR	Granted
	SC11370TH-WO	WO	PCT/US02/34955	30-Oct-2002	MOYER, WILLIAM C.	METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR	Inactive
ZL02899465.4	SC11433TH-CN	CN	02809465.4	06-Nov-2003	MOYER, WILLIAM C.	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES	Granted

Patent Number	Case Number	Country	Application Number	Filing Date	Application Inventors	Application Title	Application Status
	SC11433TH-DE	DE	10296959.0	22-Dec-2003	MOYER, WILLIAM C.	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES	Dropped
GB2380200	SC11433TH-GB	GB	0324805.1	15-May-2002	MOYER, WILLIAM C.	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES	Dropped
4199771	SC11433TH-JP	JP	2003-507709	15-May-2002	MOYER, WILLIAM C.	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES	Granted
899951	SC11433TH-KR	KR	10-2003-7016810	15-May-2002	MOYER, WILLIAM C.	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES	Granted
101201	SC11433TH-SG	SG	200307239-4	15-May-2002	MOYER, WILLIAM C.	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES	Granted
1221958	SC11433TH-TW	TW	091111284	28-May-2002	MOYER, WILLIAM C.	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES	Granted
6775727	SC11433TH-US	US	09/888278	23-Jun-2001	MOYER, WILLIAM C.	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES	Granted
	SC11433TH-WO	WO	PCT/US02/18358	15-May-2002	MOYER, WILLIAM C.	SYSTEM AND METHOD FOR CONTROLLING BUS ARBITRATION DURING CACHE MEMORY BURST CYCLES	Inactive
	SC12497TH-EP	EP	03103899.8	21-Oct-2003	MOYER, WILLIAM C.	SHARED WRITE BUFFER IN A PERIPHERAL INTERFACE AND METHOD OF OPERATING	Dropped
7096307	SC12497TH-US	US	10323313	18-Dec-2002	MOYER, WILLIAM C.	SHARED WRITE BUFFER IN A PERIPHERAL INTERFACE AND METHOD OF OPERATING	Granted
					MOYER, WILLIAM C.; FITZSIMMONS, MICHAEL D.; MURDOCK, BRETT W.	METHOD AND SYSTEM OF BUS MASTER ARBITRATION	Granted

Patent Number	Class Number	Country	Application Number	Filing Date	Application Inventors	Application Title	Application Status
7107436	SC13075TH-US	US	10/657593	08-Sep-2003	MOYER, WILLIAM C.	CONDITIONAL NEXT PORTION TRANSFERRING OF DATA STREAM TO OR FROM REGISTER BASED ON SUBSEQUENT INSTRUCTION ASPECT	Granted
200580032673.4	SC13546TH-CN	CN	200580032673.4	27-Mar-2007	MOYER, WILLIAM C.; GUMULJA, JIMMY; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Granted
	SC13546TH-EP	EP	05793539.7	16-Feb-2007	MOYER, WILLIAM C.; GUMULJA, JIMMY; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Dropped
	SC13546TH-JP	JP	2007-534610	28-Feb-2007	MOYER, WILLIAM C.; GUMULJA, JIMMY; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Inactive
	SC13546TH-KR	KR	10-2007-7007211	29-Mar-2007	MOYER, WILLIAM C.; GUMULJA, JIMMY; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Inactive
7130943	SC13546TH-US	US	10/954809	30-Sep-2004	MOYER, WILLIAM C.; GUMULJA, JIMMY; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Granted
	SC13546TH-WO	WO	PCT/US05/31115	01-Sep-2005	MOYER, WILLIAM C.; GUMULJA, JIMMY; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Inactive
7249223	SC13577TH-US	US	10/916296	11-Aug-2004	MOYER, WILLIAM C.; SHAW, CRAIG D.	PREFETCHING IN A DATA PROCESSING SYSTEM	Granted

Patent Number	Case Number	Country	Application Number	Filing Date	Application Inventors	Application Title	Application Status
	SC13577TH-WO	WO	PCT/US05/24934	24-Jun-2005	MOYER, WILLIAM C.; SHAW, CRAIG D.	PREFETCHING IN A DATA PROCESSING SYSTEM	Inactive
200580032682.3	SC13716TH-CN	CN	200580032682.3	27-Mar-2007	MOYER, WILLIAM C.; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Granted
	SC13716TH-EP	EP	05793534.8	01-Mar-2007	MOYER, WILLIAM C.; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Dropped
4846376	SC13716TH-JP	JP	2007-534609	28-Feb-2007	MOYER, WILLIAM C.; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Granted
	SC13716TH-KR	KR	10-2007-7007290	30-Mar-2007	MOYER, WILLIAM C.; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Inactive
7340542	SC13716TH-US	US	10/9855558	30-Sep-2004	MOYER, WILLIAM C.; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Granted
	SC13716TH-WO	WO	PCT/US05/31114	01-Sep-2005	MOYER, WILLIAM C.; MURDOCK, BRETT W.	DATA PROCESSING SYSTEM WITH BUS ACCESS RETRACTION	Inactive
7805580	SC14983TH-US	US	11/426633	27-Jun-2005	MOYER, WILLIAM C.; TRAYLOR, KEVIN B.	COPROCESSOR RECEIVING TARGET ADDRESS TO PROCESS A FUNCTION AND TO SEND DATA TRANSFER INSTRUCTIONS TO MAIN PROCESSOR FOR EXECUTION TO PRESERVE CACHE COHERENCE	Granted
	SC80632A-EP	EP	98114977.6	10-Aug-1998	MOYER, WILLIAM C.; MOUGHANNI, CLAUDE; ASLAM, TAIMUR	METHOD AND APPARATUS FOR PROVIDING ACCESS PROTECTION IN AN INTEGRATED CIRCUIT	Dropped
4319712	SC90632A-JP	JP	10-238030	10-Aug-1998	MOYER, WILLIAM C.; MOUGHANNI, CLAUDE; ASLAM, TAIMUR	METHOD AND APPARATUS FOR PROVIDING ACCESS PROTECTION IN AN INTEGRATED CIRCUIT	Granted

Patent Number	Case Number	Country	Application Number	Filing Date	Application Inventors	Application Title	Application Status
5920690	SC90632A-US	US	08/907980	11-Aug-1997	MOYER, WILLIAM C.; MOUGHANNI, CLAUDE; ASLAM, TAIMUR	METHOD AND APPARATUS FOR PROVIDING ACCESS PROTECTION IN AN INTEGRATED CIRCUIT	Granted
6519684	SC91232A-US	US	09/447254	23-Nov-1999	MOYER, WILLIAM C.	LOW OVERHEAD METHOD FOR SELECTING AND UPDATING AN ENTRY IN A CACHE MEMORY	Granted