

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT3094260

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
PETER RABKIN	05/15/2001
HSINGYA ARTHUR WANG	05/18/2001
KAI-CHENG CHOU	05/15/2001
RECEIVING PARTY DATA	
Name:	HYNIX SEMICONDUCTOR, INC.
Street Address:	SAN 136-1, AMI-RI, BUBAL-EUB
City:	ICHON-SI, KYOUNGKI-DO
State/Country:	KOREA, REPUBLIC OF
Postal Code:	467-701
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	11676777
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	206-224-2863
Email:	kpedersen@kilpatricktownsend.com
Correspondent Name:	KILPATRICK TOWNSEND & STOCKTON LLP/SK HY
Address Line 1:	TWO EMBARCADERO CENTER
Address Line 2:	EIGHTH FLOOR
Address Line 4:	SAN FRANCISCO, CALIFORNIA 94111-3834
ATTORNEY DOCKET NUMBER:	76819-079340US-722217
NAME OF SUBMITTER:	KELLY PEDERSEN
SIGNATURE:	/Kelly Pedersen/
DATE SIGNED:	11/04/2014
Total Attachments: 1	
source=Assignment-gparent#page1.tif	

ASSIGNMENT OF PATENT APPLICATION

JOINT

WHEREAS, Peter Rabkin, of 21631 Regnart Road, Cupertino, California 95014; Hsingya Arthur Wang, of 295 Cresta Vista Way, San Jose, California 95119; and Kai-Cheng Chou, of 3661 Jasmine Circle, San Jose, California 95135; hereinafter referred to as "Assignors," are the inventors of the invention described and set forth in the below-identified application for United States Letters Patent:

Title of Invention: Transistor and Memory Cell with Ultra-Short Gate Feature and Method of Fabricating the Same

Filing Date: March 1, 2001

Application No.: 09/797,863; and

WHEREAS, Hynix Semiconductor, Inc., a corporation of the Republic of Korea, located at San 136-1, Ami-ri, Bubal-eub, Ich'on-si, Kyoungki-do, 467-701, Korea, hereinafter referred to as "Assignee," is desirous of acquiring an interest in the invention and application and in any U.S. Letters Patent and Registrations which may be granted on the same;

For good and valuable consideration, receipt of which is hereby acknowledged by Assignors, Assignors have assigned, and by these presents do assign to Assignee all right, title and interest in and to the invention and application and to all foreign counterparts (including patent, utility model and industrial designs), and in and to any Letters Patent and Registrations which may hereafter be granted on the same in the United States and all countries throughout the world, and to claim the priority from the application as provided by the Paris Convention. The right, title and interest is to be held and enjoyed by Assignee and Assignee's successors and assigns as fully and exclusively as it would have been held and enjoyed by Assignors had this Assignment not been made, for the full term of any Letters Patent and Registrations which may be granted thereon, or of any division, renewal, continuation in whole or in part, substitution, conversion, reissue, prolongation or extension thereof.

Assignors further agree that they will, without charge to Assignee, but at Assignee's expense, (a) cooperate with Assignee in the prosecution of U.S. Patent applications and foreign counterparts on the invention and any improvements, (b) execute, verify, acknowledge and deliver all such further papers, including patent applications and instruments of transfer, and (c) perform such other acts as Assignee lawfully may request to obtain or maintain Letters Patent and Registrations for the invention and improvements in any and all countries, and to vest title thereto in Assignee, or Assignee's successors and assigns.

IN TESTIMONY WHEREOF, Assignors have signed their names on the dates indicated.

Dated: 05/15/2001 Peter Rabkin
Dated: 05/18/2001 Hsingya Arthur Wang
Dated: 05/15/2001 Kai-Cheng Chou

Witnesses: John Chang / Timothy M. Gallagher / Timothy Gallagher

PA 3144278 v1