503107197 12/18/2014

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:		NEW ASSIGNMENT			
NATURE OF CONVEYANCE:		ASSIGNMENT			
CONVEYING PARTY DA	ТА				
		Name	Execution Date		
SILANNA SEMICONDUC	TOR PT	LTD.	12/18/2014		
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Property Type		Number			
PROPERTY NUMBERS Total: 1 Property Type		Number	1		
Application Number: 1		4574707			
CORRESPONDENCE DA					
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NAME OF SUBMITTER:		ERIC R. SCHEUERLEIN			
SIGNATURE:		/Eric R. Scheuerlein/	/Eric R. Scheuerlein/		
DATE SIGNED:		12/18/2014	12/18/2014		
Total Attachments: 1 source=20141218_IOSEP	032_assig	nment_Silanna_Pty_to_Silanna_Sem	ni_USA#page1.tif		

PATENT ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, Silanna Semiconductor Pty Ltd. ("Assignor"), hereby assigns to Silanna Semiconductor U.S.A., Inc. ("Assignee"), and Assignee hereby accepts, all of its right, title and interest in and to the U.S. patent application listed below and the right to file nonprovisionals, divisionals, continuations, continuations-in-part, substitute, continued prosecution, request for continued examination, renewals, reexaminations and reissues, and foreign filing of the patent application listed below, including but not limited to the right to file, prosecute and issue patent applications and patents claiming priority to the patent application listed below for the invention set forth in the patent application listed below and, including without limitation, any and all claims and potential claims of past, present and future infringement of such patent application and the right to sue and collect for past, present and future damages and any other causes of action under such patent application ("Assigned Patent Rights"):

Serial No.	Filing Date	Title	Inventors
Unassigned	Filed herewith	LDMOS WITH ADAPTIVELY BIASED GATE-SHIELD	George Imthurn James Ballard Yashodhan Moghe

Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, letters patents, utility models or certificates of invention which may be granted upon the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

IN WITNESS WHEREOF Assignor has executed this Patent Assignment this $\frac{18}{18}$ day of 25.2, 2014

Ptv Ltd. Silanna Semiconductor Authorized signature JON AT HA ħ Name (Printed) SELREIA ComPAJ Title Silanna Semiconductor U.S.A., Inc. Authorized signature JONATHAN MURRA Name (Printed) TREASLRER Title