

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT3178100

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
ATSUSHI WADA	02/28/2001
TAKESHI OTSUKA	02/28/2001
KUNIYUKI TANI	02/28/2001
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<b>City:</b>	MORIGUCHI-SHI, OSAKA-FU
<b>State/Country:</b>	JAPAN
<b>Postal Code:</b>	570-8677
<b>PROPERTY NUMBERS Total: 2</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	10137427
<b>Application Number:</b>	10358310
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<b>NAME OF SUBMITTER:</b>	KELLY A NELSON
<b>SIGNATURE:</b>	/Kelly A Nelson/
<b>DATE SIGNED:</b>	01/12/2015
<b>Total Attachments: 2</b>	
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source=199905942_F1010025US02_Assignment#page2.tif	

Armstrong, Westerman, Hattori, McLeland & Naughton

Docket No. \_\_\_\_\_

### U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter ASSIGNOR) by

SANYO ELECTRIC CO., LTD. of 5-5 Keihanbondori 2-chome, Moriguchi-shi, Osaka-fu  
570-8677, Japan

(hereinafter ASSIGNEE), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled VARIABLE RESISTANCE CIRCUIT OPERATIONAL AMPLIFICATION CIRCUIT SEMICONDUCTOR INTEGRATED CIRCUIT TIME CONSTANT SWITCHING CIRCUIT AND WAVEFORM SHAPING CIRCUIT for which application for Letters Patent of the United States was executed on even date herewith unless otherwise indicated below:

\*filed on \_\_\_\_\_, Serial No. \_\_\_\_\_

(Armstrong, Westerman, Hattori, McLeland & Naughton is hereby authorized to insert the series code, serial number and/or filing date hereon, when known)

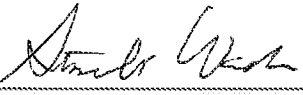
and all Letters Patent of the United States to be obtained therefor on said application or any continuation, divisional, substitute, reissue or reexamination thereof for the full term or terms for which the same may be granted.

The ASSIGNOR agrees to execute all papers necessary in connection with the application and any continuation, divisional, reissue or reexamination applications thereof and also to execute separate assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.

The ASSIGNOR agrees to execute all papers necessary in connection with any interference, litigation, or other legal proceeding which may be declared concerning this application or any continuation, divisional, reissue or reexamination thereof or Letters Patent or reissue patent issued thereon and to cooperate with the ASSIGNEE in every way possible in obtaining and producing evidence and proceeding with such interference, litigation, or other legal

proceeding.

IN WITNESS WHEREOF, the undersigned inventor(s) has(have) affixed his/her/their signature(s).

	Atsushi Wada	28/2/2001
(SIGNATURE)	(TYPE NAME)	(DATE)

Takeshi Otsuka	Takeshi Otsuka	28/2/2001
(SIGNATURE)	(TYPE NAME)	(DATE)

Kuniyuki Tani	Kuniyuki Tani	28/2/2001
(SIGNATURE)	(TYPE NAME)	(DATE)