PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT3190360

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
ST-ERICSSON SA	08/02/2013

RECEIVING PARTY DATA

Name:	STMICROELECTRONICS INTERNATIONAL N.V.
Street Address:	39 CHEMIN DU CHAMP DES FILLES, PLAN LES OUATES
City:	GENEVA
State/Country:	SWITZERLAND
Postal Code:	1228

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	14311086

CORRESPONDENCE DATA

Fax Number: (206)682-6031

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

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Correspondent Name: HAYLEY J. TALBERT
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Address Line 2: SUITE 5400
Address Line 4: SEATTLE, WASHINGTON 98104

ATTORNEY DOCKET NUMBER:	CO2417-AGX(851063.534C1)
NAME OF SUBMITTER:	HAYLEY J. TALBERT
SIGNATURE:	/Hayley J. Talbert/
DATE SIGNED:	01/21/2015

Total Attachments: 19
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PATENT ASSIGNMENT CONFIRMATION

WHEREAS, ST-Ericsson SA, a Swiss corporation having its principal office and place of business at 39 Chemin du Champ-des-Filles, 1228 Plan les Ouates, Geneva, Switzerland, (hereinafter called "ASSIGNOR") was, as of August 2, 2013, the owner of all right, title, and interest in and to Patents and Pending Patent Applications listed in Appendix A, and the inventions for which the same were made and which the same describe (hereinafter collectively referred to as "PATENT RIGHTS");

WHEREAS, STMicroelectronics International N.V., a corporation having a principal place of business at 39 Chemin du Champ des Filles, 1228 Plan les Ouates, Geneva, Switzerland, (hereinafter called "ASSIGNEE"), was, as of August 2, 2013, desirous of acquiring the entire right, title, and interest in and to said PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration, the receipt of which is acknowledged, the said ASSIGNOR, effective as of August 2, 2013, has sold, conveyed, transferred, and assigned to ASSIGNEE, subject to prior encumbrances, all its right, title and interest in and to said PATENT RIGHTS, the same to be held and enjoyed by the ASSIGNEE for its own use and benefit and for the use and benefit of its successors, assigns or other legal representatives; together with any and all patents or patent applications anywhere worldwide to which any of the PATENT RIGHTS directly or indirectly claim priority, including, but not limited to, provisional applications thereof, or for which any of the PATENT RIGHTS directly or indirectly form a basis for priority, together with all existing and/or future continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing together with all claims for damages, injunctive relief, and any other remedies of any kind by reason of past, current and future infringement of said PATENT RIGHTS, with the right to sue for and collect the same for its own use and benefit and for the use and benefit of its successors, assigns, or other legal representatives to the full end of the term for which the aforementioned rights may be granted anywhere in the world.

The assignment of the above mentioned rights includes a transfer of the whole right to use a priority (including priority according to any convention, multilateral agreement, bilateral agreement and national law).

ASSIGNOR covenants that it had, as of August 2, 2013, the full right to sell, convey, transfer, and assign its interest in said PATENT RIGHTS, and that it has not executed, and will not execute, any agreement in conflict herewith or in conflict with the sale, conveyance, transfer and assignment that is the subject hereof.

ASSIGNOR further hereby agrees to execute and deliver all further instruments of sale, conveyance, transfer, assignment, and further assurances and perform all such other acts as may desirous or required to sell, convey, transfer and assign all of the ASSIGNOR'S right, title, and interest in and to and under said PATENT RIGHTS to the ASSIGNEE, and to otherwise assist in perfecting, obtaining, and securing the aforementioned rights to

ASSIGNEE, its legal representatives, successors, and assigns, for any jurisdiction in the world.

ASSIGNOR further hereby provides ASSIGNEE the full and perpetual Power of Attorney for said PATENT RIGHTS, including but not limited the Power of Attorney to execute and deliver all such documents related to sale, conveyance, transfer, assignment and recording of the same for any jurisdiction in the world.

At the expense of ASSIGNEE, or its legal representatives, successors, or assigns, ASSIGNOR agrees to assist in any legal proceedings, sign all lawful papers, make all lawful oaths, and generally do everything reasonably possible to aid ASSIGNEE, its legal representatives, successors, and assigns, to enforce the aforementioned rights in any and all countries and regions worldwide.

Whereby, by its duly authorized officers, ASSIGNOR hereby confirms its sale, conveyance, transfer and assignment, effective as of August 2, 2013, of the PATENT RIGHTS to ASSIGNEE, and ASSIGNEE, by its duly authorized officers, hereby confirms its acceptance of such sale, conveyance, transfer and assignment.

By:		By:	
Name:		Name:	
Title:		Title:	

COMPANY: ST-Ericsson SA

By:		By:	
Name:		Name:	
Title:		Title:	

COMPANY: STMicroelectronics International N.V.

US Patents and Patent Applications

Reference	Country	Patent No	Publication No	Application No	Title
C00040-US1	US	5,483,196		08/225,374	AMPLIFIER ARCHITECTURE AND ITS USE IN A BAND-GAP VOLTAGE SOURCE
C00057-US1	US	5,684,425		08/394,671	ANALOG SWITCH FOR LOW SUPPLY VOLTAGE MOS INTEGRATED CIRCUITS
C00091-US1	US	5,787,294		08/542,498	SYSTEM FOR REDUCING THE POWER CONSUMPTION OF A COMPUTER SYST
C00097-US1	US	5,825,250		08/757,384	OPERATIONAL AMPLIFIER HAVING AN ADJUSTABLE FREQUENCY COMPENSATION
C00106-US1	US	5,774,744		08/627,986	SYSTEM USING DMA AND DESCRIPTOR FOR IMPLEMENTING PERIPHERAL
C00107-US1	US	5,845,151		08/627,992	SYSTEM USING DESCRIPTOR AND HAVING HARDWARE STATE MACHINE CO
C00122-US1	US	6,385,678	20010056515	08/715,946	WEIGHTED BUS ARBITRATION
C00136-US1	US	6,108,568		08/984,213	MULTIPLE CHARGING ON HOOK POSITIONS
C00148-US1	US	6,282,413		09/035,433	DUAL CONVERSION IMAGE-REJECT FREQUENCY CONVERTER
C00150-US1	US	5,864,310		08/822,962	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00171-US1	US	6,012,115		08/901,465	METHOD AND SYSTEM FOR ACCURATE TEMPORAL DETERMINATION OF REA
C00171-US2	US	6,226,701		09/400,738	METHOD AND SYSTEM FOR ACCURATE TEMPORAL DETERMINATION OF REA
C00178-US1	US	6,176,611		08/906,089	SYSTEM AND METHOD FOR REDUCTING POWER CONSUMPTION IN WAITING MODE
C00178-US2	US	6,411,830	20010053682	09/161,309	SYSTEM AND METHOD FOR REDUCTING POWER CONSUMPTION IN WAITING MODE
C00186-US1	US	6,184,811		09/146,019	DOUBLE SAMPLED DELTA SIGMA MODULATOR OF SECOND ORDER HAVING A SEMI-BILINEAR ARCHITECTURE
C00199-US1	US	6,218,041		09/185,003	ULTRA-THIN BATTERY PACK
C00201-US1	US	5,994,968		08/972,771	VCO HAVING A LOW SENSITIVITY TO NOISE ON THE
C00234-US1	US	6,574,288		09/087,012	METHOD AND APPARATUS FOR ADJUSTING A DIGITAL CONTROL WORD TO TUNE SYNTHESIZED HIGH-FREQUENCY SIGNALS FOR WIRELESS COMMUNICATION
C00250-US1	US	6,590,943		09/413,050	SIGMA-DELTA ADC
C00264-US1	US	6,249,686		09/218,547	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00267-US1	US	6,265,849		09/469,446	OFF STATE BATTERY REMOVAL DETECTION
C00290-US1	US	6,717,980		09/316,992	REDUCTION OF TRANSMITTER INDUCED CROSS MODULATION IN RECEIVE
C00305-US1	US	6,392,490		09/650,022	HIGH -PRECISION BIASING CIRCUIT FOR A CASCODED CMOS STAGE, PARTICULARLY FOR LOW NOISE AMPLIFIERS
C00316-US1	US	6,351,100		09/710,828	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00318-US1	US	6,823,032		09/711,230	PROGRAMMABLE CLOCK GENERATING UNIT
C00327-US1	US	6,721,548		09/469,330	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00327-US2	US		20040161030	10/780,471	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00330-US1	US	6,678,312		09/469,322	METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...
C00336-US1	US	6,433,526	20010030531	09/750,612	REGULATION INTERFACE
C00345-US1	US	6,826,389	20010034215	09/799,839	QUADRATURMODULATOR WITH COMPENSATION FOR TRANSMISS
C00353-US1	US	7,079,858	20010036238	09/814,386	ADJUSTING POWER CONTROL USING RECEIVED SIGNALS
C00353-US2	US		20060217143	11/444,096	ADJUSTING POWER CONTROL USING RECEIVED SIGNALS
C00354-US1	US	7,392,067	20010046888	09/829,794	SWITCH OFF PROBLEMS AVOIDING SYSTEM
C00359-US1	US	6,489,818	20010035781	09/846,600	POWER ADAPTIVE FREQUENCY DIVIDER
C00408-US1	US	6,538,417	20020093322	10/017,911	LDO VOLTAGE REGULATOR
C00409-US1	US	6,822,520	20030062958	10/204,170	A LOW-NOISE CHARGE PUMP FOR A PHASE LOCKED LOOP
C00410-US1	US	6,946,821	20040051508	10/250,410	A VOLTAGE REGULATOR WITH ON IMPROVED STABILITY

C00426-US1	US	7,174,136	20020127972	10/082,866	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00467-US1	US	6,897,640	20030122531	10/237,164	PWM POWER SUPPLY ADAPTED FOR LOW CURRENT AND MOBILE TELEPHONE INCLUDING THE SAME
C00475-US1	US	7,146,150	20030078026	10/253,261	DESIGN OF A LOW 1/F-NOISE MIXER
C00506-US1	US	6,838,916	20030222688	10/352,776	METHOD OF GENERATING A VOLTAGE RAMP AT THE TERMINALS OF A CAPACITOR
C00523-US1	US	7,369,577	20030220755	10/151,485	CODE GROUP ACQUISITION PROCEDURE FOR A UMTS-FDD RECEIVER
C00528-US1	US	6,822,593	20040036634	10/447,326	DIGITAL TO DIGITAL SIGMA DELTA MODULATOR AND DIGITAL FREQUENCY SYNTHETIZER COMPRISING THE SAME
C00534-US1	US	6,867,571	20040027098	10/460,039	PREDICTIVE SYNCHRONOUS RECTIFIER FOR SWITCHED MODE POWER SUPPLY
C00545-US1	US	7,536,165	20040017862	10/202,035	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS
C00569-US1	US	7,729,317	20060056357	10/535,802	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER
C00574-US1	US	7,298,116	20060072252	10/537,137	MULTIPLE-OUTPUT DC-DC CONVERTER WITH ONE REFERENCE
C00593-US1	US	7,334,040	20050086351	10/757,036	METHOD OF TRANSMISSION BETWEEN TWO PROCESSORS OF A RADIOCOMMUNICATION UNIT
C00598-US1	US	7,183,756		10/545,186	POWER MANAGEMENT SYSTEM
C00609-US1	US	7,609,117	20040207474	10/801,482	CURRENT PULSE INJECTION FOR PHASE FREQUENCY DETECTOR/CHARGE PUMP LINEARIZATION IN FREQUENCY SYNTHETISERS
C00612-US1	US	7,321,272	20060267702	10/551,966	CONFIGURATION FOR THE MEASUREMENT OF THE TEMPERATURE ON...
C00620-US1	US	7,747,808	20070011386	10/556,450	USB - SLAVE EMBEDDED HIGH SPEED USB HOST
C00621-US1	US	7,340,554	20070028011	10/556,453	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE
C00624-US1	US	7,319,851	20060135109	10/559,210	SWITCHED TRANSCONDUCTOR MIXER
C00639-US1	US	7,962,140	20070082671	10/571,815	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00659-US1	US	7,853,213	20070147480	10/584,096	UMTS RECEIVER SYMBOL SYNCHRONIZATION
C00663-US1	US	7,176,759	20050140443	11/022,050	DIFFERENTIAL LOW NOISE AMPLIFIER WITH LOW POWER CONSUMPTION
C00669-US1	US	7,583,066	20070273337	10/585,059	METHOD OF OPERATING A DC/DC UP-DOWN CONVERTER
C00697-US1	US	8,000,333	20080031168	11/579,778	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00700-US1	US	7,587,192	20080139164	11/579,884	CALIBRATION TECHNIQUE FOR IP2 MIXERS
C00700-US2	US	7,890,077	20090291662	12/533,416	CALIBRATION TECHNIQUE FOR IP2 MIXERS
C00706-US1	US	7,468,963	20050271010	11/146,904	PRIORITY SETTING SCHEME FOR A WIRELESS TERMINAL
C00720-US1	US	7,248,848	20060004938	10/881,167	COMMUNICATION APPARATUS INCLUDING DUAL TIMER UNITES
C00721-US1	US	7,218,084	20060049812	11/179,782	INTEGRATED CIRCUIT WITH SWITCHABLE LOW DROPOUT VOLTAGE REGULATOR
C00726-US1	US		20060018401	10/898,058	APPARATUS USING INTERRUPTS FOR CONTROLLING A PROCESSOR FOR RADIO ISOLATION AND ASSOCIATED METHODS
C00736-US1	US	7,737,738	20080265953	11/573,350	FREQUENCY DIVIDER
C00738-US1	US		20110204868	11/573,352	DIGITAL CONTROLLER FOR DC/DC UP AND DOWN CONVERTERS
C00739-US1	US	7,772,810	20080169796	11/573,353	DIGITAL CONTROLLER FOR CURRENT MODE CONTROLLED DC-DC AUTO
C00755-US1	US	7,325,100	20060253716	11/264,301	AN APPARATUS AND METHOD FOR ENTERING AND EXITING LOW POWER MODE METHOD TO IMPLEMENT POWER STATE TRANSITIONS BY PREFETCHING CODE IN CACHE
C00761-US1	US		20090239548	11/719,745	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED
C00762-US1	US	8,125,209	20090322305	11/719,743	REFERENCE VOLTAGE CIRCUIT
C00769-US1	US	7,609,045	20090039861	11/721,159	A NEW AND SUPERIOR REFERENCE CIRCUIT SUITABLE FOR PAST...
C00773-US1	US	7,265,636	20060132245	11/304,382	LOW NOISE PLL CHARGE PUMP

C00774-US1	US	7,949,313	20090270054	11/722,083	COMPENSATION CIRCUIT AROUND TRANSMIT DIRECT CONVERSION CIRCU
C00790-US1	US	7,978,791	20060159204	11/333,098	ADAPTIVE CHANNEL ESTIMATION IN PILOT SYMBOL ASSISTED MODULATION (PSAM) SYSTEMS
C00797-US1	US			60/650,492	INTERCHANGEABLE RECEIVE INPUTS FOR BAND AND SYSTEM SWAPPABILITY IN COMMUNICATION SYSTEMS AND RELATED METHODS
C00797-US2	US	7,333,831	20060178122	11/231,182	INTERCHANGEABLE RECEIVE INPUTS FOR BAND AND SYSTEM SWAPPABILITY IN COMMUNICATION SYSTEMS AND RELATED METHODS
C00798-US1	US	7,812,675	20090115515	11/815,965	RECEIVER
C00799-US1	US	7,746,176	20080297255	11/815,966	RECEIVER
C00806-US1	US	7,692,280	20080224288	11/910,449	SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS
C00850-US1	US	7,285,940	20070052396	11/220,958	VOLTAGE REGULATOR WITH SHUNT FEEDBACK
C00868-US1	US	7,535,209	20070075698	11/240,745	MANAGEMENT OF REGULATOR-INDUCED SWITCHING NOISE FOR SAMPLED SYSTEM
C00868-US2	US	7,812,583	20090195233	12/420,684	MANAGEMENT OF REGULATOR-INDUCED SWITCHING NOISE FOR SAMPLED SYSTEM
C00870-US1	US	7,702,294	20070105521	11/539,569	NOTCH FILTER AND APPARATUS FOR RECEIVING AND TRANSMITTING RADIO-FREQUENCY SIGNALS WHICH COMPRISES SAID FILTER
C00870-US2	US	7,983,625	20100201438	12/762,203	NOTCH FILTER AND APPARATUS FOR RECEIVING AND TRANSMITTING RADIO-FREQUENCY SIGNALS WHICH COMPRISES SAID FILTER
C00881-US1	US	8,228,041	20080315844	12/094,317	LOW COST CHARGING SYSTEM WITH APPLICATION CURRENT CONSUMPTION COMPENSATION
C00886-US1	US	8,035,353	20080309292	12/097,620	BATTERY RECHARGE PREVENTION PRINCIPLE FOR SHORT BATTERY VOLTAGE DIPS.
C00897-US1	US	7,405,608	20070200622	11/619,750	MODULATOR APPARATUS OPERATING WITH A LOW SUPPLY VOLTAGE AND RELATED MODULATION PROCESS
C00909-US1	US	7,715,486	20070201571	11/711,145	ADAPTIVE DVB CHANNEL ESTIMATION
C00913-US1	US	7,557,650	20070262815	11/716,536	CLASS D AMPLIFIER WITH DIGITAL INPUTS : TOPOLOGY TO OPTIMIZE POWER SUPPLY REJECTION AND OUTPUT DYNAMIC
C00946-US1	US	8,275,078	20100183103	12/308,539	A FAST LOW-COMPLEXITY SYNCHRONIZATION METHOD AND APPARATUS FOR COMMUNICATION SYSTEMS
C00949-US1	US	7,609,781	20080002786	11/427,800	WIRELESS COMMUNICATION DEVICE WITH SELF-CALIBRATION FEATURE FOR CONTROLLING POWER OUTPUT
C00992-US1	US	8,013,586	20080211473	11/961,401	SYNCHRONOUS RECTIFIER HAVING PRECISE ON/OFF SWITCHING TIMES
C01004-US1	US	8,185,058	20080200124	12/032,076	CHANNEL QUALITY DRIVEN MODULATION CHANGE IN MULTI-MODULATION WIRELESS SYSTEMS IN CASE OF FREQUENCY BAND SHARING
C01010-US1	US	8,355,674	20080220730	12/044,141	MULTI-CHANNEL MULTI-IF TRANSMITTER
C01028-US1	US	7,944,906	20100195514	12/452,067	A NOVEL UPLINK FEEDBACK METHOD FOR TDD MU-MIMO SYSTEM
C01028-US2	US		20110211485	13/108,109	A NOVEL UPLINK FEEDBACK METHOD FOR TDD MU-MIMO SYSTEM
C01051-US1	US			12/244,778	SERIAL LINK DEDICATED TO BLUETOOTH-WLAN COEXISTENCE ON TOP OF EXISTING PTA LINES
C01106-US1	US			61/076,957	METHOD TO DELAY CRC COMPUTATIONS IN PREEMPTING PACKET-BASE DEVICES
C01106-US2	US	8,036,245	20090323547	12/495,535	METHOD TO DELAY CRC COMPUTATIONS IN PREEMPTING PACKET-BASE DEVICES
C01122-US1	US		20110299571	13/125,112	USING GPS INFORMATIONTO AID CHANNEL ESTIMATION ALGORITHMS AND HAND-OVER PROCEDURE IN A WIRELESS OFDM
C01166-US1	US		20120115472	13/203,731	METHOD FOR SELECTING A NETWORK CELL WHEN SWITCHING ON A WIRELESS COMMUNICATION APPARATUS, AND CORRES

C01226-US1	US		20120117280	13/382,418	DETECTION OF USB CONNECTION
C02015-US1	US			61/311,901	Linear Chargepump
C02015-US2	US			13/511,603	Linear Chargepump
C02058-US1	US			61/286,147	Active eye opener for current-source driven, high-speed serial links, including USB2.0
C02058-US2	US	8,269,522	20110221521	12/966,480	Active eye opener for current-source driven, high-speed serial links, including USB2.0
C02263-US1	US			61/416,810	Sharing an inductor between buck and boost subsystems
C02263-US2	US		20120080945	13/048,100	Sharing an inductor between buck and boost subsystems
C02307-US1	US			61/388,217	ANTENNA SWITCH with ESD protection/US
C02307-US2	US			13/045,848	ANTENNA SWITCH with ESD protection
C02338-US1	US			61/388,306	Mixer Divider Layout
C02338-US2	US			13/087,749	Mixer Divider Layout
C02339-US1	US			61/388,076	High Speed RF Divider
C02339-US2	US		20120081156	13/248,143	High Speed RF Divider
C02339-US3	US			13/910,366	High Speed RF Divider
C02471-US1	US			61/579,978	Digital Microphone system with extended dynamic range
C02844-US1	US			61/525,541	Methods for handling an IP “dual-stack” connection
C02844-US2	US			13/334,750	Methods for handling an IP “dual-stack” connection
C03123-US1	US			61/694,556	Security mechanism for handheld devices equipped with Laser Pico-projector and Stereoscopic camera
C03167-US1	US			61/702,840	Image stabilization system for handheld devices equipped with Pico-projector and stereoscopic sensor

Non-US Patents and Patent Applications

Reference	Country	Registration No	Publication No	Application No	Title
C00040-DE1	DE	69416703		94410024.7	AMPLIFIER ARCHITECTURE AND ITS USE IN A BAND-GAP VOLTAGE SOURCE
C00040-EP1	EP	0619647	0619647	94410024.7	AMPLIFIER ARCHITECTURE AND ITS USE IN A BAND-GAP VOLTAGE SOURCE
C00040-FR1	FR	2703856	2703856	9304547	AMPLIFIER ARCHITECTURE AND ITS USE IN A BAND-GAP VOLTAGE SOURCE
C00040-FR2	FR	0619647		94410024.7	AMPLIFIER ARCHITECTURE AND ITS USE IN A BAND-GAP VOLTAGE SOURCE
C00040-GB1	GB	0619647		94410024.7	AMPLIFIER ARCHITECTURE AND ITS USE IN A BAND-GAP VOLTAGE SOURCE
C00040-IT1	IT	0619647		94410024.7	AMPLIFIER ARCHITECTURE AND ITS USE IN A BAND-GAP VOLTAGE SOURCE
C00040-JP1	JP			19940070298	AMPLIFIER ARCHITECTURE AND ITS USE IN A BAND-GAP VOLTAGE SOURCE
C00057-EP1	EP		0669718	94830091.8	ANALOG SWITCH FOR LOW SUPPLY VOLTAGE MOS INTEGRATED CIRCUITS
C00057-JP1	JP			19950740352	ANALOG SWITCH FOR LOW SUPPLY VOLTAGE MOS INTEGRATED CIRCUITS
C00091-JP1	JP		9204242	19960233785	SYSTEM FOR REDUCING THE POWER CONSUMPTION OF A COMPUTER SYST
C00097-EP1	EP	0777319	0777319	95830500.5	OPERATIONAL AMPLIFIER HAVING AN ADJUSTABLE FREQUENCY COMPENSATION
C00097-GB1	GB	0777319		95830500.5	OPERATIONAL AMPLIFIER HAVING AN ADJUSTABLE FREQUENCY COMPENSATION
C00097-IT1	IT	0777319		95830500.5	OPERATIONAL AMPLIFIER HAVING AN ADJUSTABLE FREQUENCY COMPENSATION
C00122-DE1	DE	0861470		97928407.2	WEIGHTED BUS ARBITRATION
C00122-EP1	EP	0861470	0861470	97928407.2	WEIGHTED BUS ARBITRATION
C00122-FR1	FR	0861470		97928407.2	WEIGHTED BUS ARBITRATION
C00122-GB1	GB	0861470		97928407.2	WEIGHTED BUS ARBITRATION
C00122-JP1	JP		2000-500895	1998-0514430	WEIGHTED BUS ARBITRATION
C00122-KR1	KR	0440657	10-19990067681	19980703710	WEIGHTED BUS ARBITRATION
C00122-WO1	WO		9812645	IB1997/000876	WEIGHTED BUS ARBITRATION
C00136-CN1	CN	1100460	1190857	19971026047.8	MULTIPLE CHARGING ON HOOK POSITIONS
C00136-DE1	DE	69730000.5		97203683.4	MULTIPLE CHARGING ON HOOK POSITIONS
C00136-EP1	EP	0847172	0847172	97203683.4	MULTIPLE CHARGING ON HOOK POSITIONS
C00136-FR1	FR			9615048	MULTIPLE CHARGING ON HOOK POSITIONS
C00136-FR2	FR	0847172		97203683.4	MULTIPLE CHARGING ON HOOK POSITIONS
C00136-GB1	GB	0847172		97203683.4	MULTIPLE CHARGING ON HOOK POSITIONS
C00136-IT1	IT	0847172		97203683.4	MULTIPLE CHARGING ON HOOK POSITIONS
C00136-JP1	JP		10178473	19970333102	MULTIPLE CHARGING ON HOOK POSITIONS
C00136-KR1	KR			19970065725	MULTIPLE CHARGING ON HOOK POSITIONS
C00148-DE1	DE	69834875.3		98903224.8	DUAL CONVERSION IMAGE-REJECT FREQUENCY CONVERTER
C00148-EP1	EP			97200739.7	DUAL CONVERSION IMAGE-REJECT FREQUENCY CONVERTER
C00148-EP2	EP	0900481	0900481	98903224.8	DUAL CONVERSION IMAGE-REJECT FREQUENCY CONVERTER
C00148-FR1	FR	0900481		98903224.8	DUAL CONVERSION IMAGE-REJECT FREQUENCY CONVERTER
C00148-GB1	GB	0900481		98903224.8	DUAL CONVERSION IMAGE-REJECT FREQUENCY CONVERTER
C00148-JP1	JP	4236059	2000511028T	19980529272T	DUAL CONVERSION IMAGE-REJECT FREQUENCY CONVERTER
C00148-WO1	WO		9840968	IB9800238	DUAL CONVERSION IMAGE-REJECT FREQUENCY CONVERTER
C00150-CN1	CN	1288847	1227017	19988000627.8	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00150-DE1	DE	69820001.2		98903230.5	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC

C00150-EP1	EP	0908015	0908015	98903230.5	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00150-ES1	ES	0908015		98903230.5	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00150-FR1	FR	0908015		98903230.5	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00150-GB1	GB	0908015		98903230.5	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00150-IT1	IT	0908015		98903230.5	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00150-JP1	JP	4124822	2002516046T	19980529276T	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00150-KR1	KR	515797		19980709392	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00150-PCT1	WO		9843357	IB9800244	WIRELESS RECEIVER WITH OFFSET COMPENSATION USING FLASH-ADC
C00186-EP1	EP	0901233	0901233	97830440.0	DOUBLE SAMPLED DELTA SIGMA MODULATOR OF SECOND ORDER HAVING A SEMI-BILINEAR ARCHITECTURE
C00186-FR1	FR	0901233		97830440.0	DOUBLE SAMPLED DELTA SIGMA MODULATOR OF SECOND ORDER HAVING A SEMI-BILINEAR ARCHITECTURE
C00186-GB1	GB	0901233		97830440.0	DOUBLE SAMPLED DELTA SIGMA MODULATOR OF SECOND ORDER HAVING A SEMI-BILINEAR ARCHITECTURE
C00186-IT1	IT	0901233		97830440.0	DOUBLE SAMPLED DELTA SIGMA MODULATOR OF SECOND ORDER HAVING A SEMI-BILINEAR ARCHITECTURE
C00186-JP1	JP	4153101	11145838	19980252850	DOUBLE SAMPLED DELTA SIGMA MODULATOR OF SECOND ORDER HAVING A SEMI-BILINEAR ARCHITECTURE
C00199-CN1	CN	1145227	1226091	19981023790.8	ULTRA-THIN BATTERY PACK
C00199-DE1	DE	69833604.6		98203592.5	ULTRA-THIN BATTERY PACK
C00199-EP1	EP			97402645.2	ULTRA-THIN BATTERY PACK
C00199-EP2	EP	0917218	0917218	98203592.5	ULTRA-THIN BATTERY PACK
C00199-ES1	ES	0917218		98203592.5	ULTRA-THIN BATTERY PACK
C00199-FR1	FR	0917218		98203592.5	ULTRA-THIN BATTERY PACK
C00199-GB1	GB	0917218		98203592.5	ULTRA-THIN BATTERY PACK
C00199-IT1	IT	0917218		98203592.5	ULTRA-THIN BATTERY PACK
C00199-JP1	JP	4514845	2000021374	19980313607	ULTRA-THIN BATTERY PACK
C00199-KR1	KR	516146		19980046996	ULTRA-THIN BATTERY PACK
C00250-CN1	CN	1114997	1290427	19998002773.1	SIGMA-DELTA ADC
C00250-DE1	DE	69917431.7		99947340.8	SIGMA-DELTA ADC
C00250-EP1	EP	1046233	1046233	99947340.8	SIGMA-DELTA ADC
C00250-FR1	FR	1046233		99947340.8	SIGMA-DELTA ADC
C00250-GB1	GB			9821839.9	SIGMA-DELTA ADC
C00250-GB2	GB	1046233		99947340.8	SIGMA-DELTA ADC
C00250-IT1	IT	1046233		99947340.8	SIGMA-DELTA ADC
C00250-JP1	JP	4267205	2002527978T	200005765437	SIGMA-DELTA ADC
C00250-KR1	KR	681324		20007006181	SIGMA-DELTA ADC
C00250-PCT1	WO		0022735	EP9906786	SIGMA-DELTA ADC
C00264-CN1	CN	1127811	1291382	19998003204.2	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-DE1	DE	69902635.0		99965510.3	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-EP1	EP	1057276	1057276	99965510.3	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-ES1	ES	1057276		99965510.3	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-FR1	FR	1057276		99965510.3	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-GB1	GB	1057276		99965510.3	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-IN1	IN			200000298	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-IT1	IT	1057276		99965510.3	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-JP1	JP	4582742	2002-533979	2000-0590314	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-KR1	KR	661537		20007009192	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-PCT1	WO		0038339	EP9910175	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION
C00264-TW1	TW	144652	462148B	089102948	INTERNAL CIRCUIT FOR ADAPTIVE MODE SELECTION

C00267-CN1	CN	1192244	1258847	19991027806.2	OFF STATE BATTERY REMOVAL DETECTION
C00267-DE1	DE	1017148		99204468.5	METHOD FOR DETECTING BATTERY-OUT
C00267-EP1	EP	1017148	1017148	99204468.5	OFF STATE BATTERY REMOVAL DETECTION
C00267-FR1	FR			9816568	OFF STATE BATTERY REMOVAL DETECTION
C00267-FR2	FR	1017148		99204468.5	METHOD FOR DETECTING BATTERY-OUT
C00267-GB1	GB	1017148		99204468.5	METHOD FOR DETECTING BATTERY-OUT
C00267-JP1	JP	4141607	2000201436	1999371461	OFF STATE BATTERY REMOVAL DETECTION
C00267-KR1	KR	0725861	20000052573	19990062541	OFF STATE BATTERY REMOVAL DETECTION
C00290-JP1	JP	4350221	2000349678	19990233293	REDUCTION OF TRANSMITTER INDUCED CROSS MODULATION IN RECEIVE
C00305-EP1	EP	1081573	1081573	99830542.9	HIGH -PRECISION BIASING CIRCUIT FOR A CASCODED CMOS STAGE, PARTICULARLY FOR LOW NOISE AMPLIFIERS
C00305-GB1	GB	1081573		99830542.9	HIGH -PRECISION BIASING CIRCUIT FOR A CASCODED CMOS STAGE, PARTICULARLY FOR LOW NOISE AMPLIFIERS
C00305-IT1	IT	1081573		99830542.9	HIGH -PRECISION BIASING CIRCUIT FOR A CASCODED CMOS STAGE, PARTICULARLY FOR LOW NOISE AMPLIFIERS
C00316-CN1	CN	ZL00802678.5	1337080	20008002678.5	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00316-DE1	DE	1145404		00972828.8	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00316-EP1	EP	1145404	1145404	00972828.8	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00316-FR1	FR	1145404		00972828.8	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00316-GB1	GB			9926609.0	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00316-GB2	GB	1145404		00972828.8	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00316-JP1	JP		2003514491T	20010537146T	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00316-KR1	KR	0725863	20010101452	20017008716	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00316-PCT1	WO		0135510	EP0010360	CONTROLLING DISCHARGE FROM HIGH CAPACITY BATTERY
C00318-DE1	DE		19954696	19991054696.7	PROGRAMMABLE CLOCK GENERATING UNIT
C00318-DE2	DE	50014471.0		00203913.9	PROGRAMMABLE CLOCK GENERATING UNIT
C00318-EP1	EP	1104129	1104129	00203913.9	PROGRAMMABLE CLOCK GENERATING UNIT
C00318-FR1	FR	1104129		00203913.9	PROGRAMMABLE CLOCK GENERATING UNIT
C00318-GB1	GB	1104129		00203913.9	PROGRAMMABLE CLOCK GENERATING UNIT
C00318-JP1	JP	4780258	2001177396	20000344963	PROGRAMMABLE CLOCK GENERATING UNIT
C00327-CN1	CN	1171404	1341302	20008004057.5	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00327-DE1	DE	60034112.7		00987368.8	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00327-EP1	EP	1157489	1157489	00987368.8	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00327-FR1	FR	1157489		00987368.8	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00327-GB1	GB	1157489		00987368.8	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00327-JP1	JP	4416981	2003518814T	20010547771T	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00327-KR1	KR	685526	20010108245	20017010613	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00327-PCT1	WO		0147154	EP0012377	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00327-TW1	TW	167595	512599B	090109893	HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF...
C00330-CN1	CN	ZL00806583.7	1348633	00806583.7	METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...
C00330-DE1	DE	60008514.7		00983146.2	METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...
C00330-EP1	EP	1171958	1171958	00983146.2	METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...
C00330-FR1	FR	1171958		00983146.2	METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...
C00330-GB1	GB	1171958		00983146.2	METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...
C00330-JP1	JP	4900754	2003-518802	2001-547749	METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...
C00330-KR1	KR	0791663	20010102309	20017010649	METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...
C00330-WO1	WO		0147128	EP0011396	METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...
C00336-DE1	DE	60043839.2		00403691.9	REGULATION INTERFACE

C00336-EP1	EP	1113352	1113352	00403691.9	REGULATION INTERFACE
C00336-FR1	FR	2803400	2803400	9916674	REGULATION INTERFACE
C00336-FR2	FR	1113352		00403691.9	REGULATION INTERFACE
C00336-GB1	GB	1113352		00403691.9	REGULATION INTERFACE
C00345-CN1	CN		1313695	20011017223.1	QUADRATURMODULATOR WITH COMPENSATION FOR TRANSMISS
C00345-DE1	DE		10011061	20001011061.4	QUADRATURMODULATOR WITH COMPENSATION FOR TRANSMISS
C00345-EP1	EP		1133127	01200758.9	QUADRATURMODULATOR WITH COMPENSATION FOR TRANSMISS
C00345-JP1	JP			20010060378	QUADRATURMODULATOR WITH COMPENSATION FOR TRANSMISS
C00345-KR1	KR			20010011328	QUADRATURMODULATOR WITH COMPENSATION FOR TRANSMISS
C00353-CN1	CN		1381102	20018001528.X	ADJUSTING POWER CONTROL USING RECEIVED SIGNALS
C00353-EP1	EP		1269652	01915380.8	ADJUSTING POWER CONTROL USING RECEIVED SIGNALS
C00353-GB1	GB			0008020.0	ADJUSTING POWER CONTROL USING RECEIVED SIGNALS
C00353-JP1	JP			20010573807T	ADJUSTING POWER CONTROL USING RECEIVED SIGNALS
C00353-KR1	KR			20017015287	ADJUSTING POWER CONTROL USING RECEIVED SIGNALS
C00353-PCT1	WO		0176263	EP0103138	ADJUSTING POWER CONTROL USING RECEIVED SIGNALS
C00354-CN1	CN	1193510	1318907	20011019657.2	SWITCH OFF PROBLEMS AVOIDING SYSTEM
C00354-DE1	DE	60130055.6		01201201.9	SWITCH OFF PROBLEMS AVOIDING SYSTEM
C00354-EP1	EP	1146655	1146655	01201201.9	SWITCH OFF PROBLEMS AVOIDING SYSTEM
C00354-FR1	FR		2807598	0004641	SWITCH OFF PROBLEMS AVOIDING SYSTEM
C00354-FR2	FR	1146655		01201201.9	SWITCH OFF PROBLEMS AVOIDING SYSTEM
C00354-GB1	GB	1146655		01201201.9	SWITCH OFF PROBLEMS AVOIDING SYSTEM
C00354-JP1	JP	4900748	2002027103	2001-110939	SWITCH OFF PROBLEMS AVOIDING SYSTEM
C00354-KR1	KR	0742236	10-20010098476	20010018594	SWITCH OFF PROBLEMS AVOIDING SYSTEM
C00359-CN1	CN	1276579	1372719	20018001163.2	POWER ADAPTIVE FREQUENCY DIVIDER
C00359-DE1	DE	60130648.1		01927915.7	POWER ADAPTIVE FREQUENCY DIVIDER
C00359-EP1	EP			00201561.8	POWER ADAPTIVE FREQUENCY DIVIDER
C00359-EP2	EP	1281237	1281237	01927915.7	POWER ADAPTIVE FREQUENCY DIVIDER
C00359-FR1	FR	1281237		01927915.7	POWER ADAPTIVE FREQUENCY DIVIDER
C00359-GB1	GB	1281237		01927915.7	POWER ADAPTIVE FREQUENCY DIVIDER
C00359-JP1	JP		2003-533084	2001-0581416	POWER ADAPTIVE FREQUENCY DIVIDER
C00359-KR1	KR		20020029872	20017016945	POWER ADAPTIVE FREQUENCY DIVIDER
C00359-PCT1	WO		0184710	EP0104321	POWER ADAPTIVE FREQUENCY DIVIDER
C00359-TW1	TW	175201	527763B	090108788	POWER ADAPTIVE FREQUENCY DIVIDER
C00408-FR1	FR	2818761	2818761	0017115	LDO VOLTAGE REGULATOR
C00409-EP1	EP		1371139	01990618.9	A LOW-NOISE CHARGE PUMP FOR A PHASE LOCKED LOOP
C00409-FR1	FR	2819123	2819123	0017293	A LOW-NOISE CHARGE PUMP FOR A PHASE LOCKED LOOP
C00409-PCT1	WO		02054597	FR014224	A LOW-NOISE CHARGE PUMP FOR A PHASE LOCKED LOOP
C00410-FR1	FR	2819064	2819064	0017296	A VOLTAGE REGULATOR WITH ON IMPROVED STABILITY
C00410-PCT1	WO		02054167	FR0104222	A VOLTAGE REGULATOR WITH ON IMPROVED STABILITY
C00426-CN1	CN	1263227	1404659	20018005354.8	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00426-DE1	DE	60102073.1		01988978.1	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00426-EP1	EP	1249076	1249076	01988978.1	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00426-FR1	FR	1249076		01988978.1	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00426-GB1	GB			0106695.0	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00426-GB2	GB	1249076		01988978.1	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00426-IT1	IT	1249076		01988978.1	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS

C00426-JP1	JP	3961422	2004512758T	20020538578T	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00426-KR1	KR	762539	20020063921	20027008257	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00426-PCT1	WO		0235718	EP0111980	DIGITISED ARCHITECTURE FOR DIRECT CONVERSION TRANSCEIVERS
C00467-DE1	DE	1292161		02368097.8	SWITCHED MODE POWER SUPPLY DEVICE ADAPTED FOR LOW CURRENT DRAINS, AND CELLULAR PHONE EQUIPPED WITH S
C00467-EP1	EP	1292161	1292161	02368097.8	PWM POWER SUPPLY ADAPTED FOR LOW CURRENT AND MOBILE TELEPHONE INCLUDING THE SAME
C00467-FR1	FR		2829653	0111568	PWM POWER SUPPLY ADAPTED FOR LOW CURRENT AND MOBILE TELEPHONE INCLUDING THE SAME
C00467-FR2	FR	1292161		02368097.8	SWITCHED MODE POWER SUPPLY DEVICE ADAPTED FOR LOW CURRENT DRAINS, AND CELLULAR PHONE EQUIPPED WITH S
C00467-GB1	GB	1292161		02368097.8	SWITCHED MODE POWER SUPPLY DEVICE ADAPTED FOR LOW CURRENT DRAINS, AND CELLULAR PHONE EQUIPPED WITH S
C00475-CN1	CN		1409480	20021049883.0	DESIGN OF A LOW 1/F-NOISE MIXER
C00475-DE1	DE	1298792		02078864.2	DESIGN OF A LOW 1/F-NOISE MIXER
C00475-EP1	EP	1298792	1298792	02078864.2	DESIGN OF A LOW 1/F-NOISE MIXER
C00475-FR1	FR			0112509	DESIGN OF A LOW 1/F-NOISE MIXER
C00475-FR2	FR	1298792		02078864.2	DESIGN OF A LOW 1/F-NOISE MIXER
C00475-GB1	GB	1298792		02078864.2	DESIGN OF A LOW 1/F-NOISE MIXER
C00475-JP1	JP			20020287302	DESIGN OF A LOW 1/F-NOISE MIXER
C00475-KR1	KR			20020058611	DESIGN OF A LOW 1/F-NOISE MIXER
C00506-FR1	FR	2835664	2835664	0201302	METHOD OF GENERATING A VOLTAGE RAMP AT THE TERMINALS OF A CAPACITOR
C00506-GB1	GB	2385472	2385472	0301963.5	METHOD OF GENERATING A VOLTAGE RAMP AT THE TERMINALS OF A CAPACITOR
C00523-EP1	EP	1363407	1363407	03253066.9	CODE GROUP ACQUISITION PROCEDURE FOR A UMTS-FDD RECEIVER
C00523-FR1	FR	1363407		03253066.9	CODE GROUP ACQUISITION PROCEDURE FOR A UMTS-FDD RECEIVER
C00523-GB1	GB	1363407		03253066.9	CODE GROUP ACQUISITION PROCEDURE FOR A UMTS-FDD RECEIVER
C00523-JP1	JP		2003332946	20030139113	CODE GROUP ACQUISITION PROCEDURE FOR A UMTS-FDD RECEIVER
C00528-EP1	EP	1367723	1367723	03291288.3	DIGITAL TO DIGITAL SIGMA DELTA MODULATOR AND DIGITAL FREQUENCY SYNTHETIZER COMPRISING THE SAME
C00528-FR1	FR		2840471	0206500	DIGITAL TO DIGITAL SIGMA DELTA MODULATOR AND DIGITAL FREQUENCY SYNTHETIZER COMPRISING THE SAME
C00528-FR2	FR	1367723		03291288.3	DIGITAL TO DIGITAL SIGMA DELTA MODULATOR AND DIGITAL FREQUENCY SYNTHETIZER COMPRISING THE SAME
C00528-GB1	GB	1367723		03291288.3	DIGITAL TO DIGITAL SIGMA DELTA MODULATOR AND DIGITAL FREQUENCY SYNTHETIZER COMPRISING THE SAME
C00534-EP1	EP		1372252	03291403.8	PREDICTIVE SYNCHRONOUS RECTIFIER FOR SWITCHED MODE POWER SUPPLY
C00534-FR1	FR		2841061	0207280	PREDICTIVE SYNCHRONOUS RECTIFIER FOR SWITCHED MODE POWER SUPPLY
C00545-CN1	CN	ZL20003826838.8	1802785	20003826838.8	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS
C00545-DE1	DE	60324204.9		03817621.0	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS
C00545-EP1	EP	1652292	1652292	03817621.0	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS
C00545-FR1	FR	1652292		03817621.0	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS
C00545-JP1	JP	4466870	2007-516624	2005-0504543	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS
C00545-PCT1	WO		2005011103	IB0303362	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS
C00545-TW1	TW	I350678	200410532	092119837	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS
C00569-CN1	CN	ZL200380104495.2	1717878	20038104495.2	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER
C00569-DE1	DE	60309350.7		03811821.2	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER
C00569-EP1	EP	1568153	1568153	03811821.2	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER

C00569-FR1	FR	1568153		03811821.2	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER
C00569-GB1	GB			0227506.3	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER
C00569-GB2	GB	1568153		03811821.2	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER
C00569-JP1	JP	04459816	2006507751T	2004-554789	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER
C00569-KR1	KR	10-1132628	20050086884	2005-7009544	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER
C00569-WO1	WO		2004049594	IB0305174	LOW-IF PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER
C00574-CN1	CN	ZL200380105082.6	1720654	20038105082.6	MULTIPLE-OUTPUT DC-DC CONVERTER WITH ONE REFERENCE
C00574-DE1	DE	1570567		03769782.8	MULTIPLE-OUTPUT DC-DC CONVERTER
C00574-EP1	EP			02080114.8	MULTIPLE-OUTPUT DC-DC CONVERTER WITH ONE REFERENCE
C00574-EP2	EP	1570567	1570567	03769782.8	MULTIPLE-OUTPUT DC-DC CONVERTER WITH ONE REFERENCE
C00574-GB1	GB	1570567		03769782.8	MULTIPLE-OUTPUT DC-DC CONVERTER
C00574-JP1	JP	4485366	2006509485T	20040556605T	MULTIPLE-OUTPUT DC-DC CONVERTER WITH ONE REFERENCE
C00574-PCT1	WO		2004051831	IB0305074	MULTIPLE-OUTPUT DC-DC CONVERTER WITH ONE REFERENCE
C00593-FR1	FR	2850232	2850232	0300516	METHOD OF TRANSMISSION BETWEEN TWO PROCESSORS OF A RADIOCOMMUNICATION UNIT
C00598-CN1	CN	100401075C	1748150	20048004091.0	POWER MANAGEMENT SYSTEM
C00598-DE1	DE	602004001216.5		04708843.0	POWER MANAGEMENT SYSTEM
C00598-EP1	EP			03100300.7	POWER MANAGEMENT SYSTEM
C00598-EP2	EP	1595151	1595151	04708843.0	POWER MANAGEMENT SYSTEM
C00598-FR1	FR	1595151		04708843.0	POWER MANAGEMENT SYSTEM
C00598-GB1	GB	1595151		04708843.0	POWER MANAGEMENT SYSTEM
C00598-JP1	JP	4810631	2006517666T	20060502567T	POWER MANAGEMENT SYSTEM
C00598-PCT1	WO		2004072663	IB0450083	POWER MANAGEMENT SYSTEM
C00609-IT1	IT	1347564		MI03A00484	CURRENT PULSE INJECTION FOR PHASE FREQUENCY DETECTOR/CHARGE PUMP LINEARIZATION IN FREQUENCY SYNTHETISERS
C00612-CN1	CN	ZL200480009571.6	1771664	20048009571.6	CONFIGURATION FOR THE MEASUREMENT OF THE TEMPERATURE ON...
C00612-DE1	DE	602004012243.2		04725131.9	CONFIGURATION FOR THE MEASUREMENT OF THE TEMPERATURE ON...
C00612-EP1	EP			03100986.3	CONFIGURATION FOR THE MEASUREMENT OF THE TEMPERATURE ON...
C00612-EP2	EP	1616389	1616389	04725131.9	CONFIGURATION FOR THE MEASUREMENT OF THE TEMPERATURE ON...
C00612-FR1	FR	1616389		04725131.9	CONFIGURATION FOR THE MEASUREMENT OF THE TEMPERATURE ON...
C00612-GB1	GB	1616389		04725131.9	CONFIGURATION FOR THE MEASUREMENT OF THE TEMPERATURE ON...
C00612-JP1	JP	4564956	2006-523063	2006-0506803	CONFIGURATION FOR THE MEASUREMENT OF THE TEMPERATURE ON...
C00612-PCT1	WO		2004091100	IB0450380	CONFIGURATION FOR THE MEASUREMENT OF THE TEMPERATURE ON...
C00620-CN1	CN	ZL200480013175.0	1788261	200480013175.0	USB - SLAVE EMBEDDED HIGH SPEED USB HOST
C00620-DE1	DE	1625506		04732377.9	USB - SLAVE EMBEDDED HIGH SPEED USB HOST
C00620-EP1	EP	1625506	1625506	04732377.9	USB - SLAVE EMBEDDED HIGH SPEED USB HOST
C00620-FR1	FR	1625506		04732377.9	USB - SLAVE EMBEDDED HIGH SPEED USB HOST
C00620-GB1	GB	1625506		04732377.9	USB - SLAVE EMBEDDED HIGH SPEED USB HOST
C00620-JP1	JP	4696199	2007-501472	2006-530802	USB - SLAVE EMBEDDED HIGH SPEED USB HOST
C00620-WO1	WO			SG03/00128	USB - SLAVE EMBEDDED HIGH SPEED USB HOST
C00620-WO2	WO		2004/102406	IB2004/050640	USB - SLAVE EMBEDDED HIGH SPEED USB HOST
C00621-CN1	CN	ZL200480012770.2	1882929	200480012770.2	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE
C00621-DE1	DE	602004008064.0		04732372.0	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE
C00621-EP1	EP	1627312	1627312	04732372.0	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE
C00621-FR1	FR	1627312		04732372.0	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE
C00621-GB1	GB	1627312		04732372.0	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE
C00621-JP1	JP	5146796	2007-502476	2006-530805	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE
C00621-WO1	WO			SG0300129	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE
C00621-WO2	WO		2004102407	IB0450643	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE

C00624-CN1	CN	ZL200480016089.5	1802786	20048016089.5	SWITCHED TRANSCONDUCTOR MIXER
C00624-DE1	DE	602004003145.3		04735640.7	SWITCHED TRANSCONDUCTOR MIXER
C00624-EP1	EP			03101685.0	SWITCHED TRANSCONDUCTOR MIXER
C00624-EP2	EP	1636901	1636901	04735640.7	SWITCHED TRANSCONDUCTOR MIXER
C00624-FR1	FR	1636901		04735640.7	SWITCHED TRANSCONDUCTOR MIXER
C00624-GB1	GB	1636901		04735640.7	SWITCHED TRANSCONDUCTOR MIXER
C00624-JP1	JP	4510013	2006527565T	20060516634T	SWITCHED TRANSCONDUCTOR MIXER
C00624-PCT1	WO		2004109904	IB0450815	SWITCHED TRANSCONDUCTOR MIXER
C00639-CN1	CN		1599337	20031024909.4	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00639-CN2	CN	ZL200480026841.4	1853436	20048026841.4	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00639-DE1	DE	602004014950.0		04769831.1	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00639-EP1	EP	1668947	1668947	04769831.1	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00639-FR1	FR	1668947		04769831.1	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00639-GB1	GB	1668947		04769831.1	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00639-JP1	JP	4677564	2007-506328	2006-0526739	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00639-JP2	JP			2010-91715	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00639-PCT1	WO		2005029901	IB0451496	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00639-TW1	TW	I374633		093126368	HANDOFFS BETWEEN WIRELESS WAN AND WIRELESS LAN
C00659-CN1	CN		1898880	20048038284.8	UMTS RECEIVER SYMBOL SYNCHRONIZATION
C00659-EP1	EP			03300281.7	UMTS RECEIVER SYMBOL SYNCHRONIZATION
C00659-EP2	EP		1700387	04801406.2	UMTS RECEIVER SYMBOL SYNCHRONIZATION
C00659-JP1	JP	4620064	2007-515889	2006-0544593	UMTS RECEIVER SYMBOL SYNCHRONIZATION
C00659-WO1	WO		2005064809	IB2004/004186	UMTS RECEIVER SYMBOL SYNCHRONIZATION
C00663-EP1	EP		1548932	03079182.6	DIFFERENTIAL LOW NOISE AMPLIFIER WITH LOW POWER CONSUMPTION
C00669-CN1	CN	ZL200480039824.4	1902810	20048039824.4	METHOD OF OPERATING A DC/DC UP-DOWN CONVERTER
C00669-DE1	DE	EP1704635B1		04806615.3	METHOD OF OPERATING A DC/DC UP/DOWN CONVERTER/DE
C00669-EP1	EP			04100006.8	METHOD OF OPERATING A DC/DC UP-DOWN CONVERTER
C00669-EP2	EP	EP1704635B1	1704635	04806615.3	METHOD OF OPERATING A DC/DC UP-DOWN CONVERTER
C00669-FR1	FR	EP1704635B1		04806615.3	METHOD OF OPERATING A DC/DC UP/DOWN CONVERTER/FR
C00669-GB1	GB	EP1704635B1		04806615.3	METHOD OF OPERATING A DC/DC UP/DOWN CONVERTER/GB
C00669-JP1	JP	4792549	2007518383T	2006-0546464T	METHOD OF OPERATING A DC/DC UP-DOWN CONVERTER
C00669-PCT1	WO		2005074112	IB0452900	METHOD OF OPERATING A DC/DC UP-DOWN CONVERTER
C00697/DE	DE	1745583		05733773.5	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00697-CN1	CN	200580014295.7	1951056	200580014295.7	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00697-EP1	EP	1745583	1745583	05733773.5	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00697-GB1	GB			0410110.1	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00697-GB2	GB			0413591.9	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00697-JP1	JP		2007536868T	20070512669T	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00697-KR1	KR			20067025731	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00697-TW1	TW		200620881	094114320	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00697-WO1	WO		2005109726	IB0551477	IMPROVED ARQ SCHEME IN SOFT HANDOVER
C00700-CN1	CN	ZL200580015382.4	1954487	20058015382.4	CALIBRATION TECHNIQUE FOR IP2 MIXERS
C00700-EP1	EP			04300277.3	CALIBRATION TECHNIQUE FOR IP2 MIXERS
C00700-EP2	EP		1751849	05736215.4	CALIBRATION TECHNIQUE FOR IP2 MIXERS
C00700-JP1	JP	4826960	2007-537647	2007-0512665	CALIBRATION TECHNIQUE FOR IP2 MIXERS
C00700-KR1	KR	10-1125539		2006-7026226	CALIBRATION TECHNIQUE FOR IP2 MIXERS
C00700-WO1	WO		2005112251	IB2005/051471	CALIBRATION TECHNIQUE FOR IP2 MIXERS
C00706-EP1	EP		1605643	04076673.5	PRIORITY SETTING SCHEME FOR A WIRELESS TERMINAL
C00720-CN1	CN	ZL200580021686.1	101057428	200580021686.1	COMMUNICATION APPARATUS INCLUDING DUAL TIMER UNITES
C00720-DE1	DE	1762024		05767044.0	COMMUNICATION APPARATUS INCLUDING DUAL TIMER UNITS
C00720-EP1	EP	1762024	1762024	05767044.0	COMMUNICATION APPARATUS INCLUDING DUAL TIMER UNITES
C00720-FR1	FR	1762024		05767044.0	COMMUNICATION APPARATUS INCLUDING DUAL TIMER UNITS

C00720-JP1	JP	5002783	2008-505547	2007-519380	COMMUNICATION APPARATUS INCLUDING DUAL TIMER UNITES
C00720-SG1	SG			200609112	COMMUNICATION APPARATUS INCLUDING DUAL TIMER UNITES
C00720-WO1	WO		2006004811	US2005/023043	COMMUNICATION APPARATUS INCLUDING DUAL TIMER UNITES
C00721-EP1	EP	1617311	1617311	05291508.9	INTEGRATED CIRCUIT WITH SWITCHABLE LOW DROPOUT VOLTAGE REGULATOR
C00721-FR1	FR		2873216	0407853	INTEGRATED CIRCUIT WITH SWITCHABLE LOW DROPOUT VOLTAGE REGULATOR
C00721-FR2	FR	1617311		05291508.9	INTEGRATED CIRCUIT WITH SWITCHABLE LOW DROPOUT VOLTAGE REGULATOR
C00721-GB1	GB	1617311		05291508.9	INTEGRATED CIRCUIT WITH SWITCHABLE LOW DROPOUT VOLTAGE REGULATOR
C00736-CN1	CN	ZL200580026197.5	101002389	20058026197.5	FREQUENCY DIVIDER
C00736-EP1	EP			04103804.3	FREQUENCY DIVIDER
C00736-EP2	EP		1776765	05772591.3	FREQUENCY DIVIDER
C00736-JP1	JP	4756135	2008509590T	20070524447T	FREQUENCY DIVIDER
C00736-KR1	KR	10-1125535	2007-0048714	2007-7002800	FREQUENCY DIVIDER
C00736-PCT1	WO		2006016312	IB0552534	FREQUENCY DIVIDER
C00738-CN1	CN	ZL200580027575.1	101014919	20058027575.1	DIGITAL CONTROLLER FOR DC/DC UP AND DOWN CONVERTERS
C00738-DE1	DE	1779212		05772747.1	DIGITAL CONTROLLER FOR DC/DC UP AND DOWN CONVERTERS
C00738-EP1	EP			04103911.6	DIGITAL CONTROLLER FOR DC/DC UP AND DOWN CONVERTERS
C00738-EP2	EP	1779212	1779212	05772747.1	DIGITAL CONTROLLER FOR DC/DC UP AND DOWN CONVERTERS
C00738-GB1	GB	1779212		05772747.1	DIGITAL CONTROLLER FOR DC/DC UP AND DOWN CONVERTERS
C00738-JP1	JP	4892689	2008-510442	2007-0525402	DIGITAL CONTROLLER FOR DC/DC UP AND DOWN CONVERTERS
C00738-NL1	NL	1779212		05772747.1	DIGITAL CONTROLLER FOR DC/DC UP AND DOWN CONVERTERS
C00738-WO1	WO		2006018755	IB2005/052520	DIGITAL CONTROLLER FOR DC/DC UP AND DOWN CONVERTERS
C00739-CN1	CN	ZL20058027350.6	101015112	20058027350.6	DIGITAL CONTROLLER FOR CURRENT MODE CONTROLLED DC-DC AUTO
C00739-EP1	EP			04103909.0	DIGITAL CONTROLLER FOR CURRENT MODE CONTROLLED DC-DC AUTO
C00739-EP2	EP		1779501	05774144.9	DIGITAL CONTROLLER FOR CURRENT MODE CONTROLLED DC-DC AUTO
C00739-JP1	JP	4894016	2008-510443	2007-0525420	DIGITAL CONTROLLER FOR CURRENT MODE CONTROLLED DC-DC AUTO
C00739-KR1	KR		2007-0056068	2007-7004263	DIGITAL CONTROLLER FOR CURRENT MODE CONTROLLED DC-DC AUTO
C00739-PCT1	WO		2006018772	IB0552606	DIGITAL CONTROLLER FOR CURRENT MODE CONTROLLED DC-DC AUTO
C00755-EP1	EP	1653331	1653331	05110131.9	AN APPARATUS AND METHOD FOR ENTERING AND EXITING LOW POWER MODE METHOD TO IMPLEMENT POWER STATE TRANSITIONS BY PREFETCHING CODE IN CACHE
C00755-FR1	FR	1653331		05110131.9	METHOD TO IMPLEMENT POWER STATE TRANSITIONS BY PREFETCHING CODE IN CACHE
C00755-IN1	IN			2155/DEL/2004	AN APPARATUS AND METHOD FOR ENTERING AND EXITING LOW POWER MODE METHOD TO IMPLEMENT POWER STATE TRANSITIONS BY PREFETCHING CODE IN CACHE
C00761-CN1	CN		101061391	20058039433.7	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED

C00761-DE1	DE	1815264		05800664.4	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED
C00761-EP1	EP	1815264	1815264	05800664.4	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED
C00761-FR1	FR	1815264		05800664.4	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED
C00761-GB1	GB			0425401.7	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED
C00761-GB2	GB	1815264		05800664.4	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED
C00761-JP1	JP		2008520995T	20070542376T	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED
C00761-KR1	KR	10-1125530	2007-0089808	2007-7013741	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED
C00761-WO1	WO		2006054196	IB0553641	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLEXED
C00762-CN1	CN	ZL20058039173.3	101061449	20058039173.3	REFERENCE VOLTAGE CIRCUIT
C00762-DE1	DE	1815303		05802337.5	REFERENCE VOLTAGE CIRCUIT
C00762-EP1	EP			04105871.0	REFERENCE VOLTAGE CIRCUIT
C00762-EP2	EP	1815303	1815303	05802337.5	REFERENCE VOLTAGE CIRCUIT
C00762-FR1	FR	1815303		05802337.5	REFERENCE VOLTAGE CIRCUIT
C00762-GB1	GB	1815303		05802337.5	REFERENCE VOLTAGE CIRCUIT
C00762-IN1	IN				REFERENCE VOLTAGE CIRCUIT
C00762-JP1	JP	5109080	2008521235T	20070542392T	REFERENCE VOLTAGE CIRCUIT
C00762-KR1	KR				REFERENCE VOLTAGE CIRCUIT
C00762-WO1	WO		2006054217	IB2005/053723	REFERENCE VOLTAGE CIRCUIT
C00769-CN1	CN	ZL200580041712.7	101443721A	20058041712.7	A NEW AND SUPERIOR REFERENCE CIRCUIT SUITABLE FOR PAST...
C00769-EP1	EP			04300854.9	A NEW AND SUPERIOR REFERENCE CIRCUIT SUITABLE FOR PAST...
C00769-EP2	EP		1846808	05821641.7	A NEW AND SUPERIOR REFERENCE CIRCUIT SUITABLE FOR PAST...
C00769-IN1	IN				A NEW AND SUPERIOR REFERENCE CIRCUIT SUITABLE FOR PAST...
C00769-JP1	JP		2008523465T	2007-543985	A NEW AND SUPERIOR REFERENCE CIRCUIT SUITABLE FOR PAST...
C00769-WO1	WO		/2006061742	IB2005/053996	A NEW AND SUPERIOR REFERENCE CIRCUIT SUITABLE FOR PAST...
C00773-FR1	FR	2879858	2879858	0413434	LOW NOISE PLL CHARGE PUMP
C00774-CN1	CN	ZL200580048134.X	101185238	20058048134.x	COMPENSATION CIRCUIT AROUND TRANSMIT DIRECT CONVERSION CIRCU
C00774-DE1	DE	1829202		05823260.4	DIRECT CONVERSION DEVICE WITH COMPENSATION MEANS FOR A TRANSMISSION PATH OF A WIRELESS COMMUNICATION
C00774-EP1	EP			04300908.3	COMPENSATION CIRCUIT AROUND TRANSMIT DIRECT CONVERSION CIRCU
C00774-EP2	EP	1829202	1829202	05823260.4	COMPENSATION CIRCUIT AROUND TRANSMIT DIRECT CONVERSION CIRCU
C00774-FR1	FR	1829202		05823260.4	DIRECT CONVERSION DEVICE WITH COMPENSATION MEANS FOR A TRANSMISSION PATH OF A WIRELESS COMMUNICATION
C00774-GB1	GB	1829202		05823260.4	DIRECT CONVERSION DEVICE WITH COMPENSATION MEANS FOR A TRANSMISSION PATH OF A WIRELESS COMMUNICATION
C00774-IN1	IN		5251DELNP2007	5251DELNP2007	COMPENSATION CIRCUIT AROUND TRANSMIT DIRECT CONVERSION CIRCU
C00774-JP1	JP	4701346	2008-524899T	2007-0546249T	COMPENSATION CIRCUIT AROUND TRANSMIT DIRECT CONVERSION CIRCU

C00774-WO1	WO		2006064425	IB2005/054128	COMPENSATION CIRCUIT AROUND TRANSMIT DIRECT CONVERSION CIRCU
C00790-SG1	SG	124304		200500298-5	ADAPTIVE CHANNEL ESTIMATION IN PILOT SYMBOL ASSISTED MODULATION (PSAM) SYSTEMS
C00797-DE1	DE	1917716		06718689.0	INTERCHANGEABLE RECEIVE INPUTS FOR BAND AND SYSTEM SWAPPABILITY IN COMMUNICATION SYSTEMS AND RELATED
C00797-EP1	EP	1917716 B1	1917716	06718689.0	INTERCHANGEABLE RECEIVE INPUTS FOR BAND AND SYSTEM SWAPPABILITY IN COMMUNICATION SYSTEMS AND RELATED METHODS
C00797-GB1	GB	1917716		06718689.0	INTERCHANGEABLE RECEIVE INPUTS FOR BAND AND SYSTEM SWAPPABILITY IN COMMUNICATION SYSTEMS AND RELATED
C00797-WO1	WO		2006086127	US2006/001653	INTERCHANGEABLE RECEIVE INPUTS FOR BAND AND SYSTEM SWAPPABILITY IN COMMUNICATION SYSTEMS AND RELATED METHODS
C00798-CN1	CN	ZL200680004286.4	101167249	20068004286.4	RECEIVER
C00798-EP1	EP			05100906.6	RECEIVER
C00798-EP2	EP		1851854	06710788.8	RECEIVER
C00798-JP1	JP	4719844	2008530883T	20070554689T	RECEIVER
C00798-PCT1	WO		2006085238	IB0650320	RECEIVER
C00799-CN1	CN	ZL200680004300.0	101164233	20068004300.0	RECEIVER
C00799-DE1	DE	1851855		06710789.6	RECEIVER
C00799-EP1	EP			05100912.4	RECEIVER
C00799-EP2	EP	1851855	1851855	06710789.6	RECEIVER
C00799-FR1	FR	1851855		06710789.6	RECEIVER
C00799-GB1	GB	1851855		06710789.6	RECEIVER
C00799-JP1	JP	4756136	2008530884T	20070554690T	RECEIVER
C00799-PCT1	WO		2006085239	IB0650321	RECEIVER
C00806-CN1	CN	ZL200680010754.9	101180632	20068010754.9	SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS
C00806-DE1	DE	1866830		06727689.9	SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS
C00806-EP1	EP			05300228.3	SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS
C00806-EP2	EP	1866830	1866830	06727689.9	SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS
C00806-FR1	FR	1866830		06727689.9	SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS
C00806-GB1	GB	1866830		06727689.9	SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS
C00806-JP1	JP	4977828	2008535076T	20080503640T	SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS
C00806-PCT1	WO		2006103592	IB0650857	SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS
C00850-CN1	CN		101300537	20068041332	VOLTAGE REGULATOR WITH SHUNT FEEDBACK
C00850-DE1	DE	1929393		06790167.8	VOLTAGE REGULATOR WITH SHUNT FEEDBACK
C00850-EP1	EP	1929393	1929393	06790167.8	VOLTAGE REGULATOR WITH SHUNT FEEDBACK
C00850-FR1	FR	1929393		06790167.8	VOLTAGE REGULATOR WITH SHUNT FEEDBACK
C00850-JP1	JP	4887470	2009-507307	2008-0530133	VOLTAGE REGULATOR WITH SHUNT FEEDBACK
C00850-WO1	WO		2007030436	US2006/034512	VOLTAGE REGULATOR WITH SHUNT FEEDBACK
C00868-CN1	CN	ZL200680035672.x	101310240	20068035672.x	MANAGEMENT OF REGULATOR-INDUCED SWITCHING NOISE FOR SAMPLED SYSTEM
C00868-EP1	EP		1934671	06803517.9	MANAGEMENT OF REGULATOR-INDUCED SWITCHING NOISE FOR SAMPLED SYSTEM
C00868-JP1	JP	4898983		2008-533406	MANAGEMENT OF REGULATOR-INDUCED SWITCHING NOISE FOR SAMPLED SYSTEM
C00868-WO1	WO		2007040939	US2006/035702	MANAGEMENT OF REGULATOR-INDUCED SWITCHING NOISE FOR SAMPLED SYSTEM
C00870-EP1	EP		1780888	05425715.9	NOTCH FILTER AND APPARATUS FOR RECEIVING AND TRANSMITTING RADIO-FREQUENCY SIGNALS WHICH COMPRISES SAID FILTER
C00881-CN1	CN	ZL200680043913.5	101313445	200680043913.5	LOW COST CHARGING SYSTEM WITH APPLICATION CURRENT CONSUMPTION COMPENSATION
C00881-EP1	EP			05300965.0	LOW COST CHARGING SYSTEM WITH APPLICATION CURRENT CONSUMPTION COMPENSATION
C00881-EP2	EP		1958310	06821432.9	LOW COST CHARGING SYSTEM WITH APPLICATION CURRENT CONSUMPTION COMPENSATION

C00881-JP1	JP		2009-517000	2008-0541860	LOW COST CHARGING SYSTEM WITH APPLICATION CURRENT CONSUMPTION COMPENSATION
C00881-WO1	WO		2007060575	IB2006/054242	LOW COST CHARGING SYSTEM WITH APPLICATION CURRENT CONSUMPTION COMPENSATION
C00886-CN1	CN		101331659	200680046956.9	BATTERY RECHARGE PREVENTION PRINCIPLE FOR SHORT BATTERY VOLTAGE DIPS.
C00886-EP1	EP			05112211.7	BATTERY RECHARGE PREVENTION PRINCIPLE FOR SHORT BATTERY VOLTAGE DIPS.
C00886-EP2	EP		1964234	06832144.7	BATTERY RECHARGE PREVENTION PRINCIPLE FOR SHORT BATTERY VOLTAGE DIPS.
C00886-JP1	JP		2009-519697	2008-0545183	BATTERY RECHARGE PREVENTION PRINCIPLE FOR SHORT BATTERY VOLTAGE DIPS.
C00886-WO1	WO		2007/085915	IB200/0654662	BATTERY RECHARGE PREVENTION PRINCIPLE FOR SHORT BATTERY VOLTAGE DIPS.
C00897-IT1	IT	1373200		TO06A000008	MODULATOR APPARATUS OPERATING WITH A LOW SUPPLY VOLTAGE AND RELATED MODULATION PROCESS
C00909-DE1	DE	1826973		07103091.0	ADAPTIVE DVB CHANNEL ESTIMATION
C00909-EP1	EP	1826973	1826973	07103091.0	ADAPTIVE DVB CHANNEL ESTIMATION
C00909-FR1	FR		2897998	0650662	ADAPTIVE DVB CHANNEL ESTIMATION
C00909-FR2	FR	1826973		07103091.0	ADAPTIVE DVB CHANNEL ESTIMATION
C00909-GB1	GB	1826973		07103091.0	ADAPTIVE DVB CHANNEL ESTIMATION
C00913-DE1	DE	1835619		07290307.3	ELECTRONIC SIGNAL GENERATOR WITH MODULATED CYCLIC RATIO, COMPENSATED FOR VARIATIONS IN ITS POWER SUP
C00913-EP1	EP	1835619	1835619	07290307.3	CLASS D AMPLIFIER WITH DIGITAL INPUTS : TOPOLOGY TO OPTIMIZE POWER SUPPLY REJECTION AND OUTPUT DYNAMIC
C00913-FR1	FR		2898442	0602189	CLASS D AMPLIFIER WITH DIGITAL INPUTS : TOPOLOGY TO OPTIMIZE POWER SUPPLY REJECTION AND OUTPUT DYNAMIC
C00913-FR2	FR	1835619		07290307.3	ELECTRONIC SIGNAL GENERATOR WITH MODULATED CYCLIC RATIO, COMPENSATED FOR VARIATIONS IN ITS POWER SUP
C00913-GB1	GB	1835619		07290307.3	ELECTRONIC SIGNAL GENERATOR WITH MODULATED CYCLIC RATIO, COMPENSATED FOR VARIATIONS IN ITS POWER SUP
C00946-CN1	CN			20061093885.x	A FAST LOW-COMPLEXITY SYNCHRONIZATION METHOD AND APPARATUS FOR COMMUNICATION SYSTEMS
C00946-PCT1	WO		2008038164	IB0752388	A FAST LOW-COMPLEXITY SYNCHRONIZATION METHOD AND APPARATUS FOR COMMUNICATION SYSTEMS
C00949-WO1	WO			US2007/072575	WIRELESS COMMUNICATION DEVICE WITH SELF-CALIBRATION FEATURE FOR CONTROLLING POWER OUTPUT
C00992-DE1	DE	1936792		06127185.4	SYNCHRONOUS RECTIFIER HAVING PRECISE ON/OFF SWITCHING TIME/DE
C00992-EP1	EP	1936792	1936792	06127185.4	SYNCHRONOUS RECTIFIER HAVING PRECISE ON/OFF SWITCHING TIMES
C00992-FR1	FR	1936792		06127185.4	SYNCHRONOUS RECTIFIER HAVING PRECISE ON/OFF SWITCHING TIME/FR
C00992-GB1	GB	1936792		06127185.4	SYNCHRONOUS RECTIFIER HAVING PRECISE ON/OFF SWITCHING TIME/GB
C01004-DE1	DE	1959612		07003400.4	RADIO COEXISTENCE MECHANISM FOR VARIABLE DATA RATE RADIO LINKS
C01004-EP1	EP	1959612	1959612	07003400.4	CHANNEL QUALITY DRIVEN MODULATION CHANGE IN MULTI-MODULATION WIRELESS SYSTEMS IN CASE OF FREQUENCY BAND SHARING
C01004-FR1	FR	1959612		07003400.4	RADIO COEXISTENCE MECHANISM FOR VARIABLE DATA RATE RADIO LINKS
C01004-GB1	GB	1959612		07003400.4	RADIO COEXISTENCE MECHANISM FOR VARIABLE DATA RATE RADIO LINKS
C01010-EP1	EP		1968218	07004752.7	MULTI-CHANNEL MULTI-IF TRANSMITTER
C01010-EP2	EP			11156202.1	MULTI-CHANNEL TRANSMITTER
C01028-CN1	CN	ZL200710128275.3	101325741	200710128275.3	A NOVEL UPLINK FEEDBACK METHOD FOR TDD MU-MIMO SYSTEM

C01028-EP1	EP		2174428	08763340.0	A NOVEL UPLINK FEEDBACK METHOD FOR TDD MU-MIMO SYSTEM
C01028-JP1	JP		2011-0514690	2010-511776	A NOVEL UPLINK FEEDBACK METHOD FOR TDD MU-MIMO SYSTEM
C01028-WO1	WO		2008152612	IB2008/052353	A NOVEL UPLINK FEEDBACK METHOD FOR TDD MU-MIMO SYSTEM
C01051-EP1	EP	C01051-EP1	C01051-EP1	07447055.0	SERIAL LINK DEDICATED TO BLUETOOTH-WLAN COEXISTENCE ON TOP OF EXISTING PTA LINES
C01051-EP2	EP	C01051-EP2	C01051-EP2	08165954.2	SERIAL LINK DEDICATED TO BLUETOOTH-WLAN COEXISTENCE ON TOP OF EXISTING PTA LINES
C01086-EP1	EP	C01086-EP1	C01086-EP1	08447007.9	DISTANCE AND POWER AWARENESS IN A PTA ALGORITHM
C01122-EP1	EP		2356785	09756249.0	USING GPS INFORMATIONTO AID CHANNEL ESTIMATION ALGORITHMS AND HAND-OVER PROCEDURE IN A WIRELESS OFDM TRANSCEIVER
C01122-FR1	FR	2937482	2937482	0805834	USING GPS INFORMATIONTO AID CHANNEL ESTIMATION ALGORITHMS AND HAND-OVER PROCEDURE IN A WIRELESS OFDM TRANSCEIVER
C01122-JP1	JP			2011-531407	USING GPS INFORMATIONTO AID CHANNEL ESTIMATION ALGORITHMS AND HAND-OVER PROCEDURE IN A WIRELESS OFDM TRANSCEIVER
C01122-WO1	WO		2010/046088	EP2009/007511	USING GPS INFORMATIONTO AID CHANNEL ESTIMATION ALGORITHMS AND HAND-OVER PROCEDURE IN A WIRELESS OFDM
C01166-CN1	CN		CN102342154A	201080010408.7	METHOD FOR SELECTING A NETWORK CELL WHEN SWITCHING ON A WIRELESS COMMUNICATION APPARATUS, AND CORRES
C01166-EP1	EP		2404465	10705617.8	METHOD FOR SELECTING A NETWORK CELL WHEN SWITCHING ON A WIRELESS COMMUNICATION APPARATUS, AND CORRES
C01166-FR1	FR	0951377	2942933	0951377	FASTER CELL SEARCH BASED ON COUNTRY CODE
C01166-WO1	WO		2010/100141	EP2010/052612	FASTER CELL SEARCH BASED ON COUNTRY CODE
C01226-DE1	DE			10732355.2	DETECTION OF USB CONNECTION
C01226-EP1	EP			10732355.2	DETECTION OF USB CONNECTION
C01226-FR1	FR	0954857	2947930	0954857	USB TRANSCEIVER AUTONOMOUS ADP DETECTION
C01226-GB1	GB			10732355.2	DETECTION OF USB CONNECTION
C01226-JP1	JP			2012-519017	DETECTION OF USB CONNECTION
C01226-WO1	WO		2011/004020	EP2010/059931	DETECTION OF USB CONNECTION
C02015-EP1	EP	2326011	2326011	09176834.1	Linear Chargepump
C02015-GB1	GB	2326011		09176834.1	Linear Chargepump
C02015-WO1	WO		2011064122A1	EP2010/067557	Linear Chargepump
C02058-EP1	EP		2514108	10795301.0	Active eye opener for current-source driven, high-speed serial links, including USB2.0
C02058-KR1	KR			2012-7015303	Active eye opener for current-source driven, high-speed serial links, including USB2.0
C02058-WO1	WO		2011/073184	EP2010/069615	Active eye opener for current-source driven, high-speed serial links, including USB2.0
C02263-EP1	EP			EP2011/067194	Sharing an inductor between buck and boost subsystems
C02263-IN1	IN			2909/CHE/2010	Sharing an inductor between buck and boost subsystems
C02263-WO1	WO		2012042050A3	EP2011/067194	Sharing an inductor between buck and boost subsystems
C02307-EP1	EP				ANTENNA SWITCH with ESD protection
C02307-WO1	WO		2012/042046	EP2011/067190	ANTENNA SWITCH with ESD protection
C02338-EP1	EP			11769821.7	Mixer Divider Layout
C02338-WO1	WO		2012/041916	EP2011/066890	Mixer Divider Layout
C02339-CN1	CN			EP2011/067188	High Speed RF Divider
C02339-EP1	EP			11763742.1	High Speed RF Divider
C02339-WO1	WO		2012/042044	EP2011/067188	High Speed RF Divider
C02471-EP1	EP			11195374.1	Digital Microphone system with extended dynamic range
C02471-WO1	WO			EP2012/076258	Digital Microphone system with extended dynamic range

C02844-WO1	WO	2013026613A2	EP2012/063415	Methods for handling an IP “dual-stack” connection
C03123-EP1	EP		12305977.6	Security mechanism for handheld devices equipped with Laser Pico-projector and Stereoscopic camera
C03123-EP2	EP		EP13305081.5	Security mechanism for handheld devices equipped with Laser Pico-projector and Stereoscopic camera
C03167-EP1	EP		12306080.8	Image stabilization system for handheld devices equipped with Pico-projector and stereoscopic sensor