503144713 01/21/2015

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT3191325

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date	
LSI CORPORATION	09/02/2014	

RECEIVING PARTY DATA

Name:	SEAGATE TECHNOLOGY LLC
Street Address:	10200 S. DE ANZA BLVD.
City:	CUPERTINO
State/Country:	CALIFORNIA
Postal Code:	95014

PROPERTY NUMBERS Total: 10

Property Type	Number
Application Number:	14305293
Application Number:	14305208
Application Number:	14311645
Application Number:	14201020
Application Number:	13342027
Application Number:	14274769
Application Number:	13053175
Application Number:	14158955
Application Number:	14158380
Application Number:	14131665

CORRESPONDENCE DATA

Fax Number: (405)553-2855

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 405-553-2828

Email: danderson@hallestill.com

Correspondent Name:HALL ESTILL ATTORNEYS AT LAWAddress Line 1:100 NORTH BROADWAY, SUITE 2900Address Line 4:OKLAHOMA CITY, OKLAHOMA 73102

ATTORNEY DOCKET NUMBER: | STL070781

PATENT 503144713 REEL: 034774 FRAME: 0418

NAME OF SUBMITTER:	RANDALL K. MCCARTHY
SIGNATURE:	/Randall K. McCarthy/
DATE SIGNED:	01/21/2015
Total Attachments 57	

Total Attachments: 57 source=2014-09-02 Patent Assignment (US) (Executed)#page1.tif source=2014-09-02 Patent Assignment (US) (Executed)#page2.tif source=2014-09-02 Patent Assignment (US) (Executed)#page3.tif source=2014-09-02 Patent Assignment (US) (Executed)#page4.tif source=2014-09-02 Patent Assignment (US) (Executed)#page5.tif source=2014-09-02 Patent Assignment (US) (Executed)#page6.tif source=2014-09-02 Patent Assignment (US) (Executed)#page7.tif source=2014-09-02 Patent Assignment (US) (Executed)#page8.tif source=2014-09-02 Patent Assignment (US) (Executed)#page9.tif source=2014-09-02 Patent Assignment (US) (Executed)#page10.tif source=2014-09-02 Patent Assignment (US) (Executed)#page11.tif source=2014-09-02 Patent Assignment (US) (Executed)#page12.tif source=2014-09-02 Patent Assignment (US) (Executed)#page13.tif source=2014-09-02 Patent Assignment (US) (Executed)#page14.tif source=2014-09-02 Patent Assignment (US) (Executed)#page15.tif source=2014-09-02 Patent Assignment (US) (Executed)#page16.tif source=2014-09-02 Patent Assignment (US) (Executed)#page17.tif source=2014-09-02 Patent Assignment (US) (Executed)#page18.tif source=2014-09-02 Patent Assignment (US) (Executed)#page19.tif source=2014-09-02 Patent Assignment (US) (Executed)#page20.tif source=2014-09-02 Patent Assignment (US) (Executed)#page21.tif source=2014-09-02 Patent Assignment (US) (Executed)#page22.tif source=2014-09-02 Patent Assignment (US) (Executed)#page23.tif source=2014-09-02 Patent Assignment (US) (Executed)#page24.tif source=2014-09-02 Patent Assignment (US) (Executed)#page25.tif source=2014-09-02 Patent Assignment (US) (Executed)#page26.tif source=2014-09-02 Patent Assignment (US) (Executed)#page27.tif source=2014-09-02 Patent Assignment (US) (Executed)#page28.tif source=2014-09-02 Patent Assignment (US) (Executed)#page29.tif source=2014-09-02 Patent Assignment (US) (Executed)#page30.tif source=2014-09-02 Patent Assignment (US) (Executed)#page31.tif source=2014-09-02 Patent Assignment (US) (Executed)#page32.tif source=2014-09-02 Patent Assignment (US) (Executed)#page33.tif source=2014-09-02 Patent Assignment (US) (Executed)#page34.tif source=2014-09-02 Patent Assignment (US) (Executed)#page35.tif source=2014-09-02 Patent Assignment (US) (Executed)#page36.tif source=2014-09-02 Patent Assignment (US) (Executed)#page37.tif source=2014-09-02 Patent Assignment (US) (Executed)#page38.tif source=2014-09-02 Patent Assignment (US) (Executed)#page39.tif source=2014-09-02 Patent Assignment (US) (Executed)#page40.tif source=2014-09-02 Patent Assignment (US) (Executed)#page41.tif source=2014-09-02 Patent Assignment (US) (Executed)#page42.tif source=2014-09-02 Patent Assignment (US) (Executed)#page43.tif

source=2014-09-02 Patent Assignment (U S) (Executed)#page44.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page45.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page46.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page47.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page48.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page49.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page50.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page51.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page52.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page53.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page54.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page55.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page56.tif
source=2014-09-02 Patent Assignment (U S) (Executed)#page57.tif

PATENT ASSIGNMENT

THIS PATENT ASSIGNMENT is made as of September 2, 2014 by LSI CORPORATION, a Delaware corporation, hereinafter referred to as the ASSIGNOR, and SEAGATE TECHNOLOGY LLC, a Delaware limited liability company, hereinafter referred to as the ASSIGNEE.

ASSIGNOR is the owner of certain patents and patent applications identified on Schedule "A" attached hereto (the "ASSIGNED PATENTS").

ASSIGNEE desires to acquire the entire right, title and interest of ASSIGNOR in and to said ASSIGNED PATENTS.

For good and valuable consideration paid by the ASSIGNEE, the receipt of which is hereby acknowledged, ASSIGNOR does hereby sell, assign and transfer to the ASSIGNEE, ASSIGNOR's entire right, title and interest in and to the ASSIGNED PATENTS, in the U.S. and every foreign country, and all patent rights, including extensions or derivations thereof, both foreign and domestic, that exist and may issue on the ASSIGNED PATENTS. ASSIGNEE hereby authorizes and requests the Commissioner of Patents that all U.S. patents that issue on the ASSIGNED PATENTS shall issue to ASSIGNEE. This assignment includes assignment to ASSIGNEE of the right to make application in its own behalf for protection of the ASSIGNED PATENTS and any patents issued on the ASSIGNED PATENTS, in the U.S. and countries foreign to the U.S., and to claim under the Patent Cooperation Treaty, the International Convention and/or other international arrangement for any such application the date of any earlier U.S. application (or any other application on the invention) to gain priority with respect to other applications. The ASSIGNED PATENTS and all patents that issue on the ASSIGNED PATENTS shall be held and enjoyed by the ASSIGNEE, its successors and assigns as fully and entirely as the same would have been held and enjoyed by the ASSIGNOR had this assignment not been made, including without limitation the exclusive right to enforce and to damages for the past, present or future infringement of such patents.

ASSIGNOR covenants and agrees to execute such further and confirmatory assignments in recordable form as the ASSIGNEE may require to vest record title of said respective ASSIGNED PATENTS in ASSIGNEE.

[Signature page follows]

IN WITNESS WHEREOF, the ASSIGNOR has caused this Assignment to be executed by a duly authorized officer.

- 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1	**		
		By: Name: Hoo Title: Chie	J.
	ACKNOWL	<u>EDGMENT</u>	
State of California County of Santa Clara)) ss:)		
On this 2nd day of September, 2 known to me or proved to me or on behalf of the corporation nan capacity.	n the basis of satisfactor	v evidence to be the:	appeared Hock E. Tan, personally person who executed this instrument uted it in such representative
IN WITNESS WHEREOF, 11	nave hereunto set my har	nd and official seal.	jen)
Notary Public		- XII	atacher C
My Commission Expires on			

[Project Maui - Patent Assignment (U.S.)]

State of California		
County of SANTA	/1 G# 1	
on 9/2/20/4 heter	The first 5 hands	CO or an an annual contract of
On		SANDU FAUA Here likerit Name and Title of the Officer
personally appeared	HOCK	E. TAN
		Name(s) of Signer(s)
		who proved to me on the basis of satisfacto
		evidence to be the person(s) whose name(s) is/a subscribed to the within instrument and acknowledge
		to me that he/she/they executed the same
SANDY FARIA	*******	his/her/their authorized capacity(ies), and that t
2 Commission # 1956		his/her/their signature(s) on the instrument the
Notary Public - Calife Santa Clara Count		person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.
My Comm. Expires Oct 1		A manda, mand, excourses no mentionests.
A STATE OF THE PROPERTY OF THE	sultrativiting.	I certify under PENALTY OF PERJURY under the
		laws of the State of California that the foregoin
		paragraph is true and correct.
		WITNESS my hand and official sear.
		Signature A T
Place Notary Seal Above	OP710	Submatings of Newson: Professional
Though the information below is r	iot required by law.	If may prove validation parents colling on the design
and could prevent traud Description of Attached Docu	sulent removal and	realischment of this form to another document.
Title or Type of Document:	PATEN	T ASSIGNMENT
December Date (Caram		~ 1 4 d
cocument pale:	Carry of Hand	O / 4 Number of Pages:
Signer(s) Other Than Named Above	20	
Capacity(ies) Claimed by Sign		
Signer's Name:		
□ Corporate Officer — Title(s): □ Individual	The state of the s	
		☐ Individual
☐ Partner — ☐ Limited ☐ General ☐ ☐ Attorney in Fact	Top of thumb here	☐ Partner — ☐ Limited ☐ General Top of thumb hars
Trustee		☐ Attorney in Fact
Guardian or Conservator		☐ Trustee
Other:		☐ Guardian or Conservator
		Other:
Signer Is Representing:		Signat in Banyananting.
		Signer is Representing:
9		

SCHEDULE A ASSIGNED PATENTS

L09-0615US1	Context Execution in a Media Controller Architecture	Nov 23, 2010	12952205	Oct 23, 2012	8296480	US	Issued
L09-0616US1	Coalescing Multiple Contexts into a Single Data Transfer in a Media Controller Architecture	Nov 23, 2010	12952201	Jun 12, 2012	8200857	US	Issued
L09-0689US1	Command Tag Checking in a Multi-Initiator Media Controller Architecture	Nov 23, 2010	12952207	Jan 8, 2013	8352689	US	Issued
L09-0690US1	Context Processing for Multiple Active Write Commands in a Media Controller Architecture	Nov 23, 2010	12952200	Nov 12, 2013	8583839	US	Issued
L12-0066US1	Memory device having collaborative filtering to reduce noise	Aug 17, 2012	13588043	Apr 29, 2014	8711620	US	Issued
L12-0099US1	<u>Multi-Tier</u>	Dec 31,	13731551			US	Pending

	Data stire and	2012				
	Detection and Decoding in Flash Memories	2012				
L12-0248US1	Detection and Decoding in Flash Memories with Selective Binary and Non-Binary	Jan 31, 2013	13755717		US	Pending
L12-0286US1	Detection and Decoding in Flash Memories Using Correlation of Neighboring Bits	Dec 31, 2012	13731766		US	Pending
L12-0326US1	Accelerated soft read for multi-level cell nonvolatile memories	Oct 15, 2012	13651975		US	Pending
L12-0484US1	Soft Decision Compensation for Flash Channel Variation	Aug 4, 2012	13567025		US	Allowed
L12-0598US1	Managing a dynamically variable cache space	Mar 23, 2012	61614548		US	Expired
L12-0598US2	System for Dynamically Adaptive Caching	Aug 3, 2012	13566204		US	Pending

	1	,	T	1			
L12-0645US1	Fast tracking for flash channels	Jun 26, 2012	13533130			US	Pending
L12-0667US1	Combined Wu and Chase Decoding of Cyclic Codes	Jul 12, 2012	13547463	Mar 18, 2014	8674860	US	Issued
L12-0668US1	Combined Koetter-Vardy and Chase Decoding of Cyclic Codes	Jun 27, 2012	13534287			US	Allowed
L12-0674US1	Mixed Granularity Higher-Level Redundancy for Non- Volatile Memory	Aug 2, 2012	13565752			US	Allowed
L12-0689US1	Lookup Engine with Pipelined Access, Speculative Add and Lock- in-Hit Function	Aug 31, 2012	13600464	Jul 8, 2014	8775727	US	Issued
L12-0704US1	Single-Read Based Soft- Decision Decoding of Non-Volatile Memory	Aug 4, 2012	13567024			US	Pending
L12-0781US1	Detection and Decoding in Flash Memories with Error Correlations	Feb 28, 2013	13780203			US	Pending

	ī	T		•		ī	
	for a Plurality of Bits within a Sliding Window						
L12-0810US1	Threshold Optimization For Flash Memory	Aug 24, 2012	13593811			US	Allowed
L12-1126US1	Flash Memory Read Error Rate Reduction	Jul 23, 2012	13555444			US	Pending
L12-1254US1	Method to Shorten Hash Chains in Lempel-Ziv Compression of Data with Repetitive Symbols	Oct 24, 2012	13659036			US	Pending
L12-1260US1	Flash Memory Read Scrub and Channel Tracking	Aug 29, 2012	13597489			US	Allowed
L12-1358US1	Flash channel parameter management with read scrub	Oct 1, 2012	13632294			US	Pending
L12-1398US1	Optimizing BitStream Encoding for Compression	Oct 15, 2012	13651627	Jul 29, 2014	8791843	US	Issued
L12-1540US1	Optimizing Compression Engine Throughput via Run Pre-	Oct 15, 2012	13651655			US	Pending

	Processing					
L12-1556US1	Inter-cell interference cancellation in flash memories	Feb 27, 2013	13778860		US	Allowed
L12-1638US1	Read Disturb Effect Determination	Nov 15, 2012	13677938		US	Pending
L12-1954US1	Reduced Complexity Reliability Computations for Flash Memories	Feb 26, 2013	13777484		US	Pending
L12-1959US1	<u>Fractional</u> <u>RAISE(TM)</u>	Aug 13, 2012	61682561		US	Expired
L12-1959US2	Fractional Redundant Array of Silicon Independent Elements	Nov 13, 2012	13675874		US	Pending
L12-2076US1	Storage Device Out-of- Space Handling	Jan 23, 2013	13748260		US	Pending
L12-2090US1	Integrated- Interleaved LDPC Codes	Jan 31, 2013	13755757		US	Pending
L12-2092US1	Cell-level statistics collection for detection and decoding in flash memories	Feb 27, 2013	13778728		US	Pending

L12-2102US1	Low-Density Parity-Check	Jan 30, 2013	13753987		US	Pending
	<u>Decoder</u> <u>Disparity</u> <u>Preprocessing</u>					
L12-2112US1	Chained, Scalable Storage Devices	Feb 12, 2013	13765253		US	Pending
L12-2232US1	Read Disturb Handling for Non-Volatile Solid State Media	Dec 28, 2012	13729966		US	Pending
L12-2283US1	Method to apply fine grain wear leveling and garbage collection	Dec 21, 2012	13724905		US	Pending
L12-2283US2	Method to apply fine grain wear leveling and garbage collection	Jan 17, 2013	13743858		US	Pending
L12-2306US1	Maintaining Ordering via a Multi-Level Map of a Solid-State Media	Sep 10, 2013	14022781		US	Pending
L12-2313US1	<u>Device Sleep</u> <u>Partitioning</u> <u>and Keys</u>	Mar 15, 2013	61789337		US	Expired
L12-2313US2	<u>Device Sleep</u> <u>Partitioning</u>	May 1, 2013	13874590		US	Pending

	and Keys					
L12-2381US1	Trim Mechanism Using Multi- Level Mapping in a Solid- State Media	Mar 14, 2013	61783555		US	Expired
L12-2381US2	Trim Mechanism Using Multi- Level Mapping in a Solid- State Media	Aug 9, 2013	13963074		US	Pending
L12-2419US1	Lempel-Ziv Data Compression with Shortened Hash Chains Based on Repetitive Patterns	Mar 14, 2013	13803946		US	Allowed
L12-2514US2	<u>Scalable</u> <u>Storage</u> <u>Protection</u>	Nov 29, 2012	13688654		US	Pending
L12-2611US1	Management of non-valid decision patterns of soft read retry	Dec 20, 2012	13721739		US	Pending
L12-2612US1	An adaptive architecture of channel detector for NAND flash channels	Feb 20, 2013	61766853		US	Expired
L12-2612US2	An adaptive architecture	Feb 25, 2013	13775559		US	Pending

	1			•		
	of channel detector for NAND flash channels					
L12-2748US1	Compensation Loop for Read Voltage Adaptation	Feb 1, 2013	13757027		US	Allowed
L12-2753US1	Unified Message- Based Communicatio ns	Mar 15, 2013	61789574		US	Expired
L12-2753US2	Unified Message- Based Communicatio ns	Apr 17, 2013	13864494		US	Pending
L12-2913US1	Non-Volatile Memory Channel Control Using a General Purpose Programmabl e Processor in Combination with a Low Level Programmabl e Seguencer	Feb 15, 2013	13768215		US	Pending
L12-2940US1	Statistical Adaptive Error Correction for FLASH	Feb 12, 2013	13765034		US	Allowed
L12-3180US1	Use Physical- to-logical Address Map speed up	Feb 28, 2013	13780229		US	Pending

	Recycle in SSD					
L12-3274US1	Self Recoverable Solid State Drive Concept and Methodology	Mar 6, 2013	61773337		US	Expired
L12-3274US2	Self Recoverable Solid State Drive Concept and Methodology	Mar 12, 2013	13796264		US	Pending
L12-3290US1	Log-Likelihood Ratio (LLR) Dampening in Low-Density Parity-Check (LDPC) Decoders	Nov 21, 2012	61729178		US	Expired
L12-3290US2	Log-Likelihood Ratio (LLR) Dampening in Low-Density Parity-Check (LDPC) Decoders	Jul 3, 2013	13934999		US	Pending
L12-3305US1	A Priori Information Based Post- Processing in Low-Density Parity-Check Code Decoders	Mar 26, 2013	61805195		US	Expired
L12-3305US2	A Priori Information Based Post- Processing in	Apr 10, 2013	13860068		US	Pending

	Ī	<u> </u>				
	Low-Density Parity-Check Code Decoders					
L12-3321US1	Host-based, Controller- Opaque Manufacturin g-Testing of NVM	Jan 25, 2013	13750200		US	Pending
L12-3397US1	Partial R-Block Recycling	Mar 6, 2013	61773437		US	Expired
L12-3397US2	Partial R-Block Recycling	Mar 7, 2013	13788303		US	Pending
L12-3607US1	Policy for flash memory read operations	Dec 11, 2012	61735908		US	Expired
L12-3607US2	Policy for flash memory read operations	Mar 14, 2013	13804671		US	Pending
L12-3608US1	Method of optimizing solid state drive soft retry voltages	Mar 25, 2013	61804853		US	Expired
L12-3608US2	Method of optimizing solid state drive soft retry voltages	Apr 3, 2013	13856179		US	Pending
L12-3609US1	Dynamic LLR guantization for SSD controllers	Mar 22, 2013	61804265		US	Expired
L12-3609US2	<u>Dynamic Log</u> <u>Likelihood</u>	Mar 29, 2013	13853282		US	Pending

	·					
	Ratio Quantization for Solid State Drive Controllers					
L12-3617US1	A Recovery Strategy that Reduces Errors Misidentified as Reliable	Mar 14, 2013	13804495		US	Pending
L12-3619US1	Error correction code rate management for flash memory	Dec 11, 2012	61735917		US	Expired
L12-3619US2	Error correction code rate management for flash memory	Mar 13, 2013	13798696		US	Pending
L12-3620US1	Flash memory read error recovery policy with soft-decision LDPC decoder	Dec 11, 2012	61735923		US	Expired
L12-3620US2	Flash memory read error recovery policy with soft-decision LDPC decoder	Mar 13, 2013	13798864		US	Pending
L12-3679US1	Retention detection and channel tracking policy	Mar 12, 2013	61777048		US	Expired

	in floats					
	<u>in flash</u> memory					
	based storage					
	<u>systems</u>					
L12-3679US2	Retention detection and channel tracking policy in flash memory based storage systems	Mar 15, 2013	13832633		US	Pending
L12-3685US1	Generating Partially Sparse Generator Matrix for a Quasi-Cyclic Low-Density Parity-Check Encoder	Mar 26, 2013	61805270		US	Expired
L12-3685US2	Generating Partially Sparse Generator Matrix for a Quasi-Cyclic Low-Density Parity-Check Encoder	Mar 28, 2013	13852852		US	Pending
L13-0088US1	Flash memory data recovery after power failure	Mar 14, 2013	61781744		US	Expired
L13-0088US2	Flash memory data recovery after power failure	Apr 1, 2013	13854263		US	Pending
L13-0102US1	<u>Storage</u>	Mar 14,	61782122		US	Expired

	Device	2013				
	Assisted Data	2013				
	<u>De-</u>					
	<u>Duplication</u>					
L13-0102US2	Storage Device Assisted Data De- Duplication	Jul 11, 2013	13939345		US	Pending
L13-0258US1	Host Command Based Read Disturb Methodology	Apr 2, 2013	13855116		US	Pending
L13-0287US1	I/O Device with Multi- Reducible- Unit Mapping	Mar 14, 2013	61782089		US	Expired
L13-0287US2	Locating Data in Non- Volatile Memory	Aug 8, 2013	13962558		US	Pending
L13-0371US1	Systems and Methods for Enhanced Data Recovery in a Solid State Memory System	Sep 26, 2013	61883123		US	Pending
L13-0371US2	Systems and Methods for Enhanced Data Recovery in a Solid System	Oct 7, 2013	14047423		US	Pending
L13-0547US1	A Novel Flash Translation	Apr 30, 2013	61817441		US	Expired

	1 m. m. 1920					
	<u>Layer with</u> Lower Write					
	Amplification					
L13-0547US2	A Novel Flash Translation Layer with Lower Write Amplification	May 8, 2013	13889521		US	Pending
L13-0699US1	Systems and Methods for Inter-cell Interference Mitigation in a Flash Memory	May 22, 2013	61826464		US	Expired
L13-0699US2	Systems and Methods for Inter-cell Interference Mitigation in a Flash Memory	Jun 6, 2013	13912063		US	Pending
L13-0720US1	Method and Apparatus for Generation of Soft Decision Error Correction Code Information	Mar 14, 2013	61782837		US	Expired
L13-0720US2	Method and Apparatus for Generation of Soft Decision Error Correction Code Information	May 14, 2013	13893832		US	Pending
L13-0741US1	Method to Protect Data in DRAM	May 29, 2013	61828425		US	Expired

	Ī	ī		T	ī	
L13-0741US2	Method to Protect Data in DRAM	Jun 6, 2013	13911443		US	Pending
L13-0765US1	Data Decoder with Trapping Set Flip Bit Mapper	Jul 19, 2013	61856359		US	Expired
L13-0765US2	<u>Data Decoder</u> with Trapping <u>Set Flip Bit</u> <u>Mapper</u>	Aug 2, 2013	13958162		US	Pending
L13-0781US1	Elastic RAISE (TM)	May 24, 2013	61827190		US	Expired
L13-0781US2	Elastic RAISE (TM)	May 29, 2013	13904362		US	Pending
L13-0835US1	Detection/Era sure of Random Write Errors using Converged Hard Decisions	Nov 11, 2013	61902450		US	Pending
L13-0835US2	Detection/Era sure of Random Write Errors using Converged Hard Decisions	Jan 9, 2014	14150810		US	Pending
L13-0853US1	Incremental programming pulse optimization to reduce write errors	Nov 11, 2013	61902380		US	Pending
L13-0853US2	Incremental	Jan 8, 2014	14150482		US	Pending

	T	ī -		1		
	programming pulse optimization to reduce write errors					
L13-0863US1	Flash Memory Reference Voltage Detection with Tracking of Cross- Points of Cell Voltage Distributions Using Histograms	Sep 27, 2013	61883491		US	Pending
L13-0863US2	Flash Memory Reference Voltage Detection with Tracking of Cross- Points of Cell Voltage Distributions Using Histograms	Oct 21, 2013	14059229		US	Pending
L13-0895US1	Shortening of Polar Codes	Sep 16, 2013	61878163		US	Pending
L13-0895US2	Shortening of Polar Codes	Sep 23, 2013	14033854		US	Pending
L13-0920US1	Flash Memory Read Retry Using Histograms	Oct 3, 2013	61886180		US	Pending
L13-0920US2	Flash Memory Read Retry Using Histograms	Oct 11, 2013	14052252		US	Pending

L13-0922US1	Generating Soft Decoding Information for Flash Memory Error Correction Using Hard Decision Patterns	Oct 17, 2013	61892328		US	Pending
L13-0922US2	Generating Soft Decoding Information for Flash Memory Error Correction Using Hard Decision Patterns	Oct 28, 2013	14064524		US	Pending
L13-0923US1	Reduction or Elimination of Latency Penalty Associated with Adjusting Read Thresholds for Non-Volatile Memory	Nov 1, 2013	61898947		US	Pending
L13-0923US2	Reduction or Elimination of a Latency Penalty Associated with Adjusting Read Thresholds for Non-Volatile Memory	Nov 22, 2013	14087520		US	Pending
L13-0928US1	Virtual Bands Concentration	Aug 29, 2013	61870936		US	Pending

	I	1		1		
	(VBC) Method for Self Encrypting Drives					
L13-0928US2	Virtual Bands Concentration (VBC) Method for Self Encrypting Drives	Sep 23, 2013	14033753		US	Pending
L13-0932US1	Eliminating or Reducing Programming Errors When Programming Flash Memory Cells	Nov 26, 2013	61909211		US	Pending
L13-0932US2	Eliminating or Reducing Programming Errors When Programming Flash Memory Cells	Dec 3, 2013	14094900		US	Pending
L13-0936US1	Preventing Programming Errors from Occurring When Programming Flash Memory Cells	Dec 20, 2013	61918778		US	Pending
L13-0936US2	Preventing Programming Errors from Occurring When Programming Flash Memory Cells	Jan 6, 2014	14147825		US	Pending

L13-0953US1	Active	Nov 15,	14080950		US	Pending
	Recycling for Solid State Drive	2013	555550			· c.iuiiig
L13-0993US1	A Method of Erase State Handling in NAND Flash Channel Tracking	Sep 18, 2013	61879318		US	Pending
L13-0993US2	A Method of Erase State Handling in NAND Flash Channel Tracking	Sep 30, 2013	14041110		US	Pending
L13-1021US1	Non-Volatile Memory with Channel Tracking- Based Retry Read Voltage	Nov 27, 2013	61909447		US	Pending
L13-1021US2	Non-Volatile Memory with Channel Tracking- Based Retry Read Voltage	Dec 20, 2013	14135837		US	Pending
L13-1025US1	Systems and Methods for Latency Based Data Recycling in a Solid State Memory System	Oct 17, 2013	61892429		US	Pending
L13-1025US2	Systems and Methods for Latency Based	Nov 5, 2013	14072530		US	Pending

	T	,	1			
	Data Recycling in a Solid State Memory System					
L13-1043US1	Non-Uniform Channel LLR Quantization for SSD Controllers	Oct 21, 2013	61893335		US	Pending
L13-1043US2	Systems and Methods for Soft Data Utilization in a Solid State Memory System	Nov 5, 2013	14072574		US	Pending
L13-1045US1	Method to Implement Unaligned Data Bands for Self Encrypting SSDs	Oct 30, 2013	61897366		US	Pending
L13-1045US2	Method to Implement Unaligned Data Bands for Self Encrypting SSDs	Nov 22, 2013	14088143		US	Pending
L13-1048US1	Mitigating Write Errors in MLC FLASH through adaptive ECC decoding	Feb 11, 2014	61938292		US	Pending
L13-1048US2	Mitigating Write Errors in MLC FLASH	Feb 28, 2014	14194180		US	Pending

	diama . C					
	through adaptive ECC					
	decoding					
L13-1051US1	A LLR Generation Scheme Using Pre-Defined (fixed) LLR LUTs for SSD Controllers	Nov 27, 2013	61909529		US	Pending
L13-1051US2	A LLR Generation Scheme Using Pre-Defined (fixed) LLR LUTs for SSD Controllers	Dec 20, 2013	14136380		US	Pending
L13-1085US1	Tracking and Utilizing Second Level Map Index for Recycling of Solid State Drive Blocks	Nov 27, 2013	14092285		US	Pending
L13-1088US1	Flash Channel with Selective Decoder Likelihood Dampening	Nov 28, 2013	61910100		US	Pending
L13-1088US2	Flash Channel with Selective Decoder Likelihood Dampening	Nov 29, 2013	14093110		US	Pending
L13-1096US1	Capacitance Coupling Parameter Estimation in flash	Jan 6, 2014	61923915		US	Pending

	memories					
L13-1096US2	Capacitance Coupling Parameter Estimation in flash memories	Jan 15, 2014	14155687		US	Pending
L13-1100US1	A Variable: Size Flash Translation Layer Optimized for Sequential Read	Aug 16, 2013	61866672		US	Expired
L13-1100US2	A Variable- Size Flash Translation Layer Optimized for Sequential Read	Oct 9, 2013	61888681		US	Pending
L13-1100US3	A Variable- Size Flash Translation Layer Optimized for Sequential Read	Oct 16, 2013	14055336		US	Pending
L13-1115US1	Systems and Methods for Suz-Zero Threshold Characterizati on in a Memory Cell	Oct 28, 2013	61896590		US	Pending
L13-1115US2	Systems and Methods for Suz-Zero Threshold	Nov 20, 2013	14085553		US	Pending

	64			1		
	<u>Characterizati</u>					
	<u>on in a</u> Memory Cell					
L13-1128US1	Systems and	Nov 16,	61905185		US	Pending
	Methods for Soft Decision Generation in a Solid State Memory System	2013				
L13-1128US2	Systems and Methods for Soft Decision Generation in a Solid State Memory System	Dec 16, 2013	14108226		US	Pending
L13-1144US1	TRANSLATION LAYER PARTITIONED BETWEEN A HOST AND A CONTROLLER	Sep 3, 2013	61873357		US	Pending
L13-1144US2	TRANSLATION LAYER PARTITIONED BETWEEN A HOST AND A CONTROLLER	Oct 21, 2013	61893383		US	Pending
L13-1144US3	TRANSLATION LAYER PARTITIONED BETWEEN A HOST AND A CONTROLLER	Oct 23, 2013	14060799		US	Pending
L13-1158US1	Dynamic Per- Decoder Control of LLR and Decoding	Nov 11, 2013	61902407		US	Pending

	Parameters					
L13-1158US2	Dynamic Per- Decoder Control of LLR and Decoding Parameters	Nov 27, 2013	14092215		US	Pending
L13-1174US1	Using unsatisfied parity checks in an LDPC- encoded storage systems as quality metric	Nov 7, 2013	61901064		US	Pending
L13-1174US2	Using unsatisfied parity checks in an LDPC- encoded storage systems as guality metric	Dec 10, 2013	14101679		US	Pending
L13-1183US1	Interleaving Codewords over Multiple Flash Planes for Increased Reliability and Predictable High Performance	Jan 13, 2014	61926516		US	Pending
L13-1183US2	Interleaving Codewords over Multiple Flash Planes for Increased Reliability and Predictable High Performance	Jan 16, 2014	14156693		US	Pending

L13-1192US1	Decoding System and Method for Electronic Non-Volatile Computer Storage Apparatus	Feb 7, 2014	61937040		US	Pending
L13-1192US2	Decoding System and Method for Electronic Non-Volatile Computer Storage Apparatus	Mar 7, 2014	14200659		US	Pending
L13-1205US1	Multi-read retry for NVM read channels in the presence of a limited read window	Dec 10, 2013	61914056		US	Pending
L13-1205US2	Multi-read retry for NVM read channels in the presence of a limited read window	Dec 20, 2013	14136283		US	Pending
L13-1206US1	Bit-line defect detection using unsatisfied parity checks in an LDPC- encoded storage systems	Nov 27, 2013	61909540		US	Pending

L13-1206US2	Bit-line defect detection using unsatisfied parity checks in an LDPC- encoded storage systems	Dec 9, 2013	14100280		US	Pending
L13-1215US1	A New Method to Distribute RAISE R-Page over Different Page Types by Leveraging Their Error Rates Variation	Dec 20, 2013	61918952		US	Pending
L13-1215US2	A New Method to Distribute RAISE R-Page over Different Page Types by Leveraging Their Error Rates Variation	Feb 5, 2014	14173108		US	Pending
L13-1219US1	Systems and Methods for Differential Message Scaling in a Decoding Proces	Apr 3, 2014	61974894		US	Pending
L13-1219US2	Systems and Methods for Differential Message	Apr 24, 2014	14261333		US	Pending

	Scaling in a					
	Decoding Proces					
L13-1248US1	Methods for soft decoding of polar codes	Mar 6, 2014	61948777		US	Pending
L13-1248US2	Methods for soft decoding of polar codes	Mar 19, 2014	14219279		US	Pending
L13-1252US1	General Framework for Collecting Statistics from Soft Reads	Jan 13, 2014	61926488		US	Pending
L13-1252US2	General Framework for Collecting Statistics from Soft Reads	Feb 17, 2014	14181893		US	Pending
L13-1257US1	Refresh, Run, Aggregate Decoder Recovery	Feb 18, 2014	61941378		US	Pending
L13-1257US2	Refresh, Run, Aggregate Decoder Recovery	Mar 18, 2014	14218626		US	Pending
L13-1298US1	Systems and Methods for Hard Error Reduction in a Solid State Memory Device	Feb 6, 2014	61936599		US	Pending
L13-1298US2	Systems and Methods for Hard Error	Feb 11, 2014	14178201		US	Pending

	1	1		1		
	Reduction in a Solid State Memory Device					
L13-1301US1	Systems and Methods for Last Written Page Handling in a Memory Device	Feb 11, 2014	61938241		US	Pending
L13-1301US2	Systems and Methods for Last Written Page Handling in a Memory Device	Feb 27, 2014	14192831		US	Pending
L13-1307US1	Improve System Performance via Control the Threshold Voltage Distribution Width of Hotread Data	Feb 17, 2014	61940561		US	Pending
L13-1307US2	Improve System Performance via Control the Threshold Voltage Distribution Width of Hot- read Data	Feb 26, 2014	14191263		US	Pending
L13-1314US1	Periodically Updating a Log Likelihood Ratio (LLR) Table in a Flash Memory	Feb 27, 2014	61945268		US	Pending

	Controller					
L13-1314US2	Periodically Updating a Log Likelihood Ratio (LLR) Table in a Flash Memory Controller	Mar 6, 2014	14198862		US	Pending
L13-1388US1	Hot-read Data Aggregation and Code Selection	Feb 14, 2014	61938936		US	Pending
L13-1388US2	Hot-read Data Aggregation and Code Selection	Feb 27, 2014	14192110		US	Pending
L13-1428US1	A method of adjusting LLR values to compensate misplacement of read voltages	Feb 26, 2014	61944696		US	Pending
L13-1428US2	A method of adjusting LLR values to compensate misplacement of read voltages	Mar 3, 2014	14195058		US	Pending
L14-0030US1	Methods and Systems for Reducing Decoder Error Floor for an Electronic Non-Volatile Computer Storage	Apr 9, 2014	61977399		US	Pending

L14-0030US2	Methods and Systems for Reducing Decoder Error Floor for an Electronic Non-Volatile Computer Storage	May 9, 2014	14273719		US	Pending
L14-0038US1	ECC Selection in NAND flash Controller with Multiple ECCs	Apr 4, 2014	61975202		US	Pending
L14-0038US2	ECC Selection in NAND flash Controller with Multiple ECCs	Apr 29, 2014	14264419		US	Pending
L14-0050US1	Manufacturer self-test for solid-state drives	Mar 24, 2014	14223407		US	Pending
L14-0055US1	Estimating Read Reference Voltage Based on Disparity and Derivative Metrics	Jun 9, 2014	14299195		US	Pending
L14-0062US1	Online Histogram and Soft- information Learning	Apr 10, 2014	14249714		US	Pending
L14-0075US1	Read operation policy for	Apr 8, 2014	61976854		US	Pending

				Г		
	system data during					
	booting up of					
	SSD drives					
L14-0075US2	Read operation policy for system data during booting up of SSD drives	May 5, 2014	14269590		US	Pending
L14-0080US1	Bad die detection in solid state drives	Apr 2, 2014	61974078		US	Pending
L14-0080US2	Bad die detection in solid state drives	Apr 28, 2014	14263189		US	Pending
L14-0087US1	Soft Read Handling of Read Noise	Apr 10, 2014	14249450		US	Pending
L14-0109US1	Data Recovery once ECC Fails to Correct the Data	Apr 29, 2014	61985610		US	Pending
L14-0109US2	Data Recovery once ECC Fails to Correct the Data	May 9, 2014	14273920		US	Pending
L14-0184US1	Fixed point conversion of LLR values based on correlation	May 20, 2014	14282380		US	Pending
L14-0197US1	A New Flash Command	May 22, 2014	14284875		US	Pending

	1				1	
	that Reports Count of Cell Program Failures					
L14-0236US1	A method of dynamically updating LLRs for SSDs	May 19, 2014	14280884		US	Pending
L14-0242US1	On-Die Flash ECC to mitigate write errors in multi step programming of MLC and TLC memories	Jun 19, 2014	14308911		US	Pending
L14-0243US1	Directed LLR perturbation in layered LDPC decoders	Jul 2, 2014	14322376		US	Pending
L14-0244US1	Inter-cell interference estimation based on pattern dependent histogram	Jun 16, 2014	14305293		US	Pending
L14-0245US1	Cell-to-cell Program Interference Aware Data Recovery when ECC Fails with Optimum Reading	Jun 16, 2014	14305208		US	Pending
L14-0273US1	Methods for write mapping	Jun 23, 2014	14311645		US	Pending

	T	ı	1		1		,
	to handle						
	hard errors in soft ECC						
SAF1P001	SYSTEM, METHOD. AND COMPUTER PROGRAM PRODUCT FOR DELAYING AN OPERATION THAT REDUCES A LIFETIME OF MEMORY	Sep 7, 2007	11852082	Oct 5, 2010	7809900	US	Issued
SAF1P001+	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR DELAYING AN OPERATION THAT REDUCES A UFETIME OF MEMORY	Nov 20, 2006	60860843			US	Expired
SAF1P002	SYSTEM. METHOD. AND COMPUTER PROGRAM PRODUCT FOR REDUCING MEMORY WRITE OPERATIONS USING DIFFERENCE INFORMATIO N	Sep 7, 2007	11852104	Mar 8, 2011	7904619	US	Issued

SAF1P002+	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR REDUCING MEMORY WRITE OPERATIONS USING DIFFERENCE INFORMATIO N	Dec 4, 2006	60873630			US	Expired
SAF1P003	MULTI- MEMORY DEVICE SYSTEM AND METHOD FOR MANAGING A LIFETIME THEREOF	Sep 7, 2007	11852117	Jun 29, 2010	7747813	US	Issued
SAF1P003+	MULTI- MEMORY DEVICE SYSTEM AND METHOD FOR MANAGING A LIFETIME THEREOF	Jan 7, 2007	60878242			US	Expired
SAF1P004	MEMORY LIFETIME GAUGING SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT	Sep 7, 2007	11852129	Mar 8, 2011	7904764	US	Issued
SAF1P006	SYSTEM, METHOD,	Nov 19, 2007	11942623	Jan 3, 2012	8090980	US	Issued

	AND COMPUTER PROGRAM PRODUCT FOR PROVIDING DATA REDUNDANCY IN A PLURALITY OF STORAGE DEVICES						
SAF1P008	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR INCREASING SPARE SPACE IN MEMORY TO EXTEND A LIFETIME OF THE MEMORY	Nov 28, 2007	11946753			US	Pending
SAF1P009	SYSTEM. METHOD. AND COMPUTER PROGRAM PRODUCT FOR INCREASING A LIFETIME OF A PLURALITY OF BLOCKS OF MEMORY	Nov 19, 2007	11942631	Mar 8, 2011	7903486	US	Issued
SAF1P010	SYSTEM AND METHOD FOR PROVIDING DATA REDUNDANCY AFTER REDUCING MEMORY	Nov 19, 2007	11942629	Mar 8, 2011	7904672	US	Issued

	WRITES						
SAF1P011	SYSTEM, METHOD AND A COMPUTER PROGRAM PRODUCT FOR WRITING DATA TO DIFFERENT STORAGE DEVICES BASED ON WRITE FREQUENCY	Nov 19, 2007	11942640	Dec 7, 2010	7849275	US	Issued
SAF1P014	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR PROVIDING AN EXTENDED CAPABILITY TO A SYSTEM	Jul 15, 2008	12173631			US	Pending
SAF1P015	SYSTEM, METHOD. AND COMPUTER PROGRAM PRODUCT FOR TRANSPAREN I COMMUNICA TION BETWEEN A STORAGE DEVICE AND AN APPLICATION	Jul 15, 2008	12173643			US	Pending
SAF1P016	SYSTEM, METHOD,	Jul 15, 2008	12173654			US	Abando ned

	AND COMPUTER PROGRAM PRODUCT FOR INTERFACING COMPUTING DEVICE HARDWARE OF A COMPUTING DEVICE AND AN OPERATING SYSTEM UTILIZING A VIRTUALIZATI ON LAYER					
SAF1P018	SYSTEM. METHOD. AND COMPUTER PROGRAM PRODUCT FOR CONVERTING DATA IN A BINARY REPRESENTAT ION TO A NON-POWER OF TWO REPRESENTAT ION	Sep 9, 2008	12207379		US	Pending
SAF1P019	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR RENDERING AT LEAST A PORTION OF DATA USELESS IN	Dec 8, 2008	12330183		US	Pending

	T						
	IMMEDIATE RESPONSE TO						
	<u>A DELETE</u> COMMAND						
SAF1P025	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR CONVERTING LOGICAL BLOCK ADDRESS DE- ALLOCATION INFORMATIO N IN A FIRST FORMAT TO A SECOND FORMAT	Mar 27, 2009	12413307	Jan 3, 2012	8090905	US	Issued
SAF1P026	SYSTEM. METHOD. AND COMPUTER PROGRAM PRODUCT FOR SENDING LOGICAL BLOCK ADDRESS DE- ALLOCATION STATUS INFORMATIO N	Mar 27, 2009	12413312	Jul 24, 2012	8230159	US	Issued
SAF1P027	SYSTEM. METHOD. AND COMPUTER PROGRAM PRODUCT FOR HARDENING DATA STORED	Mar 27, 2009	12413329			US	Abando ned

	ON A COUR						
	ON A SOLID						
	STATE DISK						
SAF1P028	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR ORDERING A PLURALITY OF WRITE COMMANDS ASSOCIATED WITH A STORAGE DEVICE	Jul 2, 2009	12497327			US	Abando ned
SAF1P029	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR INTERFACING ONE OR MORE STORAGE DEVICES WITH A PLURALITY OF BRIDGE CHIPS	Jul 6, 2009	12498162			US	Pending
SAF1P031	SYSTEM. METHOD. AND COMPUTER PROGRAM PRODUCT FOR INSERTING A GAP IN INFORMATIO N SENT FROM A DRIVE TO A HOST DEVICE	Jul 17, 2009	12505333	Mar 20, 2012	8140712	US	Issued

SAF1P032	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR REDUCING A RATE OF DATA TRANSFER TO AT LEAST A PORTION OF MEMORY	Jul 20, 2009	12506068	Aug 20, 2013	8516166	US	Issued
SAF1P033	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR MAINTAINING A DIRECT CONNECTION BETWEEN AN INITIATOR AND A DRIVE	Jul 23, 2009	12508259			US	Abando ned
SAF1P034	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR SENDING FAILURE INFORMATIO N FROM A SERIAL ATA (SATA) SOLID STATE DRIVE (SSD) TO A HOST DEVICE	Oct 5, 2009	12573792	Jan 31, 2012	8108737	US	Issued
SF-001-1	DELAYING AN OPERATION	Jul 26, 2010	12843851	Jul 24, 2012	8230183	US	Issued

	•						_
	THAT REDUCES A UFETIME OF MEMORY						
SF-002-1	TECHNIQUES FOR REDUCING MEMORY WRITE OPERATIONS USING COALESCING MEMORY BUFFERS AND DIFFERENCE INFORMATIO N	Mar 7, 2011	13042231	Mar 19, 2013	8402184	US	Issued
SF-002-2	TECHNIQUES FOR REDUCING MEMORY WRITE OPERATIONS USING COALESCING MEMORY BUFFERS AND DIFFERENCE INFORMATIO N	Mar 15, 2013	13840609	Mar 11, 2014	8671233	US	Issued
SF-002-3	TECHNIQUES FOR REDUCING MEMORY WRITE OPERATIONS USING COALESCING MEMORY BUFFERS AND DIFFERENCE INFORMATIO	Mar 7, 2014	14201020			US	Pending

	N						
SF-003-1	MULTI- MEMORY DEVICE SYSTEM AND METHOD FOR MANAGING A LIFETIME THEREOF	Jun 24, 2010	12822442	Jul 24, 2012	8230164	US	Issued
SF-004-1	MEMORY LIFETIME GAUGING SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT	Mar 7, 2011	13042237	May 1, 2012	8171356	US	Issued
SF-006-1	TECHNIQUES FOR PROVIDING DATA REDUNDANCY IN A PLURALITY OF STORAGE DEVICES	Dec 31, 2011	13342027			US	Pending
SF-009-1	Techniques For Increasing A Lifetime of Blocks of Memory	Mar 7, 2011	13042242	Dec 25, 2012	8339881	US	Issued
SF-009-2	Techniques For Increasing A Lifetime of Blocks of Memory	Aug 10, 2012	13572257	Sep 10, 2013	8531900	US	Issued
SF-009-3	Techniques For Increasing A Lifetime of	Aug 8, 2013	13962779	Apr 15, 2014	8699287	US	Issued

	Blocks of						
	Memory						
SF-009-4	Techniques For Increasing A Lifetime of Blocks of Memory	Apr 8, 2014	14247399			US	Pending
SF-010-1	Techniques for providing data redundancy after reducing memory writes	Mar 7, 2011	13042252	Aug 6, 2013	8504783	US	Issued
SF-010-2	Techniques for providing data redundancy after reducing memory writes	Jul 16, 2013	13943446	May 13, 2014	8725960	US	Issued
SF-010-2US2	Techniques for providing data redundancy after reducing memory writes	May 11, 2014	14274769			US	Pending
SF-011-1	SYSTEM. METHOD AND A COMPUTER PROGRAM PRODUCT FOR WRITING DATA TO DIFFERENT STORAGE DEVICES BASED ON WRITE	Nov 30, 2010	12956926	Jul 24, 2012	8230184	US	Issued

	FREQUENCY						
SF-025-1	LOGICAL BLOCK ADDRESS DE- ALLOCATION INFORMATIO N FORMAT CONVERSION TECHNIQUES	Jan 1, 2012	13342099			US	Abando ned
SF-031-1	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR INSERTING A GAP IN INFORMATIO N SENT FROM A DRIVE TO A HOST DEVICE	Mar 19, 2012	13423808	Apr 16, 2013	8423680	US	Issued
SF-031-2	SYSTEM, METHOD. AND COMPUTER PROGRAM PRODUCT FOR INSERTING A GAP IN INFORMATIO N SENT FROM A DRIVE TO A HOST DEVICE	Apr 7, 2013	13858064	Dec 24, 2013	8615609	US	Issued
SF-031-3	SYSTEM. METHOD. AND COMPUTER PROGRAM PRODUCT FOR INSERTING A GAP IN	Dec 12, 2013	14104468			US	Pending

	T	,	T	1		T	
	INFORMATIO N SENT FROM A DRIVE TO A HOST DEVICE						
SF-032-1	Techniques for Reducing a Rate of Data Transfer to at Least a Portion of Memory	Aug 14, 2013	13966457			US	Pending
SF-034-1	Sending Failure Information From A Solid State Drive (SSD) To A Host Device	Jan 30, 2012	13361654	May 28, 2013	8453018	US	Issued
SF-034-2	Sending Failure Information From A Solid State Drive (SSD) To A Host Device	May 22, 2013	13899780	Jul 15, 2014	8782473	US	Issued
SF-10-01	LDPC Erasure Decoding for Flash Memories	Mar 12, 2010	61313681			US	Expired
SF-10-01BUS	LDPC Erasure Decoding for Flash Memories	Mar 11, 2011	13583617			US	Pending
SF-10-02	Accessing Compressed Data of Varying-Sized Quanta in Non-Volatile	Mar 22, 2010	61316373			US	Expired

	Memory						
SF-10-025US	STORAGE SYSTEM LOGICAL BLOCK ADDRESS DE- ALLOCATION MANAGEMEN I	Dec 12, 2011	13260709	Mar 11, 2014	8671258	US	Issued
SF-10- 025US2	STORAGE SYSTEM DATA HARDENING	Dec 31, 2012	13732318	Mar 11, 2014	8671259	US	Issued
SF-10- 025US3	STORAGE SYSTEM LOGICAL BLOCK ADDRESS DE- ALLOCATION MANAGEMEN T AND DATA HARDENING	Mar 7, 2014	14201058			US	Pending
SF-10-028US	SYSTEM, METHOD, AND COMPUTER PROGRAM PRODUCT FOR ORDERING A PLURALITY OF WRITE COMMANDS ASSOCIATED WITH A STORAGE DEVICE	Dec 20, 2011	13379607			US	Pending
SF-10-02US	Accessing Compressed Data of Varying-Sized Quanta in	Mar 21, 2011	13053175			US	Pending

	Non Unintila						
	Non-Volatile						
SF-10-03	Adaptive ECC Techniques for Flash Memory Based Data	Oct 27, 2010	61407178			US	Expired
SF-10-03US	Storage ADAPTIVE ECC TECHNIQUES FOR FLASH MEMORY BASED DATA	Apr 12, 2013	13879383			US	Pending
SF-10-04	STORAGE Cross- Decoding for Non-Volatile Storage	Dec 12, 2010	61422198			US	Expired
SF-10-04US	Cross- Decoding for Non-Volatile Storage	Dec 12, 2011	13323769	May 6, 2014	8719663	US	Issued
SF-10-04US2	Cross- Decoding for Non-Volatile Storage	May 1, 2014	14267814			US	Pending
SF-10-05	SCALABLE STORAGE DEVICES	Jun 18, 2010	61356443			US	Expired
SF-10-05B	SCALABLE STORAGE DEVICES	Jun 16, 2011	61497525			US	Expired
SF-10-05-US2	SCALABLE STORAGE DEVICES	Dec 7, 2012	13702976	Mar 18, 2014	8677068	US	Issued
SF-10-05-US3	<u>SCALABLE</u>	Mar 4, 2014	14197010			US	Pending

	CTODACE					
	STORAGE DEVICES					
	<u>DEVICES</u>					
SF-10-06	SELF- JOURNALING AND HIERARCHICAL CONSISTENCY FOR NON- VOLATILE STORAGE	Oct 5, 2011	61543707		US	Expired
SF-10-06-PCT	SELF- JOURNALING AND HIERARCHICAL CONSISTENCY FOR NON- YOLATILE STORAGE	Oct 4, 2012	PCTUS20120585 83		US	Pending
SF-10-06US	SELF- JOURNALING AND HIERARCHICAL CONSISTENCY FOR NON- VOLATILE STORAGE	May 17, 2013			US	Pending
SF-10-08	Encrypted- Transport Solid-State Disk Controller	Apr 29, 2011	61480518		US	Expired
SF-10-08US1	Encrypted- Transport Solid-State Disk Controller	Oct 17, 2013	14112322		US	Pending
SF-10-09	Adaptive ECC Techniques for Flash	Sep 15, 2010	61383017		US	Expired

				1		
	<u>Memory</u> Based Data					
	Storage					
SF-10-09NP	Encryption Key Destruction For Secure Data Erasure	Sep 15, 2011	13234134		US	Pending
SF-10-10	DYNAMIC HIGHER-LEVEL REDUNDANCY MODE MANAGEMEN T WITH INDEPENDENT SILICON ELEMENTS	Dec 1, 2010	61418846		US	Expired
SF-10-10US	DYNAMIC HIGHER-LEVEL REDUNDANCY MODE MANAGEMEN T WITH INDEPENDENT SILICON ELEMENTS	May 24, 2013	13989659		US	Pending
SF-10-11US1	Non- Deterministic Encryption	Jan 23, 2013	61755951		US	Expired
SF-10-11US2	Non- Deterministic Encryption	Jan 20, 2014	14158955		US	Pending
SF-10-12US1	Storage Address Space to NVM Address, Span, and Length Mapping/Con verting	Jan 22, 2013	61755169		US	Expired

SF-10-12US2	<u>Storage</u>	Jan 17, 2014	14158380			US	Pending
	Address Space to NVM Address, Span, and Length Mapping/Con verting						
SF-10-13US1	<u>Tamper</u> <u>Sensor</u>	Mar 14, 2013				US	Expired
SF-10-13US2	<u>Tamper</u> <u>Sensor</u>	Mar 6, 2014	14199761			US	Pending
SF-10-14	Higher-Level Redundancy Information Computation	Jan 18, 2011	61433918			US	Expired
SF-10-14US	Higher-Level Redundancy Information Computation	Jul 15, 2013	13979805	Feb 18, 2014	8656101	US	Issued
SF-10-14US2	Higher-Level Redundancy Information Computation	Feb 14, 2014	14181252			US	Pending
SF-11-01US1	Selective Data Banding	Jan 22, 2013	61755442			US	Expired
SF-11-01US2	Management of and Region Selection for Writes to Non-Volatile Memory	Jan 19, 2014	14158827			US	Pending
SF-11-02US1	Zero/One Balance Management in a Solid- State Disk	May 4, 2012	13464433			US	Pending

	Controller					
SF-11-03US1	Scalable Data Structures for Control and Management of Non- Volatile Storage	Oct 15, 2012	13652301		US	Pending
SF-11-04	Variable Over- Provisioning for Non- Volatile Storage	Apr 26, 2011	61479249		US	Expired
SF-11-04US1	Variable Over- Provisioning for Non- Volatile Storage	Nov 11, 2013	14112221		US	Pending
SF-11-05	Selective Enablement Of Status Coalescing Via Host Transfer Rate Detection	Feb 8, 2011	61440710		US	Expired
SF-11-05US2	Selective Enablement Of Status Coalescing Via Host Transfer Rate Detection	Aug 1, 2013	13983251		US	Pending
SF-11-07US1	<u>Device Power</u> <u>Control</u>	Mar 14, 2013	61786110		US	Expired
SF-11-08US1	Storage Device Power Failure Infrastructure	Mar 10, 2013			US	Expired

SF-11-08US2	Storage Device Power Failure Infrastructure	Mar 7, 2014	14201106		US	Pending
SF-11-09US1	Enhanced Queue Management	Mar 14, 2013			US	Expired
SF-11-09US2	Enhanced Queue Management	Mar 13, 2014	14209028		US	Pending
SF-11-10US1	Lock-Free Communicatio n Storage Request Reordering	Mar 14, 2013	61786170		US	Expired
SF-11-10US2	Lock-Free Communicatio n Storage Request Reordering	Mar 6, 2014	14199691		US	Pending
SF-11-11	Techniques For Secure Storage Hijacking Protection	Jul 27, 2011	61511989		US	Expired
SF-11-11US1	Techniques For Secure Storage Hijacking Protection	Jul 26, 2012	14131665		US	Pending
SF-11-12	Management Of Device Firmware Update Effects As Seen By A Host	Jun 7, 2011	61494262		US	Expired

SF-11-12US1	Management Of Device Firmware Update Effects As Seen By A Host	Nov 21, 2013	14119375		US	Pending
SF-11-14US1	Measuring Cell Damage for Wear Leveling in a Non-Volatile Memory	Sep 15, 2012	13620982		US	Pending
SF-11-15	I/O DEVICE AND COMPUTING HOST INTEROPERATI ON	Aug 9, 2011	61521739		US	Expired
SF-11-15A	I/O DEVICE AND COMPUTING HOST INTEROPERATI ON	Sep 6, 2011	61531551		US	Expired
SF-11-15B	I/O DEVICE AND COMPUTING HOST INTEROPERATI ON	Oct 5, 2011	61543666		US	Expired
SF-11-15-US1	I/O DEVICE AND COMPUTING HOST INTEROPERATION	Jul 5, 2013	13936010		US	Pending
SF-11-15US2	I/O DEVICE	Feb 5, 2014	14237331		US	Pending

	AND					
	COMPUTING HOST INTEROPERATI					
SF-11-16US1	<u>Decoupled</u> <u>Locking DMA</u> <u>Architecture</u>	Jan 31, 2013	61758770		US	Expired
SF-11-16US2	<u>Decoupled</u> <u>Locking DMA</u> <u>Architecture</u>	Jan 22, 2014	14161290		US	Pending
SF-11-17	<u>Virtual Drive</u> <u>Manufacturin</u> g <u>Model</u>	Nov 14, 2011	61599201		US	Expired
SF-11-17US	Storage Device Firmware and Manufacturin g Software	May 21, 2013	13988741		US	Pending
SF-11-17US1	<u>Virtual Drive</u> <u>Manufacturin</u> g <u>Model</u>	Mar 30, 2012	13436639		US	Pending
SF-11-18US1	Non-Volatile Memory Program Failure Recovery via Redundant Arrays	Dec 27, 2012	13728573		US	Pending
SF-11-19US1	Retention Drift Clock	Feb 10, 2013	61762955		US	Expired
SF-11-19US2	Retention Drift Clock	Jan 31, 2014	14170008		US	Pending

RECORDED: 01/21/2015