

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC	01/29/2015
RECEIVING PARTY DATA	
Name:	TESSERA ADVANCED TECHNOLOGIES, INC.
Street Address:	3025 ORCHARD PARKWAY
City:	SAN JOSE
State/Country:	CALIFORNIA
Postal Code:	95134
PROPERTY NUMBERS Total: 9	
Property Type	Number
Patent Number:	5666046
Patent Number:	5703473
Patent Number:	5900772
Patent Number:	6256702
Patent Number:	6300833
Patent Number:	6304206
Patent Number:	6525614
Patent Number:	6554469
Patent Number:	6683554
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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NAME OF SUBMITTER:	JENNIFER KOSTIC

SIGNATURE:	/Jennifer Kostic/
DATE SIGNED:	01/30/2015
Total Attachments: 5 source=2015 01 28 ON - Tessera Executed Confirmatory Patent Assignment#page1.tif source=2015 01 28 ON - Tessera Executed Confirmatory Patent Assignment#page2.tif source=2015 01 28 ON - Tessera Executed Confirmatory Patent Assignment#page3.tif source=2015 01 28 ON - Tessera Executed Confirmatory Patent Assignment#page4.tif source=2015 01 28 ON - Tessera Executed Confirmatory Patent Assignment#page5.tif	

EXHIBIT C

CONFIRMATORY ASSIGNMENT OF PATENT RIGHTS

This Assignment of Patent Rights (the "**Assignment**") is executed, acknowledged and delivered by Semiconductor Components Industries, LLC, a Delaware limited liability company having offices at 5005 E. McDowell Road, Phoenix, Arizona 85008 ("**Assignor**"), in accordance with, and pursuant to the terms and conditions of the Patent Purchase Agreement having an Effective Date of January 28, 2015 (the "**Agreement**") between Assignor and Tessera Advanced Technologies, Inc., a Delaware corporation with its principal place of business at 3025 Orchard Parkway, San Jose, California 95134 ("**Assignee**"). Capitalized terms used herein and not expressly defined shall have the meaning ascribed to such terms in the Agreement.

"**Listed Patents**" means the provisional patent applications, pending patent applications, and patents listed on **Exhibit A**.

"**Patents**" means, all (a) Listed Patents; (b) patents or patent applications (i) to which any of the Listed Patents claims priority, (ii) for which any of the Listed Patents forms a basis for priority and/or (c) reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, and registrations of any item in any of the foregoing categories (a) and (b); (d) national (of any country of origin) and multinational patents, patent applications and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention and utility models; (e) rights provided by multinational treaties or conventions for any item in any of the foregoing categories (a) through (d); and (e) any item in any of the foregoing categories (b) through (d) whether or not expressly listed as Listed Patents and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like.

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN:

For good and valuable consideration received pursuant to the Agreement, the receipt of which is hereby acknowledged, Assignor agrees to and does hereby irrevocably sell, assign, transfer and convey unto said Assignee, and Assignee hereby accepts, all of Assignor's right, title, and interest (i) in and to the Patents, the same to be held and enjoyed by said Assignee for its own use, and for the use of its successors, assigns, or other legal representatives to the end of the term or terms for which said Patents may be granted as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment had not been made; (ii) in and to causes of action and enforcement rights for the Patents including all rights to pursue damages, injunctive relief and other remedies for past and future infringement of the Patents; and (iii) to apply in any and all countries for the world for patents; certificates of invention or other governmental grants for the Patents. Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents or certificates of invention which may be granted upon any of the Patents in the name of Assignee, as the assignee to the entire interest therein.

Notwithstanding anything to the contrary herein, Assignor is executing and delivering this Assignment in accordance with and subject to all of the terms and provisions of the Agreement. In

the event of any conflict between the terms of this Assignment and those of the Agreement, the terms of the Agreement shall be controlling.

This Assignment shall be binding upon and shall inure to the benefit of the parties and their respective successors and assigns.

This Assignment shall be governed by, and construed in accordance with, the laws of the United States in respect to patent issues and in all other respects by the laws of the State of California, without giving effect to the conflict of laws rules thereof.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be executed as of this 29th day of January, 2015.

ASSIGNOR:

Semiconductor Components Industries, LLC

By: George H. Cave

Name: George H. Cave

Title: Executive Vice President and General Counsel

NOTARIZATION MUST BE ON THIS PAGE

State of Arizona)

)

County of Maricopa)

On this 29th day of January, 2015, before me personally appeared **George H. Cave**, whose identity was proven to me on the basis of satisfactory evidence to be the person who he/she claims to be, and acknowledged that he/she signed the above/attached document.

(seal)

Rebecca Newnam
Notary Public

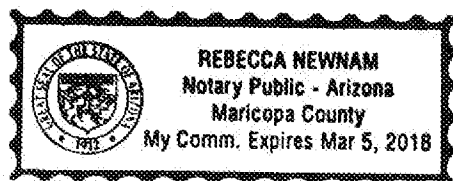


EXHIBIT A
LISTED PATENTS

US Issued Patents

Patent Number	Filing Date	Application Number	Issue Date	Publication Number	Title
5666046	08/24/1995	08/518,768	09/09/1997		Reference voltage circuit having a substantially zero temperature coefficient
5703473	06/27/1996	08/672,267	12/30/1997		Programmable PWM output voltage independent of supply
5900772	03/18/1997	08/819,899	05/04/1999		Bandgap reference circuit and method
6256702	09/10/1998	09/150,808	07/03/2001		Nonvolatile memory device with extended storage and high reliability through writing the same data into two memory cells
6300833	12/26/1999	09/449,996	10/09/2001		DC gain enhancement for operational amplifiers
6304206	09/02/1998	09/145,285	10/16/2001		Voltage comparator, operational amplifier and analog-to-digital conversion circuit employing the same
6525614	11/16/2001	09/995,976	02/25/2003	20020060917	Voltage boost system having feedback control
6554469	04/17/2001	09/837,816	04/29/2003		Four current transistor temperature sensor and method
6683554	06/18/2002	10/173,124	01/27/2004	20030006926	Analog-to-digital conversion circuit having increased conversion speed and high conversion accuracy

1. Foreign Listed Issued Patents and Pending Patent Applications

Foreign Issued Patents

Patent Number	Country	Filing Date	Application Number	Issue Date	Publication Number	Title
ZL98105705.5	CN	03/17/1998	98105705.5	02/15/2006	1202039	Bandgap reference circuit and method

Patent Number	Country	Filing Date	Application Number	Issue Date	Publication Number	Title
ZL02123364.0	CN	06/18/2002	02123364.0	11/15/2006	1399410	A/D converter circuit
69828669.3	DE	09/11/1998	98117244.8	01/19/2005	69828669	Nonvolatile semiconductor memory device
903749	FI	09/11/1998	98117244.8	01/19/2005	903749	Nonvolatile semiconductor memory device
903749	GB	09/11/1998	98117244.8	01/19/2005	903749	Nonvolatile semiconductor memory device
3384717	JP	09/04/1997	9-239810	12/27/2002	11088172	Voltage Comparator, Operational Amplifier and Analog-to-Digital Conversion Circuit Employing the Same
3263636	JP	09/18/1997	9-253594	12/21/2001	11096781	Nonvolatile Memory Device with Extended Storage and High Reliability through Writing the Same Data into Two Memory Cells
3258945	JP	09/30/1997	9-267142	12/07/2001	11110980	Nonvolatile Semiconductor Memory Device
3454689	JP	09/30/1997	9-267402	07/25/2003	11112305	Voltage Comparator, Operational Amplifier, Analog-to-Digital Converter and Analog-to-Digital Conversion Circuit
4380812	JP	03/16/1998	10-088018	10/02/2009	10-260746	Bandgap Reference Circuit and Method
3733062	JP	12/18/2001	2001-384369	10/21/2005	2003188727	Analog-to-Digital Conversion Circuit
10-313065	KR	09/18/1998	10-1998-0038669	10/16/2001		Nonvolatile Memory Device with Extended Storage and High Reliability through Writing the Same Data into Two Memory Cells
903749	NL	09/11/1998	98117244.8	01/19/2005	903749	Nonvolatile semiconductor memory device
386302 / 113070	TW	11/11/1997	086116829	04/01/2000		Bandgap reference circuit and method
397982 / 119028	TW	08/26/1998	087114029	07/11/2000		Nonvolatile semiconductor memory device

Foreign Pending Applications

Country	Filing Date	Application Number	Publication Number	Title
DE	02/06/1998	19804747.9	19804747	Band separation reference circuit
EP	09/02/1998	98307056.6	901232	Voltage comparator, operational amplifier and analog-to-digital conversion circuit employing the same

2. US and Foreign Listed Abandoned/Expired Patents and Applications

Patent Number	Country	Filing Date	Application Number	Issue Date	Publication Number	Title
903749	EP	09/11/1998	98117244.8	01/19/2005	903749	Nonvolatile semiconductor memory device
	JP	11/17/2000	2000-35124 7			Voltage Boost System Having Feedback Control
3942383	JP	06/18/2001	2001-18389 8	04/13/2007	2003008439	Analog-to-Digital Conversion Circuit Having Increased Conversion Speed and High Conversion Accuracy
4073192	JP	10/23/2001	2001-32469 3	02/01/2008	2002218738	Voltage Boost System Having Feedback Control
	KR	03/18/1998	10-1998-009 160			Bandgap Reference Circuit and Method
10-43541 0	KR	11/16/2001	10-2001-007 1299	06/01/2004	2002038537	Voltage Boost System Having Feedback Control
556400 / 188429	TW	09/21/2001	090123288	10/01/2003		Voltage boosting device
	US	01/02/1996	60/009,985			Programmable PWM Output Voltage Independent of Supply