

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT3234788

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
RENESAS ELECTRONICS CORPORATION	02/16/2015
RECEIVING PARTY DATA	
Name:	ACACIA RESEARCH GROUP LLC
Street Address:	2400 DALLAS PARKWAY SUITE 200
City:	PLANO
State/Country:	TEXAS
Postal Code:	75093
PROPERTY NUMBERS Total: 3	
Property Type	Number
Patent Number:	5805504
Patent Number:	5943260
Patent Number:	5894441
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Email:	jgraff@acacaires.com
Correspondent Name:	JENNIFER GRAFF
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Address Line 4:	PLANO, TEXAS 75093
NAME OF SUBMITTER:	JENNIFER GRAFF
SIGNATURE:	/Jennifer Graff/
DATE SIGNED:	02/20/2015
Total Attachments: 3	
source=Renesas #15 to ARG - Assignment ('504, '260, '441) 2-16-15#page1.tif	
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source=Renesas #15 to ARG - Assignment ('504, '260, '441) 2-16-15#page3.tif	

ASSIGNMENT

WHEREAS, RENESAS ELECTRONICS CORPORATION, a corporation formed under the laws of Japan, having a place of business at 2-6-2, Ote-machi, Chiyoda-ku, Tokyo 100-0004, Japan hereafter, together with any successors, legal representatives or assigns thereof, called "Assignor") is the owner of the entire right, title, and interest and assignee of the **U.S. Patent Nos. listed on Exhibit A attached hereto**;

AND WHEREAS, ACACIA RESEARCH GROUP LLC, a Texas limited liability company having a place of business at 2400 Dallas Parkway, Suite 200, Plano, Texas 75093 (hereafter, together with any successors, legal representatives or assigns thereof, called "ASSIGNEE") wants to acquire the entire right, title and interest in and to said **U.S. Patent Nos. listed on Exhibit A attached hereto**, and all the inventions therein, and Assignor is willing to enter into such assignment.

NOW, THEREFORE, in consideration of the sum of One Dollar (\$1.00) in hand paid and other good and valuable consideration the receipt of which from ASSIGNEE is hereby acknowledged, Assignor has sold, assigned, transferred and set over, and does hereby sell, assign, transfer and set over to ASSIGNEE the entire right, title and interest in and to the **U.S. Patent Nos. listed on Exhibit A attached hereto**, and all continuations, divisionals, reexaminations, reissues and substitutes thereof, all inventions therein, including without limitation all rights to claim priority on the basis thereof, and any and all applications for patents which are or may hereafter be filed for this invention in any foreign country and all patents which may be granted on these inventions in any foreign country, and all extensions, renewals, and reissues thereof; and including without limitation, all rights to sue for past, present and future infringement, including the right to collect and receive any damages, royalties, or settlements for such infringements, all rights to sue for injunctive or other equitable relief, and any and all causes of action relating to any of the inventions or discoveries thereof;

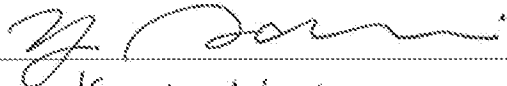
Assignor hereby covenants that it has full right to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict with this Assignment;

Assignor hereby further covenants and agrees that it will communicate to ASSIGNEE any and all facts known to it respecting said patents, and testify in any legal proceeding, sign all lawful papers, execute and deliver all papers and take any actions that may be necessary or desirable to perfect the title to any aforementioned patents and inventions, execute all divisional, continuation, reexamination, reissue and substitute applications, and make all rightful oaths and generally do everything possible to aid ASSIGNEE to obtain and enforce proper patent protection for said inventions in all countries.

ASSIGNMENT

IN TESTIMONY WHEREOF, I hereunto set my hand this 16th day of February, 2015.

RENESAS ELECTRONICS CORPORATION
(Assignor)

By 
Name Yasushi Adachi
Title General Manager, Intellectual Property Div.

ASSIGNMENT

EXHIBIT A

U.S. PATENTS & APPLICATIONS

Fam #	Patent Number	Issue Date	Application Number	App. Date	Title
1	5805504	1998-09-08	08/758367	1996-11-29	Synchronous semiconductor memory having a burst transfer mode with a plurality of subarrays accessible in parallel via an input buffer
2	5943260	1999-08-24	09/027215	1998-02-20	Method for high-speed programming of a nonvolatile semiconductor memory device
3	5894441	1999-04-13	09/050354	1998-03-31	Semiconductor memory device with redundancy circuit

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