

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

EPAS ID: PAT3231240

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
CHRISTOPHE CHEVALLIER	01/28/2010
CHANG HUA SIAU	01/28/2010
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	UNITY SEMICONDUCTOR CORPORATION
<b>Street Address:</b>	1050 ENTERPRISE WAY
<b>Internal Address:</b>	SUITE 700
<b>City:</b>	SUNNYVALE
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	94089
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
Application Number:	14624891
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	408-462-8000
<b>Email:</b>	twain@rambus.com
<b>Correspondent Name:</b>	TARISA WAIN
<b>Address Line 1:</b>	1050 ENTERPRISE WAY #700
<b>Address Line 4:</b>	SUNNYVALE, CALIFORNIA 94089
<b>ATTORNEY DOCKET NUMBER:</b>	RA1274.D1C1C1C1.US
<b>NAME OF SUBMITTER:</b>	TARISA WAIN
<b>SIGNATURE:</b>	/Tarisa Wain/
<b>DATE SIGNED:</b>	02/18/2015
<b>Total Attachments: 1</b>	
source=RA1274.P.US Assignment#page1.tif	

## ASSIGNMENT OF PATENT APPLICATION

Whereas I, an undersigned inventor, have invented certain new and useful improvements as set forth in the patent application entitled:

### **Access Signal Adjustment Circuits and Methods for Memory Cells In a Cross-Point Array**

(Atty. Docket No. **P158.10**),

for which I have executed a U.S. patent application on even date herewith.

I hereby authorize ASSIGNEE (see below), to insert in the following parentheses the filing date and application number of said application when known (U.S. Application No. 12/657,895 filed on 11/29/2010).

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I, an undersigned inventor, hereby:

- 1) Sell, assign and transfer to UNITY SEMICONDUCTOR CORPORATION, a Delaware corporation having a place of business at 255 Santa Ana Court, Sunnyvale, CA 94085 ("ASSIGNEE"), the entire right, title and interest in any and all improvements and inventions disclosed in, applications based upon, and patents granted upon (including foreign patents and the right to claim priority), the above-referenced application.
- 2) Authorize and request the Commissioner of Patents to issue any and all United States Patents resulting from said application or any division, continuation, substitute, renewal, re-examination or reissue thereof to the ASSIGNEE.
- 3) Agree to execute all papers and documents and, entirely at the ASSIGNEE's expense, perform any acts which are reasonably necessary in connection with the prosecution of said application, as well as any derivative applications thereof, foreign applications based thereon, and/or the enforcement of patents resulting from such applications.
- 4) Agree that the terms, covenants and conditions of this assignment shall inure to the benefit of the ASSIGNEE, its successors, assigns and other legal representatives, and shall be binding upon the inventor, as well as the inventor's heirs, legal representatives and assigns.
- 5) Warrant and represent that I have not entered and will not enter into any assignment, contract, or understanding that conflicts with this assignment.

Signed on the date indicated beside my signature.

1. Signature:	<u>Christophe J. Chevallier</u>	Date:	<u>1/28/10</u>
Typed Name:	Christophe Chevallier		
2. Signature:	<u>Chang Hua Siau</u>	Date:	<u>1/28/10</u>
Typed Name:	Chang Hua Siau		