

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT3239742

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the PATENT NUMBER TYPED INCORRECTLY. THE INCORRECT PATENT NUMBER IS 5624675 AND THE CORRECT PATENT NUMBER IS 6624675. previously recorded on Reel 034826 Frame 0047. Assignor(s) hereby confirms the THE ASSIGNMENT FROM ST-ERICSSON SA, EN LIQUIDATION TO SNAPTRACK, INC..

CONVEYING PARTY DATA

Name	Execution Date
ST-ERICSSON SA, EN LIQUIDATION	12/09/2014

RECEIVING PARTY DATA

Name:	SNAPTRACK, INC.
Street Address:	5775 MOREHOUSE DRIVE
City:	SAN DIEGO
State/Country:	CALIFORNIA
Postal Code:	92121

PROPERTY NUMBERS Total: 50

Property Type	Number
Patent Number:	6624675
Patent Number:	6445034
Patent Number:	6438700
Application Number:	09379923
Patent Number:	6229346
Patent Number:	7761644
Patent Number:	6583607
Patent Number:	6801794
Patent Number:	6518901
Patent Number:	6642794
Patent Number:	6650169
Patent Number:	6700435
Patent Number:	6580257
Patent Number:	6914489
Patent Number:	7043258
Patent Number:	7042257
Patent Number:	7272370

PATENT

Property Type	Number
Patent Number:	7157979
Patent Number:	7643847
Patent Number:	7649911
Patent Number:	7298198
Patent Number:	7302003
Patent Number:	7221549
Patent Number:	7376111
Patent Number:	7019571
Patent Number:	7564832
Patent Number:	7693485
Patent Number:	7653017
Patent Number:	7486582
Patent Number:	7759915
Patent Number:	7848718
Patent Number:	8589908
Patent Number:	7930577
Patent Number:	8014379
Patent Number:	7882374
PCT Number:	IB2006051659
Patent Number:	8004322
Patent Number:	7911192
PCT Number:	IB2006054585
PCT Number:	IB2007050606
Patent Number:	8427125
Patent Number:	8217707
PCT Number:	IB2007053194
PCT Number:	IB2007053458
Patent Number:	8200912
Patent Number:	7949367
Application Number:	12726557
Patent Number:	8400195
Patent Number:	8548410
PCT Number:	EP2012072220

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Email: c_tcodd@qualcomm.com

Correspondent Name: SNAPTRACK, INC.
Address Line 1: 5775 MOREHOUSE DRIVE
Address Line 4: SAN DIEGO, CALIFORNIA 92121

NAME OF SUBMITTER: TANYA CODD

SIGNATURE: /Tanya Codd/

DATE SIGNED: 02/24/2015

This document serves as an Oath/Declaration (37 CFR 1.63).

Total Attachments: 23

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PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT3199376

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the ASSIGNEE ON THE ASSIGNMENT. ASSIGNEE WAS RECORDED INCORRECTLY AS SNAPTRACK, INC AND SHOULD BE SNAPTRACK, INC. previously recorded on Reel 034588 Frame 0822. Assignor(s) hereby confirms the ASSIGNMENT FROM ST-ERICSSON SA, EN LIQUIDATION TO SNAPTRACK, INC..

CONVEYING PARTY DATA

Name	Execution Date
ST-ERICSSON SA, EN LIQUIDATION	12/09/2014

RECEIVING PARTY DATA

Name:	SNAPTRACK, INC.
Street Address:	5775 MOREHOUSE DRIVE
City:	SAN DIEGO
State/Country:	CALIFORNIA
Postal Code:	92121

PROPERTY NUMBERS Total: 50

Property Type	Number
Patent Number:	6700435
Patent Number:	7157979
Patent Number:	8004322
Patent Number:	8400195
Patent Number:	8200912
Patent Number:	7693485
Application Number:	12726557
Patent Number:	7653017
Patent Number:	7882374
Application Number:	09379923
Patent Number:	5624675
Patent Number:	7564832
Patent Number:	7221549
Patent Number:	7376111
Patent Number:	7043258
Patent Number:	6801794
Patent Number:	6650169
Patent Number:	7302003

PATENT

REEL: 035092 FRAME: 0829

ASSIGNMENT OF PATENT RIGHTS

THIS ASSIGNMENT OF PATENT RIGHTS (this "Assignment") is entered into as of the Effective Date set forth below by and between ST-Ericsson SA, en liquidation (formerly known as ST-Ericsson SA), a Swiss corporation, having its principal office and place of business at 39 Chemin du Champ-des-Filles, 1228 Plan les Ouares, Geneva, Switzerland ("Assignor"), and SnapTrack, Inc., a California corporation, having its registered office at 5775 Morehouse Drive, San Diego, California 92121 USA ("Assignee").

WHEREAS, for good and valuable consideration, the receipt of which is hereby acknowledged:

Assignor, does hereby irrevocably sell, assign, transfer, convey, and deliver onto Assignee:

(i) all right, title, and interest in and to the patents and patent applications listed in Appendix 1, together with all reissues, reexaminations, extensions, continuations, continuing prosecution applications, renewals, requests for continuing examinations, divisionals, counterparts (whether foreign or domestic) claiming priority to any of the foregoing items, and inventions, discoveries, and improvements claimed or described in any of the foregoing items that is required to be owned, and only to the extent necessary to be owned, in order to file, or claim an ownership right in or to, any patent application thereon or to obtain any patent issuing therefrom (hereinafter collectively referred to as the "Assigned Patent Rights"), and

(ii) any causes of action and rights of enforcement under the Assigned Patent Rights including, but not limited to, the right to sue for past, present, and future infringement and to recover damages for past, present, and future infringement of any of the Assigned Patent Rights or any violation(s) thereof, whether or not accrued prior to the Effective Date (defined below), and to seek, recover, and retain damages and all other remedies of any kind, including injunctive relief, for past, current and future infringement, misappropriation or other violations of any rights under the Assigned Patent Rights, such rights to be held and enjoyed by Assignee, its successors, legal representatives and assigns, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment had not been made.

The terms and conditions of this Assignment will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

Assignor hereby authorizes and requests the Commissioner of Patents of the United States of America, and any official of any country or countries foreign to the United States of America, whose duty it is to issue patents on applications, to issue all patents for the Assigned Patent Rights to Assignee, its successors, legal representatives and assigns, in accordance with the terms of this Assignment.

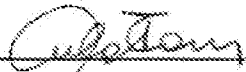
Assignor hereby to Assignee and its successors, legal representatives, and assigns a limited power-of-attorney to execute documents, including any additional assignments or transfers related to the Assigned Patent Rights, in the name of Seller in order to effectuate the recordation of the transfers of the Assigned Patent Rights to Assignee in any governmental office in the world.

This Patent Assignment may be executed and delivered in two or more counterparts (including the delivery by way of facsimile transmission), each of which shall be an original counterpart, with the same effect as if the signatures thereto and hereto were upon the same instrument.

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IN WITNESS WHEREOF, intending to be legally bound, Assignor has executed this Assignment as of this 3 day of December, 2014 (the "Effective Date").

On behalf of Assignor,
ST-Ericsson SA, en liquidation
(formerly known as ST-Ericsson SA)

By: 
Name: Carlo Ferro
Title: Authorized Representative

On behalf of Assignor,
ST-Ericsson SA en liquidation
(formerly known as ST-Ericsson SA)

By: _____
Name: _____
Title: _____

IN WITNESS WHEREOF, intending to be legally bound, Assignee has executed this Assignment as of this _____ day of _____, 2014.

On behalf of Assignee,
SnapTrack, Inc.

By: _____
Name: _____
Title: _____

IN WITNESS WHEREOF, intending to be legally bound, Assignor has executed this Assignment as of this 7 day of December, 2014 (the "Effective Date").

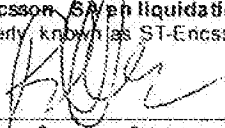
On behalf of Assignor,
ST-Ericsson SA, en liquidation
(formerly known as ST-Ericsson SA)

By: _____

Name: _____

Title: _____

On behalf of Assignor,
ST-Ericsson SA, en liquidation
(formerly known as ST-Ericsson SA)

By:  _____

Name: Per Oscarsson

Title: Authorized Representative

IN WITNESS WHEREOF, intending to be legally bound, Assignee has executed this Assignment as of this _____ day of _____, 2014.

On behalf of Assignee,
SnapTrack, Inc.

By: _____

Name: _____

Title: _____

IN WITNESS WHEREOF, intending to be legally bound, Assignor has executed this Assignment as of this _____ day of _____, 2014 (the "Effective Date").

On behalf of Assignor,
ST-Ericsson SA, en liquidation
(formerly known as ST-Ericsson SA)

By: _____

Name: _____

Title: _____

On behalf of Assignor,
ST-Ericsson SA en liquidation
(formerly known as ST-Ericsson SA)

By: _____

Name: _____

Title: _____

IN WITNESS WHEREOF, intending to be legally bound, Assignee has executed this Assignment as of this 10th day of December, 2014.

On behalf of Assignee,
SnapTrack, Inc.

By: Philip J. Fries, Jr.

Name: Philip J. Fries, Jr.

Title: Assistant Secretary

Appendix 1

PATENTS AND PATENT APPLICATIONS

*Application Title is from internal records and may not be the same as the official title registered with the corresponding patent office

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title *
1	02729490.9	EP			LOCAL SUPPLY GENERATOR FOR A DIGITAL CMOS INTEGRATED CIRCUIT HAVING AN ANALOG SIGNAL PROCESSING CIRCUITRY
1	2002-556999	JP	4156368	7/18/2008	LOCAL SUPPLY GENERATOR FOR A DIGITAL CMOS INTEGRATED CIRCUIT HAVING AN ANALOG SIGNAL PROCESSING CIRCUITRY
1	10/204,745	US	6,700,435	3/2/2004	LOCAL SUPPLY GENERATOR FOR A DIGITAL CMOS INTEGRATED CIRCUIT HAVING AN ANALOG SIGNAL PROCESSING CIRCUITRY
2	03821099.1	CN	Z1038210 99.1	6/23/2010	Equipment comprising phase-lock loops and method for calibrating phase-lock loop



Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
2	03793972.5	EP			IMPROVEMENTS RELATING TO PHASE-LOCK LOOPS
2	2004-533739	JP	4858868	11/11/2011	IMPROVEMENTS RELATING TO PHASE-LOCK LOOPS
2	10-2005-7003751	KR	10-1023394	3/10/2011	APPARATUS INCLUDING PHASE-LOCK LOOPS AND METHOD FOR CALIBRATING PHASE-LOCK LOOPS
2	10/526,197	US	7,157,979	1/2/2007	Phase-lock loops
3	200680023992.3	CN	ZL200680023992.3	8/31/2011	SYNCHRONIZATION SCHEME WITH ADAPTIVE REFERENCE FREQUENCY CORRECTION
3	06765866.6	EP	EP1900138	11/18/2009	SYNCHRONIZATION SCHEME WITH ADAPTIVE REFERENCE FREQUENCY CORRECTION
3	602006010553.3	DE	EP1900138	11/18/2009	SYNCHRONIZATION SCHEME WITH ADAPTIVE REFERENCE FREQUENCY CORRECTION
3	06765866.6	FR	EP1900138	11/18/2009	SYNCHRONIZATION SCHEME WITH ADAPTIVE REFERENCE FREQUENCY CORRECTION
3	06765866.6	GB	EP1900138	11/18/2009	SYNCHRONIZATION SCHEME WITH ADAPTIVE REFERENCE FREQUENCY CORRECTION

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Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
3	2008-519054	JP	4826968	9/22/2011	SYNCHRONIZATION SCHEME WITH ADAPTIVE REFERENCE FREQUENCY CORRECTION
3	11/993,625	US	8,004,322	8/23/2011	Synchronization scheme with adaptive reference frequency correction
3	13/163,486	US	8,400,195	3/19/2013	SYNCHRONIZATION SCHEME WITH ADAPTIVE REFERENCE FREQUENCY CORRECTION
4	06300905.4	EP			MULTI-CORE DEVICE WITH OPTIMIZED MEMORY CONFIGURATION
4	PCT/IB2007/05345 8	WO			MULTI-CORE DEVICE WITH OPTIMIZED MEMORY CONFIGURATION
4	07826177.3	EP			MULTI-CORE DEVICE WITH OPTIMIZED MEMORY CONFIGURATION
4	12/439,508	US	8,200,912	6/12/2012	MULTI-CORE DEVICE WITH OPTIMIZED MEMORY CONFIGURATION
5	03078715.4	EP	EP1548994	6/27/2012	BLUETOOTH POLLING WITH FEWER POLL PACKETS
5	60341385.4	DE	EP1548994	6/27/2012	BLUETOOTH POLLING WITH FEWER POLL PACKETS
5	03078715.4	GB	EP1548994	6/27/2012	BLUETOOTH POLLING WITH FEWER POLL PACKETS
5	03078715.4	IT	EP1548994	6/27/2012	BLUETOOTH POLLING WITH FEWER POLL PACKETS
5	10/996,124	US	7,693,485	4/6/2010	BLUETOOTH POLLING WITH FEWER POLL PACKETS
5	12/726,557	US			

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
6	10/572,723	US	7,649,911	1/19/2010	METHOD OF DATA HANDLING IN A WLAN
7	05447013.3	EP			Bluetooth sniff mode power saving
7	11/045,806	US	7,653,017	1/26/2010	Bluetooth sniff mode power saving
8	PCT/IB2006/051659	WO			A METHOD OF POWERING UP A TERMINAL, MEMORY AND TERMINAL THEREOF
8	200680018433.3	CN	ZL200680018433.3	6/9/2010	A METHOD OF POWERING UP A TERMINAL, MEMORY AND TERMINAL THEREOF
8	06745017.1	EP	EP1891521	9/30/2009	A METHOD OF POWERING UP A TERMINAL, MEMORY AND TERMINAL THEREOF
8	602006009510.4	DE	EP1891521	9/30/2009	A METHOD OF POWERING UP A TERMINAL, MEMORY AND TERMINAL THEREOF
8	06745017.1	FR	EP1891521	9/30/2009	A METHOD OF POWERING UP A TERMINAL, MEMORY AND TERMINAL THEREOF
8	06745017.1	GB	EP1891521	9/30/2009	A METHOD OF POWERING UP A TERMINAL, MEMORY AND TERMINAL THEREOF
8	2008-0513002	JP	4756137	6/10/2011	A METHOD OF POWERING UP A TERMINAL, MEMORY AND TERMINAL THEREOF
8	10-2007-7030013	KR	10-1263529	5/6/2013	A METHOD OF POWERING UP, MEMORY AND TERMINAL THEREOF

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
8	11/915,452	US	7,882,374	2/1/2011	Method of powering up a portable terminal and avoiding undesirable powering up
9	09/379,923	US			FREE-RUNNING MODE DEVICE FOR PHASE LOCKED LOOP
9	09/852,629	US	6,624,675	9/23/2003	FREE-RUNNING MODE DEVICE FOR PHASE LOCKED LOOP
9	60023833.4	DE	EP1123580	11/9/2005	CHARGE PUMP PHASE LOCKED LOOP CIRCUIT
9	00961353.0	FR	EP1123580	11/9/2005	CHARGE PUMP PHASE LOCKED LOOP CIRCUIT
9	00961353.0	GB	EP1123580	11/9/2005	CHARGE PUMP PHASE LOCKED LOOP CIRCUIT
9	2001-518934	JP	4646100	12/17/2010	CHARGE PUMP PHASE LOCKED LOOP CIRCUIT
10	03078716.2	EP			Scheduling poll packets in bluetooth sniff mode
10	10/996,121	US	7,564,832	7/21/2009	Scheduling poll packets in bluetooth sniff mode
11	FR0214962	FR	FR2848026/ FR0214962	3/11/2005	Device for protecting an electronic circuit against electrostatic discharges (ESD), comprises a connection pad, two strip conductors and two diodes implemented on a substrate
11	10/724,012	US	7,221,549	5/22/2007	CIRCUITRY FOR PROTECTING ELECTRONIC CIRCUITS AGAINST ELECTROSTATIC DISCHARGES AND METHODS OF OPERATING THE SAME

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
12	0228396.8	GB			Terminating frame reception
12	0325422.4	GB	GB2396081	2/28/2006	Terminating frame reception
12	10/729,680	US	7,376,111	5/20/2008	Terminating frame reception
13	0223351.8	GB			Mobile position fixing
13	0321590.2	GB	GB2394134	10/26/2004	Mobile position fixing
13	10/360,916	US	7,043,258	5/9/2006	Mobile position fixing
14	50012403.5	DE	EP1120918	3/15/2006	Mobile telephone with display device; has control device, which switches off at least one part of display device according to each operation state of telephone
14	00204531.8	FR	EP1120918	3/15/2006	Power saving circuit in mobile terminal with control of the display dependent on the state of operation
14	00204531.8	GB	EP1120918	3/15/2006	Power saving circuit in mobile terminal with control of the display dependent on the state of operation
14	00137438.9	CN	2100137438.9	9/7/2005	Energy saving circuit for controlling mobile communication terminal display by working status
14	2000-385065	JP			ENERGY SAVING CIRCUIT UNDER CONTROL DEPENDING UPON OPERATION STATE OF DISPLAY DEVICE OF MOBILE COMMUNICATION TERMINAL DEVICE

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
14	10-2000-0078903	KR	10-0776839	11/8/2007	MOBILE COMMUNICATION TERMINAL
14	090107732	TW	159867	8/1/2002	Energy-saving circuit based on control of a display device of a terminal for mobile communication in dependence on the operating state
14	09/741,973	US	6,801,794	10/5/2004	Operating state based energy-saving display device
15	09/968,093	US	6,650,169	11/18/2003	Gate driver apparatus having an energy recovering circuit
15	02819319.9	CN	21028193 19.9	3/27/2013	Gate driver apparatus having an energy recovering circuit
15	02765233.8	EP			GATE DRIVER APPARATUS HAVING AN ENERGY RECOVERING CIRCUIT
15	2003-533434	JP	4581070	9/10/2010	Gate driver apparatus having an energy recovering circuit
15	10-2004-7004746	KR	10-0977912	8/18/2010	GATE DRIVER APPARATUS, AN ENERGY RECOVERING METHOD AND AN ENERGY RECOVERING CIRCUIT
16	FR0210889	FR	FR2844131// FR0210889	11/12/2004	Mobile telephone/digital personal assistant video display having first/second images and estimator preselecting vectors/selecting movement vector minimising costs

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
16	10/654,582	US	7,302,003	11/27/2007	Method and device for image interpolation with motion compensation
17	993/DEL/2004	IN			A METHOD FOR REMOTELY UPGRADING THE FIRMWARE OF A TARGET DEVICE USING WIRELESS TECHNOLOGY
17	05754554.3	EP			A METHOD FOR REMOTELY UPGRADING THE FIRMWARE OF A TARGET DEVICE USING WIRELESS TECHNOLOGY
17	11/606,644	US	8,589,908	11/19/2013	Method for remotely upgrading the firmware of a target device using wireless technology
18	11/362,982	US	7,759,915	7/20/2010	SYSTEM WITH LINEAR AND SWITCHING REGULATOR CIRCUITS
19	02821714.4	CN	ZL02921714.4	10/10/2007	FREQUENCY DIVIDER WITH REDUCED JITTER AND TRANSMITTER BASED THEREON

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
19	02758730.2	EP			IMPROVED FREQUENCY DIVIDER WITH REDUCED JITTER AND TRANSMITTER BASED THEREON
19	2003-524117	JP	4386725	10/9/2009	IMPROVED FREQUENCY DIVIDER WITH REDUCED JITTER AND TRANSMITTER BASED THEREON
19	10/487,640	US	7,042,257	5/9/2006	FREQUENCY DIVIDER WITH REDUCED JITTER AND TRANSMITTER BASED THEREON
20	01958008.3	EP	EP1310042	11/21/2007	NOISE AND INPUT IMPEDANCE MATCHED AMPLIFIER
20	60131542.1	DE	EP1310042	11/21/2007	NOISE AND INPUT IMPEDANCE MATCHED AMPLIFIER
20	01958008.3	FR	EP1310042	11/21/2007	NOISE AND INPUT IMPEDANCE MATCHED AMPLIFIER
20	01958008.3	GB	EP1310042	11/21/2007	NOISE AND INPUT IMPEDANCE MATCHED AMPLIFIER
20	2002-518614	JP			NOISE AND INPUT IMPEDANCE MATCHED AMPLIFIER
20	09/920,117	US	6,642,794	11/4/2003	NOISE AND INPUT IMPEDANCE MATCHED AMPLIFIER
21	10/259,012	US	6,914,489	7/5/2005	VOLTAGE-CONTROLLED OSCILLATOR PRESETTING CIRCUIT
21	03822930.7	CN	ZL03822930.7	10/17/2007	VOLTAGE-CONTROLLED OSCILLATOR PRESETTING CIRCUIT
21	03798305.3	EP	EP1547249	4/20/2011	VOLTAGE-CONTROLLED OSCILLATOR PRESETTING CIRCUIT

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
21	60336832.8	DE	EP1547249	4/20/2011	VOLTAGE-CONTROLLED OSCILLATOR PRESETTING CIRCUIT
21	03798305.3	GB	EP1547249	4/20/2011	VOLTAGE-CONTROLLED OSCILLATOR PRESETTING CIRCUIT
21	2004-539325	JP	4742219	5/20/2011	VOLTAGE-CONTROLLED OSCILLATOR PRESETTING CIRCUIT
21	10-2005-7005028	KR	10-1035827	5/12/2011	VOLTAGE-CONTROLLED OSCILLATOR PRESETTING CIRCUIT
21	092126206	TW	I325692	6/1/2010	Voltage-controlled oscillator presetting circuit and method for presetting a voltage-controlled oscillator
22	03819108.3	CN	ZL03819108.3	11/5/2008	Improved mixers with a plurality of local oscillators and systems based thereon
22	60310569.6	DE	EP1529338	12/20/2006	IMPROVED MIXERS WITH A PLURALITY OF LOCAL OSCILLATORS AND SYSTEMS BASED THEREON
22	03784322.4	FR	EP1529338	12/20/2006	IMPROVED MIXERS WITH A PLURALITY OF LOCAL OSCILLATORS AND SYSTEMS BASED THEREON

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Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
22	03784322.4	GB	EP1529338	12/20/2006	IMPROVED MIXERS WITH A PLURALITY OF LOCAL OSCILLATORS AND SYSTEMS BASED THEREON
22	03784322.4	IT	EP1529338	12/20/2006	IMPROVED MIXERS WITH A PLURALITY OF LOCAL OSCILLATORS AND SYSTEMS BASED THEREON
22	2004-527130	JP	4335135	7/3/2009	IMPROVED MIXERS WITH A PLURALITY OF LOCAL OSCILLATORS AND SYSTEMS BASED THEREON
22	10/523,667	US	7,272,370	9/18/2007	MIXERS WITH A PLURALITY OF LOCAL OSCILLATORS AND SYSTEMS BASED THEREON
23	11/863,865	US	8,014,379	9/6/2011	CORRELATION OF ACCESS CODE FOR BLUETOOTH SYNCHRONIZATION
24	09/510,323	US	6,229,346	5/8/2001	HIGH FREQUENCY SUPPLY COMPATIBLE HYSTERESIS COMPARATOR WITH LOW DYNAMICS DIFFERENTIAL INPUT
25	09/313,933	US	6,438,700	8/20/2002	SYSTEM AND METHOD TO REDUCE POWER CONSUMPTION IN ADVANCED RISC MACHINE (ARM) BASED SYSTEMS
25	00801391.8	CN	2100801391.8	5/6/2009	System and method to reduce power consumption

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
25	60034526.2	DE	EP1099151	4/25/2007	A SYSTEM TO REDUCE POWER CONSUMPTION
25	00930452.8	FR	EP1099151	4/25/2007	A SYSTEM TO REDUCE POWER CONSUMPTION
25	00930452.8	GB	EP1099151	4/25/2007	A SYSTEM TO REDUCE POWER CONSUMPTION
25	2000-618810	JP	4632279	11/26/2001 0	A SYSTEM AND A METHOD TO REDUCE POWER CONSUMPTION
26	69615536.2	DE	EP0806057	9/26/2001	MOS TRANSISTOR
26	08/753,556	US	6,445,034	9/3/2002	MOS TRANSISTOR HAVING FIRST AND SECOND CHANNEL SEGMENTS WITH DIFFERENT WIDTHS AND LENGTHS
27	00830430.5	EP			Boosted switch device for a sampler of an analogue/digital converter, and operating method thereof
27	09/884,872	US	6,518,901	2/11/2003	BOOSTED SWITCH DEVICE FOR A SAMPLER OF AN ANALOG/DIGITAL CONVERTER, AND OPERATING METHOD THEREOF
28	FR0112315	FR	FR2830091// FR0112315	9/10/2004	Voltage regulator incorporating stabilizing resistance and current output limiting circuit
28	10/253,399	US	6,580,257	6/17/2003	VOLTAGE REGULATOR INCORPORATING A STABILIZATION

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Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
					RESISTOR AND A CIRCUIT FOR LIMITING THE OUTPUT CURRENT
29	09/672,892	US	6,583,607	6/24/2003	LINEAR REGULATOR WITH A SELECTABLE OUTPUT VOLTAGE
30	PCT/182007/05060 6	WO			IMPROVED WINDOW COMPARATOR WITH ACCURATE LEVELS FOR USE IN DC-DC CONVERTERS
30	200780007151.8	CN	ZL200780007151.8	1/2/2013	Improved window comparator with accurate levels for use in DC-DC converters
30	07705949.1	EP			IMPROVED WINDOW COMPARATOR WITH ACCURATE LEVELS FOR USE IN DC-DC CONVERTERS
30	2008-555932	JP	4977829	4/27/2012	IMPROVED WINDOW COMPARATOR WITH ACCURATE LEVELS FOR USE IN DC-DC CONVERTERS
30	12/280,864	US	8,427,125	4/23/2013	WINDOW COMPARATOR WITH ACCURATE LEVELS FOR USE IN DC-DC CONVERTERS
31	13/307,743	US	8,548,410	10/1/2013	RF Front-End for Intra-Band Carrier Aggregation
31	PCT/EP2012/0722 20	WO			RF FRONT-END FOR RECEIVING INTRA-BAND CARRIER AGGREGATED SIGNALS
32	10/814,026	US	7,019,571	3/28/2006	FREQUENCY SYNTHESIZER FOR A WIRELESS

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
					COMMUNICATION SYSTEM
33	04770270.9	EP	EP1678816	10/5/2011	CHARGE PUMP WITH INPUT VOLTAGE FEEDFORWARD
33	602004034704.3	DE	EP1678816	10/5/2011	CHARGE PUMP WITH INPUT VOLTAGE FEEDFORWARD
33	04770270.9	FR	EP1678816	10/5/2011	CHARGE PUMP WITH INPUT VOLTAGE FEEDFORWARD
33	04770270.9	GB	EP1678816	10/5/2011	CHARGE PUMP WITH INPUT VOLTAGE FEEDFORWARD
33	200480030987.6	CN	ZL200480030987.6	7/27/2011	A CHARGE PUMP
33	2006-0536240	JP	4394124	10/23/2009	A CHARGE PUMP
33	10-2006-7007625	KR			A CHARGE PUMP
33	10/575,480	US	7,298,198	11/20/2007	CHARGE PUMP
34	200580014255.2	CN	ZL200580014255.2	5/11/2011	A mobile apparatus comprising integrated circuit and method of powering down and switching on such circuit
34	05732778.5	EP			A MOBILE APPARATUS COMPRISING INTEGRATED CIRCUIT AND METHOD OF POWERING DOWN SUCH CIRCUIT

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
34	11/578,899	US	7,848,718	12/7/2010	METHOD APPARATUS COMPRISING INTEGRATED CIRCUIT AND METHOD OF POWERING DOWN SUCH CIRCUIT
35	200580027651.9	CN	ZL200580027651.9	12/30/2009	Closed-loop control for performance tuning
35	05745513.1	EP			CLOSED-LOOP CONTROL FOR PERFORMANCE TUNING
35	2007-516107	JP	5051582	8/3/2012	Closed-loop control for performance tuning
35	11/629,716	US	7,930,577	4/19/2011	CLOSED-LOOP CONTROL FOR PERFORMANCE TUNING
36	00202910.6	EP	EP1081599	11/5/2008	Multiprocessor system
36	50015434.1	DE	EP1081599	11/5/2008	Multiprocessor system
36	00202910.6	FR	EP1081599	11/5/2008	Multiprocessor system
36	00202910.6	GB	EP1081599	11/5/2008	Multiprocessor system
36	2000-246735	JP			MULTIPROCESSOR SYSTEM
36	09/640,729	US	7,761,644	7/20/2010	Memory sharing arrangement for an integrated multiprocessor system

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
37	06101285.2	EP	EP1688956	1/28/2009	Dynamic memory for mobile phone
37	06101285.2	FR	EP1688956	1/28/2009	Dynamic memory for mobile phone
37	06101285.2	GB	EP1688956	1/28/2009	Dynamic memory for mobile phone
37	2006-27011	JP	4991160	5/11/2012	DYNAMIC MEMORY FOR PERSONAL DIGITAL ASSISTANT
37	11/346,796	US	7,486,582	2/3/2009	DYNAMIC MEMORY FOR A CELLULAR TERMINAL
38	06118979.1	EP			OPERATING AN INTEGRATED CIRCUIT
38	PCT/IB2007/05319 4	WO			OPERATING AN INTEGRATED CIRCUIT
38	07805382.4	EP	EP2080075	5/4/2011	OPERATING AN INTEGRATED CIRCUIT
38	602007014392.6	DE	EP2080075	5/4/2011	OPERATING AN INTEGRATED CIRCUIT
38	07805382.4	FR	EP2080075	5/4/2011	OPERATING AN INTEGRATED CIRCUIT
38	07805382.4	GB	EP2080075	5/4/2011	OPERATING AN INTEGRATED CIRCUIT
38	12/310,214	US	8,217,707	7/10/2012	THERMAL CONTROL SYSTEM AND METHOD FOR OPERATING AN INTEGRATED CIRCUIT
39	200480003598.4	CN	ZL200480003598.4	9/5/2012	Versatile baseband signal input current splitter
39	04709448.7	EP	EP1704649	12/30/2009	Versatile baseband signal input current splitter
39	602004024926.2	DE	EP1704649	12/30/2009	Versatile baseband signal input current splitter
39	04709448.7	FR	EP1704649	12/30/2009	Versatile baseband signal input current splitter
39	04709448.7	GB	EP1704649	12/30/2009	Versatile baseband signal input current splitter

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
39	2006-503438	JP	4682331	2/18/2011	VERSATILE BASEBAND SIGNAL INPUT CURRENT SPLITTER
39	10-2005-7014453	KR	10-1058973	8/17/2011	INTERFACING METHOD AND INTERFACING CIRCUIT
39	10/544,888	US	7,643,847	1/5/2010	VERSATILE BASEBAND SIGNAL INPUT CURRENT SPLITTER
39	12/625,388	US	7,949,367	5/24/2011	BASEBAND SIGNAL INPUT CURRENT SPLITTER
40	PCT/IB2006/05458 5	WO			HIGH VOLTAGE POWER SWITCHES USING LOW VOLTAGE TRANSISTORS
40	200680046011.7	CN	ZL200680046011.7	4/20/2011	HIGH VOLTAGE POWER SWITCHES USING LOW VOLTAGE TRANSISTORS
40	06832074.6	EP	EP1961118	6/16/2010	HIGH VOLTAGE POWER SWITCHES USING LOW VOLTAGE TRANSISTORS
40	602006014994.8	DE	EP1961118	6/16/2010	HIGH VOLTAGE POWER SWITCHES USING LOW VOLTAGE TRANSISTORS
40	06832074.5	FR	EP1961118	6/16/2010	HIGH VOLTAGE POWER SWITCHES USING LOW VOLTAGE TRANSISTORS
40	06832074.6	GB	EP1961118	6/16/2010	HIGH VOLTAGE POWER SWITCHES USING LOW VOLTAGE TRANSISTORS
40	2008-543967	JP	4756138	6/10/2011	HIGH VOLTAGE POWER SWITCHES USING LOW VOLTAGE TRANSISTORS

Family No.	Application Number	Country	Patent Number	Grant Date	Application Title
40	12/096,232	US	7,911,192	3/22/2011	High voltage power regulation using two power switches with low voltage transistors

