503208796 03/06/2015

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
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MITSUYA FUKAZAWA	06/04/2013

RECEIVING PARTY DATA

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State/Country:	JAPAN

PROPERTY NUMBERS Total: 1

Property Type	Number		
Application Number:	14641272		

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ATTORNEY DOCKET NUMBER:	067237-0886
NAME OF SUBMITTER:	BERNARD P. CODD
SIGNATURE:	/Bernard P. Codd/
DATE SIGNED:	03/06/2015

Total Attachments: 4

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> **PATENT REEL: 035109 FRAME: 0265** 503208796

Docket No.: Only After Sept 16, 2012

ASSIGNMENT WITH DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION (37 CFR 1.63)

Whereas, I/We, the undersigned inventor(s) hereinafter called assignor(s), have invented certain improvements described in the application identified below; and

Whereas, Renesas Electronics Corporation of Kanagawa, Japan, (assignee), desires to acquire the entire right, title, and interest in the application and invention, and to any United States patents to be obtained therefor,

Now therefore, for valuable consideration, receipt whereof is hereby acknowledged,

I/We, the above named assignor(s), hereby sell, assign and transfer to the above named assignee, its successors and assigns, the entire right, title and interest in the application and the invention disclosed therein for the United States of America, including all divisions, and continuations thereof, and all Letters Patent of the United States that may be granted thereon, and all reissues thereof, including the right granted upon the invention set forth in the application to the assignee, its successors and assigns; and I/we will execute without further consideration all papers deemed necessary by the assignee in connection with the United States application when called upon to do so by

(Legalization not required for recording	g but is prima facie evidence of execution under 35 USC 261)
As the below named inventor, I hereby	
This assignment with declaration is directed to:	The attached application, or
	United States Application or PCT International Application Number 13/738546 filed on January 10, 2013.
The application is entitled:	
	REFERENCE VOLTAGE GENERATING CIRCUIT
The above identified application was ma	ade or was authorized to be made by me.
	or an original joint inventor of a claimed invention in the application.
	ntents of the application for which this assignment with declaration is being submitted.
I am aware of the duty to disclose to the	ne Office all information known to me to be material to patentability as defined in 37 CFR 1.56.
I hereby acknowledge that any willful fa or imprisonment of not more than five (se statement made in this againment will be
Authorization To Permit Access To Ap	oplication by Participating Office
If checked, the undersigned hereby (JPO), the Korean Intellectual Property property offices in which a foreign application, See 37 CFR 1.14(c).	grants the USPTO authority to provide the European Patent Office (EPO), the Japan Patent Office Office (KIPO), the World Intellectual Property Office (WIPO), and any other intellectual cation claiming priority to the above-identified application is filed access to the above-identified and (h). This box should not be checked if the applicant does not wish the EPO, JPO, KIPO, or had foreign application claiming priority to the above-identified application is filed to have
in accordance with 37 CFR 1.14(h)(3), and dentified patent application—as—filed, 2)	ccess will be provided to a copy of the application—as—filed with respect to: 1) the above—any foreign application to which the above—identified application claims priority under 25 USC

119(a)-(d) if a copy of the foreign application that satisfies the certified copy requirement of 37 CFR 1.55 has been filed in the above-identified patent application, and 3) any U.S. application—as—filed from which benefit is sought in the above-identified patent application.

In accordance with 37 CFR 1.14(c), access may be provided to information concerning the date of filing the Authorization to Permit Access to Application by Participating Office.

Logal Name of Inventor	Kenji FURUSA	\WA		
Inventor's Signature	<u>Kenjî</u>	Furusawa	Date June 9, 2013	
Legal Name of Additional [n	ventor, If Any	Mitsuya FUKAZAWA		
inventor's Signature			Data	

Docket No.: Only After Sept 16, 2012

ASSIGNMENT WITH DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION (37 CFR 1.63)

Whereas, I/We, the undersigned inventor(s) hereinafter called assignor(s), have invented certain improvements described in the application identified below; and

Whereas, Renesas Electronics Corporation of Kanagawa, Japan, (assignee), desires to acquire the entire right, title, and interest in the application and invention, and to any United States patents to be obtained therefor;

Now therefore, for valuable consideration, receipt whereof is hereby acknowledged,

I/We, the above named assignor(s), hereby sell, assign and transfer to the above named assignee, its successors and assigns, the entire right, title and interest in the application and the invention disclosed therein for the United States of America, including all divisions, and continuations thereof, and all Letters Patent of the United States that may be granted thereon, and all reissues thereof, including the right to claim priority under 35 USC 119, and I/we request the Director of the U.S. Patent and Trademark Office to issue any Letters Patent

Pranted about the invention set lottly i	n the ar	request the Director of the U.S. Patent and Trademark Office to issue any Letters Patent oplication to the assignee, its successors and assigns; and I/we will execute without further y the assignee in connection with the United States application when called upon to do so by
(Legalization not required for recording	g but is	prima facie evidence of execution under 35 USC 261)
As the below named inventor, I hereby	/ declar	e that:
This assignment with declaration is directed to:		The attached application, or
	X	United States Application or PCT International Application Number 13/738546
		filed on January 10, 2013.
The application is entitled:		
	į	REFERENCE VOLTAGE GENERATING CIRCUIT
The above identified application was m	ade or v	was authorized to be made by me.
I believe that I am the original inventor	or an o	riginal joint inventor of a claimed invention in the application.
I have reviewed and understand the co	ntents	of the application for which this assignment with declaration is being submitted.
		e all information known to me to be material to patentability as defined in 37 CFR 1.56.
	ilse stat	tement made in this assignment with declaration is purishable and a USO 4004 to
Authorization To Permit Access To A	pplication	on by Participating Office
property offices in which a foreign application. See 37 CFR 1.14(c)	ication of and (h).	the USPTO authority to provide the European Patent Office (EPO), the Japan Patent Office (KIPO), the World Intellectual Property Office (WIPO), and any other intellectual claiming priority to the above-identified application is filed access to the above-identified. This box should not be checked if the applicant does not wish the EPO, JPO, KIPO, or eign application claiming priority to the above-identified application is filed to have
n accordance with 37 CFR 1.14(h)(3), a dentified patent application—as—filed, 2)	any for	will be provided to a copy of the application—as—filed with respect to: 1) the above—reign application to which the above—identified application claims priority under 35 USC

19(a)-(d) if a copy of the foreign application that satisfies the certified copy requirement of 37 CFR 1.55 has been filed in the aboveidentified patent application, and 3) any U.S. application-as-filed from which benefit is sought in the above-identified patent application.

In accordance with 37 CFR 1.14(c), access may be provided to information concerning the date of filing the Authorization to Permit Access to Application by Participating Office.

> Page 1 of 2

PATENT REEL: 035109 FRAME: 0268

Folia Harrie OL TITAGLICOL	Keilli FUKUSAN			
Inventor's Signature			Date_	
Legal Name of Additional	Inventor, If Any	Mitsuya FUKAZAWA		
Inventor's Signature	Mitsuva	Fukazawa	Date	June 4, 2013

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