PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT3308627

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
FREDERICK A. WARE	10/25/2012
AMIR AMIRKHANY	11/12/2012
SURESH RAJAN	10/26/2012
MOHAMMAD HEKMAT	11/01/2012
DINESH PATIL	10/25/2012

RECEIVING PARTY DATA

Name:	Rambus Inc.	
Street Address:	t Address: 1050 Enterprise Way, Suite 700	
City:	Sunnyvale	
State/Country:	CALIFORNIA	
Postal Code:	94089	

PROPERTY NUMBERS Total: 2

Property Type	Number
Application Number:	14023970
Application Number:	14683080

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 408-462-8000 twain@rambus.com
Correspondent Name: TARISA WAIN

Address Line 1: 1050 ENTERPRISE WAY #700
Address Line 4: SUNNYVALE, CALIFORNIA 94089

ATTORNEY DOCKET NUMBER: RA1421.C1.US +1

NAME OF SUBMITTER: TARISA WAIN

SIGNATURE: /Tarisa Wain/

DATE SIGNED: 04/13/2015

Total Attachments: 6

PATENT REEL: 035398 FRAME: 0242

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PATENT REEL: 035398 FRAME: 0243

ASSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, and pursuant to my obligation to Rambus Inc. and/or its affiliates to assign intellectual property, and pursuant to any other applicable agreements regarding the assignment of intellectual property among Rambus Inc. and its affiliates, I, as one of the person(s) named below

Frederick A. Ware of Los Altos Hills, CA Amir Amirkhany of Sunnyvale, CA Suresh Rajan of Fremont, CA Mohammad Hekmat of Mountain View, CA Dinesh Patil of Sunnyvale, CA

have sold, assigned, and transferred and/or I do hereby sell, assign, and transfer to Rambus Inc., a Delaware corporation having a place of business at 1050 Enterprise Way, Suite 700, Sunnyvale CA 94089 ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. My entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the patent application ("APPLICATION")

titled	l "Semiconductor Memory Syst	ems With On-Die Data Buffer	ing", having:
X	United States Patent Application No. 61/714,666 , filed on October 16, 2012 ;		
	which claims priority to U.S. I NUMBER] filed on [Enter DA	Patent Application No. [Enter / ATE] ; or	APPLICATION
	inserted by (), filed () on () and request ASSIGNEE or its age ation number and filing date w and signed initials of the perso), initials () ents to insert here in hen known, along

2. The entire worldwide right, title, and interest in and to: (a) the APPLICATION; (b) all applications claiming priority from the APPLICATION; (c) all provisional, utility, utility model, design, divisional, continuation, continued prosecution, continuation-in-part, substitute, renewal, and other applications related thereto which have been or may be filed in the United States or elsewhere in the world related to the SUBJECT MATTER; (d) all patents (including reissues, re-examinations, and extensions) which may be granted on the applications set forth in (a), (b), and (c) above; and (e) all right of priority in the APPLICATION and in any underlying provisional or foreign application, together with all rights to recover damages for infringement of provisional rights.

I agree that ASSIGNEE may apply for and receive patents for SUBJECT MATTER in ASSIGNEE's own name.

As part of this Assignment, I further agree to do the following, when requested, and without further consideration: (1) execute all oaths, declarations, assignments, powers of attorney, applications, and other papers necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and

Docket No.: RA1421.Prov1.US Page 1 of 2 App. No.: 61/714,666

PATENT

enforcing worldwide patent protection relating to the SUBJECT MATTER and for vesting in ASSIGNEE the rights, titles, and interests conveyed. I further agree to provide any successor, assign, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

I represent that I had or still have the right, title, and interest to convey as set forth herein, and covenant with ASSIGNEE that I have not made and will not make any assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests conveyed.

I grant ASSIGNEE or its agents the power to insert on this Assignment any further identification that may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

The terms and covenants of this Assignment shall inure to the benefit of ASSIGNEE, its successors, assigns, and legal representatives, and shall be binding upon me, and my heirs, legal representatives and assigns.

This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

Each Inventor: Please Sign and Date Below:

Inventor Name: Frederick A. Ware	(0/25/12 Date
Inventor Name: Amir Amirkhany	Date
Inventor Name: Suresh Rajan	10/26/12 Date
Inventor Name: Mohammad Hekmat	Date
Inventor Name: Dinesh Patil	10/25/2012
miventor Name: Dinesh Patil	Date

Docket No.: RA1421.Prov1.US Page 2 of 2 App. No.: 61/714,666

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•	JBJECT MATTER") that are disclosed in the patent application ("APPLICATION") d "Semiconductor Memory Systems With On-Die Data Buffering", having:
X	United States Patent Application No. 61/714,666, filed on October 16, 2012;
	which claims priority to U.S. Patent Application No. [Enter APPLICATION NUMBER] filed on [Enter DATE]; or
	Application number (), filed (), inserted by () on (), initials (
ı	The entire worldwide right, title, and interest in and to: (a) the APPLICATION:

2. The entire worldwide right, title, and interest in and to: (a) the APPLICATION; (b) all applications claiming priority from the APPLICATION; (c) all provisional, utility, utility model, design, divisional, continuation, continued prosecution, continuation-in-part, substitute, renewal, and other applications related thereto which have been or may be filed in the United States or elsewhere in the world related to the SUBJECT MATTER; (d) all patents (including reissues, re-examinations, and extensions) which may be granted on the applications set forth in (a), (b), and (c) above; and (e) all right of priority in the APPLICATION and in any underlying provisional or foreign application, together with all rights to recover damages for infringement of provisional rights.

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<u> </u>	
Inventor Name: Frederick A. Ware	Date
	,
Inventor Name: Amir Amirkhany	Date
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Inventor Name: Suresh Rajan	Date
Jan July of Consequence of Consequen	
m. Hekural	Nov. 1, 2012
Inventor Name: Mohammad Hekmat	Date
	•
<u> </u>	
Inventor Name: Dinesh Patil	Date

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•	"Semiconductor Memory Sys	'		•
<u>X</u>	United States Patent Applic	ation No. 61/714,66	6, filed on Oct	ober 16, 2012;
	which claims priority to U.S. NUMBER] filed on [Enter D	• • •	No. [Enter AP F	PLICATION
	Application number (inserted by ([Where I hereby authorize a each parentheses the applic with the printed name, date section].	on () on (and request ASSIGNE cation number and fi	iling date wher	_), initials () s to insert here in n known, along

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Inventor Name: Dinesh Patil	Date

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PATENT REEL: 035398 FRAME: 0249

RECORDED: 04/13/2015