

## PATENT ASSIGNMENT COVER SHEET

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<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT	
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT	
<b>CONVEYING PARTY DATA</b>		
<b>Name</b>		<b>Execution Date</b>
SONY CORPORATION		11/12/2014
<b>RECEIVING PARTY DATA</b>		
<b>Name:</b>	TESSERA ADVANCED TECHNOLOGIES, INC.	
<b>Street Address:</b>	3025 ORCHARD PARKWAY	
<b>City:</b>	SAN JOSE	
<b>State/Country:</b>	CALIFORNIA	
<b>Postal Code:</b>	95134	
<b>PROPERTY NUMBERS Total: 38</b>		
<b>Property Type</b>	<b>Number</b>	
Patent Number:	6043145	
Patent Number:	6043699	
Patent Number:	6552943	
Patent Number:	6518829	
Patent Number:	7064005	
Patent Number:	7502350	
Patent Number:	6914561	
Patent Number:	7116277	
Patent Number:	7202820	
Patent Number:	7295163	
Patent Number:	7091709	
Patent Number:	7193923	
Patent Number:	7439118	
Patent Number:	7601579	
Patent Number:	7571282	
Patent Number:	8035230	
Patent Number:	7691672	
Patent Number:	7542355	
Patent Number:	8019314	
Patent Number:	5987574	

PATENT

Property Type	Number
Patent Number:	6472922
Patent Number:	7718524
Patent Number:	8405221
Patent Number:	8150906
Application Number:	13371116
Application Number:	14478905
Patent Number:	7406649
Patent Number:	8166371
Patent Number:	8175976
Application Number:	13449640
Application Number:	13584933
Application Number:	13867770
Patent Number:	6101120
Application Number:	14061192
Patent Number:	7051291
Patent Number:	7581195
Patent Number:	8352882
Application Number:	13691922

#### CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	ACQ-SONY
NAME OF SUBMITTER:	ANDREW ARANDA
SIGNATURE:	/Andrew Aranda/
DATE SIGNED:	04/16/2015

##### Total Attachments: 8

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## DEED OF ASSIGNMENT

THIS DEED OF ASSIGNMENT ("**Assignment**"), EFFECTIVE AS OF October 12, 2014, IS MADE BY AND BETWEEN

Sony Corporation (hereinafter "**ASSIGNOR**"), a corporation organized under the laws of Japan with its principal place of business located at 1-7-1 Konan Minato-ku, Tokyo, 108-0075 Japan; and

TESSERA ADVANCED TECHNOLOGIES, INC. (hereinafter "**ASSIGNEE**"), a Delaware corporation having a place of business at 3025 Orchard Parkway, San Jose, California 95134, United States.

WHEREAS:

- A **ASSIGNOR** is the sole owner in respect of the patents and patent applications listed in the attached Appendix (hereinafter "**the PATENTS**"); and
- B **ASSIGNEE** is desirous of acquiring all of the worldwide right, title and interest in and to the **PATENTS** and the inventions disclosed therein.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, **ASSIGNOR** has sold, assigned and transferred, and does hereby sell, assign and transfer to **ASSIGNEE** all of the worldwide right, title and interest in (i) the **PATENTS** and the inventions and improvements first disclosed therein; (ii) all reissues, divisionals, continuations, continuations-in-part, extensions, renewals, reexaminations and foreign counterparts thereof, and other patents, patent applications, certificates of invention other governmental grants resulting from the **PATENTS**; (iii) all patents and applications which claim priority to or have common priority with any such patents or patent applications, and (iv) all rights corresponding to any of the foregoing throughout the world (including the right to claim the priority date of any of the **PATENTS** and the right to sue for and recover damages for any past, present or future infringement of the Patents), the same to be held and enjoyed by **ASSIGNEE** for its own use and enjoyment, and for the use and enjoyment of its successors, assigns and other legal representatives, to the end of the term or terms of said **PATENTS** granted or reissued or reexamined as fully and entirely as the same would have been held and enjoyed by **ASSIGNOR**, if this assignment and sale had not been made.

IN WITNESS WHEREOF, **ASSIGNOR** has caused these presents to be signed by its duly appointed trustee having full authority to convey its property; and **ASSIGNEE** has caused these presents to be signed by its duly appointed trustee.

And if the issue date and/or patent number of any of the **PATENTS** is unknown to **ASSIGNOR** and **ASSIGNEE** at the time this Assignment is executed, **ASSIGNOR** does hereby authorize its attorneys to insert on this Assignment the issue date and patent number of said any patent when known.

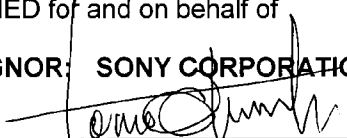
**ASSIGNOR** hereby declares that **ASSIGNEE** may take the steps for recordal of this assignment in the sole name of **ASSIGNEE**.

*Signature page follows*

SIGNED for and on behalf of

ASSIGNOR: SONY CORPORATION

By

  
(Signature)

on

Nov. 12, 2014  
(Date)

Tomonori Okuwaki  
General Manager, Patent Department  
Intellectual Property Division  
(Print Name and Title)

*Assignor's Signature page to Deed of Assignment*

SIGNED for and on behalf of

**ASSIGNEE: TESSERA ADVANCED TECHNOLOGIES, INC.**

by \_\_\_\_\_ on \_\_\_\_\_  
(Signature) (Date)

\_\_\_\_\_  
(Print Name and Title)

*Assignee's Signature page to Deed of Assignment*

APPENDIX TO DEED OF ASSIGNMENT  
ASSIGNED PATENTS AND PATENT APPLICATIONS

Date Format (MM/DD/YYYY)

Patent Number	Filing Date	Application Number	Issue Date	Title
US 6043145	3/7/97	08/813350	3/28/00	Method for making multilayer wiring structure
US 6043699	11/3/98	09/184975	3/28/00	Level shift circuit
JP 3731322	11/4/97	409301889	10/21/05	LEVEL SHIFT CIRCUIT
US 6552943	8/31/00	09/652512	4/22/03	Sense amplifier for dynamic random access memory ("DRAM") devices having enhanced read and write speed
US 6518829	12/4/00	09/730207	2/11/03	Driver timing and circuit technique for a low noise charge pump circuit
JP 3462184	3/19/01	2001078532	8/15/03	DRIVER TIMING AND CIRCUIT TECHNIQUE FOR A LOW NOISE CHARGE PUMP CIRCUIT
US 7064005	5/10/02	10/474863	6/20/06	Semiconductor apparatus and method of manufacturing same
US 7502350	1/9/03	10/338914	3/10/09	Communication apparatus, method and programs
JP 3606259	1/16/02	2002007476	10/15/04	
US 6914561	3/25/03	10/395078	7/5/05	Wide band antenna
JP 4029274	4/9/02	2002106417	10/26/07	
JP 4379470	12/28/06	2006353767	10/2/09	
US 7116277	4/18/05	11/107723	10/3/06	Wide band antenna
US 7202820	4/18/05	11/107801	4/10/07	Wide band antenna
US 7295163	6/27/06	11/475218	11/13/07	Wide band antenna
US 7091709	9/7/04	10/935654	8/15/06	Constant voltage power supply circuit
JP 4029812	9/8/03	2003315249	10/26/07	
US 7193923	8/11/05	11/201309	3/20/07	Semiconductor memory device and access method and memory control system for same

Patent Number	Filing Date	Application Number	Issue Date	Title
Pending (DE)		Entering the national phase for pending EU application number 05291757.2		A Semiconductor Memory System
Pending (EP)	8/19/05	05291757.2		Semiconductor memory device and access method and memory control system for same
Pending (FR)		Entering the national phase for pending EU application number 05291757.2		A Semiconductor Memory System
Pending (UK)		Entering the national phase for pending EU application number 05291757.2		A Semiconductor Memory System
JP 5007485	8/26/04	2004246249	6/8/12	
KR 1165429	8/23/05	20050077501	7/6/12	A Semiconductor Memory System
US 7439118	2/17/06	11/356930	10/21/08	Method of manufacturing semiconductor integrated circuit
CN ZL200610004128.0	2/21/06	200610004128.0	4/22/09	Method of manufacturing semiconductor integrated circuit
JP 4453572	2/22/05	2005045629	2/12/10	
KR 1190444	2/21/06	20060016784	10/5/12	A Fabrication Method of A Semiconductor Integrated Circuit
TW I306603	2/14/06	95104909	2/21/09	A Fabrication Method of A Semiconductor Integrated Circuit
US 7601579	12/5/07	11/950675	10/13/09	Method of manufacturing semiconductor integrated circuit
US 7571282	7/27/06	11/493658	8/4/09	Computer system having a flash memory storage device
JP 4961693	7/29/05	2005220940	4/6/12	
US 8035230	11/20/06	11/561590	10/11/11	Semiconductor device and method for manufacturing same
CN ZL200610145779.1	11/21/06	200610145779.1	10/27/10	Semiconductor device and its production method
JP 4529880	11/21/05	2005335309	6/18/10	



Patent Number	Filing Date	Application Number	Issue Date	Title
TW I371083	11/9/06	95141557	8/21/12	Semiconductor device and method of manufacturing thereof
US 7691672	4/30/07	11/799023	4/6/10	Substrate treating method and method of manufacturing semiconductor apparatus
CN ZL200710104506.7	5/25/07	200710104506.7	1/13/10	Substrate treating method and method of manufacturing semiconductor apparatus
US 7542355	9/25/07	11/902721	6/2/09	Semiconductor storage device
KR 1402100	10/4/07	20070099600	5/26/14	a semiconductor memory device
US 8019314	6/11/08	12/137091	9/13/11	Radio communication apparatus
CN ZL200810130511.X	6/26/08	200810130511.X	2/13/13	Radio communication apparatus
JP 4416014	6/26/07	2007167376	12/4/09	
US 5987574	4/30/97	08/846941	11/16/99	Bank arbitration for SDRAM memory control
US 6472922	1/14/99	09/231940	10/29/02	System and method for flexibly distributing timing signals
US 7718524	9/11/06	11/530710	5/18/10	Method of manufacturing semiconductor device
US 8405221	6/17/11	13/067660	3/26/13	Semiconductor device and multilayer semiconductor device
Pending (CN)	7/22/11	201110206870.0		Semiconductor device and multilayer semiconductor device
8150906	11/12/02	10/471608	4/3/12	Information delivery system for generating a data stream with a server system based on a content file received from a client device
ZL02809481.6	11/12/02	02809481.6	10/4/06	Information delivery system, method, information processing apparatus, and method
Pending (US)	2/10/12	13/371116		INFORMATION DELIVERY SYSTEM FOR GENERATING A DATA STREAM WITH A SERVER SYSTEM BASED ON A CONTENT FILE RECEIVED FROM A CLIENT DEVICE
Pending (US)	9/5/14	14/478,905		INFORMATION DELIVERY SYSTEM, METHOD, INFORMATION PROCESSING APPARATUS, AND METHOD
US 7406649	5/26/05	11/137442	7/29/08	Semiconductor memory device and signal processing system

Patent Number	Filing Date	Application Number	Issue Date	Title
JP 4135680	5/31/04	2004161810	6/13/08	
KR 1159289	5/24/05	20050043750	6/18/12	A Semiconductor Memory System
KR 1162271	2/22/12	20120018086	6/27/12	
US 8166371	11/21/07	11/984718	4/24/12	Semiconductor memory system and signal processing system
US 8175976	1/29/03	10/353819	5/8/12	Method for providing and obtaining content
JP 3871201	1/29/02	2002020767	10/27/06	
Pending (US)	4/18/12	13/449640		METHOD FOR PROVIDING AND OBTAINING CONTENT
Pending (US)	8/14/12	13/584933		METHOD FOR PROVIDING AND OBTAINING CONTENT
Pending (US)	4/22/13	13/867770		CONTROL DEVICE, STORAGE DEVICE, AND STORAGE CONTROL METHOD
Pending (CN)	4/24/13	201310146488.4		Control device, storage device, and storage control method
Pending (JP)	5/1/12	2012104459		Nonvolatile memory
US 6101120	7/19/99	09/356364	8/8/00	Semiconductor memory device
JP 4214428	7/17/98	410203240	11/14/08	
Pending (US)	10/23/13	14/061192		
US 7051291	4/19/01	09/838484	5/23/06	System for managing data objects
DE 1148412	4/19/01	01303589.4	10/1/08	System for managing dataobjects
Pending (DE)		Entering the national phase for pending EU application number 07006951.3		System for managing dataobjects
Pending (DE)		Entering the national phase for pending EU application number 10179547.4		System for managing dataobjects
Pending (EP)	4/3/07	07006951.3		Managing data objects

<b>Patent Number</b>	<b>Filing Date</b>	<b>Application Number</b>	<b>Issue Date</b>	<b>Title</b>
Pending (EP)	9/24/10	10179547.4		Managing data objects
FR 1148412	4/19/01	01303589.4	10/1/08	System for managing dataobjects
Pending (FR)		Entering the national phase for pending EU application number 07006951.3		System for managing dataobjects
Pending (FR)		Entering the national phase for pending EU application number 10179547.4		System for managing dataobjects
UK 1148412	4/19/01	01303589.4	10/1/08	System for managing dataobjects
Pending (UK)		Entering the national phase for pending EU application number 07006951.3		System for managing dataobjects
Pending (UK)		Entering the national phase for pending EU application number 10179547.4		System for managing dataobjects
JP 4325075	4/21/00	2000121298	6/19/09	
US 7581195	3/11/05	11/078790	8/25/09	System for managing data objects
US 8352882	4/26/07	11/789911	1/8/13	System for managing data objects
Pending (US)	12/3/12	13/691922		SYSTEM FOR MANAGING DATA OBJECTS