503283499 04/28/2015

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT3330117

SUBMISSION TYPE:		NEW ASSIGNMENT	
ATURE OF CONVEYAN	NCE:	ASSIGNMENT	
CONVEYING PARTY D	ΑΤΑ		
		Name	Execution Date
ATMEL CORPORATION	1		09/28/2012
RECEIVING PARTY DA	ТА		
Name:	ARTEMI	S ACQUISITION	
Street Address:	1250 BO	RREGAS AVE.	
City:	SUNNYV	/ALE	
State/Country:	CALIFOR	RNIA	
Postal Code:	94089		
PROPERTY NUMBERS	Total: 1		
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Application Number:	1	4698205	
CORRESPONDENCE D			
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> PATENT REEL: 035516 FRAME: 0988

PATENT ASSIGNMENT

This Patent Assignment (the "<u>Assignment</u>") is entered into as of September 28, 2012 by and between Atmel Corporation, a Delaware corporation ("<u>Assignor</u>") and Artemis Acquisition LLC, a California Limited Liability Company ("<u>Assignee</u>").

RECITALS

WHEREAS, pursuant to that certain Purchase and Sale Agreement, dated as of July 12, 2012 by and between Assignor and Assignee, as amended to date, Assignor has assigned and/or agreed to assign to Assignee, all right, title and interest in and to those patents and patent applications listed in <u>Schedule 1</u> to this Assignment;

WHEREAS, Assignor and Assignee desire to execute and record this Assignment, in order to effect and/or confirm the assignment of the Transferred Patents (as defined below).

NOW, THEREFORE, the parties hereby agree as follows:

1. Assignor does hereby sell, assign, and transfer to Assignee, for itself and its successors, transferees, and assignees, the entire worldwide right, title, and interest in and to (a) the patents and patent applications listed in <u>Schedule 1</u>, (b) all patents and patent applications that claim priority to any of the foregoing, including all continuations, divisionals, reexaminations, reissues, extensions, and foreign counterparts of the foregoing; (c) all patents and patent applications from which any of the foregoing claims priority; (d) all patents that issue from any of the foregoing; and (e) the right to sue in its own name and to recover for past infringement of any applications or patents issuing therefrom together with all rights to recover damages for infringement of provisional rights (collectively, the "<u>Transferred Patents</u>").

2. Assignor agrees to execute all oaths, assignments, powers of attorney, applications, and other papers necessary or desirable to fully secure to Assignee the legal rights, titles and interests herein conveyed, when requested, and without further consideration, in order to carry out the intent of this Assignment.

Duly Authorized Representative of	ASSIGNOR	Date of Signa	ture
Atmel Corporation By: Name: Sterrifer Sussi Title: SVP		September 28	, 2012
State of			
On [DATE]	before me,	INOTARY PUBLICI	_personally
appeared [REPRESENTA]	nersonally kno	* 11 3	
basis of satisfactory evidence to linstrument and acknowledged to m capacity, and that by [his/her] signal of which the person acted, executed	be the person whose that [he/she].execute ture on the jøstrument t	d the same in [his/he	r] authorized
Notary Seal	Notary	5	
	ed California	All-Riypaec A	cknowledgment"

[ASSIGNOR SIGNATURE PAGE TO PATENT ASSIGNMENT]

PATENT REEL: 035516 FRAME: 0990

CALIFORNIA ALL-PURPOSE ACKNOWLEDGMENT

Date Personally appeared Signature: Signature: PATRICIA NNG Commission # 1897322 Notary Public - California Santa Clara Coultity My Comm. Expires Jul 27, 2014 Signature: Paten Metary Seat Above Place Metar	county of <u>Santa Clara</u>	
personally appeared	Date	Here inset Name and Title of the Officer
ATHICIA KING PATHICIA KING Commission # 1897322 Notary Public - Childrent Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Commission # 1897322 Santa Clara Countly My Comm. Expires Jul 27, 2014 Control Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Control Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly My Comm. Expires Jul 27, 2014 Santa Clara Countly Sant	personally appeared	n Skaqas
PATRICIA KING PATRICIA KING Commission # 1897322 Notary Public - California Santa Clara County My Comm Expires Jul 27, 2014 Pace Notary Seal Above Place Notary Seal Above Number of Place Notary Seal Above Number of Places Signature (Seal Above) Number of Places Signature (Seal Above) Number of Places Signate(s) Other Than Named Above:		J (Mame(s) of Signer(s)
OPTIONAL Signature of Netary Public OPTIONAL Though the information below is not required by law, it may prove valuable to persone relying on the abcument and could prevent fraudulent removal and reattachment of this form to another document. Description of Attached Document Title or Type of Document: Document Date: September 28, 2012 Number of Pages: Signer(s) Other Than Named Above:	Commission # 1897322 Notary Public - California Santa Clara County	evidence to be the person(x) whose name(x) is/x subscribed to the within instrument and acknowledge to me that he(x)(AKEy executed the same i his/bx(/)(X) and that b his/bx(/)(X) is signature(x) on the instrument th person(x), or the entity upon behalf of which th person(x) acted, executed the instrument. I certify under PENALTY OF PERJURY under th laws of the State of California that the foregoin paragraph is true and correct.
Document Date: <u>September 28, 2012</u> Number of Pages: Signer(s) Other Than Named Above:	Though the information below is not required by and could prevent fraudulent removal Description of Attached Document	View, it may prove valuable to persone relying on the document and reattachment of this form to another document.
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Capacity(ies) Claimed by Signer(s) Signer's Name:	Signer(s) Other Than Named Above:	
Signer is Representing: Signer is Representing:	Capacity(ies) Claimed by Signer(s) Signer's Name:	Signer's Name:

PATENT REEL: 035516 FRAME: 0991

Date of Signature

2012

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Duly Authorized Representative of ASSIGNEE <u>M.D.J.J.J.</u> Name: NARBEH DERHACOBIAN Title: CEO

State of	
County of	

	On	before me,					P	162280	mal	ly –
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	[REPRESENTATIVE]									

basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that [he/she] executed the same in [his/her] authorized capacity, and that by [his/her] signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

e.l

PATENT

Notary

Notary Seal

ISIGNATURE PAGE TO PATENT ASSIGNMENT

CALIFORNIA ALL-PURPOSE CERTIFICATE OF ACKNOWLEDGMENT

State of California	
Countral South Claren	
County of	
On 9/28/12 before me, Sha	
On $Con $ before me, $SV(\gamma)$	(Here insert name and title of the officer)
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the within instrument and acknowledged to me that	ice to be the person(s) whose name(s) is/are subscribed to he/she/they executed the same in his/her/their authorized i the instrument the person(s), or the entity upon behalf of
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DESCRIPTION OF THE ATTACHED DOCUMENT	annears above in the natary section or a separate acknowledgment form must be
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	verbiage does not require the notary to do something that is illegal for a nanary in California (i.e. certifying the authorized capacity of the tagner). Please check the
(Title or description of attached document communed)	document carefully for proper notarial wording and attack this form if required.
(The of assurption of analytic document community)	* State and County information must be the State and County where the document
Number of Pages Document Date 7/20//2	signer(s) personally appeared before the notary public for acknowledgment.
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(Additional information)	must also be the same date the acknowledgment is completed. • The notary public must print his or her name as it appears within his or her
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	 prim the name(s) of document signer(s) who personally appear at the time of notarization
CAPACITY CLAIMED BY THE SIGNER	 Indicate the correct singular or plural forms by crossing off incontect forms (i.e.
Individual (s)	he/she/they, is /are) or enving the correct forms. Failure to correctly indicate this
Corporate Officer	information may lead to rejection of document recording. • The notary scal impression must be clear and photographically reproducible
	Impression must not cover text or lines. If seal impression smudges, re-seal if a
(Title)	sufficient area permits, otherwise complete a different acknowledgment form. Signature of the notary public must match the signature on file with the office of
Pariner(s)	the county clerk.
Attorney-in-Fact	 Additional information is not required but could help to ensure this acknowledgment is not misused or attached to a different document.
☐ Trustee(s) □ Other	 indicate title or type of anached document, tamber of pages and date
	4 Indicate the capacity claimed by the signer. If the claimed capacity is a

Indicess the capacity claimed by the signer. If the claimed capacity is a corporate officer, indicate the title (i.e. CEO, CFO, Secretary).
 Securely attach this document to the signed document

2008 Version CAPA v12.10.07 800-873-9865 www.NotaryClasses.com

PATENT REEL 035516 FRAME 0993

SCHEDULE 1 to PATENT ASSIGNMENT

List of Patents and Patent Applications

Application Title	Country	Application Atmber	Filing Date	Patern Number	issue Date	inventors
Method and System to Access Memory	US	12/205,518	05-Sep- 2008	7,929,356	19-Apr- 2011	DeCaro, Richard; Manea, Danut
Method and System to Access Memory	TW	98129756	03-Sep- 2009			DeCaro, Richard; Manea, Danut
Configurable Number for Dummy Bytes for a Sequential Access Memory Device	CN	2009- 100087964	04-Sep- 2009			DeCaro, Richard; Manea, Danut
Method and System to Access Memory	US	13/052,810	21-Mar- 2011	8,208,315	26- Jun- 2012	DeCaro, Richard; Manea, Danut
Method and System for Reducing Soft-Writing in a Multi-Level Flash Memory	US	11/144,174	02-Jun- 2005	7,522,455	21-Apr- 2009	Oddone, Giorgio; Bartoli, Simone; Caser, Fabio T.; Bedarida, Lorenzo
Reference Cell for High Speed Sensing in Non- Volatile Memories	US	09/602,108	21-Jun- 2000	6,411,549	25- Jun- 2002	Pathak, Jagdish; Payne, James E; Pathak (S), Saroj
Reference Cell for High Speed Sensing in Non- Volatile Memories	CN	01801049.0	14-May- 2001	ZL 01801049. 0	04- May- 2005	Pathak, Jagdish; Payne, James E; Pathak (S), Saroj
Reference Cell for High Speed Sensing in Non- Volatile Memories	TW	90114695	18-Jun- 2001	NI-167346	03-Apr- 2003	Pathak, Jagdish; Payne, James E; Pathak (S), Saroj
Low Power Voltage Regulator Circuit for Use in an Integrated Circuit Device	US	09/586,664	01-Jun- 2000	6,320,454	20- Nov- 2001	Kuo, Harry H; Payne, James E; Pathak (S), Saroj
Low Power Voltage Regulator Circuit for Use in an Integrated Circuit Device	TW	90112073	21-May- 2001	NI-166239	01- Nov- 2002	Kuo, Harry H; Payne, James E; Pathak (S), Saroj
Low Power Voltage Regulator Circuit for Use in an Integrated Circuit Device	CN	01810120.8	20-Apr- 2001	ZL 01810120. 8	08- Jun- 2005	Kuo, Harry H; Payne, James E; Pathak (S), Saroj
Low Power Voltage Regulator Circuit for Use in an Integrated Circuit Device	DE	01 928 713.5	20-Apr- 2001	1 301 982	02- Aug- 2006	Kuo, Harry H; Payne, James E; Pathak (S), Saroj
Row Decoder Circuit for Use in Programming a Memory Device	US	10/174,632	18-Jun- 2002	6,621,745	16- Sep- 2003	Manea, Danut
Row Decoder Circuit for Use in Programming a Memory Device	TW	92112834	12-May- 2003	I-238417	21- Aug- 2005	Manea, Danut

Row Decoder Circuit for Use in Programming a Memory Device	CN	03819561.5	14-Apr- 2003	ZL 03819561. 5	08-Oct- 2008	Manea, Danut
Method of Programming a Multi-Level Memory Device	US	10/190,374	02-Jul- 2002	6,714,448	30- Mar- 2004	Manea, Danut
Method of Programming a Multi-Level Memory Device	TW	92113850	22-May- 2003	I-238532	21- Aug- 2005	Manea, Danut
Method of Programming a Multi-Level Memory Device	CN	03820720.6	30-Apr- 2003	ZL 03820720. 6	20-Apr- 2011	Manea, Danut
Method of Establishing Reference Levels for Sensing Multilevel Memory Cell States	US	10/211,437	02-Aug- 2002	6,618,297	09- Sep- 2003	Manea, Danut
Method of Recovering Overerased Bits in a Memory Device	US	10/235,265	04-Sep- 2002	6,724,662	20-Apr- 2004	Manea, Danut
Dual Stage Voltage Regulation Circuit	US	10/666,324	17-Sep- 2003	7,064,529	20- Jun- 2006	Telecco, Nicola
Method for Identification of SPI	US	10/284,597	30-Oct- 2002	7,032,039	18-Apr- 2006	DeCaro, Richard
Method for Identification of SPI	TW	92128993	20-Oct- 2003	I-289748	11- Nov- 2007	DeCaro, Richard
Method for Identification of SPI	CN	03825730.0	25-Sep- 2003	ZL 03825730. 0	04-Apr- 2007	DeCaro, Richard
Method and Apparatus of a Smart Decoding Scheme for Fast Synchronous Read in a Memory System	US	10/686,243	14-Oct- 2003	7,143,257	28- Nov- 2006	Schumann, Steven; Ching, Fai; Kowshik, Vikram
Functional Register Decoding System for Multiple Plane Operation	US	10/686,401	14-Oct- 2003	7,099,226	29- Aug- 2006	Yuan, Yolanda; Guo, Jason; Tsang, Sai; Kowshik, Vikram; Schumann, Steven
Approach for Zero Dummy Byte Flash Memory Read Operation	US	10/929,899	30-Aug- 2004	6,879,535	12-Apr- 2005	Perisetty, Srinivas
Redundant Column Read in a Memory Array	US	11/131,017	17-May- 2005	7,296,196	13- Nov- 2007	Perisetty, Srinivas
Channel Discharging After Erasing Flash Memory Devices	US	11/190,722	27-Jul- 2005	7,397,699	08-Jul- 2008	Trinh, Stephen P
Column/Sector Redundancy CAM Fast Programming Scheme Using Regular Memory Core Array in Mulit-Plane Flash Memory Device	US	11/295,878	07-Dec- 2005	7,196,952	27- Mar- 2007	Trinh, Stephen P

A Method For Preventing Over-Erasing of Unused Redundt Memory Cells in a Flash Memory Having Single-Transistor Memory Cells	US	11/553,393	26-Oct- 2006	7,457,167	25- Nov- 2008	Patrascu, Dinu; Ho, On-Pong R.; Kuo (W), Wei-Yen
Device and Method for Access Time Reduction by Speculatively Decoding Non-Memory Read Commands on a Serial Interface	US	11/566,555	04-Dec- 2006	7,769,909	03- Aug- 2010	Ho, On-Pong R.; Nguyen, Dixie; Patrascu, Dinu
A New Implementation of Column Redundancy for a Flash Memory With a High Write Parallelism	US	11/611,452	15-Dec- 2006	7,551,498	23- Jun- 2009	Bartoli, Simone; Surico, Stefano; Sacco, Andrea; Mostola, Maria
A New Implementation of Column Redundancy for a Flash Memory With a High Write Parallelism	TW	096143993	20-Nov- 2007			Bartoli, Simone; Surico, Stefano; Sacco, Andrea; Mostola, Maria
A New Implementation of Column Redundancy for a Flash Memory With a High Write Parallelism	CN	2007- 80046296.9	12-Nov- 2007			Bartoli, Simone; Surico, Stefano; Sacco, Andrea; Mostola, Maria
Ultra-Deep Power-Down Mode for Memory Devices	US	13/559,320	26-Jul- 2012			DeCaro, Richard, Manea, Danut, Wang, Bill, Trinh, Stephen; Hill, Paul