

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT3338918

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
CAMBRIDGE SEMICONDUCTOR LIMITED	01/01/2015
RECEIVING PARTY DATA	
Name:	CAMBRIDGE MICROELECTRONICS LTD.
Street Address:	CAPE BUILDING, 9 JJ THOMSON AVENUE, CAMBRIDGE
City:	CAMBRIDGESHIRE
State/Country:	UNITED KINGDOM
Postal Code:	CB3 0FA
PROPERTY NUMBERS Total: 17	
Property Type	Number
Patent Number:	7531875
Patent Number:	7355226
Patent Number:	7589379
Patent Number:	6566240
Patent Number:	7230314
Patent Number:	6703684
Patent Number:	6927102
Patent Number:	6900518
Patent Number:	7411272
Patent Number:	7235439
Patent Number:	6858884
Patent Number:	7679160
Patent Number:	7301220
Patent Number:	7381606
Patent Number:	7465964
Patent Number:	8304316
Patent Number:	8174069
CORRESPONDENCE DATA	
Fax Number:	(216)621-6165
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent</i>	
PATENT	

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 2166211113
Email: carndt@rennerotto.com
Correspondent Name: MARK D SARALINO
Address Line 1: RENNER, OTTO, BOISSELLE & SKLAR, LLP
Address Line 2: 1621 EUCLID AVENUE, NINETEENTH FLOOR
Address Line 4: CLEVELAND, OHIO 44115

ATTORNEY DOCKET NUMBER:	MARSG0108
--------------------------------	-----------

NAME OF SUBMITTER:	MARK D. SARALINO
---------------------------	------------------

SIGNATURE:	/Mark D. Saralino/
-------------------	--------------------

DATE SIGNED:	05/05/2015
---------------------	------------

Total Attachments: 12

source=ConfirmationofpatentstoCamutronicsfromCamsemi#page1.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page2.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page3.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page4.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page5.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page6.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page7.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page8.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page9.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page10.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page11.tif
source=ConfirmationofpatentstoCamutronicsfromCamsemi#page12.tif

CONFIRMATION OF ASSIGNMENT OF PATENTS AND PATENT APPLICATIONS

In consideration of good and valuable consideration, the past receipt of which is hereby acknowledged, **CAMBRIDGE SEMICONDUCTOR LIMITED**, registered number 04025031, ("Assignor") hereby confirms the prior assignment and transfer, pursuant to the **PATENT ASSIGNMENT** dated January 1, 2015 and obligations thereunder, the prior sale, conveyance, assignment, and transfer to **CAMBRIDGE MICROELECTRONICS LTD**, registered number 08096560, ("Assignee"), and its successors, assigns, and legal representatives, of all right, title, and interest in and to any and all improvements that are disclosed in the issued patents and patent applications listed on the attached **EXHIBIT A** ("Assigned Patents and Applications"), together with all invention(s), modification(s), and improvement(s) set forth therein.

Said Assignee, its successors, assigns and legal representatives or nominees, be authorized and empowered to invoke and claim for any application for patent or other form of protection for said inventions filed by it or them, the benefit of the right of priority provided by the International Convention for the Protection of Industrial Property, as amended, or by any convention which may henceforth be substituted for it, and to invoke and claim such right of priority without further written or oral authorization from Assignor.

IN WITNESS WHEREOF, I have hereunto set my hand.

by

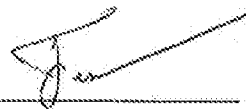
CAMBRIDGE MICROELECTRONICS LTD

Cape Building

9 JJ Thomson Avenue

Cambridge, Cambridgeshire

CB3 0FA, United Kingdom



Florin Udrea
Director

by

Cambridge Semiconductor Limited

St Andrews House

St Andrews Road

Cambridge, Cambridgeshire

CB4 1DL, United Kingdom



Martin Stephenson
Chief Financial Officer

PATENT

REEL: 035562 FRAME: 0535

EXHIBIT A

PATENT OFFICE TITLE	APPLICATION NO.	PATENT OR PUB. NO.	PRIORITY DATE	FILE DATE	ISSUE DATE
LATERAL SOI SEMICONDUCTOR DEVICE	10/556,927	US 7531875	13-May-03	22-Jan-07	12-May-09
LATERAL SOI SEMICONDUCTOR DEVICE	AU 2003227926		13-May-03		
LATERAL SOI SEMICONDUCTOR DEVICE	PCT/GB2003/002025	WO 2004/102672	13-May-03	13-May-03	NA
POWER SEMICONDUCTOR AND METHOD OF FABRICATION	11/414,308	US 7355226	8-Sep-04	1-May-06	8-Apr-08
POWER SEMICONDUCTOR AND METHOD OF FABRICATION	10/936,721	US 7589379	8-Sep-04	9-Sep-04	15-Sep-09
SOI POWER DEVICE	0419919.6	GB 2418063	8-Sep-04	8-Sep-04	
SOI RESISTOR FORMED BY UNDERLYING SUBSTRATE FOR CURRENT SENSING IN POWER SEMICONDUCTOR STRUCTURES	0526117.7	GB 2433649B	22-Dec-05	22-Dec-05	19-Jan-11
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	09/957,609	US 6566240	21-Sep-00	21-Sep-01	20-May-03
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	10/400,541	US 7230314	21-Sep-00	28-Mar-03	12-Jun-07
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	0122695.0	GB 2371922	21-Sep-00	20-Sep-01	15-Dec-04

Exhibit Page 1 of 5

EXHIBIT A

	60/234,292	NA		21-Sep-00	21-Sep-00	NA
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE						
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	09/957,547	US 6703684		21-Sep-00	21-Sep-01	9-Mar-04
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	10/694,736	US 6927102		21-Sep-00	29-Oct-03	9-Aug-05
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	10/694,735	US 6900518		21-Sep-00	29-Oct-03	31-May-05
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	11/174,605	US 7411272		21-Sep-00	6-Jul-05	12-Aug-08
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	11/174,606	US 7235439		21-Sep-00	6-Jul-05	26-Jun-07
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	200311170/28	RU 2276429 C2		21-Sep-00	20-Sep-01	10-May-06
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	10-2003-7004115	KR 10-0841141		21-Sep-00	21-Mar-03	24-Jun-08
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	JP 2002-529813	JP 5392959		21-Sep-00	20-Sep-01	25-Oct-13
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	IL 154945	IL 154945 (A)		21-Sep-00	20-Sep-01	20-Sep-07

Exhibit Page 2 of 5

EXHIBIT A

SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	EP 01969945.3	EP 1319252	21-Sep-00	20-Sep-01	15-Feb-12
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	EP 01969945	EP 1319252	21-Sep-00	20-Sep-01	15-Feb-12
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	601 46 132.0	EP 1319252	21-Sep-00	20-Sep-01	15-Feb-12
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	01969945.3	EP 1319252	21-Sep-00	20-Sep-01	15-Feb-12
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	01817643.7	CN 1233041 (C)	21-Sep-00	20-Sep-01	21-Dec-05
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	CA 2423028		21-Sep-00	20-Sep-01	NA
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	PCT/GB01/04211	WO 02/25700	21-Sep-00	20-Sep-01	NA
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	2003/02065	ZA 200302065	21-Sep-00	14-Mar-03	28-Apr-04
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	AU 2001290068	AU 2001290068	21-Sep-00	20-Sep-01	20-Jun-02

Exhibit Page 3 of 5

EXHIBIT A

SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	AU 2006200447	AU 2006200447	21-Sep-00	2-Feb-06	18-Oct-07
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	2003011640	SG 95357	21-Sep-00	11-Mar-03	29-Dec-06
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	PH/1/2003/500137	PH/1/2003/500137	21-Sep-00	18-Mar-03	31-Mar-09
SEMICONDUCTOR DEVICE	60/734,219	NA	21-Sep-00	21-Sep-00	NA
LATERAL SEMICONDUCTOR DEVICE	10/602,065	US 6858884	26-Jun-02	24-Jun-03	22-Feb-05
LATERAL SEMICONDUCTOR DEVICE	2004-516920	JP 2005-531153	26-Jun-02	19-Jun-03	
LATERAL SEMICONDUCTOR DEVICE	03730377.3	EP1516369	26-Jun-02	19-Jun-03	NA
LATERAL SEMICONDUCTOR DEVICE	03814880.3	CN 1663049A	26-Jun-02	19-Jun-03	
LATERAL SEMICONDUCTOR DEVICE	AU 2003241057	AU 2003241057 A1	26-Jun-02	19-Jun-03	
LATERAL SEMICONDUCTOR DEVICE	PCT/GB2003/002635	WO 2004/004013	26-Jun-02	19-Jun-03	NA
	60/391,431	NA	26-Jun-02	26-Jun-02	NA
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	11/216,197	US 7679160	3-Sep-04	1-Sep-05	16-Mar-10
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	05777611.4	EP 1794799	3-Sep-04	1-Sep-05	25-May-11
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	DE 60 2005 028 263.7	EP 1794799	3-Sep-04	1-Sep-05	25-May-11

EXHIBIT A

SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	PCT/GB2005/003382 60/506,899	WO 2006/024857 NA	3-Sep-04 3-Sep-04	1-Sep-05 3-Sep-04	NA NA
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	11/133,445	US 7301220	20-May-05	20-May-05	27-Nov-07
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	06727075.1	EP 1882272	20-May-05	11-May-06	
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	11/873,966	US 7381606	20-May-05	17-Oct-07	3-Jun-08
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	PCT/GB2006/001724	WO 2006/123105	20-May-05	11-May-06	NA
SEMICONDUCTOR DEVICE IN WHICH AN INJECTOR REGION IS ISOLATED FROM A SUBSTRATE	11/321,051	US 7465964	30-Dec-05	30-Dec-05	16-Dec-08
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE	11/961,410	US 8304316	20-Dec-07	20-Dec-07	6-Nov-12
POWER SEMICONDUCTOR DEVICE AND METHOD OF FORMING A POWER SEMICONDUCTOR DEVICE	12/185,231	US 8174069	5-Aug-08	5-Aug-08	8-May-12



EVERSHEDS

Dated

1 JANUARY 2015

~~2014~~

- (1) CAMBRIDGE SEMICONDUCTOR LIMITED
- (2) CAMBRIDGE MICROELECTRONICS LTD

PATENT ASSIGNMENT

THIS DEED is made on

1 JANUARY 2015

40141

BETWEEN

- (1) **CAMBRIDGE SEMICONDUCTOR LIMITED** (registered number 04025031) whose registered office is at ST Andrews House, ST Andrews Road, Cambridge, Cambridgeshire, CB4 1DL (the "**Assignor**"); and
- (2) **CAMBRIDGE MICROELECTRONICS LTD** (registered number 08096560) whose registered office is at Cape Building, 9 JJ Thomson Avenue, Cambridge, Cambridgeshire CB3 0FA (the "**Assignee**").

BACKGROUND

- (A) The Assignor is the registered proprietor of the UK and US patents detailed in **Schedule 1** and equivalent patents in other jurisdictions (the "**Patents**"); and
- (B) The Assignor has agreed to assign the Patents to the Assignee on the terms and conditions set out in this Deed.

OPERATIVE PROVISIONS

1. ASSIGNMENT

In consideration of the payment of £1 by the Assignee to the Assignor, receipt of which is hereby acknowledged, the Assignor assigns to the Assignee with full title guarantee:

- 1.1 the Assignor's right to apply for, prosecute and obtain patent or similar protection throughout the world for the Patents and the right to claim priority therefrom (including under the Paris Convention when making applications in countries or territories outside the UK), so that the grant of any patent or similar protection shall be in the name of and vest in the Assignee.
- 1.2 all the rights of the Assignor to, and its title to and interest in, the Patents; and
- 1.3 subject to clause 2, all and any other rights and powers arising or accruing from the Patents, including the right to sue for damages and to have the benefit of any other remedies for infringement of the Patents occurring before the date of this Deed.

2. UNINTENTIONAL USE

The Assignee and its successors in title shall not, at any time, bring any claim or commence any proceedings against the Assignor, or any of the Assignor's group companies, in connection with unintentional use of the Patents by the Assignor or its group companies after the date of this Deed.

3. **ASSIGNOR'S AND ASSIGNEE'S COVENANTS AND WARRANTIES**

3.1 The Assignor agrees at the expense of the Assignee to execute such further documents, and take such reasonable actions and do such reasonable things as may be reasonably requested by the Assignee, to give full effect to the terms of this Deed, and to secure the full right title and interest of the Assignee in the Patents.

3.2 The Assignee agrees to indemnify the Assignor against any cost or expenses incurred due to the Assignor failing or unreasonably delaying to record the assignment and its rights in the Patents on all the relevant registers.

4. **CONTRACTS (RIGHTS OF THIRD PARTIES) ACT 1999**

The parties to this Deed do not intend that any of its terms will be enforceable by virtue of the Contracts (Rights of Third Parties) Act 1999 by any person not a party to it.

5. **GOVERNING LAW AND JURISDICTION**

The formation, existence, construction, performance, validity and all aspects whatsoever of this Deed or of any term of this Deed shall be governed by the law of England and Wales. The courts of England and Wales will have exclusive jurisdiction to settle any disputes which may arise out of or in connection with this Deed. The parties irrevocably agree to submit to that jurisdiction except that each party may seek Injunctive relief in any court of competent jurisdiction.

This document has been executed and delivered as a deed on the date stated at the beginning of this document.

SCHEDULE 1

The Patents

PATENT TITLE	OFFICE	APPLICATION NO.	PATENT OR PUB. NO.	Patent Office File Date	ISSUE DATE
LATERAL SOI SEMICONDUCTOR DEVICE		10/556,927	US7531875	22-Jan-2007	12-May-09
POWER SEMICONDUCTOR AND METHOD OF FABRICATION		11/414,308	US7355226	1-May-2006	8-Apr-08
SEMICONDUCTOR-ON-INSULATOR STRUCTURES		0526117.7	GB2433649B	22-Dec-2005	19-Jan-11
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE		09/957,609	US6566240	21-Sept-2001	20-May-03
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE		09/957,547	US6703684	21-Sept-2001	9-Mar-04
LATERAL SEMICONDUCTOR DEVICE		10/602,065	US6858884	24-Jun-2003	22-Feb-05
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE		11/216,197	US7679160	1-Sept-2005	16-Mar-10
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A SEMICONDUCTOR DEVICE		11/133,445	US7301220	20-May-2005	27-Nov-07
SEMICONDUCTOR DEVICE IN WHICH AN INJECTOR REGION IS ISOLATED FROM A SUBSTRATE		11/321,051	US7465964	30-Dec-2005	16-Dec-08

EXECUTED as a DEED)
by Cambridge Semiconductor Limited)
acting by)



Director

in the presence of:

Witness Signature: *Olivia Halliday*

Witness Name: OLIVIA HALLIDAY

Witness Address: 10 TERESA AVENUE, CAMBERLEY, SURREY, GU15 2LB

Witness Occupation: MEDICAL STUDENT

EXECUTED as a DEED)
by Cambridge Microelectronics Ltd acting by)
)
)

Director

in the presence of:

Witness Signature:

Witness Name:

Witness Address:

Witness Occupation:

EXECUTED as a **DEED**)
by Cambridge Semiconductor Limited)
acting by)

Director

In the presence of:

Witness Signature:

Witness Name:

Witness Address:

Witness Occupation:

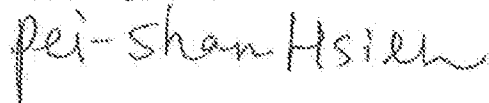
EXECUTED as a **DEED**)
by Cambridge Microelectronics Ltd acting by)



Dr. Florin Udrea
Director

In the presence of:

Witness Signature:



Witness Name: Pei-Shan Hsieh

Witness Address: 77 Canterbury Court, Canterbury Street, Cambridge, CB4 3QU, UK

Witness Occupation: Research Associate