

PATENT ASSIGNMENT COVER SHEET

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| SUBMISSION TYPE: | NEW ASSIGNMENT |
| NATURE OF CONVEYANCE: | ASSIGNMENT |
| CONVEYING PARTY DATA | |
| Name | Execution Date |
| HUAXIANG YIN | 04/13/2015 |
| CHANGLIANG QIN | 04/13/2015 |
| ZUOZHEN FU | 04/13/2015 |
| XIAOLONG MA | 04/13/2015 |
| DAPENG CHEN | 04/13/2015 |
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| Internal Address: | CHAOYANG DISTRICT |
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| State/Country: | CHINA |
| Postal Code: | 100029 |
| PROPERTY NUMBERS Total: 1 | |
| Property Type | Number |
| Application Number: | 14688788 |
| CORRESPONDENCE DATA | |
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| ATTORNEY DOCKET NUMBER: | CSPTAL56.013C1 |
| NAME OF SUBMITTER: | JOHN M. CARSON |
| SIGNATURE: | /John M. Carson/ |
| DATE SIGNED: | 05/08/2015 |

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| | This document serves as an Oath/Declaration (37 CFR 1.63). |
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Total Attachments: 3

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COMBINED DECLARATION & ASSIGNMENT (37 CFR 1.63(e))

Application Data Sheet filed previously or concurrently

Docket No.:

Page 1 of 3

Title: METHOD OF MANUFACTURING STACKED NANOWIRE MOS TRANSISTOR

Inventors: Huaxiang YIN; Changliang QIN; Zuozen FU; Xiaolong MA; Dapeng CHEN

Declaration

This Declaration is directed to the application identified above that:

Is attached, where "attached" means filed concurrently with.

As a named inventor, I declare that:

The above-identified application was made or authorized to be made by me.

I believe that I am the original inventor or an original joint inventor in the application.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 USC 1001 by fine or imprisonment of not more than five (5) years, or both.

I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

Assignment from Inventors

WHEREAS, above-identified inventors (individual(s) hereinafter "ASSIGNOR") invented certain new and useful improvements, technology, inventions, developments, ideas, ornamental designs, or discoveries, and hereby assign or are under an obligation to assign to the below identified Assignee the above-titled application (collectively hereinafter referred to as the "Work") for which an application for Letters Patent in the United States (identified above) has been prepared for filing with the United States Patent and Trademark Office (hereinafter the "Application").

AND WHEREAS, INSTITUTE OF MICROELECTRONICS, CHINESE ACADEMY OF SCIENCES, with its principal place of business at No.3 Beitucheng West Road, Chaoyang District, Beijing, 100029 P.R.China (hereinafter the "ASSIGNEE"), desires to acquire the entire right, title, and interest in and to the Application and the Work.

NOW, THEREFORE, for good and valuable consideration of which receipt is hereby acknowledged, ASSIGNOR hereby acknowledges that ASSIGNOR has sold, assigned, transferred and set over, and by these presents does hereby sell, assign, transfer and set over, unto said ASSIGNEE, its successors, legal representatives and assigns, the entire right, title, and interest throughout the world in the Application and the Work, including all patent properties filed or issued upon the Application and the Work; where "Patent Properties" include, but are not limited to:

all provisional applications relating thereto (including but not limited to U.S. Provisional Application No(s)., filed (respectively if plural applications));

all nonprovisional applications claiming priority to aforementioned provisional(s) and/or the present Application, including, all divisions, continuations, continuations-in-part, and reissues, and all Letters Patent of the United States which may be granted thereon and all reissues and extensions thereof; and

all rights of priority under International Conventions and any related Letters Patent which may hereafter be granted or filed in any country or countries foreign to the United States, all extensions, renewals and reissues thereof.

ASSIGNOR hereby acknowledges the ASSIGNEE as the Applicant for all aforementioned patent properties, and authorizes and requests the Commissioner of Patents of the United States, and any

PATENT

REEL: 035601 FRAME: 0364

COMBINED DECLARATION & ASSIGNMENT (37 CFR 1.63(e))*Application Data Sheet filed previously or concurrently*

Docket No.:

Page 2 of 3

Title: METHOD OF MANUFACTURING STACKED NANOWIRE MOS TRANSISTOR

Inventors: Huaxiang YIN; Changliang QIN; Zuozen FU; Xiaolong MA; Dapeng CHEN

Official of any country or countries foreign to the United States, whose duty it is to issue patents on applications as aforesaid, to issue all related Letters Patent to the ASSIGNEE, its successors, legal representatives and assigns, in accordance with the terms of this instrument.

AND ASSIGNOR DOES HEREBY sell, assign, transfer, and convey to ASSIGNEE, its successors, legal representatives, and assigns all claims for damages and all remedies arising out of any violation of the rights assigned hereby that may have accrued prior to the date of assignment to ASSIGNEE, or may accrue hereafter, including, but not limited to, the right to sue for, collect, and retain damages for past infringements of said Letters Patent before or after issuance.

AND ASSIGNOR DOES HEREBY covenant and agree that ASSIGNOR will communicate to said ASSIGNEE, its successors, legal representatives and assigns, any facts known to ASSIGNOR respecting the Work, and testify in any legal proceeding, assist in the preparation of any other Patent Property relating to the Application and the Work or any improvements made thereto, sign/execute all lawful papers, authorize the filing of and execute and make all rightful oaths and/or declarations in connection with the Application and the Work including any improvements made thereto, any patent applications filed therefrom, and any continuing application filed from any of the aforementioned applications, and generally do everything possible to aid the ASSIGNEE, its successors, legal representatives and assigns, to obtain and enforce proper patent protection for the Work in all countries.

Legal Name of inventor: Huaxiang YIN

Signature: _____

Date: _____

Witnessed by: _____

Witness Name (printed): Fan YANG

Legal Name of inventor: Changliang QIN

Signature: _____

Date: _____

Witnessed by: _____

Witness Name (printed): Fan YANG

PATENT**REEL: 035601 FRAME: 0365**

COMBINED DECLARATION & ASSIGNMENT (37 CFR 1.63(e))

Application Data Sheet filed previously or concurrently

Docket No.:

Page 3 of 3

Title: METHOD OF MANUFACTURING STACKED NANOWIRE MOS TRANSISTOR

Inventors: Huaxiang YIN; Changliang QIN; Zuozhen FU; Xiaolong MA; Dapeng CHEN

Legal Name of Inventor: Zuozhen FU

Signature: 付作振

Date: 4/13/2015

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Signature: 陈大鹏

Date: 4/13/2015

Witnessed by: Fan Yang

Witness Name (printed): Fan YANG

DECASSIGN

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