

PATENT ASSIGNMENT COVER SHEET

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| SUBMISSION TYPE: | NEW ASSIGNMENT |
| NATURE OF CONVEYANCE: | ASSIGNMENT |
| CONVEYING PARTY DATA | |
| Name | Execution Date |
| IXYS CH GMBH | 05/15/2015 |
| RECEIVING PARTY DATA | |
| Name: | IXYS INTL LIMITED |
| Street Address: | 103 SOUTH CHURCH STREET |
| Internal Address: | HARBOUR PLACE, 4TH FL, PO BOX 1034 |
| City: | GRAND CAYMAN |
| State/Country: | CAYMAN ISLANDS |
| Postal Code: | KY1-1102 |
| PROPERTY NUMBERS Total: 10 | |
| Property Type | Number |
| Patent Number: | 5428746 |
| Patent Number: | 5146115 |
| Patent Number: | 5263166 |
| Patent Number: | 5345564 |
| Patent Number: | 5278957 |
| Patent Number: | 5093633 |
| Patent Number: | 5109163 |
| Patent Number: | 5495594 |
| Patent Number: | 5287464 |
| Patent Number: | 5363383 |
| CORRESPONDENCE DATA | |
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|--------------------------------|------------------------|
| ATTORNEY DOCKET NUMBER: | IXYS CH TO IXYS CAYMAN |
| NAME OF SUBMITTER: | DARIEN K WALLACE |
| SIGNATURE: | /Darien K Wallace/ |
| DATE SIGNED: | 05/19/2015 |

Total Attachments: 6

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ASSIGNMENT

THIS ASSIGNMENT is made the 15th day of May 2015

BETWEEN:

IXYS CH GmbH, a Swiss limited liability company with a business address at Mattenstrasse 6, 2555 Bruegg bei Biel, Switzerland (hereinafter referred to as the "Assignor") of the one part

and

IXYS Intl Limited, a Cayman Islands company with a business address at P.O. Box 1034, Harbour Place, 4th Fl., 103 South Church St., Grand Cayman KY1-1102, Cayman Islands (hereinafter referred to as the "Assignee") of the other part.

WHEREAS:

1. The Assignor is the owner of the patents and Patent Property listed in Exhibit A ("the Patent Property").
2. In pursuance of an agreement between the Assignor and Assignee, the Assignor has agreed to assign all right title and interest in the Patent Property to the Assignee.

WHEREAS

In consideration for the payment of \$1000 USD by the Assignee to the Assignor, the receipt and sufficiency of which is hereby acknowledged:

1. The Assignor hereby assigns all his rights title and interest in the Patent Property, including right to request limitation and/or revocation of patents granted for the Patent Property, with the intent that the Assignee shall have and hold the rights, title and interest hereby assigned as fully and entirely as the same would have been held and enjoyed by the Assignor had this assignment not been made and to the extent that any patents granted pursuant to the Patent Property shall vest in the Assignee including the right to take action and claim damages and other remedies in respect of any infringements of the patent application before on or after the date hereof and to retain any damages obtained as a result of such action.
2. The Assignor hereby UNDERTAKES that at the request and cost of the Assignee or its successors or assigns, it will at all times hereafter promptly do all such acts and execute all such documents as may be reasonably necessary or desirable to secure the vesting in the Assignee or its successors or assigns of all rights, title and interest assigned to the Assignee hereunder.

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3. The Assignor hereby UNDERTAKES to confirm the title of the Assignee or its successors or assigns to all rights, title and interest assigned to the Assignee hereunder and to assist in the resolution of any question concerning the Patent Property.
4. This assignment and undertaking shall be binding upon the Assignor's heirs, executors, administrators, successors and/or assigns and shall enure to the benefit of any heirs, executors, administrators, successors and/or assigns of the Assignee.
5. This assignment is governed by Swiss law.

AS WITNESS the signature of (or of duly authorised officers of) the parties hereto the day and year first before written

SIGNED for and on behalf of)
the ASSIGNOR)

Signature: 

Name: Uzi Sasson

Position: Director

Witness

Signature: 

Name: Ramon Lopez

Address: 1570 Buckeye Dr.
Milpitas, CA 95035

SIGNED for and on behalf of)
the ASSIGNEE)

Signature: 

Name: ARON P. AEBLYKWI

Position: DIRECTOR

Witness

Signature: 

Name: Ashijeet Deodhar

Address: 1590 Buckeye Dr
Milpitas CA 95035

| Docket | Serial No. | Patent No. | Inventors | Filed | Issued | Status | Title |
|------------|------------|------------|--------------------------|------------|------------|----------|--|
| ZIL-848 | 12/590,084 | 8,106,807 | Steven M. Pope | 10/30/2009 | 1/31/2012 | patented | Network Master For Wireless Fluorescent Lamp Lighting Control Networks |
| ZIL-848-1C | 13/255,420 | 8,188,686 | Steven M. Pope | 1/20/2012 | 5/29/2012 | patented | System and Method for Processing and Forwarding Transmitted Information |
| ZIL-847 | 12/588,106 | 8,184,574 | Steven M. Pope | 9/30/2009 | 5/22/2012 | patented | Time-Hopping Low-Power Wireless Network For Turning Off And On Fluorescent Lamps |
| ZIL-846 | 12/587,062 | 8,653,935 | Murray C. Baker | 9/30/2009 | 2/18/2014 | patented | Low-Power Wireless Network Beacon For Turning Off And On Fluorescent Lamps |
| ZIL-845 | 12/587,169 | 8,653,935 | Kamrajpati et al. | 10/3/2009 | pending | pending | Dimming a Multi-Lamp Fluorescent Light Fixture By Turning Off an Individual Lamp Using a Wireless Fluorescent Lamp Starter |
| ZIL-844 | 12/587,130 | 8,653,935 | Kamrajpati et al. | 10/3/2009 | pending | pending | Turning Off Multiple Fluorescent Lamps Using RF-Enabled Lamp Starter Units |
| ZIL-843 | 12/587,152 | 8,653,935 | Saab et al. | 10/12/2009 | pending | pending | Registering a Replaceable RF-Enabled Fluorescent Lamp Starter Unit to a Master Unit |
| ZIL-840 | 12/583,167 | 8,587,415 | Christopher W. Followell | 03/26/2009 | 1/17/2012 | patented | Receiving an IR Control Signal Through a Fresnel Lens of a Motion Sensor |
| ZIL-829 | 12/291,574 | 8,165,832 | Low et al. | 11/12/2008 | 4/24/2012 | patented | Wall Plug Power Monitor |
| ZIL-802 | 12/296,562 | 8,754,584 | Lloyd et al. | 9/30/2008 | 12/16/2011 | patented | Pulse Matching Delay-Locked Loop |
| ZIL-790 | 12/319,775 | 8,074,033 | Gyle D. Yearsley | 11/12/2009 | 8/7/2011 | patented | Cooperating Memory Controllers that Share Data Bus Terminals for Accessing Wide External Devices |
| ZIL-780-1C | 13/317,674 | 8,244,994 | Gyle D. Yearsley | 10/25/2011 | 8/7/4/2012 | patented | Cooperating Memory Controllers that Share Data Bus Terminals for Accessing Wide External Devices |
| ZIL-778 | 12/229,574 | 8,244,994 | Roberts et al. | 8/22/2008 | pending | pending | Bit-Flipping Memory Controller to Prevent Data Remanence |
| ZIL-773 | 12/154,611 | 7,675,447 | David S. Coulson | 5/24/2008 | 3/9/2010 | patented | Low-Cost and Noise-insensitive Motion Detector |
| ZIL-772 | 12/221,161 | 8,210,440 | Hoang Minh Phai | 7/31/2008 | 7/3/2012 | patented | Low-Cost Magnetic Stripe Reader Using Independent Switching Thresholds |
| ZIL-772-1C | 13/482,891 | 8,356,759 | Hoang Minh Phai | 5/29/2012 | 1/22/2013 | patented | Low-Cost Magnetic Stripe Reader Using Independent Switching Thresholds |
| ZIL-764 | 12/214,479 | 7,583,494 | Stortini et al. | 3/23/2010 | 3/23/2010 | patented | Press-Fit Integrated Circuit Package Involving Compressed Spring Contact Beams |
| ZIL-764-1C | 12/657,994 | 7,984,489 | Stortini et al. | 1/12/2010 | 2/8/2011 | patented | Press-Fit Integrated Circuit Package Involving Compressed Spring Contact Beams |
| ZIL-744 | 11/904,749 | 7,998,090 | David D. Eaton | 9/28/2007 | 6/12/2011 | patented | General Purpose Ball Grid Array Security Cap |
| ZIL-744-1C | 12/932,141 | 8,198,142 | David D. Eaton | 2/18/2011 | 6/12/2012 | patented | General Purpose Ball Grid Array Security Cap |
| ZIL-725 | 11/796,844 | 7,525,828 | Joshua J. Nefti | 4/27/2007 | 12/8/2009 | patented | Switch-Free Clock Multiplexer That Provides an Output Clock Signal Based on Edge Deflection |
| ZIL-725-1C | 12/286,590 | 7,993,748 | Joshua J. Nefti | 4/27/2007 | 12/8/2009 | patented | Switch-Free Clock Multiplexer That Provides an Output Clock Signal Based on Edge Deflection |
| ZIL-678 | 11/701,137 | 7,952,099 | Paul G. Clark | 1/31/2007 | 2/14/2010 | patented | Frequency Trimming for Internal Oscillator for Test-Time Reduction |
| ZIL-678-1C | 12/927,843 | 8,068,893 | Paul G. Clark | 11/27/2010 | 11/15/2011 | patented | Frequency Trimming for Internal Oscillator for Test-Time Reduction |
| ZIL-662 | 11/601,003 | 7,414,553 | Anatoliy V. Tsyrganovich | 11/17/2006 | 8/19/2008 | patented | Microcontroller Having In-Situ Autocalibrated Integrating Analog-to-Digital Converter |
| ZIL-662-1C | 12/228,824 | 7,733,250 | Anatoliy V. Tsyrganovich | 8/15/2008 | 6/8/2010 | patented | Microcontroller Having In-Situ Autocalibrated Integrating Analog-to-Digital Converter |
| ZIL-654 | 11/479,037 | 8,259,562 | David R. Staab | 6/30/2006 | 9/18/2012 | patented | Open-Loop Transimpedance Amplifier for Infrared Diodes |
| ZIL-654-1C | 13/572,688 | 8,334,724 | David R. Staab | 1/14/2012 | 12/18/2012 | patented | Open-Loop Transimpedance Amplifier for Infrared Diodes |
| ZIL-649 | 11/345,803 | 8,606,183 | Gyle D. Yearsley | 2/1/2005 | 8/12/2014 | patented | Blank Bit And Processor Instructions Employing The Blank Bit |
| ZIL-649-1C | 14/246,283 | 8,606,183 | Gyle D. Yearsley | 4/8/2014 | pending | pending | Blank Bit And Processor Instructions Employing The Blank Bit |
| ZIL-646 | 11/039,233 | 7,688,307 | Anatoliy V. Tsyrganovich | 12/4/2006 | 3/30/2010 | patented | Determining the Distance an Object Has Moved Using an Accelerometer |
| ZIL-642 | 11/406,059 | 7,450,966 | Hidetsumi Hatton | 4/18/2006 | 12/2/2008 | patented | Microcontroller That Maintains Capacitors Of An Analog Circuit in A Charged State During Low Power Operation |
| ZIL-639 | 11/771,453 | 8,051,235 | Steven K. Fong | 11/1/2006 | 11/1/2011 | patented | Conditional Back-to-back Interrupt Vectoring |
| ZIL-631 | 11/495,477 | 7,411,427 | Steven K. Fong | 7/28/2006 | 8/7/2008 | patented | Clock Input Filter Circuit |
| ZIL-631-1C | 12/221,783 | 7,592,843 | Steven K. Fong | 8/5/2008 | 9/22/2009 | patented | Clock Input Filter Circuit |
| ZIL-631-2C | 12/564,803 | 7,768,319 | Steven K. Fong | 9/1/2009 | 9/3/2010 | patented | Clock Input Filter Circuit |
| ZIL-631-3C | 12/603,907 | 7,928,772 | Steven K. Fong | 7/9/2010 | 4/19/2011 | patented | Clock Input Filter Circuit |
| ZIL-628 | 11/378,785 | 7,362,255 | Anatoliy V. Tsyrganovich | 3/18/2006 | 4/22/2008 | patented | Chopping And Oversampling ADC Having Reduced Low Frequency Drift |
| ZIL-628-1C | 12/148,715 | 7,551,110 | Anatoliy V. Tsyrganovich | 4/27/2008 | 6/23/2009 | patented | Chopping And Oversampling ADC Having Reduced Low Frequency Drift |
| ZIL-627 | 11/118,680 | 7,508,038 | Ransom et al. | 4/29/2005 | 3/24/2009 | patented | ESD Protection Transistor |
| ZIL-627-1C | 12/383,534 | 7,867,528 | Ransom et al. | 3/24/2009 | 10/5/2010 | patented | ESD Protection Transistor |
| ZIL-627-2C | 12/607,669 | 7,927,944 | Ransom et al. | 9/10/2010 | 4/19/2011 | patented | ESD Protection Transistor |
| ZIL-627-3C | 13/065,940 | 8,062,941 | Ransom et al. | 4/2/2011 | 11/20/2011 | patented | ESD Protection Transistor |
| ZIL-627-4C | 13/246,520 | 8,093,121 | Ransom et al. | 9/29/2011 | 1/10/2012 | patented | ESD Protection Transistor |
| ZIL-611 | 11/512,295 | 7,414,554 | Anatoliy V. Tsyrganovich | 8/29/2006 | 8/19/2008 | patented | Linearity Correction For Analog-To-Digital Converters |
| ZIL-609 | 11/265,421 | 7,415,559 | Thomas Henry Hildebrandt | 11/1/2005 | 8/19/2008 | patented | Operation and Operand Inference for Extended-Precision Computation |
| ZIL-609-1C | 12/228,223 | 7,631,166 | Thomas Henry Hildebrandt | 6/11/2008 | 12/8/2009 | patented | Operation and Operand Inference for Extended-Precision Computation |
| ZIL-609-4C | 12/590,070 | 7,631,166 | Thomas Henry Hildebrandt | 11/2/2008 | 12/12/2006 | patented | Operation and Operand Inference for Extended-Precision Computation |
| ZIL-607 | 11/081,292 | 7,148,672 | Steven L. Holmes | 3/16/2005 | 10/4/2011 | patented | Low-Voltage Bandgap Reference Circuit With Startup Control |
| ZIL-602 | 11/495,873 | 8,033,867 | Rex L. Allison | 7/29/2006 | 10/4/2011 | patented | Sample and Hold Time Stamp for Sensing Zero Crossing of Back Electromotive Force in 3-Phase Brushless DC Motors |
| ZIL-602-1C | 13/134,141 | 8,359,093 | Rex L. Allison | 5/3/2011 | 12/22/2011 | patented | Sample and Hold Time Stamp for Sensing Zero Crossing of Back Electromotive Force in 3-Phase Brushless DC Motors |
| ZIL-602-2C | 13/740,966 | 8,647,531 | Rex L. Allison | 1/11/2013 | 9/30/2014 | patented | Sample and Hold Time Stamp for Sensing Zero Crossing of Back Electromotive Force in 3-Phase Brushless DC Motors |
| ZIL-597 | 11/448,514 | 7,725,769 | David R. Staab | 6/7/2006 | 5/25/2010 | patented | Latent VBO Reset Circuit |
| ZIL-596 | 11/026,750 | 7,102,452 | Steven L. Holmes | 12/12/2004 | 9/5/2006 | patented | Temperature-Compensated RC Oscillator |
| ZIL-596-1P | 11/200,329 | 7,176,765 | Shorb et al. | 8/9/2005 | 2/13/2007 | patented | Programmable Temperature-Compensated RC Oscillator |
| ZIL-591 | 11/437,884 | 8,543,062 | Daniel Saifu Moi | 5/19/2006 | 9/24/2013 | patented | Controlling Transmission Power in an kDAIRC Transmitter Circuit |
| ZIL-591-1C | 13/975,299 | 8,543,062 | Daniel Saifu Moi | 8/27/2004 | 5/1/2007 | patented | Controlling Transmission Power in an kDAIRC Transmitter Circuit |
| ZIL-589 | 10/528,492 | 7,212,042 | Hoang Minh Phai | 8/27/2004 | 10/28/2008 | patented | Below-Ground Sensor Interface Amplifier |
| ZIL-587 | 10/956,506 | 7,444,090 | Grace et al. | 11/22/2004 | 10/28/2008 | patented | kDAIRC Transmitter Circuit Having Low Energy Consumption |
| ZIL-586 | 10/887,290 | 7,474,857 | Alan Grace | 7/21/2004 | 1/6/2009 | patented | Recovering Energy From An kDAIRC Remote Control Transmitter Circuit |

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| ZIL-585-1C | 12/315,214 | 8,682,168 | Alan Grace | 1/21/2008 | 3/25/2014 | patented | Recovering Energy From An IRDA/Remote Control Transmitter Circuit |
| ZIL-586-2C | 14/084,172 | 7,127,538 | Joshua J. Neki | 5/21/2004 | 10/24/2006 | patented | Single-Pin, Serial Communication Link With Start-Bit Flow Control |
| ZIL-584-1D | 10/651,460 | 7,260,660 | Joshua J. Neki | 5/21/2004 | 8/21/2007 | patented | Flow Control By Supplying A Remote Start Bit Onto A Single-wire Bus |
| ZIL-584-2D-1C | 11/653,333 | 7,574,544 | Joshua J. Neki | 8/15/2007 | 8/15/2009 | patented | Flow Control By Supplying A Remote Start Bit Onto A Single-wire Bus |
| ZIL-579 | 10/844,903 | 6,993,441 | Anatoliy V. Tsyrganovich | 5/12/2004 | 1/31/2006 | patented | Adaptive Error Correction in an Oversampled ADC |
| ZIL-579-1C | 11/267,722 | 7,095,563 | Anatoliy V. Tsyrganovich | 11/4/2005 | 8/1/2006 | patented | Adaptive Error Correction in an Oversampled ADC |
| ZIL-579-1C-1P | 11/472,192 | 7,379,831 | Anatoliy V. Tsyrganovich | 6/20/2006 | 5/27/2008 | patented | Error Correction in an Oversampled ADC Using Few Stored Calibration Coefficients |
| ZIL-576 | 10/788,180 | 7,509,057 | Alan Grace | 2/25/2004 | 3/24/2009 | patented | IRDA Transceiver Module That Also Functions As Remote Control IR Transmitter |
| ZIL-576-1C | 12/363,510 | 7,711,269 | Alan Grace | 3/04/2009 | 5/4/2010 | patented | IRDA Transceiver Module That Also Functions As Remote Control IR Transmitter |
| ZIL-576-2C | 12/738,678 | 7,962,041 | Alan Grace | 4/9/2010 | 6/14/2011 | patented | IRDA Transceiver Module That Also Functions As Remote Control IR Transmitter |
| ZIL-576-3C | 10/938,578 | 7,336,076 | Provis et al. | 8/27/2004 | 1/3/2007 | patented | Generating a Mark-Space Table And A String Of Timing Information On A Remote Control Device |
| ZIL-570-2D | 10/928,520 | 7,339,533 | Provis et al. | 8/27/2004 | 3/4/2008 | patented | Interpreting A Script To Generate An Operational Signal On A Remote Control Device |
| ZIL-570-3D | 10/928,501 | 7,227,492 | Provis et al. | 8/27/2004 | 6/5/2008 | patented | Interpreting A Common Script Block To Output Various Forms Of Data According To A Common Protocol |
| ZIL-570-4D | 10/928,830 | 7,362,256 | Provis et al. | 8/27/2004 | 4/22/2008 | patented | Loading A Machine Code API Onto An 8-Bit Virtual Machine To Enable New Functionality |
| ZIL-570-4D-1C | 12/148,925 | 8,962,840 | Provis et al. | 4/22/2008 | 2/10/2015 | patented | Loading A Machine Code API Onto An 8-Bit Virtual Machine To Enable New Functionality |
| ZIL-570-4D-1C-2C | 14/617,928 | 7,230,562 | Provis et al. | 2/29/2015 | | pending | Loading A Machine Code API Onto An 8-Bit Virtual Machine To Enable New Functionality |
| ZIL-570-6D | 10/928,502 | 7,435,345 | Provis et al. | 8/27/2004 | 6/12/2007 | patented | Sending A Script To A Virtual Machine For Immediate Interpreting |
| ZIL-570-7D | 10/928,014 | 7,346,095 | Hide Hattori | 8/27/2004 | 10/14/2008 | patented | A Compact Register-Based Virtual Machine And Its Use On Resource-Constrained Devices |
| ZIL-564 | 12/077,282 | 7,742,552 | Hide Hattori | 2/20/2004 | 3/18/2008 | patented | Spread Spectrum Clock Generator with Controlled Delay Elements |
| ZIL-564-1C | 12/693,100 | 8,320,428 | Hide Hattori | 6/19/2010 | 8/22/2010 | patented | Spread Spectrum Clock Generator with Controlled Delay Elements |
| ZIL-564-2C | 10/750,232 | 6,954,083 | Thornley et al. | 12/29/2003 | 10/11/2005 | patented | Circuit For Deflection Of Hardware Faults Due To Temporary Power Supply Fluctuations |
| ZIL-563 | 10/619,544 | 7,296,187 | Fritz et al. | 7/14/2003 | 11/13/2007 | patented | Hardware Debug Device Having Script-Based Host Interface |
| ZIL-548-1P | 11/087,784 | 7,162,010 | Anatoliy V. Tsyrganovich | 3/23/2005 | 12/19/2006 | patented | Calibrating an Analog-to-Digital Converter Using a Test Signal with a Precise dc Voltage |
| ZIL-548 | 10/354,114 | 6,907,374 | Anatoliy V. Tsyrganovich | 3/19/2003 | 8/14/2005 | patented | Self-Calibrating Sigma-Delta Analog-to-Digital Converter |
| ZIL-543-1P | 10/467,390 | 7,342,984 | Yeansley et al. | 4/3/2003 | 3/11/2008 | patented | Counting Clock Cycles Over The Duration Of A First Character And Using a Remainder Value To Determine When to Sample A Bit Of A |
| ZIL-543-1C | 11/518,048 | 7,340,023 | Yeansley et al. | 9/9/2006 | 3/4/2008 | patented | Auto Baud System And Method And Single Pin Communication Interface |
| ZIL-543 | 10/284,500 | 7,116,739 | Yeansley et al. | 10/31/2002 | 10/23/2006 | patented | Auto Baud System And Method And Single Pin Communication Interface |
| ZIL-537-1P | 10/821,517 | 7,066,197 | Anatoliy V. Tsyrganovich | 4/9/2004 | 6/27/2006 | patented | ADC With Reduced Quantization Noise and Programmable Bit Resolution |
| ZIL-537 | 10/331,037 | 6,839,010 | Anatoliy V. Tsyrganovich | 12/27/2002 | 1/4/2005 | patented | Sigma-Delta Analog-To-Digital Converter With Reduced Quantization Noise |
| ZIL-528 | 09/113,036 | 6,281,999 | Watson et al. | 7/6/1998 | 9/28/2001 | patented | Optics System For Infrared Signal Transceivers |
| ZIL-524-1C | 11/146,246 | 7,353,327 | Jeffrey R. Dorst | 6/3/2005 | 4/1/2008 | patented | A Dedicated Command Port For Memory Controllers |
| ZIL-524 | 09/971,196 | 6,941,416 | Jeffrey R. Dorst | 10/4/2001 | 9/6/2005 | patented | Apparatus And Methods For Dedicated Command Port In Memory Controllers |
| ZIL-523 | 09/909,507 | 6,915,414 | Cyle D. Yeansley | 7/20/2001 | 7/5/2005 | patented | Context Switching Pipelined Microprocessor |
| ZIL-522 | 10/038,091 | 6,614,253 | Anatoliy V. Tsyrganovich | 1/3/2002 | 1/6/2004 | patented | Scan Velocity Modulation Technique |
| ZIL-521-1P | 11/305,291 | 7,091,795 | Anatoliy V. Tsyrganovich | 12/16/2005 | 8/15/2006 | patented | Modifying Ramp Angle in a Digital Frequency Locked Loop |
| ZIL-521 | 09/973,978 | 7,002,415 | Anatoliy V. Tsyrganovich | 10/21/2003 | 2/21/2006 | patented | Frequency Locked Loop |
| ZIL-520 | 09/981,578 | 6,522,091 | Anatoliy V. Tsyrganovich | 10/7/2001 | 2/18/2003 | patented | Circuit and Method That Allows the Amplitudes of Vertical Correction Signal Components to be Adjusted |
| ZIL-519-1C-1P | 10/876,358 | 7,176,239 | Anatoliy V. Tsyrganovich | 6/23/2004 | 10/20/2007 | patented | Circuit and Method for Reducing East-West Geometry Mismatch Between the Top and Bottom of a Raster Display |
| ZIL-519-1C | 10/820,237 | 7,766,217 | Anatoliy V. Tsyrganovich | 4/5/2004 | 8/3/2010 | patented | Circuit and Method for Reducing East-West Geometry Mismatch Between the Top and Bottom of a Raster Display |
| ZIL-511 | 10/033,824 | 6,717,377 | Anatoliy V. Tsyrganovich | 10/31/2001 | 4/6/2004 | patented | Digital-To-Analog Converters With Reduced Parasitics and Associated Methods |
| ZIL-314 | 09/664,858 | 7,768,440 | Bruce Troutman | 3/28/2003 | 7/27/2004 | patented | Method and System for Electronic Data Sales and Distribution Over Wide Area Computer Networks |
| ZIL-306 | 08/982,433 | 5,949,953 | DAVID CARROLL FRITZ | 9/5/2000 | 1/4/2011 | patented | Method for the Generation of ISA Simulators and Assemblers from a Machine Language |
| ZIL-305 | 08/195,311 | 5,841,365 | JOSEPH S. RIMKUS | 2/10/1994 | 11/24/1998 | patented | Method and Apparatus for Communicating with a Product Label |
| ZIL-304 | 08/125,518 | 5,667,061 | THOMAS E. KRUEGER | 8/18/1998 | 11/13/2001 | patented | Method and Apparatus for Providing Information Concerning Products Using Radio Frequency Transmissions |
| ZIL-303 | 08/099,058 | 5,335,249 | THOMAS E. KRUEGER | 7/29/1993 | 8/21/1994 | patented | METHOD AND APPARATUS FOR SPREAD SPECTRUM COMMUNICATIONS |
| ZIL-305-1C | 11/090,161 | 7,212,749 | Hamilton et al. | 3/15/2005 | 5/1/2007 | patented | Signal Receiver Having Wide Band Amplification Capability |
| ZIL-306 | 09/212,203 | 6,915,083 | Hamilton et al. | 12/15/1998 | 7/5/2005 | patented | Signal Receiver Having Wide Band Amplification Capability |
| ZIL-303-1C | 09/143,150 | 6,317,235 | T. ALLAN HAMILTON | 8/28/1998 | 11/13/2001 | patented | Method and System for Preventing Burn-out of Infrared Transmitter Diodes |
| ZIL-303 | 10/349,735 | 7,123,840 | T. Allan Hamilton | 11/22/2003 | 10/17/2006 | patented | Infrared Signal Communication System and Method Including Transmission Means Having Automatic Gain Control |
| ZIL-302 | 09/131,825 | 6,590,582 | T. Allan Hamilton | 8/10/1998 | 7/8/2003 | patented | Infrared Signal Communication System and Method Including Transmission Means Having Automatic Gain Control |
| ZIL-300-1P-1C | 11/596,956 | 7,722,244 | Hamilton et al. | 10/25/2006 | 4/20/2010 | patented | Circuit Design and Optics System for Infrared Signal Transceivers |
| ZIL-300-1P-2C | 09/580,224 | 7,181,144 | Hamilton et al. | 4/28/2000 | 2/20/2007 | patented | Improved Circuit Design and Optics System for Infrared Signal Transceivers |
| ZIL-300-1P-3C | 09/580,224 | 6,642,112 | Lowie et al. | 7/30/2001 | 11/4/2003 | patented | Non-Oxidizing Spacer Denatation Method for Manufacturing Semiconductor Devices |
| ZIL-244 | 09/135,154 | 7,221,295 | T. Allan Hamilton | 8/7/1998 | 5/22/2007 | patented | System and Method for Providing an Improved Standby Mode for Infrared Data Transceivers |
| ZIL-244 | 09/567,948 | 6,340,903 | James W. Leith | 9/8/2003 | 11/29/2005 | patented | Architecture to Relax Memory Performance Requirements |
| ZIL-235 | 09/539,764 | 6,339,368 | James W. Leith | 5/10/2000 | 1/22/2002 | patented | Architecture to Relax Memory Performance Requirements |
| ZIL-232 | 09/511,512 | 6,255,902 | Troy N. Gilliland | 3/3/2000 | 1/15/2002 | patented | Circuit for Automatically Having Mechanical Device at its Resonance Frequency |
| ZIL-231 | | | | 2/23/2000 | 7/3/2001 | patented | Switch Amplifier Circuit |

CMOS Switch circuit Having Concurrently Switching Complementary Outputs Independently From Process Variations
 Using a Reduced Memory Lookup Table for Gamma Correction Through Interpolation
 Current Regulator with Low Voltage Detection Capability
 Non-Volatile Memory Program Driver and Read Reference Circuits
 Method and Apparatus for an Enhanced Processor
 Memory Mapped Programmable Generator Outputting Series of Events with Timing Derived from Event Block
 Low Ripple Power Distribution System
 Method and Apparatus for Improved Signal Restoration
 Method of Forming Semiconductor Memory Device with LDD
 Method and Apparatus for Improved Signal Filtering
 Data Flow Enhancement for Processor Architectures with Cache
 Process To Improve High-Performance Capacitors in Integrated MOS Technologies
 Process To Improve High-Performance Capacitors in Integrated MOS Technologies
 Process To Improve High-Performance Capacitors in Integrated MOS Technologies
 Phase and Frequency Locked Clock Generator
 High Voltage PMOS Level Shifter
 Method of Eliminating Gate Leakage in Nitrogen Annealed Oxides
 High Aspect Ratio Photolithographic Method for High Energy Implantation
 Externally Synchronized Voltage Controlled Oscillator in Phase Locked Loop
 Low Voltage Charge Pump Circuit
 Method of Fabricating a High Quality Thin Oxide
 Dot-Crawl Reduction in NTSC/PAL Graphic Encoder
 Circle Correction in Digital Low-Pass Filter
 Reduction of Color Transition Distortions in NTSC/PAL Encoder
 Reconfigurable Infinite Impulse Response Digital Filter
 Integrated Television Processor
 Method of Fabricating a MOS Device
 Method of Fabricating a MOS Device
 Layout Solution for Electromagnetic Interference Reduction
 Light Phase Grating Device
 In-Circuit Emulation System with Minimal Impact on Target Environment
 Flexible Interrupt System for an Integrated Circuit
 I/O Port and RAM Memory Addressing Technique
 Apparatus and Method for Digital Amplitude and Phase Detection
 Apparatus and Method for Data Synchronizing and Tracking
 Apparatus and Method for Data Synchronizing and Tracking
 Asynchronous Analog or Digital Frequency Measurement on Digital Test Equipment
 Method of Making MOS Precision Capacitor with Low Voltage Coefficient
 MOS Precision Capacitor with Low Voltage Coefficient
 Dual Latch Data Transfer Pacing Logic Using a Timer to Maintain a Data Transfer Interval
 Circuit for Sensing Whether or not an Add-In Board is Inserted into a Bus Connector of a Mother Board
 Efficient Functional Test Scheme Incorporated in a Programmable Duration Binary Counter
 Method of Demodulating Chirp Spread Spectrum
 Flash Analog-to-Digital Converter
 Frequency Multiplying Clock Signal Generator
 Technique for Generating On-Screen Display Characters Using Software Implementation
 Technique for Generating On-Screen Display Characters Using Software Implementation
 Circuit for Automatically Detecting Off-Chip, Crystal or On-Chip, RC Oscillator Option
 Method of Fabricating High Threshold Metal Oxide Silicon Read-Only-Memory Transistors
 High Threshold Metal Oxide Silicon Read-Only-Memory Transistors
 TECHNIQUE FOR ELIMINATING DATA TRANSFER MEMORY UNDERFLANS
 ANALOG WAVELET TRANSFORM CIRCUITRY
 TECHNIQUE FOR ACCESSING AND REFRESHING MEMORY LOCATIONS WITHIN ELECTRONIC STORAGE DEVICES WHICH NEED EFFICIENT PSEUDORANDOM VALUE GENERATOR
 METHOD OF ELIMINATING METAL VOIDING IN A TITANIUM NITRIDE/ALUMINUM PROCESSING
 ELECTROSTATIC DISCHARGE PROTECTION FOR METAL-OXIDE-SILICON FEEDBACK ELEMENTS BETWEEN PINS
 DELAYED FIFO STATUS FOR SERIAL SHIFT EMULATION
 FAST INSTRUCTION DECODING IN A PIPELINE PROCESSOR
 TECHNIQUE FOR ACCELERATING INSTRUCTION DECODING OF INSTRUCTION SETS WITH VARIABLE LENGTH OPERANDS IN A SELF ARBITRATING AUTO RESETTABLE FLAG CIRCUIT
 TITANIUM CONTACT METALLIZATION
 TITANIUM CONTACT METALLIZATION
 DECODER CIRCUIT WITH BYPASS CIRCUITRY AND REDUCED INPUT CAPACITANCE FOR GREATER SPEED

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|------------|------------|------------|----------|-----------|--------------------------|
| 08/15/2000 | 5/12/2000 | 10/16/2000 | patented | 6,304,111 | Mohammad R. Pijabean |
| 09/30/2004 | 12/16/2000 | 12/16/2000 | patented | 6,833,876 | Mohammad R. Pijabean |
| 09/16/2002 | 3/19/2002 | 3/19/2002 | patented | 6,359,442 | MHAI MAING ESCU |
| 09/16/2002 | 4/17/2001 | 4/17/2001 | patented | 6,219,279 | MHAI MAING ESCU |
| 09/16/2002 | 12/31/2002 | 12/31/2002 | patented | 6,592,181 | CRAIG MACKENNA |
| 09/16/2002 | 5/13/2003 | 5/13/2003 | patented | 6,564,334 | Dennis G. Zaffiro |
| 09/16/2002 | 1/28/2000 | 1/28/2000 | patented | 6,154,966 | MHAI C. MANOLESCU |
| 09/16/2002 | 3/4/2003 | 3/4/2003 | patented | 6,529,246 | Anatoliy V. Tsyrganovich |
| 09/16/2002 | 1/17/2001 | 1/17/2001 | patented | 6,161,999 | SUNGKWON LEE |
| 09/16/2002 | 7/23/2002 | 7/23/2002 | patented | 6,424,384 | ANATOLIY V. TSYRGANOVIC |
| 09/16/2002 | 6/3/2003 | 6/3/2003 | patented | 6,574,682 | STEPHEN H. CHAN |
| 09/16/2002 | 8/3/2010 | 8/3/2010 | patented | 7,060,584 | Cams et al. |
| 09/16/2002 | 9/3/2011 | 9/3/2011 | patented | 7,168,652 | Cams et al. |
| 09/16/2002 | 12/26/2000 | 12/26/2000 | patented | 6,166,806 | ANATOLIY V. TSYRGANOVIC |
| 09/16/2002 | 12/26/2000 | 12/26/2000 | patented | 6,300,795 | BRUCE LEE TROUTMAN |
| 09/16/2002 | 8/10/2003 | 8/10/2003 | patented | 6,165,846 | TIMOTHY K. CARNS |
| 09/16/2002 | 1/25/2000 | 1/25/2000 | patented | 6,576,405 | STEPHAN J. BLUFFAT |
| 09/16/2002 | 8/17/1999 | 8/17/1999 | patented | 6,018,273 | ANATOLIY V. TSYRGANOVIC |
| 09/16/2002 | 2/20/2001 | 2/20/2001 | patented | 6,190,873 | JOHN E. BERG |
| 09/16/2002 | 12/19/2000 | 12/19/2000 | patented | 6,163,346 | ANATOLIY V. TSYRGANOVIC |
| 09/16/2002 | 11/21/2006 | 11/21/2006 | patented | 7,138,037 | Anatoliy V. Tsyrganovich |
| 09/16/2002 | 10/19/1999 | 10/19/1999 | patented | 6,129,199 | JOHN KRANZEN |
| 09/16/2002 | 1/30/1999 | 1/30/1999 | patented | 6,129,199 | JOHN KRANZEN |
| 09/16/2002 | 12/28/1999 | 12/28/1999 | patented | 6,009,445 | ANATOLIY V. TSYRGANOVIC |
| 09/16/2002 | 12/14/1999 | 12/14/1999 | patented | 6,002,449 | Anatoliy V. Tsyrganovich |
| 09/16/2002 | 8/20/2002 | 8/20/2002 | patented | 6,438,195 | Smythe et al. |
| 09/16/2002 | 1/25/2000 | 1/25/2000 | patented | 6,155,853 | Smythe et al. |
| 09/16/2002 | 10/19/1999 | 10/19/1999 | patented | 6,369,590 | BRUNO KRANZEN |
| 09/16/2002 | 11/21/1999 | 11/21/1999 | patented | 6,378,127 | JOHN E. BERG |
| 09/16/2002 | 9/26/1999 | 9/26/1999 | patented | 6,261,999 | CRAIG MACKENNA |
| 09/16/2002 | 7/20/1999 | 7/20/1999 | patented | 6,261,999 | Stephen H. Chan |
| 09/16/2002 | 6/28/1998 | 6/28/1998 | patented | 5,925,848 | OSCAR AYZENBERG |
| 09/16/2002 | 2/19/2002 | 2/19/2002 | patented | 6,785,865 | OSCAR AYZENBERG |
| 09/16/2002 | 5/16/2000 | 5/16/2000 | patented | 6,349,122 | GILBERT R. WOODMAN, JR. |
| 09/16/2002 | 8/6/2000 | 8/6/2000 | patented | 6,084,767 | GILBERT R. WOODMAN, JR. |
| 09/16/2002 | 5/12/1998 | 5/12/1998 | patented | 6,099,161 | IGOR FURLAN |
| 09/16/2002 | 3/4/1997 | 3/4/1997 | patented | 5,750,426 | KAMAL RAJAKANAN |
| 09/16/2002 | 1/27/1995 | 1/27/1995 | patented | 5,608,258 | KAMAL RAJAKANAN |
| 09/16/2002 | 5/5/1998 | 5/5/1998 | patented | 6,399,742 | GRANT B. RICHARDS |
| 09/16/2002 | 3/18/1997 | 3/18/1997 | patented | 6,162,634 | CRAIG MACKENNA |
| 09/16/2002 | 1/19/1995 | 1/19/1995 | patented | 5,381,453 | STEPHEN H. CHAN |
| 09/16/2002 | 6/18/1996 | 6/18/1996 | patented | 6,248,670 | LYN R. ZASTROW |
| 09/16/2002 | 12/26/1995 | 12/26/1995 | patented | 6,228,242 | NIRAJ KUMAR |
| 09/16/2002 | 6/6/2000 | 6/6/2000 | patented | 6,072,955 | JOHN TRAN |
| 09/16/2002 | 3/4/1997 | 3/4/1997 | patented | 6,008,425 | ALEXSANDR MOVSOVICH |
| 09/16/2002 | 1/12/1994 | 1/12/1994 | patented | 5,369,577 | BOUBEKUR BEZHAMDA |
| 09/16/2002 | 2/12/1995 | 2/12/1995 | patented | 5,389,565 | ALEX GYURE |
| 09/16/2002 | 6/27/1995 | 6/27/1995 | patented | 5,456,896 | ALEX GYURE |
| 09/16/2002 | 8/16/1994 | 8/16/1994 | patented | 5,602,537 | MONTE J. DALRYMPLE |
| 09/16/2002 | 1/26/1996 | 1/26/1996 | patented | 5,485,654 | R. TIMOTHY EDWARDS |
| 09/16/2002 | 3/6/1993 | 3/6/1993 | patented | 5,337,564 | ASHER HAZANCHUK |
| 09/16/2002 | 6/27/1995 | 6/27/1995 | patented | 5,428,561 | DAVID FRYANT |
| 09/16/2002 | 8/16/1994 | 8/16/1994 | patented | 5,338,423 | GREGORY HINDMAN |
| 09/16/2002 | 1/26/1996 | 1/26/1996 | patented | 5,579,200 | KAMAL RAJAKANAN |
| 09/16/2002 | 6/30/1993 | 6/30/1993 | patented | 5,619,681 | BOUBEKUR BEZHAMDA |
| 09/16/2002 | 3/31/1998 | 3/31/1998 | patented | 6,161,997 | STEPHEN H. CHAN |
| 09/16/2002 | 5/24/1994 | 5/24/1994 | patented | 5,734,854 | STEPHEN H. CHAN |
| 09/16/2002 | 1/21/1997 | 1/21/1997 | patented | 5,592,635 | STEPHEN H. CHAN |
| 09/16/2002 | 5/31/1994 | 5/31/1994 | patented | 5,315,184 | BOUBEKUR BEZHAMDA |
| 09/16/2002 | 5/31/1994 | 5/31/1994 | patented | 5,317,187 | GREGORY HINDMAN |
| 09/16/2002 | 3/9/1992 | 3/9/1992 | patented | 5,240,880 | GREGORY HINDMAN |
| 09/16/2002 | 1/17/1993 | 1/17/1993 | patented | 5,262,887 | BOUBEKUR BEZHAMDA |

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| ZILG083 | 07/677,871 | 5,244,831 | GREGORY HINDMAN | 5/11/1992 | 9/14/1993 | patented | METHOD OF DOPING A POLYSILOXON LAYER ON A SEMICONDUCTOR WAFER |
| ZILG080 | 07/665,246 | 5,442,736 | LYN R. ZAISTROW | 3/23/1992 | 8/15/1993 | patented | PULSE GENERATOR AND DEMODULATOR WITH CONTROLLING PROCESSOR AND DECREMENTING COUNTERS |
| ZILG079C | 08/094,350 | 5,293,562 | STEVEN M. POPE | 5/28/1993 | 3/10/1994 | patented | DEVICE WITH MULTIPLEXED AND NON-MULTIPLEXED ADDRESS AND DATA I/O CAPABILITY |
| ZILG079 | 07/797,111 | 5,262,991 | STEVEN M. POPE | 1/22/1991 | 11/16/1993 | patented | DEVICE WITH MULTIPLEXED AND NON-MULTIPLEXED ADDRESS AND DATA I/O CAPABILITY |
| ZILG076 | 07/953,732 | 5,319,753 | CRAIG A. MACKENNA | 9/29/1992 | 6/7/1994 | patented | QUEUED INTERRUPT MECHANISM WITH SUPPLEMENTARY COMMAND/STATUS MESSAGE INFORMATION |
| ZILG075C | 08/487,711 | 5,659,688 | HANUMANTHRAO NIMISHAK, | 5/7/1995 | 8/19/1997 | patented | TECHNIQUE AND CIRCUIT FOR PROVIDING TWO OR MORE PROCESSORS WITH TIME MULTIPLEXED ACCESS TO A SHARED |
| ZILG075 | 07/991,889 | 5,471,588 | HANUMANTHRAO NIMISHAK, | 11/25/1992 | 11/28/1995 | patented | TECHNIQUE AND CIRCUIT FOR PROVIDING TWO OR MORE PROCESSORS WITH TIME MULTIPLEXED ACCESS TO A SHARED |
| ZILG074 | 07/818,909 | 5,317,745 | STEPHEN H. CHAN | 1/10/1992 | 5/31/1994 | patented | MINIMAL INTERRUPT LATENCY SCHEME USING MULTIPLE PROGRAM COUNTERS |
| ZILG073 | 07/817,465 | 5,426,252 | ANDRE B. WALKER | 1/31/1992 | 9/27/1995 | patented | POWER SUPPLY INTERRUPTION DETECTION AND RESPONSE SYSTEM FOR A MICROCONTROLLER |
| ZILG072 | 07/679,739 | 5,265,038 | PAK-ON KWOK | 4/3/1991 | 11/23/1993 | patented | COMPUTER SYSTEM PERIPHERAL CONNECTION PULSE FILTERING TECHNIQUE AND CIRCUIT |
| ZILG070 | 07/855,526 | 5,428,746 | MONTÉ J. DALRYMPLE | 3/23/1992 | 9/27/1995 | patented | INTEGRATED MICROPROCESSOR UNIT GENERATING SEPARATE MEMORY AND INPUT-OUTPUT DEVICE CONTROL SIGNALS |
| ZILG068 | 07/735,403 | 5,145,715 | BOUBEKUR BEKHAMBA | 7/26/1991 | 9/8/1992 | patented | DOMINO LOGIC DECODER |
| ZILG067 | 07/753,729 | 5,263,166 | DIMITRIC, DESMONS | 9/3/1991 | 11/16/1993 | patented | TECHNIQUE OF PROGRAMMING INTEGRATED CIRCUIT CONTROL REGISTERS |
| ZILG064 | 07/661,298 | 5,345,564 | BRADLEY D. JENSEN | 3/31/1992 | 9/6/1994 | patented | SERIAL COMMUNICATION PERIPHERAL INTEGRATED ELECTRONIC CIRCUIT THAT RECOGNIZES ITS UNIQUE ADDRESS BEFORE |
| ZILG063 | 07/686,244 | 5,278,957 | STEPHEN H. CHAN | 4/16/1991 | 1/11/1994 | patented | DATA TRANSFER CIRCUIT FOR INTERFACING TWO BUS SYSTEMS THAT OPERATE ASYNCHRONOUSLY WITH RESPECT TO EACH |
| ZILG061 | 07/659,004 | 5,093,533 | BOUBEKUR BEKHAMBA | 2/20/1991 | 3/3/1992 | patented | EXTERNALLY TRIMMED INTEGRATED CIRCUIT RC OSCILLATOR |
| ZILG060 | 07/656,419 | 5,109,163 | BOUBEKUR BEKHAMBA | 2/15/1991 | 4/28/1992 | patented | INTEGRATED POWER-ON RESET CIRCUIT |
| ZILG-59C/P | 08/248,468 | 5,495,594 | CRAIG A. MACKENNA | 5/24/1994 | 2/27/1996 | patented | TECHNIQUE FOR AUTOMATICALLY ADAPTING A PERIPHERAL INTEGRATED CIRCUIT FOR OPERATION WITH A VARIETY OF MICRO |
| ZILG057 | 07/602,502 | 5,287,464 | NIRAJ KUMAR | 10/24/1990 | 2/15/1994 | patented | SEMICONDUCTOR MULTIDevice SYSTEM WITH LOGIC MEANS FOR CONTROLLING THE OPERATIONAL MODE OF A SET OF MICRO |
| ZILG055C | 08/098,750 | 5,363,383 | HANUMANTHRAO NIMISHAK | 7/29/1993 | 11/8/1994 | patented | CIRCUIT FOR GENERATING A MODE CONTROL SIGNAL |
| ZILG054C | 08/034,510 | 5,313,621 | STEPHEN H. CHAN | 3/22/1993 | 5/17/1994 | patented | PROGRAMMABLE WAIT STATES GENERATOR FOR A MICROPROCESSOR AND COMPUTER SYSTEM UTILIZING IT |
| ZILG053 | 07/520,777 | 5,230,058 | NIRAJ KUMAR | 5/8/1990 | 7/20/1993 | patented | IC CHIP HAVING VOLATILE MEMORY CELLS SIMULTANEOUSLY LOADED WITH INITIALIZATION DATA FROM UNIQUELY ASSOCIATED |
| ZILG048 | 07/544,520 | 5,222,218 | DON SMITH | 6/27/1990 | 6/22/1992 | patented | SYSTEM WITH DEVICES CONNECTED IN SEQUENCE TO RECEIVE INFORMATION IN A PREDETERMINED ORDER |
| ZILG047 | 07/446,158 | 5,175,631 | NIRAJ KUMAR | 12/5/1989 | 12/29/1992 | patented | NON-VOLATILE READ-ONLY MEMORY RESPONSIVE TO A SINGLE INITIALIZATION INSTRUCTION FOR LOADING INITIAL BITS IN |
| ZILG045D/V | 07/959,507 | 5,239,237 | JOHN TRAN | 10/8/1992 | 8/24/1993 | patented | CONTROL CIRCUIT HAVING OUTPUTS WITH DIFFERING RISE AND FALL |
| ZILG045 | 07/479,865 | 5,187,686 | JOHN TRAN | 2/14/1990 | 2/16/1993 | patented | CONTROL CIRCUIT HAVING OUTPUTS WITH DIFFERING RISE AND FALL |
| ZILG043C | 08/047,162 | 5,784,292 | NIRAJ KUMAR | 4/4/1993 | 7/21/1998 | patented | MERGING INTEGRATED CIRCUIT MASK DATABASES FORMED BY DIFFERENT DESIGN RULES THROUGH GLOBAL MASK DATA |
| ZILG042 | 07/420,912 | 5,231,590 | NIRAJ KUMAR | 10/13/1989 | 7/27/1993 | patented | TECHNIQUE FOR MODIFYING AN INTEGRATED CIRCUIT LAYOUT |
| ZILG035C2 | 08/214,019 | 5,625,842 | MONTÉ J. DALRYMPLE | 3/15/1994 | 4/29/1997 | patented | SYSTEM FOR THE AUTOMATIC TRANSFER OF MESSAGE STATUS IN DIGITAL DATA COMMUNICATION |
| ZILG031C | 07/742,152 | 5,193,199 | MONTÉ J. DALRYMPLE | 8/1/1991 | 3/9/1993 | patented | DEVICE AND METHOD FOR PROGRAMMING CRITICAL HARDWARE PARAMETERS |

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