## 503320894 05/26/2015

# PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date
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TEZASWI RAJA	01/02/2014
JESSE MAX GUSS	01/02/2014
BRIAN SMITH	01/03/2014

## **RECEIVING PARTY DATA**

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#### **PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	14149915

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ATTORNEY DOCKET NUMBER:	13-SC-1094-US1
NAME OF SUBMITTER:	J. JOEL JUSTISS
SIGNATURE:	/J. Joel Justiss/
DATE SIGNED:	05/26/2015

## **Total Attachments: 4**

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PATENT 503320894 REEL: 035708 FRAME: 0795

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## **ASSIGNMENT**

WHEREAS, I, the undersigned inventor (or one of the undersigned joint inventors), of residence as listed, having invented certain new and useful improvements as below entitled, for which application for United States Letters Patent is made, the said application having been executed on the date set forth below; and

WHEREAS, NVIDIA, a corporation organized and existing under the laws of the State of Delaware, with a place of business at 2701 San Tomas Expressway, Santa Clara, CA 95050 U.S.A, is desirous of acquiring my entire right, title and interest in and to the said invention, and to the said application and any Letters Patent that may issue thereon;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, I hereby sell and assign to the said NVIDIA, its successors and assigns, my entire right, title and interest in and to the said invention and in to the said application and all patents which may be granted therefor, and all divisions, reissues, substitutions, continuations, and extensions thereof; and I hereby authorize and request the Commissioner of Patents and Trademarks to issue all patents for said invention, or patent resulting therefrom, insofar as my interest is concerned, to the said NVIDIA, as assignee of my entire right, title and interest.

I also hereby sell and assign to NVIDIA, its successors and assigns, my foreign rights to the invention disclosed in said application, in all countries of the world, including the right to file applications and obtain patents under the terms of the International Convention for the Protection of Industrial Property, and of the European Patent Convention, and further agree to execute any and all patent applications, assignments, affidavits, and any other papers in connection therewith necessary to perfect such patent rights.

I hereby further agree that I will communicate to said NVIDIA, or to its successors, assigns, and legal representatives, any facts known to me respecting said invention, and at the expense of said assignee company, testify in any legal proceedings, sign all lawful papers, execute all divisional, continuation, reissue and substitute applications, make all lawful oaths, and generally do everything possible to aid said NVIDIA, its successors, assigns and nominees to obtain and enforce proper patent protection for said invention in all countries.

IN WITNESS WHEREOF, I hereunto set hand and seal this day and year;

TITLE OF INVENTION	A VOLTAGE OPTIMIZATION CIRCUIT AND MANAGING VOLTAGE MARGINS OF AN INTEGRATED CIRCUIT
SIGNATURE OF INVENTOR AND NAME	Stephen Felix
(CITY AND COUNTRY)	Sambridge, England
DATE SIGNED	6-JAN-2014

TITLE OF INVENTION	A VOLTAGE OPTIMIZATION CIRCUIT AND MANAGING VOLTAGE MARGINS OF AN INTEGRATED CIRCUIT
SIGNATURE OF INVENTOR AND NAME	Lassui Raja Tezaswi Raja
(CITY AND STATE)	Santa Clara, California
DATE SIGNED	01/02/14

TITLE OF INVENTION	A VOLTAGE OPTIMIZATION CIRCUIT AND MANAGING VOLTAGE MARGINS OF AN INTEGRATED CIRCUIT
SIGNATURE OF INVENTOR AND NAME	Jesse Max Guss
(CITY AND STATE)	Santa Clara, California
DATE SIGNED	Jan. 2, 2014

TITLE OF INVENTION	A VOLTAGE OPTIMIZATION CIRCUIT AND MANAGING VOLTAGE MARGINS OF AN INTEGRATED CIRCUIT
SIGNATURE OF INVENTOR AND NAME	Brian Smith
(CITY AND STATE)	Santa Clara, California
DATE SIGNED	1/3/14