

## PATENT ASSIGNMENT COVER SHEET

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<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
ANIRBAN ROY	06/05/2015
JON S. CHOY	06/05/2015
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	Freescale Semiconductor, Inc.
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<b>Postal Code:</b>	78735
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
Application Number:	14734736
<b>CORRESPONDENCE DATA</b>	
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<b>ATTORNEY DOCKET NUMBER:</b>	AM21020TP
<b>NAME OF SUBMITTER:</b>	SHANNON WILLIAMS
<b>SIGNATURE:</b>	/Shannon Williams/
<b>DATE SIGNED:</b>	06/09/2015
<b>Total Attachments: 2</b>	
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## ASSIGNMENT AND AGREEMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, we, **ROY, Anirban, an individual; and CHOY, Jon S., an individual;** have sold, assigned and transferred, and do hereby sell, assign and transfer, unto **FREESCALE SEMICONDUCTOR, INC.**, a corporation of the State of Delaware, having its principal office in Austin, State of Texas, United States of America, and its successors, assigns, and legal representatives, the entire right, title and interest for the United States of America in and to certain inventions relating to improvements in **SYSTEMS AND METHODS FOR DRIVING A CONTROL GATE WITH A SELECT GATE SIGNAL IN A SPLIT-GATE NONVOLATILE MEMORY CELL** (Docket No. **AM21020TP**), described, illustrated and claimed in an application for Letters Patent of the United States of America executed by us on the dates indicated by our signatures below, together with the entire right, title and interest in and to the application, and in and to Letters Patent which may be issued upon the application, and upon any division, extension, continuation or reissue thereof.

We hereby also sell, assign and transfer unto **FREESCALE SEMICONDUCTOR, INC.**, the entire right, title and interest in and to the invention and in and to applications for Letters Patent therefor in all countries foreign to the United States of America, including all rights under any and all international conventions and treaties in respect of the invention and the applications for Letters Patent in foreign countries, and we further authorize **FREESCALE SEMICONDUCTOR, INC.** to apply for Letters Patent in foreign countries directly in its own name, and to claim priority of the filing date of the application for Letters Patent of the United States of America under the provisions of any and all international conventions and treaties.

We hereby authorize and request the Commissioner of Patents of the United States of America to issue Letters Patent upon the aforesaid application, division, extension, continuation or reissue, to **FREESCALE SEMICONDUCTOR, INC.**, for the sole use and benefit of **FREESCALE SEMICONDUCTOR, INC.**, its successors, assigns and legal representatives, to the full end of the term for which Letters Patent may be granted, the same as they would have been held and enjoyed by me had this assignment not been made, and we hereby authorize and request the equivalent authorities in foreign countries to issue the patents of their respective countries to **FREESCALE SEMICONDUCTOR, INC.**

We agree that, when requested, we will, without charge to **FREESCALE SEMICONDUCTOR, INC.**, but at its expense, sign all papers, take all rightful oaths, and do all acts which may be necessary, desirable or convenient for securing and maintaining patents for the inventions in any and all countries and for vesting title thereto in **FREESCALE SEMICONDUCTOR, INC.**, its successors, assigns and legal representatives or nominees.

AM21020TP

**PATENT**  
**REEL: 035811 FRAME: 0057**

