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PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
JUNGKYU LEE	05/07/2015
DOHYUN KWON	05/07/2015
TAEHYUN KIM	05/07/2015
SUYEON SIM	05/07/2015
SEUNGHWAN CHO	05/07/2015
JONGMO YEO	05/07/2015
JOONGEOL LEE	05/07/2015

RECEIVING PARTY DATA

Name:	SAMSUNG DISPLAY CO., LTD.
Street Address:	95, SAMSUNG 2 RO, GIHEUNG-GU
City:	YONGIN-CITY, GYEONGGI-DO
State/Country:	KOREA, REPUBLIC OF

PROPERTY NUMBERS Total: 1

Property Type	Number	
Application Number:	14712703	

CORRESPONDENCE DATA

Fax Number: (949)760-9502

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 9497600404

Email: efiling@knobbe.com

KNOBBE MARTENS OLSON & BEAR LLP **Correspondent Name:**

Address Line 1: 2040 MAIN STREET

Address Line 2: 14TH FLOOR

Address Line 4: IRVINE, CALIFORNIA 92614

ATTORNEY DOCKET NUMBER:	SDCYPQ.440AUS
NAME OF SUBMITTER:	JOHN M. CARSON
SIGNATURE:	/John M. Carson/
DATE SIGNED:	06/12/2015
	This document serves as an Oath/Declaration (37 CFR 1.63).

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REEL: 035831 FRAME: 0330 503348972

Total Attachments: 3

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> PATENT REEL: 035831 FRAME: 0331

COMBINED DECLARATION & ASSIGNMENT (37 CFR 1.63(e))

Application Data Sheet filed previously or concurrently

Docket No.: SDCYPQ.440AUS Page 1 of 3

TITLE: THIN FILM TRANSISTOR ARRAY SUBSTRATE AND ORGANIC LIGHT-EMITTING DIODE

DISPLAY INCLUDING THE SAME

Inventor(s): Jungkyu Lee, Dohyun Kwon; Taehyun Kim; Suyeon Sim; Seunghwan Cho; Jongmo Yeo; Joongeol Lee

Declaration

This Declaration is directed to the invention identified above that:

Is attached, where "attached" means filed concurrently with.

As a named inventor, I declare that:

The above-identified application was made or authorized to be made by me.

I believe that I am the original inventor or an original joint inventor in the application.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 USC 1001 by fine or imprisonment of not more than five (5) years, or both.

I hereby state that I have reviewed and understand the contents of the above identified specification and acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, § 1.56.

Assignment from inventor(s)

WHEREAS, above-identified inventor(s) (individual(s) hereinafter "ASSIGNOR") invented certain new and useful improvements, technology, inventions, developments, ideas or discoveries, and hereby assign or are under an obligation to assign to the below identified Assignee the above-titled application (collectively hereinafter referred to as the "Work") for which an application for Letters Patent in the United States (identified above) has been prepared for filing with the United States Patent and Trademark Office (hereinafter the "Application").

AND WHEREAS, Samsung Display Co., Ltd., with its principal place of business at 95, Samsung 2 Ro, Giheung-gu, Yongin-city, Gyeonggi-do, Korea (hereinafter the "ASSIGNEE"), desires to acquire the entire right, title, and interest in and to the Application and the Work.

NOW, THEREFORE, for good and valuable consideration of which receipt is hereby acknowledged, ASSIGNOR hereby acknowledges that ASSIGNOR has sold, assigned, transferred and set over, and by these presents does hereby sell, assign, transfer and set over, unto said ASSIGNEE, its successors, legal representatives and assigns, the entire right, title, and interest in the Application, including all patent properties filed or issued upon the Application; where "Fatent Properties" include, but are not limited to:

all provisional applications to which the Application claims priority;

all nonprovisional applications claiming priority to aforementloned provisional(s) and/or the present Application, including, all divisions, continuations, continuations-in-part, and relsaues, and all Letters Patent of the United States which may be granted thereon and all reisaues and extensions thereof;

ASSIGNOR hereby acknowledges the ASSIGNEE as the Applicant for all aforementioned patent properties, and authorizes and requests the Commissioner of Patents of the United States to Issue all related Letters Patent to the ASSIGNEE, its successors, legal representatives and assigns, in accordance with the terms of this instrument.

AND ASSIGNOR DOES HEREBY sell, assign, transfer, and convey to ASSIGNEE, its successors, legal representatives, and assigns all claims for damages and all remedies arising out of any violation of the

LO-201404-207-1-US0 DP-46877-US Y.P.Lee,Mock&Partners

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COMBINED DECLARATION & ASSIGNMENT (37 CFR 1.63(e))

Application Data Sheet filed previously or concurrently

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TIME: THIN FILM TRANSISTOR ARRAY SUBSTRATE AND ORGANIC LIGHT-EMITTING DIODE DISPLAY INCLUDING THE SAME

Inventor(s): Jungkyu Lee, Dohyun Kwon; Taehyun Kim; Suyeon Sim; Seunghwan Cho; Jongmo Yeo; Joongeol Lee

rights assigned hereby that may have accrued prior to the date of assignment to ASSIGNEE, or may accrue hereafter, including, but not limited to, the right to sue for, collect, and retain damages for past infringements of said Letters Patent before or after issuance.

AND ASSIGNOR DOES HEREBY covenant and agree that ASSIGNOR will communicate to said ASSIGNEE, its successors, legal representatives and assigns, any facts known to ASSIGNOR respecting the Work, and testify in any legal proceeding, assist in the preparation of any other Patent Property relating to the Application, and the Work or any improvements made thereto, sign/execute all lawful papers, execute and make all rightful oaths and/or declarations in connection with the Application and the Work including any improvements made thereto, any patent applications filed therefrom, and any continuing application filed from any of the aforementioned applications, and generally do everything possible to aid the ASSIGNEE, its successors, legal representatives and assigns, to obtain and enforce proper patent protection for the Patent Properties.

Legal Name of Inventor 1: Jungkyu Lee				
Signature:	Jurskyn Lee	Date:	2015.5.1	
Legal Name of Inventor 2: Dohyun Kwon				
Signature:	Dohyunkwan	Date:	2015.5.1	
Legal Name of Inventor 3: Taehyun Kim				
Signature:	Tachyun Kin	Date:	2013.5.A	
Legal Name of Inventor 4: Suyeon Sim				
Signature:	Sayeon Sim	Date: _	2015. 5.1	
Legal Name of Inventor 5: Seunghwan Cho				
Signature:	Cho Seng Huam	Date:	2015.5.11	
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COMBINED DECLARATION & ASSIGNMENT (37 CFR 1.63(e))

Application Data Sheet filed previously or concurrently

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Title: THIN FILM TRANSISTOR ARRAY SUBSTRATE AND ORGANIC LIGHT-EMITTING DIODE DISPLAY INCLUDING THE SAME

Inventor(s): Jungkyu Lee, Dohyun Kwon; Taehyun Kim; Suyeon Sim; Seunghwan Cho; Jongmo Yeo; Joongeol Lee

Legal Name of inventor 6: Jongmo Yeo

Jongmo Yeo

Legal Name of inventor 7: Joongeol Lee

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