

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT3387395

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST
CONVEYING PARTY DATA	
Name	Execution Date
WACHOVIA CAPITAL FINANCE CORPORATION (NEW ENGLAND)	06/01/2015
RECEIVING PARTY DATA	
Name:	VIASYSTEMS TECHNOLOGIES CORP., L.L.C.
Street Address:	101 SOUTH HANLEY ROAD
Internal Address:	SUITE 1800
City:	ST. LOUIS
State/Country:	MISSOURI
Postal Code:	63105
PROPERTY NUMBERS Total: 5	
Property Type	Number
Application Number:	09992810
Application Number:	07576304
Application Number:	07628789
Application Number:	07119941
Application Number:	06326102
CORRESPONDENCE DATA	
Fax Number:	(650)251-5002
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	(650) 251-5047
Email:	ksolomon@stblaw.com
Correspondent Name:	MICHELLE T. MORAD, ESQ.
Address Line 1:	SIMPSON THACHER & BARTLETT LLP
Address Line 2:	2475 HANOVER STREET
Address Line 4:	PALO ALTO, CALIFORNIA 94304
ATTORNEY DOCKET NUMBER:	509265/1879
NAME OF SUBMITTER:	MICHELLE T. MORAD
SIGNATURE:	/mtm/
DATE SIGNED:	06/08/2015

Total Attachments: 5

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RELEASE OF SECURITY INTEREST IN PATENTS

WHEREAS, Viasystems Technologies Corp., L.L.C., a Delaware limited liability company ("Debtor"), granted a continuing security interest in and lien upon, and right of set off against, (a) all of Debtor's right, title and interest in and to all of Debtor's patents and all applications, registrations and recordings relating to the foregoing as may at any time be filed in the United States Patent and Trademark Office or in any similar office or agency of the United States of America, any State thereof, any political subdivision thereof or in any other country, including, without limitation, the patents, applications, registrations and recordings described in Schedule A hereto, together with all rights and privileges arising under applicable law with respect to Debtor's use of any patents and all reissues, divisions, continuations, extension and renewals thereof (all of the foregoing being collectively referred to herein as the "Patents"); (b) all present and future inventions and improvements described and claimed therein; (c) all income, fees, royalties and other payments at any time due or payable with respect thereto, including, without limitation, payments under all licenses at any time entered into in connection therewith; (d) the right to sue for past, present and future infringements thereof; (e) all rights corresponding thereto throughout the world; and (f) any and all proceeds of any of the foregoing, including, without limitation, all damages and payments or claims by Debtor against third parties for past or future infringement of the Patents (collectively, the "Patent Collateral"), to Wells Fargo Capital Finance, LLC, a Delaware limited liability company, successor by merger to Wachovia Capital Finance Corporation (New England), a Massachusetts corporation, in its capacity as agent (in such capacity, "Secured Party") for the benefit of itself and the benefit of other certain secured parties, as set forth in the Patent Security Agreement, dated February 16, 2010, between Debtor and Secured Party (the "Patent Security Agreement");

WHEREAS, an executed copy of the Patent Security Agreement was recorded in the United States Patent and Trademark Office (the "USPTO") on February 22, 2010 at Reel 023972, Frame 0985;

WHEREAS, Debtor has requested, and Secured Party has agreed, that Secured Party will fully terminate, release, discharge and reassign its interest in the Patent Collateral, including the Patents identified in Schedule A annexed hereto and made a part hereof, pursuant to this release;

NOW, THEREFORE, for good and valuable consideration, receipt and adequacy of which is hereby acknowledged, Secured Party hereby:


1. fully terminates, releases, discharges and reassigns to Debtor its security interest in and lien upon the Patent Collateral, including the Patents identified in Schedule A , without recourse or representation or warranty, express or implied, of any kind;
2. agrees that any right, title or interest of the Secured Party in the Patent Collateral shall hereby cease and become void and any power of attorney or similar rights granted by Debtor to Secured Party pursuant to or in connection with the Patent Security Agreement is terminated; and
3. authorizes and requests that the Commissioner of Patents note and record the existence of the release hereby given.

The validity, interpretation and enforcement of this release and any dispute arising out of the relationship between the parties hereto, whether in contract, tort, equity or otherwise, shall be governed by the internal laws of the State of New York, but excluding any principles of conflicts of law or other rule of law that would cause the application of the law of any jurisdiction other than the laws of New York.

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IN WITNESS WHEREOF, Secured Party has caused this Release of Security Interest in Patents to be executed by its duly authorized corporate officer this 1st day of June, 2015.

WELLS FARGO CAPITAL FINANCE, LLC, successor by merger to Wachovia Capital Finance Corporation (New England)

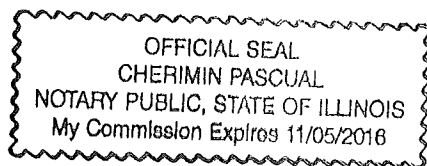
By: 

Name: Barry Keller

Title: Authorized Signatory

STATE OF ILLINOIS)
) ss.:
COUNTY OF COOK)

On the 1st day of June, 2015, before me personally came Barry Felker, to me known, who being by me duly sworn, did depose and say, that he is an Authorized Signatory of Wells Fargo Capital Finance, LLC and that the foregoing instrument was signed on behalf of such limited liability company by order of said limited liability company.





Notary Public

SCHEDULE A
TO
RELEASE OF SECURITY INTEREST IN PATENTS

LIST OF PATENTS AND PATENT APPLICATIONS

<u>Patent</u>	<u>Serial Number</u>	<u>Filed Date</u>
PCB embedded and surface mounted optical distribution systems	09992810	11/19/2001
Metrology system for analyzing panel misregistration in a panel manufacturing process and providing appropriate information for adjusting panel manufacturing processes	07576304	8/31/1990
Method and apparatus for solder leveling of printed circuit boards	07628789	12/17/1990
Method for positioning a panel	07119941	11/13/1987
Methods of and apparatus for inserting pins into a substrate	06326102	11/30/1981