

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
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EPAS ID: PAT3388579

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	TO CORRECT AN ERROR IN A COVER SHEET PREVIOUSLY RECORDED AT REEL 035699, FRAME 0679, CORRECTION TO THE ADDRESS OF ASSIGNEE		
CONVEYING PARTY DATA			
Name			Execution Date
SYMETRIX CORPORATION			05/22/2015
RECEIVING PARTY DATA			
Name:	SYMETRIX MEMORY, LLC		
Street Address:	5555 TECH CENTER DRIVE, SUITE 100		
City:	COLORADO SPRINGS		
State/Country:	COLORADO		
Postal Code:	80919		
PROPERTY NUMBERS Total: 5			
Property Type	Number		
Application Number:	62089518		
Patent Number:	7639523		
Patent Number:	7778063		
Patent Number:	7872900		
Patent Number:	8816719		
CORRESPONDENCE DATA			
Fax Number:	(602)253-8129		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
Phone:	602.528.4000		
Email:	phxip@squirepb.com		
Correspondent Name:	SQUIRE PATTON BOGGS (US) LLP		
Address Line 1:	1 E. WASHINGTON STREET, SUITE 2700		
Address Line 4:	PHOENIX, ARIZONA 85004		
ATTORNEY DOCKET NUMBER:	413176.00100		
NAME OF SUBMITTER:	Cammie R. TEEMS		
SIGNATURE:	/Cammie R. Teems/		
DATE SIGNED:	06/09/2015		
Total Attachments: 3			

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SYMETRIX CORPORATION	05/22/2015
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Phone:	602.528.4000
Email:	phxip@squirepb.com
Correspondent Name:	SQUIRE PATTON BOGGS (US) LLP
Address Line 1:	1 E. WASHINGTON STREET, SUITE 2700
Address Line 4:	PHOENIX, ARIZONA 85004
ATTORNEY DOCKET NUMBER:	413176.00100
NAME OF SUBMITTER:	CAMMIE R. TEEMS
SIGNATURE:	/Cammie R. Teems/
DATE SIGNED:	05/22/2015
Total Attachments: 2 source=Assignment#page1.tif	

ASSIGNMENT

WHEREAS, Symetrix Corporation (hereinafter "ASSIGNOR"), a corporation having a principal place of business at 5555 Tech Center Drive, Suite 100, Colorado Springs, CO 80919, is the sole and exclusive owner of the patents and patent application set forth in Schedule A;

WHEREAS, Symetrix Memory, LLC (hereinafter "ASSIGNEE"), a corporation having a principal place of business at 5555 Tech Center Drive, Suite 100, Colorado Springs, CO 80919, is desirous of securing the entire right, title, and interest in and to the inventions described in the patents and patent application set forth in Schedule A, in all countries throughout the world, as well as the patents and patent application identified in Schedule A and any and all applications related to the patents and patent application set forth in Schedule A, now or hereafter filed anywhere in the world and all Letters Patent granted from any of the foregoing;

NOW THEREFORE, be it known that for good and valuable consideration the receipt and sufficiency of which are hereby acknowledged, ASSIGNOR by these presents hereby sells, assigns and transfers unto ASSIGNEE, its lawful successors, legal representatives, and assigns, its entire right, title, and interest in and to the inventions, patents and patent application identified in Schedule A, and all divisions, continuations, and continuations-in-part thereof, and all Letters Patent of the United States which may be granted thereon, and all reexamination certificates and reissues thereof, as well as all rights to claim priority on the basis of the patents and patent application set forth in Schedule A, and all applications for Letters Patent which may now or hereafter be filed for the inventions set forth in the patents and patent application identified in Schedule A in any foreign country and all Letters Patent which may be granted on these inventions in any foreign country, and all extensions, renewals, and reissues thereof in any country; and it hereby authorizes and requests the United States Patent and Trademark Office and any official of any foreign country whose duty it is to issue Letters Patent on the applications, as described above, to issue all Letters Patent for these inventions to ASSIGNEE, its successors, legal representatives and assigns, in accordance with the terms of this Assignment; and we hereby assign to ASSIGNEE all right, title and interest to sue for past, present or future infringement of any patent that has issued or will issue from any of the foregoing applications anywhere in the world;

AND, ASSIGNOR HEREBY covenants that it has the full right to convey the interest assigned by this Assignment, and it has not executed and will not execute any agreement in conflict with this Assignment;

AND, ASSIGNOR HEREBY further covenants and agrees that it will, without further consideration, communicate with ASSIGNEE, its successors, legal representatives and assigns, any facts known to it respecting the inventions, patents and patent application listed in Schedule A, and testify in any legal proceeding, sign all lawful papers when called upon to do so, execute and deliver any and all papers that may be necessary or desirable to perfect title to these inventions, patents and patent application for said ASSIGNEE, its successors, legal representatives or assigns, execute all divisional, continuation, continuation-in-part and reissue applications, make all rightful oaths and generally do everything possible to aid ASSIGNEE, its successors, legal representatives and assigns, to obtain and enforce proper patent protection for these inventions, patents and patent application in the United States and any foreign country, it being understood that any expense incident to the execution of such papers shall be borne by the ASSIGNEE, its successors, legal representatives and assigns.

EXECUTED as of the date(s) written below:

Signed by 

Date 5/22/2015

Printed Name SUE A. DOUCETTE

as CONTROLLER on behalf of Symetrix Corporation (Assignor)

SCHEDULE A

U.S. PATENT APPLICATION		
Title	Application No.	Filing Date
TRANSITION METAL OXIDE RESISTIVE SWITCHING DEVICE WITH DOPED BUFFER LAYER	62/089,518	December 9, 2014

U.S. PATENTS		
Title	Patent No.	Issue Date
STABILIZED RESISTIVE SWITCHING MEMORY	7,639,523	December 29, 2009
NON-VOLATILE RESISTANCE SWITCHING MEMORIES AND METHODS OF MAKING SAME	7,778,063	August 17, 2010
CORRELATED ELECTRON MEMORY	7,872,900	January 18, 2011
RE-PROGRAMMABLE ANTIFUSE FPGA UTILIZING RESISTIVE CERAM ELEMENTS	8,816,719	August 26, 2014

FOREIGN PATENTS			
Country	Title	Patent No.	Issue Date
China	CORRELATED ELECTRON MEMORY	ZL2500780041664.0	September 28, 2011
EPO	CORRELATED ELECTRON MEMORY	2082426	December 26, 2012
France	CORRELATED ELECTRON MEMORY	2086426	December 26, 2012
Germany	CORRELATED ELECTRON MEMORY	602007027702.7-08	December 26, 2012
Japan	CORRELATED ELECTRON MEMORY	5156023	December 14, 2012
S. Korea	CORRELATED ELECTRON MEMORY	10-1133832	March 29, 2012
Brazil	CORRELATED ELECTRON MEMORY	PI0719055-7	November 26, 2013

