PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
TAKAMASA SUZUKI	09/11/2015

RECEIVING PARTY DATA

Name:	MICRON TECHNOLOGY, INC.
Street Address:	8000 SOUTH FEDERAL WAY
City:	BOISE
State/Country:	IDAHO
Postal Code:	83716

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	14852259

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	P254177.US.01
NAME OF SUBMITTER:	KIMTON N. ENG
SIGNATURE:	/Kimton N. Eng/
DATE SIGNED:	09/11/2015

Total Attachments: 2

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PATENT 503476557 REEL: 036547 FRAME: 0354

pain THE UNITED STATES PATENT AND TRADEMARK OFFICE

Takamasa Suzuki Filed: Concurrently herewith For SEMICONDUCTOR DEVICE HAVING ERROR CORRECTION CODE (ECC) CIRCUIT	In re Application of:	Designation Processors as
For : SEMICONDUCTOR DEVICE HAVING ERROR CORRECTION	Takamasa Suzuki	Docket No. P254177.US.01 Disclosure No. ELPA-0363.00/US
HAVING ERROR CORRECTION		
	HAVING ERROR CORRECTION	

ASSIGNMENT:

X	Enclosed for recording
	Previously recorded
Date:	
Reel:	

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned do hereby:

SELL, ASSIGN AND TRANSFER to Micron Technology, Inc. (the "Assignee"), a corporation of Delaware, having a place of business at 8000 South Federal Way, Boise, Idaho 83716-9632, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith and is entitled "SEMICONDUCTOR DEVICE HAVING ERROR CORRECTION CODE (ECC) CIRCUIT"; which claims priority to JP patent application filed on Jan 8, 2015, assigned Application No. 2015-002415, entitled, "SEMICONDUCTOR DEVICE HAVING ECC (ERROR CORRECTION CODE) CIRCUIT", such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States of America;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

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WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee;

Signature: John John Date: 9/11/2015

Witness # 1:

Signature: Shoko Tomita

Witness # 2:

Signature: Miku Takemasa

Miku Takemasa

Miku Takemasa

Miku Takemasa

Miku Takemasa

Date: 9/11/2015

Miku Takemasa

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