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PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:		NE	NEW ASSIGNMENT			
NATURE OF CONVEYANCE:		AS	ASSIGNMENT			
CONVEYING PARTY I	DATA					
		Nar	ne		Execution Date	
FLASHSILICON, INCORPORATION				09/15/2015		
RECEIVING PARTY D	ΑΤΑ					
Name:	PEGAS	SUS SEM	ICONDUCTOR INC.			
Street Address:	46 DON	IGZHIME	ENWAI DAJIE			
Internal Address:	TIANHE	ENG BUI	LDING, SUITE 1005, DOM	IGCH	ENG DISTRICT	
City:	BEIJING	G				
State/Country:	CHINA	CHINA				
Postal Code:	100027	,				
PROPERTY NUMBERS Total: 1 Property Type			Number			
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	DATA					
CORRESPONDENCE		(719)448	-5922			
CORRESPONDENCE Fax Number: Correspondence will	be sent to	the e-m	ail address first; if that i		uccessful, it will be sent	
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CORRESPONDENCE Fax Number: <i>Correspondence will using a fax number, in</i> Phone: Email: Correspondent Name Address Line 1:	be sent to f provided	<i>the e-m</i> <i>i; if that i</i> 719-448- patentcol HOGAN 2 NORTH	<i>ail address first; if that i</i> <i>s unsuccessful, it will b</i> 5900 oradosprings@hoganlove LOVELLS US LLP I CASCADE AVENUE, S ⁻	e sent ells.cor FE. 13	t via US Mail. m 00	
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CORRESPONDENCE Fax Number: <i>Correspondence will using a fax number, in</i> Phone: Email: Correspondent Name Address Line 1: Address Line 4:	be sent to f provided	<i>the e-m</i> <i>i; if that i</i> 719-448- patentcol HOGAN 2 NORTH COLORA	<i>ail address first; if that i</i> <i>s unsuccessful, it will b</i> 5900 oradosprings@hoganlove LOVELLS US LLP I CASCADE AVENUE, S ⁻	e sent ells.cor FE. 13	t via US Mail. m 00	
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ASSIGNMENT

Patent Number: 7,400,527 Application No.: 11/378,074 Application Title: BIT SYMBOL RECOGNITION METHOD AND STRUCTURE FOR MULTIPLE BIT STORAGE IN NON-VOLATILE MEMORIES

Assignee: Pegasus Semiconductor Inc. Assignee being a company, corporation, or juristic entity of: Peoples Republic of China Assignee's principal place of business: 46 Dongzhimenwai Dajie, Tianheng Building, Suite 1005, Dongcheng District, Beijing 100027

WHEREAS, Assignor, FlashSilicon, Incorporation, a Delaware corporation, was assigned certain rights in and to certain inventions, and application for Letters Patent of the United States entitled BIT SYMBOL RECOGNITION METHOD AND STRUCTURE FOR MULTIPLE BIT STORAGE IN NON-VOLATILE MEMORIES, filed on March 16, 2006 as U.S. Application No. 11/378,074; and issued as 7,400,527.

WHEREAS, Assignee, Pegasus Semiconductor Inc., residing or having a principal place of business at 46 Dongzhimenwai Dajie, Tianheng Building, Suite 1005, Dongcheng District, Beijing 100027, is desirous of acquiring the entire right, title and interest in and to the inventions and the application for Letters Patent of the United States, and in and to any Letters Patent to be obtained therefor and thereon worldwide.

Now, therefore, for and in consideration of good and valuable consideration, the receipt, sufficiency, and adequacy of which are hereby acknowledged, Assignor hereby transfers and assigns to Assignee, its legal representatives, successors, and assigns, its entire right, title, and interest in and to the inventions for which the above identified patent application is made and describes (hereinafter referred to as "the Patent Rights"), together with any and all patents or patent applications anywhere worldwide to which any of the Patent Rights directly or indirectly claims priority, including but not limited to provisional applications, thereof, or for which any of the Patent Rights directly or indirectly claims priority, including but not limited to provisional applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current, and future infringement thereof, together with all rights worldwide in the Patent Rights; said entire right, title, and interest, to be held and enjoyed by the Assignee for its own use and enjoyment and for the use and enjoyment of its legal representatives, successors, and assigns to the full end of the term for which the aforementioned rights may be granted anywhere in the world.

Assignor hereby further agrees to assist in and execute all documents needed now or in the future to perfect, obtain, and secure the aforementioned rights to Assignee, its legal representatives, successors, and assigns, for any jurisdiction in the world. At the expense of Assignee, or its legal representatives, successors, or assigns, Assignor agrees to assist in any legal proceedings, sign all lawful papers, make all lawful oaths, and generally do everything possible to aid Assignee, its legal representatives, successors, and assigns, to enforce the aforementioned rights in any and all countries and regions worldwide.

Assignor hereby grants Assignee, along with the following Assignee representatives, the power to insert in this Assignment any further identification that may be necessary or desirable in order to comply with the rules for recordation of this document any place in the world: All practitioners at Customer Number 25235.

Date:	9/15/2015	Signature:	
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PATENT REEL: 036582 FRAME: 0044

RECORDED: 09/16/2015