503497222 09/25/2015

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT3543847

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
KRISHNASWAMY RAMKUMAR	03/22/2012

RECEIVING PARTY DATA

Name:	Cypress Semiconductor Corporation	
Street Address:	198 Champion Court	
City:	San Jose	
State/Country:	CALIFORNIA	
Postal Code:	95134	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	14201456

CORRESPONDENCE DATA

Fax Number: (408)545-6911

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 408-943-6878 Email: pbj@cypress.com

Correspondent Name: CYPRESS SEMICONDUCTOR CORPORATION

Address Line 1: 198 CHAMPION COURT

Address Line 4: SAN JOSE, CALIFORNIA 95134

ATTORNEY DOCKET NUMBER:	CD11072C1	
NAME OF SUBMITTER: CHRISTOPHER JORDAN		
SIGNATURE:	/Christopher Jordan/	
DATE SIGNED:	09/25/2015	

Total Attachments: 2

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PATENT REEL: 036661 FRAME: 0968 503497222

legal representatives; and

ASSIGNMENT OF PATENT APPLICATION	Attorney Docket No.: CD11072		
For good and valuable consideration, the receipt and sufficiency of which is hereby			
acknowledged, I/we, the undersigned, Krishnaswamy Ramku	mar		
and the second s			
hereby sell, assign, and transfer to <u>Cypress Semiconduct</u>	or Corporation		
•	of Marie and		
a <u>corporation</u> (Type of Assignee: e.g., corporation, company, partnership, university, et	of <u>Delaware</u> ,		
having a principal place of business at 198 Champion Cou	rt. San Jose, California 95134-1709		
("Assignee"), and its successors, assigns, and legal represe for the United States and all foreign countries, In and to any are disclosed in the application (provisional or non-provision have a declaration executed by the undersigned prior hereto	and all inventions or improvements that hall for the United States patent which may		
METHOD OF INTEGRATING A CHARGE-TRA	APPING GATE STACK		
INTO A CMOS FLOW			
said patent application also identified as follows (when know	vn):		
[l/we hereby authorize an attorney or agent for said number and filing date of said patent application who			
United States Patent Application Number 13/428,201	filed March 23, 2012		
and in and to said application (provisional or non-provisional provisional applications, utility applications, design applications applications, continued prosecution applications, continuated applications, renewal applications, reissue applications, receptant applications that have been or shall be filed in the Unany of said inventions or improvements; and in and to all or reexamination certificates, and extensions, that have been call foreign countries on said inventions or improvements; and from the filing of said United States application;	ons, divisional applications, continuation on-in-part applications, substitute xaminations, extensions, and all other nited States and all foreign countries on ginal patents, reissued patents, or shall be issued in the United States and		
agree that said Assignee may apply for and receive improvements in its own name; and that, when requested, we said Assignee, its successors, assigns, and legal represents and purpose of this Assignment, the undersigned will execult provisional applications, utility applications, design applications, continued prosecution applications, continuated applications, renewal applications, reissue applications, received applications on any and all said inventions or improve assignments, powers of attorney, and other papers; communassigns, and representatives all facts known to the undersign improvements and the history thereof; and generally assists representatives in securing and maintaining proper patent proper patent provements and for vesting title to said inventions or improvements and for vesting title to said inventions or improvements.	vithout charge to, but at the expense of, atives, to carry out in good faith the intent ite all provisional applications, non-ons, divisional applications, continuation on-in-part applications, substitute xaminations, extensions, and all other ements; execute all rightful oaths, inicate to said Assignee, its successors, and relating to said inventions or said Assignee, its successors, assigns, or protection for said inventions or		

and all patents on said inventions or improvements, in said Assignee, its successors, assigns, and

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Rev. 8/03/2011

PATENT

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covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Each Inventor: Please Sign and Date Below:

3 /22 /2012	Kraneeu
Date	Signature
	Krishnaswamy Ramkumar
	Printed/Typed Name
	and the grown of the potential of the second
Date	Signature
	Printed/Typed Name
, 20 <u></u> , 20 <u></u>	Signature
	Printed/Typed Name

<u>Assignment Document Return Address:</u> CYPRESS SEMICONDUCTOR CORPORATION 198 Champion Court San Jose, CA 95134 (408) 545-7194

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Rev. 8/04/2011

PATENT

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RECORDED: 03/23/2012 REEL: 036661 FRAME: 0970 **RECORDED: 09/25/2015**