

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE	09/12/2014
RECEIVING PARTY DATA	
Name:	INVENSAS CORPORATION
Street Address:	3025 ORCHARD PARKWAY
City:	SAN JOSE
State/Country:	CALIFORNIA
Postal Code:	95134
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	14740184
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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Correspondent Name:	INVENSAS CORPORATION
Address Line 1:	3025 ORCHARD PARKWAY
Address Line 2:	ATTN: PATENT DEPT.
Address Line 4:	SAN JOSE, CALIFORNIA 95134
ATTORNEY DOCKET NUMBER:	IN131-0001-US-06_ITRI2INV
NAME OF SUBMITTER:	CHRISTOPHER W. LATTIN
SIGNATURE:	/Christopher W. Lattin/
DATE SIGNED:	09/30/2015
Total Attachments: 3	
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DEED OF ASSIGNMENT

THIS DEED OF ASSIGNMENT ("Assignment"), EFFECTIVE AS OF July 12 2012, IS MADE BY AND BETWEEN

INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE (hereinafter "ASSIGNOR"), a non-profit research institute organized under the laws of Taiwan, Republic of China with its principal place of business located at Rm 135, Bldg. 52, No. 195, Sec. 4, Chung Hsing Rd., Chutung, Hsinchu 310, Taiwan, R.O.C.; and

INVENSAS CORPORATION (hereinafter "INVENSAS"), a Delaware corporation having a place of business at 2702 Orchard Parkway, San Jose, California 95134, United States.

WHEREAS:

- A ASSIGNOR is the sole owner in respect of the patents and patent applications listed in the attached Appendix (hereinafter "the PATENTS"); and
- B INVENSAS is desirous of acquiring all of the worldwide right, title and interest in and to the PATENTS and the inventions claimed therein.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, ASSIGNOR has sold, assigned and transferred, and does hereby sell, assign and transfer to INVENSAS all of the worldwide right, title and interest in (i) the PATENTS and the inventions and improvements disclosed therein; (ii) all reissues, divisionals, continuations, continuations-in-part, extensions, renewals, reexaminations and foreign counterparts thereof, and other patents, patent applications, certificates of invention other governmental grants resulting from the PATENTS; (iii) all patents and applications which claim priority to or have substantially identical disclosure or common priority with any such patents or patent applications, and (iv) any Purchased Patent that as of the Effective Date is subject to a Disclaimer Issue with respect to any other such patent or patent application; and (v) all rights corresponding to any of the foregoing throughout the world (including the right to claim the priority date of any of the PATENTS and the right to sue for and recover damages for any past, present or future infringement of the Patents), the same to be held and enjoyed by INVENSAS for its own use and enjoyment, and for the use and enjoyment of its successors, assigns and other legal representatives, to the end of the term or terms of said PATENTS granted or reissued or reexamined as fully and entirely as the same would have been held and enjoyed by ASSIGNOR, if this assignment and sale had not been made.

IN WITNESS WHEREOF, ASSIGNOR has caused these presents to be signed by its duly appointed trustee having full authority to convey its property; and INVENSAS has caused these presents to be signed by its duly appointed trustee.

And if the issue date and/or patent number of any of the PATENTS is unknown to ASSIGNOR and INVENSAS at the time this Assignment is executed, ASSIGNOR does hereby authorize its attorneys to insert on this Assignment the issue date and patent number of said any patent when known.

ASSIGNOR hereby declares that INVENSAS may take the steps for recordal of this assignment in the sole name of INVENSAS.

SIGNED for and on behalf of

INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE

By Pin-Pin Chang on July 12, 2012
(Signature) (Date)

Pin-Pin Chang (Director)
(Print Name and Title)

ATTESTATION OF SIGNATURE

The undersigned witnessed the signature of Pin-Pin Chang to the above DBED OF ASSIGNMENT on behalf of Industrial Technology Research Institute and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
2. Pin-Pin Chang is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on July 12 2012 to execute the above Assignment of Patent Rights on behalf of Industrial Technology Research Institute.
3. Pin-Pin Chang subscribed to the above Assignment of Patent Rights on behalf of Industrial Technology Research Institute.

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on July 12, 2012 (date)

Jean S.C. Liao
Print Name: Su-Chen Liao

SIGNED for and on behalf of

INVENSAS CORPORATION

by [Signature] on July 30, 2012
(Signature) (Date)

SIMON MCELREA, PRESIDENT
(Print Name and Title)

State of California) SS:

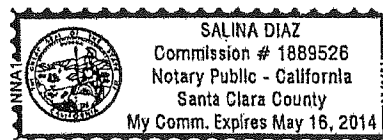
County of Santa Clara)

On this 30th day of July, 2012 before me, Salina Diaz, Notary Public, personally appeared SIMON MCELREA, who proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the instrument and acknowledged to me that he executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

[Signature]
(Notary Public)



APPENDIX – LISTED PATENTS AND PATENT APPLICATIONS

U.S. Issued Patents

Patent Number	Filing Date	Application Number	Issue Date	Title
7,294,920	07/22/2005	11/186,840	11/13/2007	Wafer-leveled chip packaging structure and method thereof
7,528,009	04/19/2007	11/785,612	05/05/2009	Wafer-leveled chip packaging structure and method thereof

U.S. Pending Applications

Application Number	Filing Date	Publication Number	Title
11/905,869	10/05/2007	US 2008/0029870	Wafer-leveled chip packaging structure and method thereof
13/533,251	06/26/2012		Wafer-Leveled Chip Package Structure And Method Thereof

Foreign Issued Patents

Country	Application Number	Filing Date	Patent Number	Title
TW	93122038	07/23/2004	I250596	Wafer-leveled chip packaging structure and method thereof